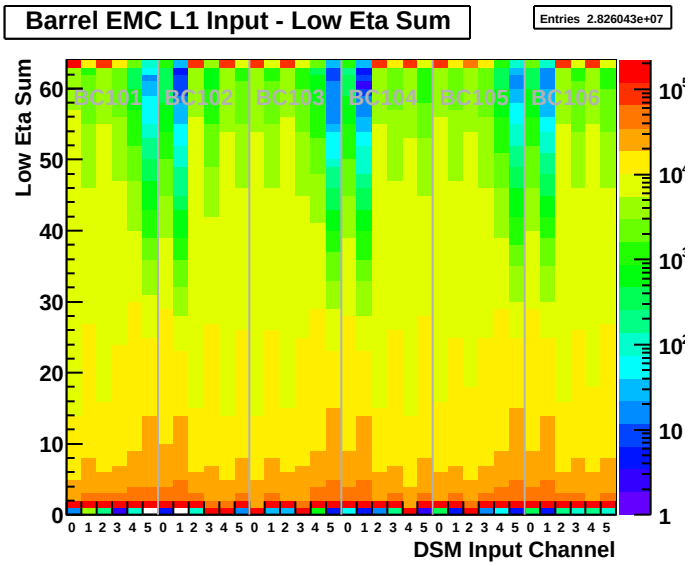
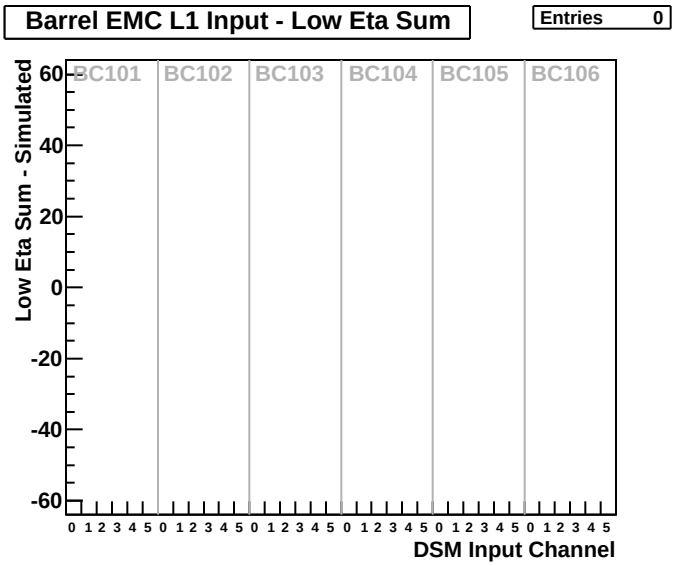
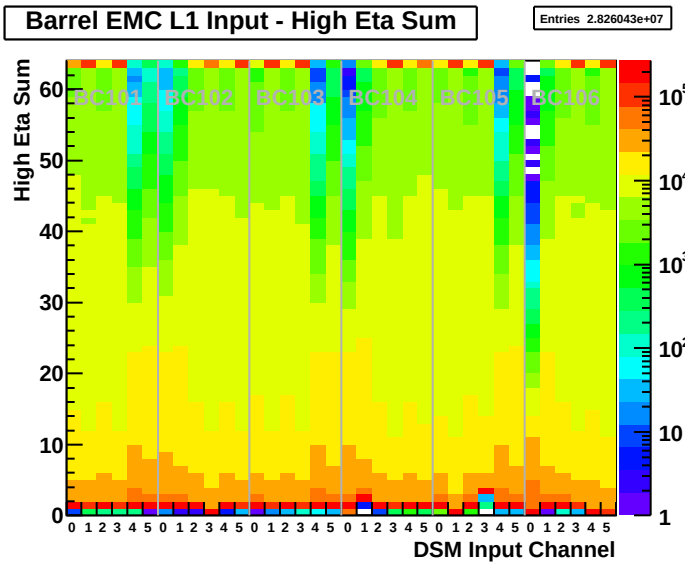
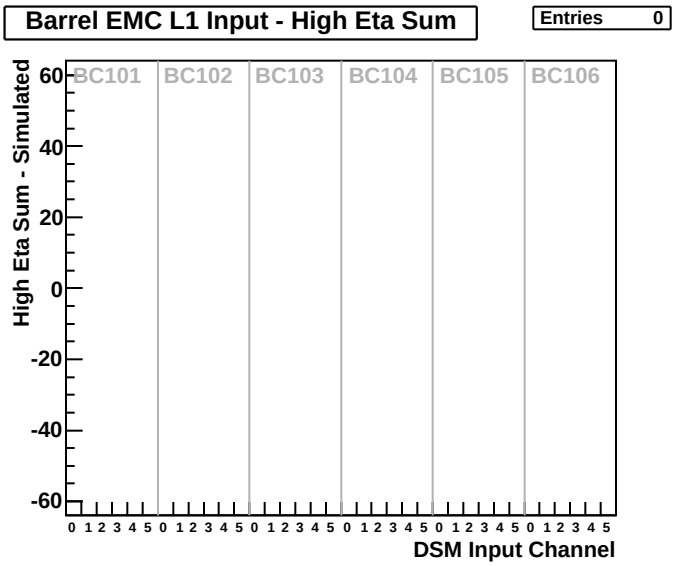
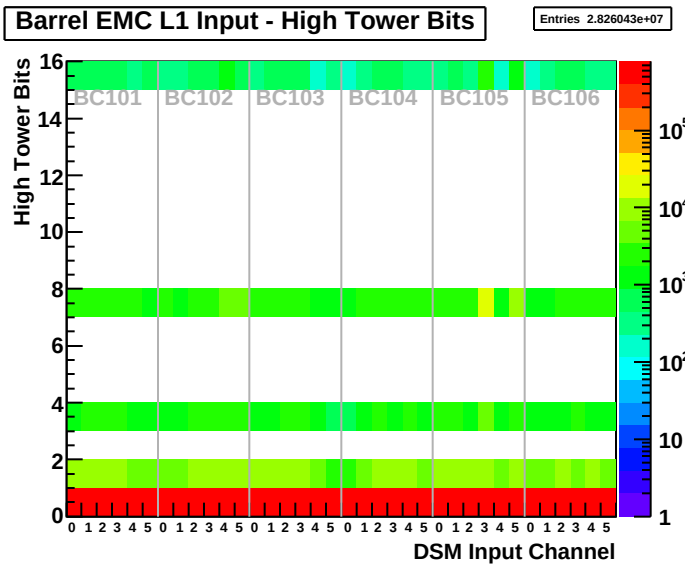
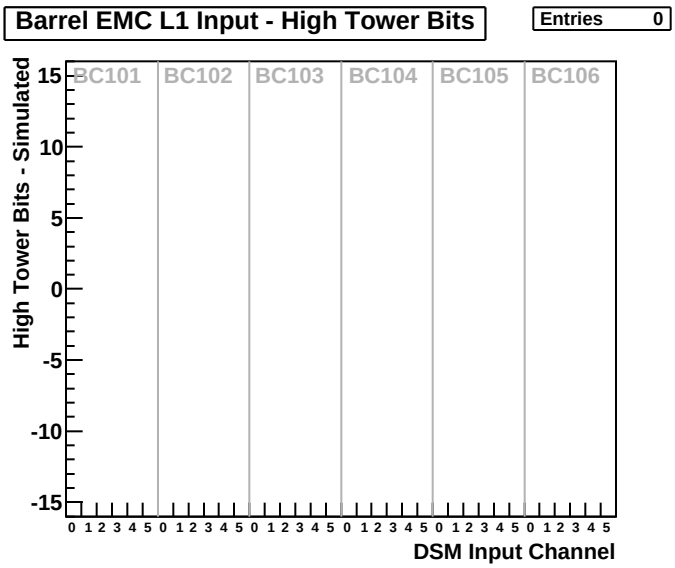

Barrel EMC L1 Input - Low Eta Sum
Entries 2.826043e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

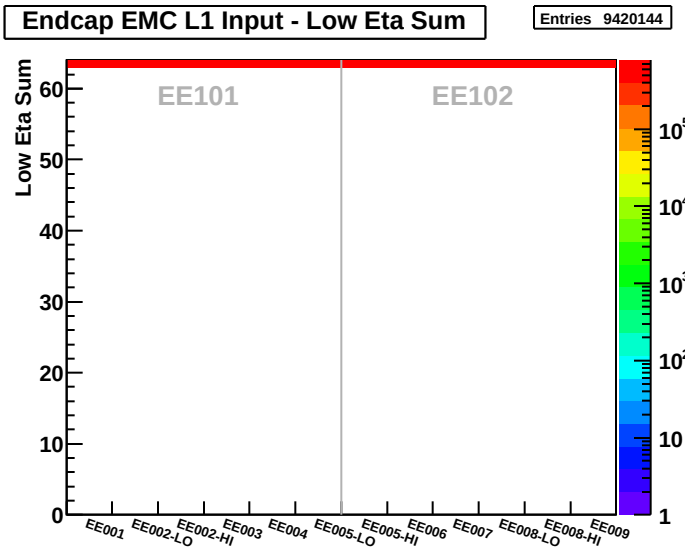

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

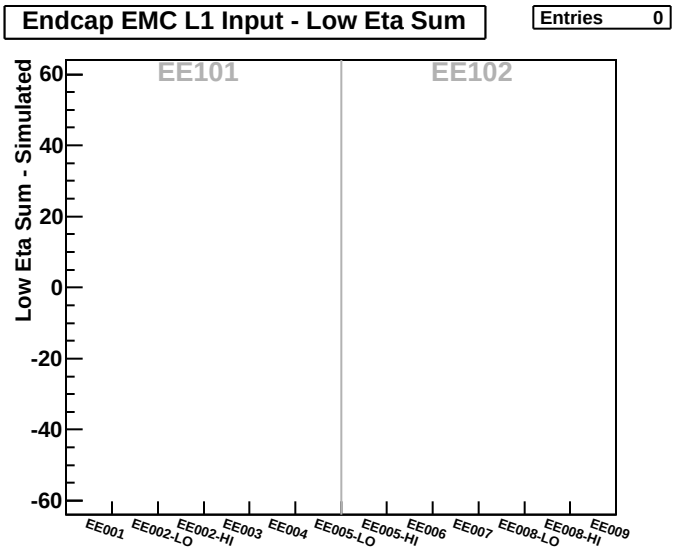

Barrel EMC L1 Input - High Eta Sum
Entries 2.826043e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

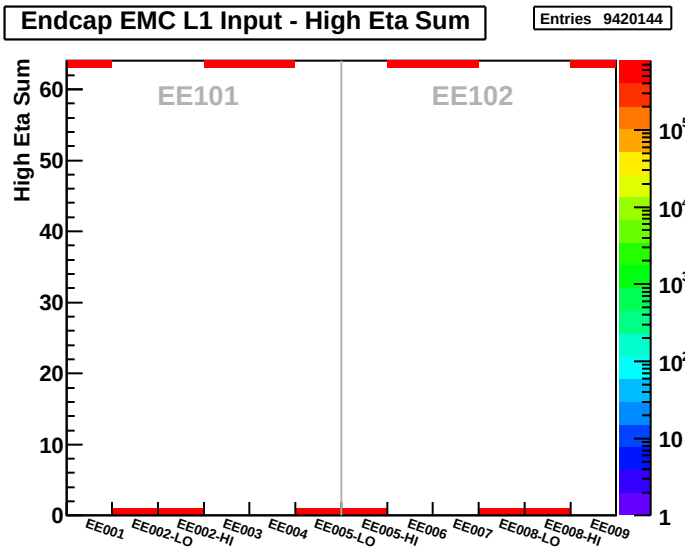

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

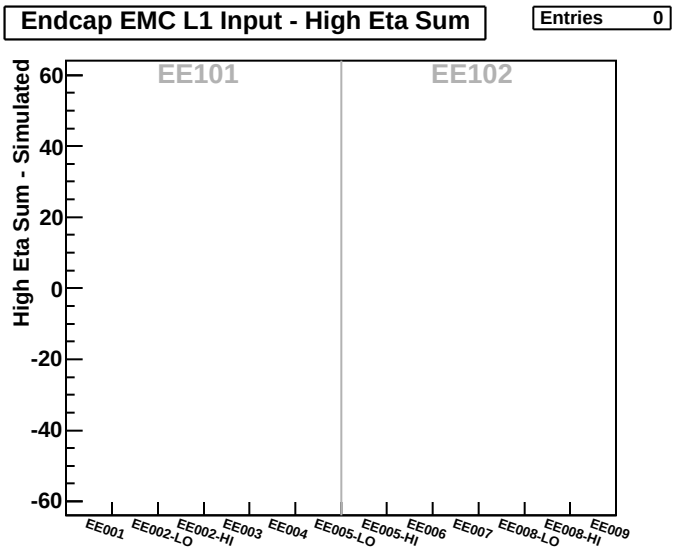

Barrel EMC L1 Input - High Tower Bits
Entries 2.826043e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

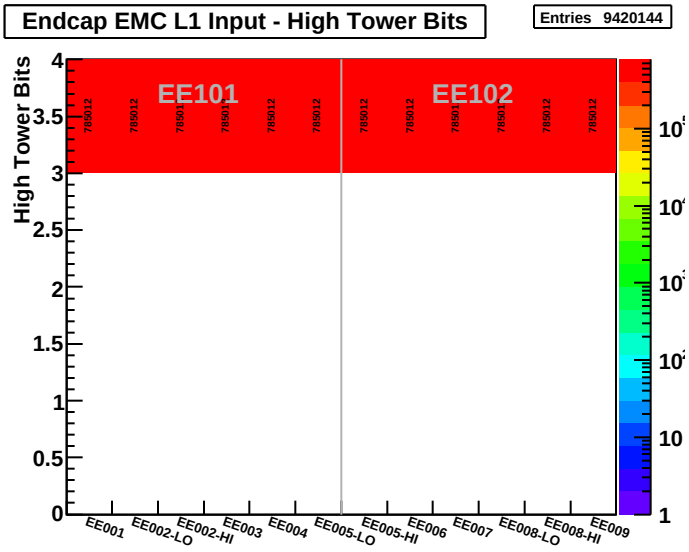

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

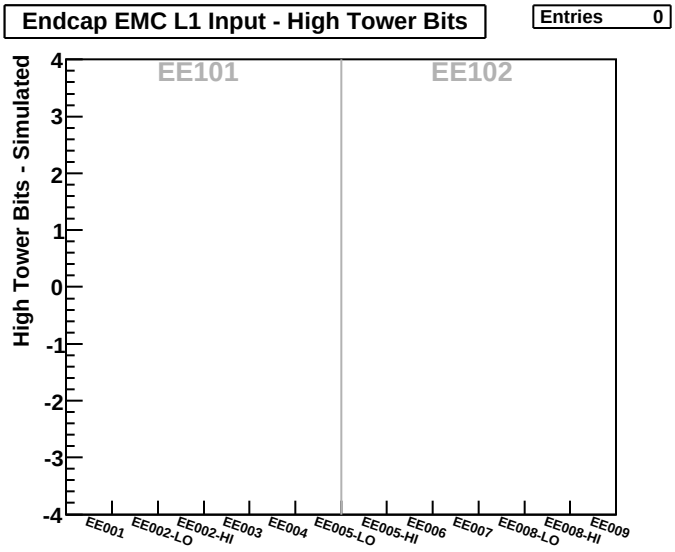

Endcap EMC L1 Input - Low Eta Sum
Entries 9420144
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

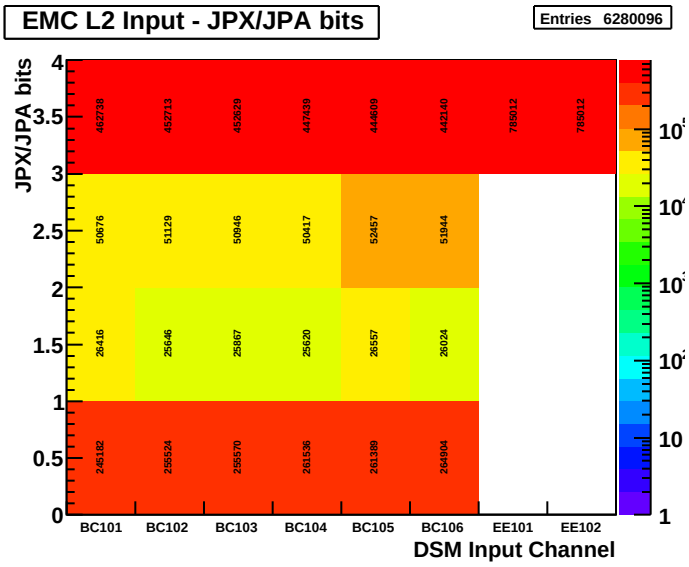

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

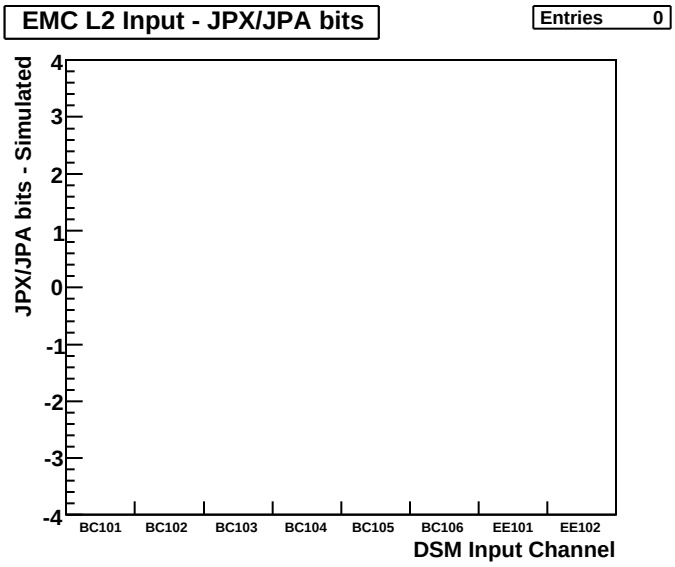

Endcap EMC L1 Input - High Eta Sum
Entries 9420144
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

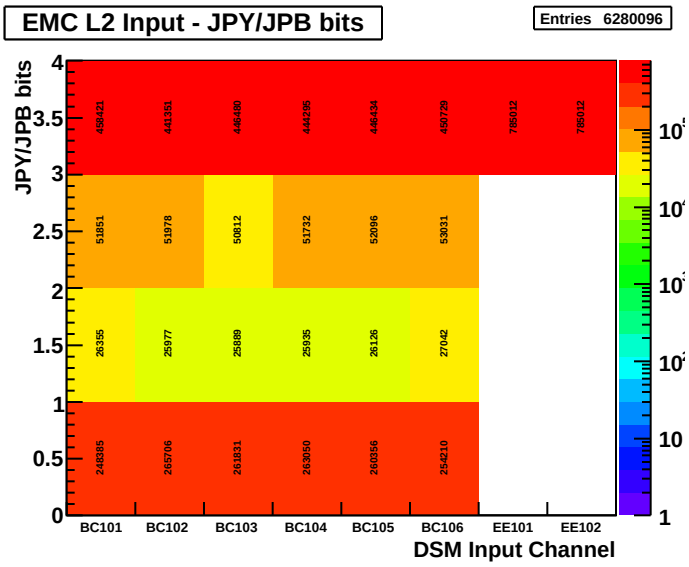

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

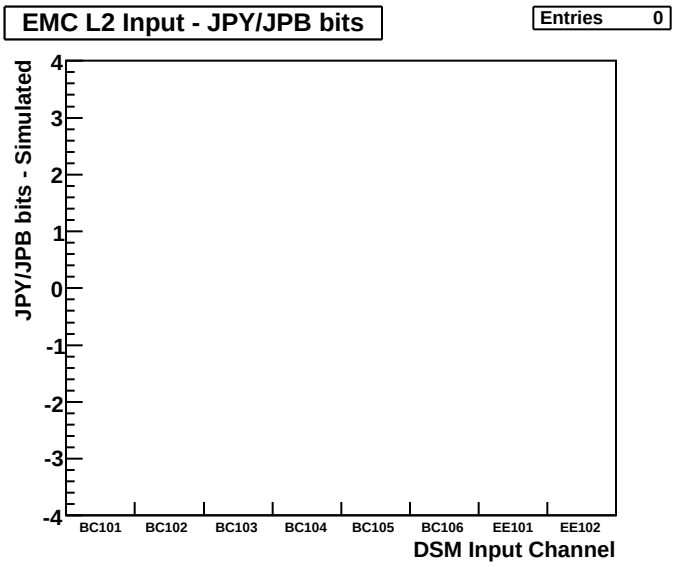

Endcap EMC L1 Input - High Tower Bits
Entries 9420144
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

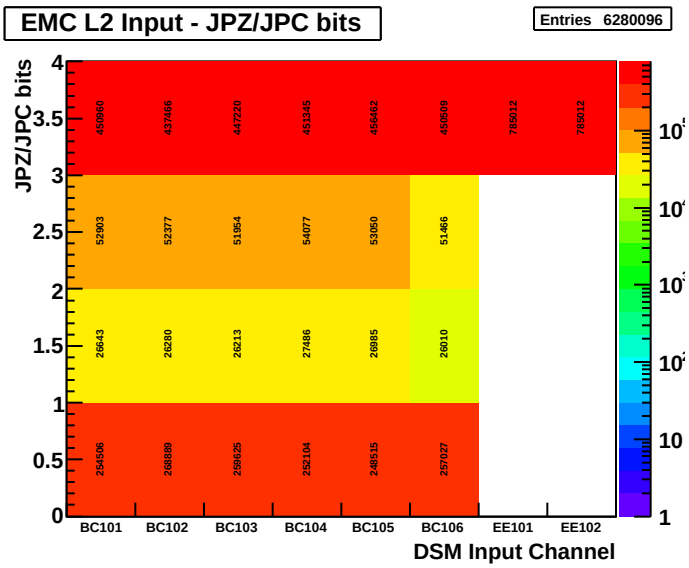

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

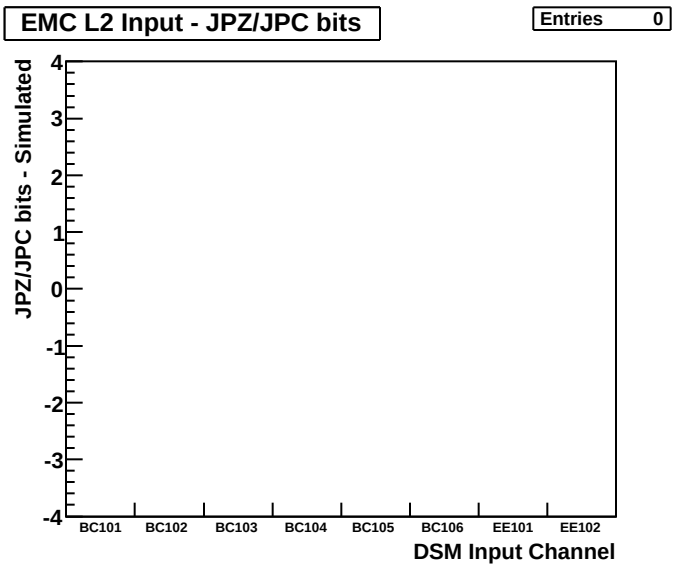

EMC L2 Input - JPX/JPA bits
Entries 6280096
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
452738 452713 452829 447439 444609 442140 785012 785012
50676 51129 50946 50417 52457 51944
26416 25646 25867 25620 26557 26024
245182 255524 255570 261536 261389 264904

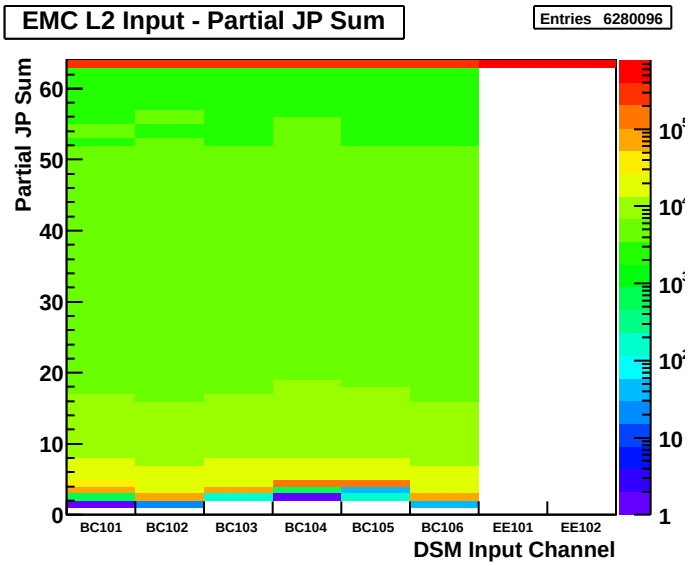

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

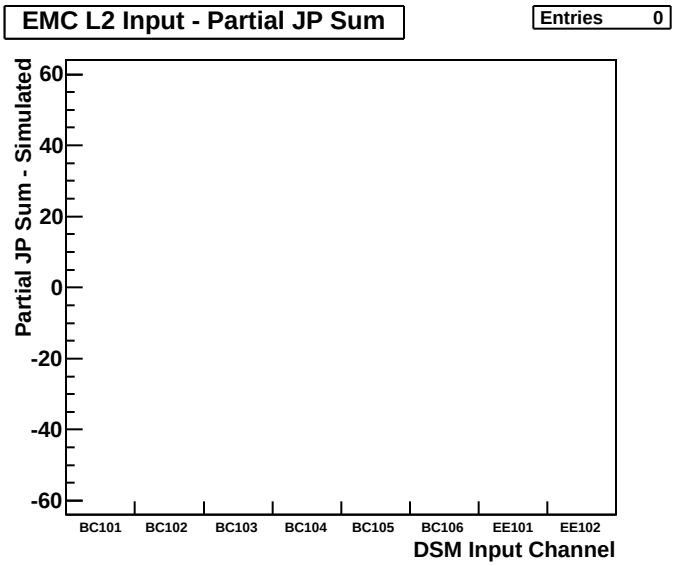

EMC L2 Input - JPY/JPB bits
Entries 6280096
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
458421 441351 446480 444295 446434 450729 785012 785012
53851 53978 50812 51732 52096 53031
26355 25977 25889 25935 26126 27042
246385 265706 261831 263050 260356 254210

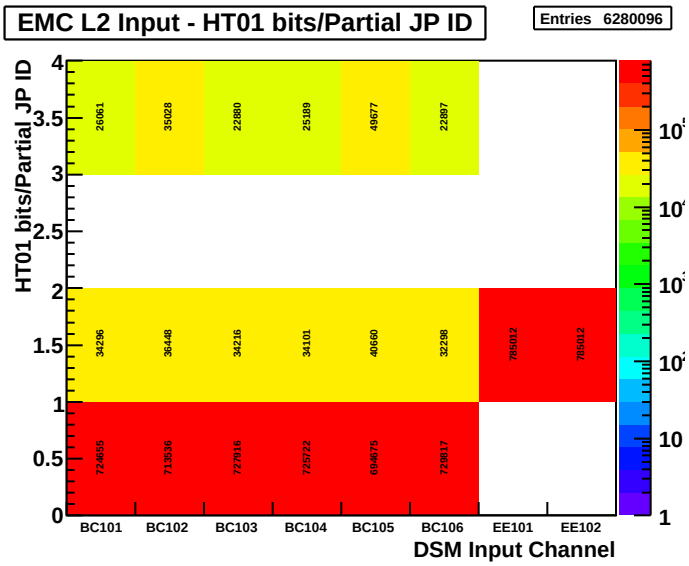

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

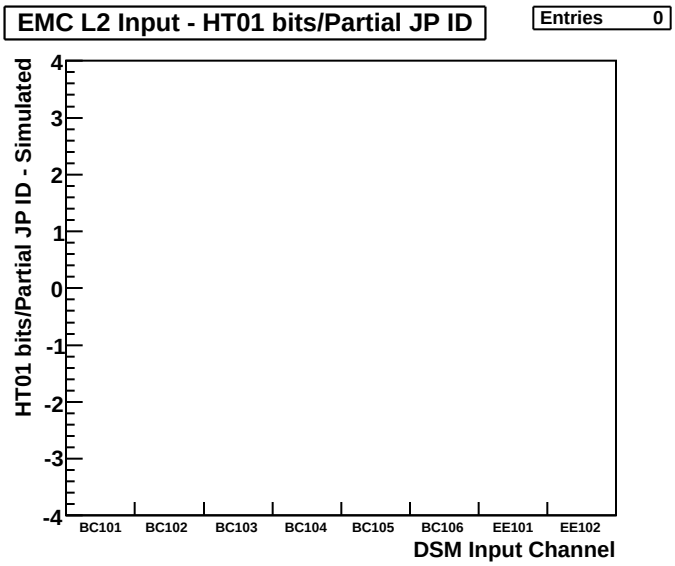

EMC L2 Input - JPZ/JPC bits
Entries 6280096
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
450960 437466 447220 451345 455482 450509 785012 785012
52903 52377 51954 54077 53050 51466
26643 26280 26213 27486 26985 26010
254506 268389 259625 262104 248515 257027

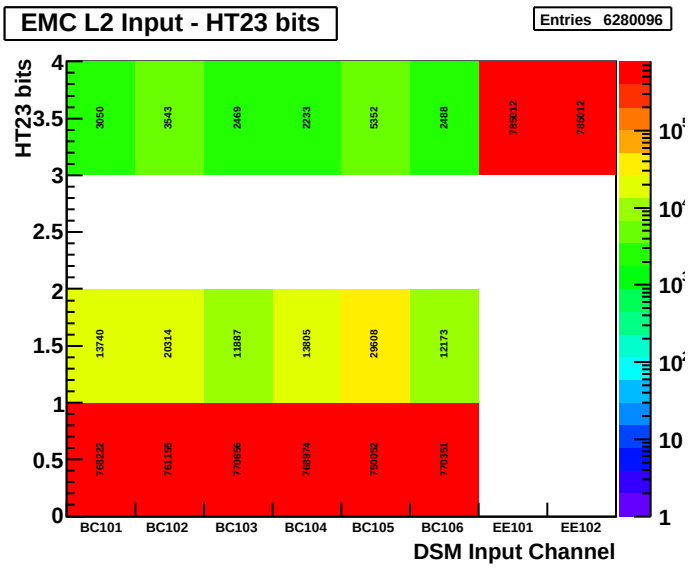

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

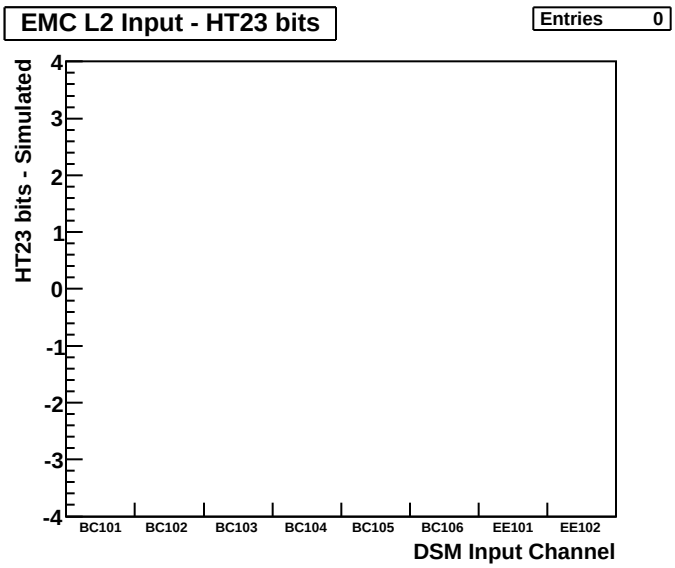

EMC L2 Input - Partial JP Sum
Entries 6280096
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

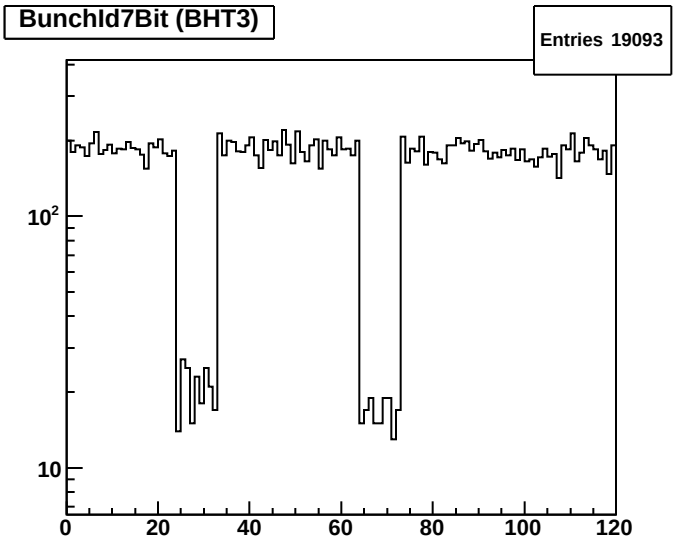

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

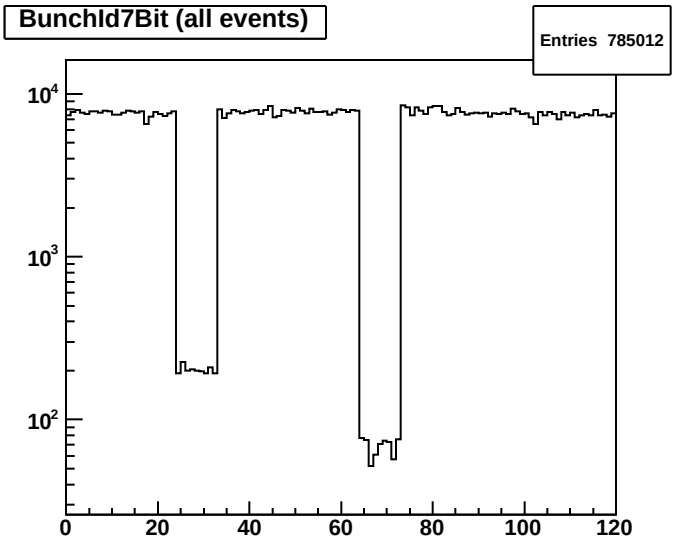

EMC L2 Input - HT01 bits/Partial JP ID
Entries 6280096
HT01 bits/Partial JP ID
26061 35028 22880 25189 49677 22897
34296 36448 34216 34101 40660 32298 785012 785012
724655 713536 727916 725722 694675 729817
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

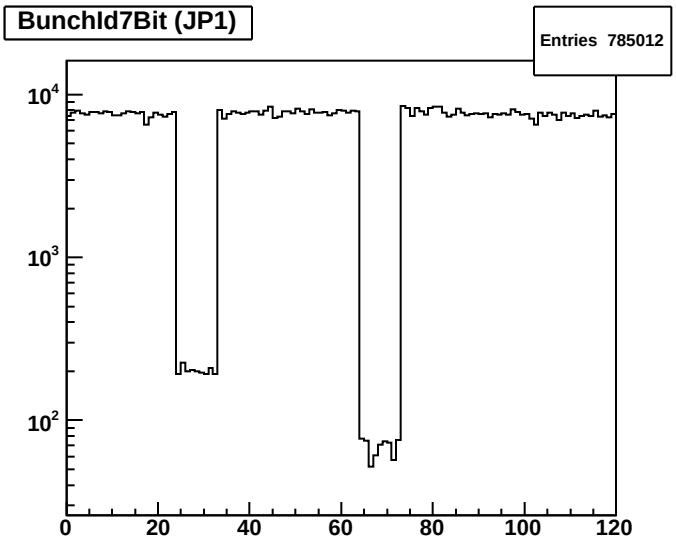

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

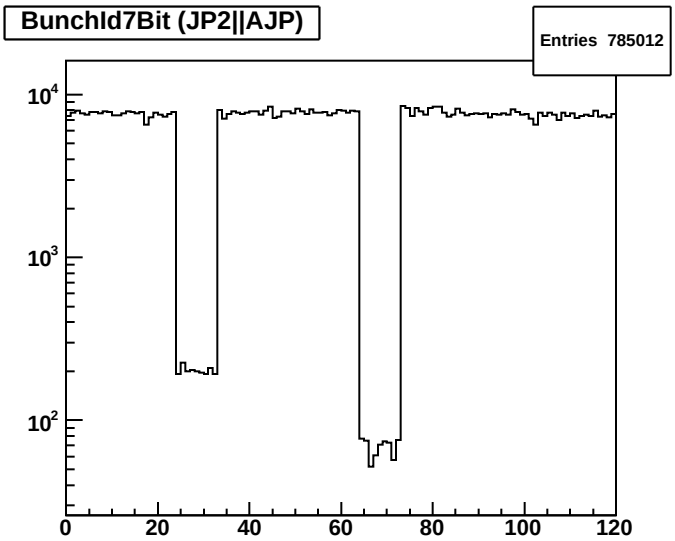

EMC L2 Input - HT23 bits
Entries 6280096
HT23 bits
3050 3543 2469 2233 5352 2488 785012 785012
13740 20314 11887 13805 29608 12173
768222 761155 770556 768974 750052 770351
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

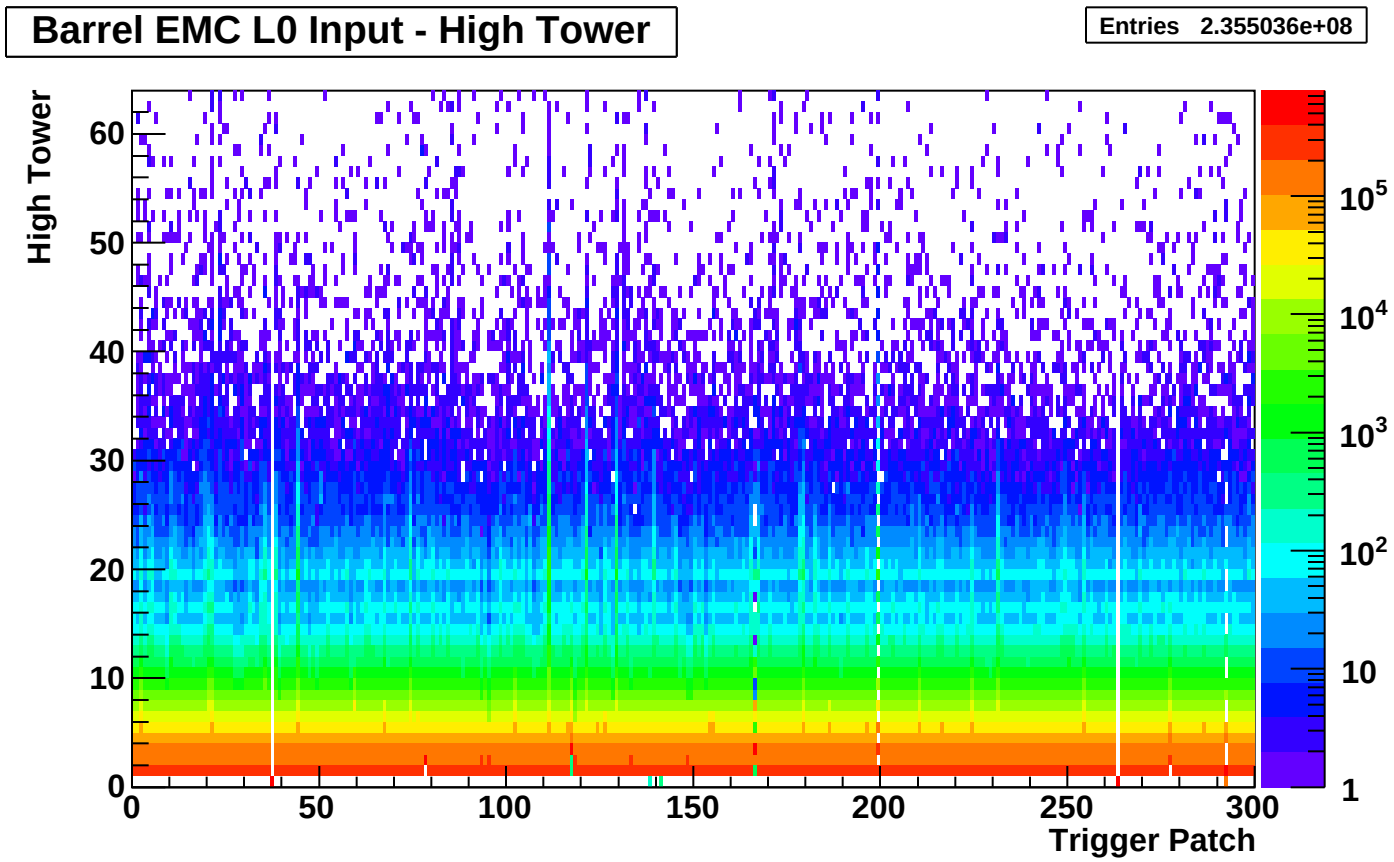

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

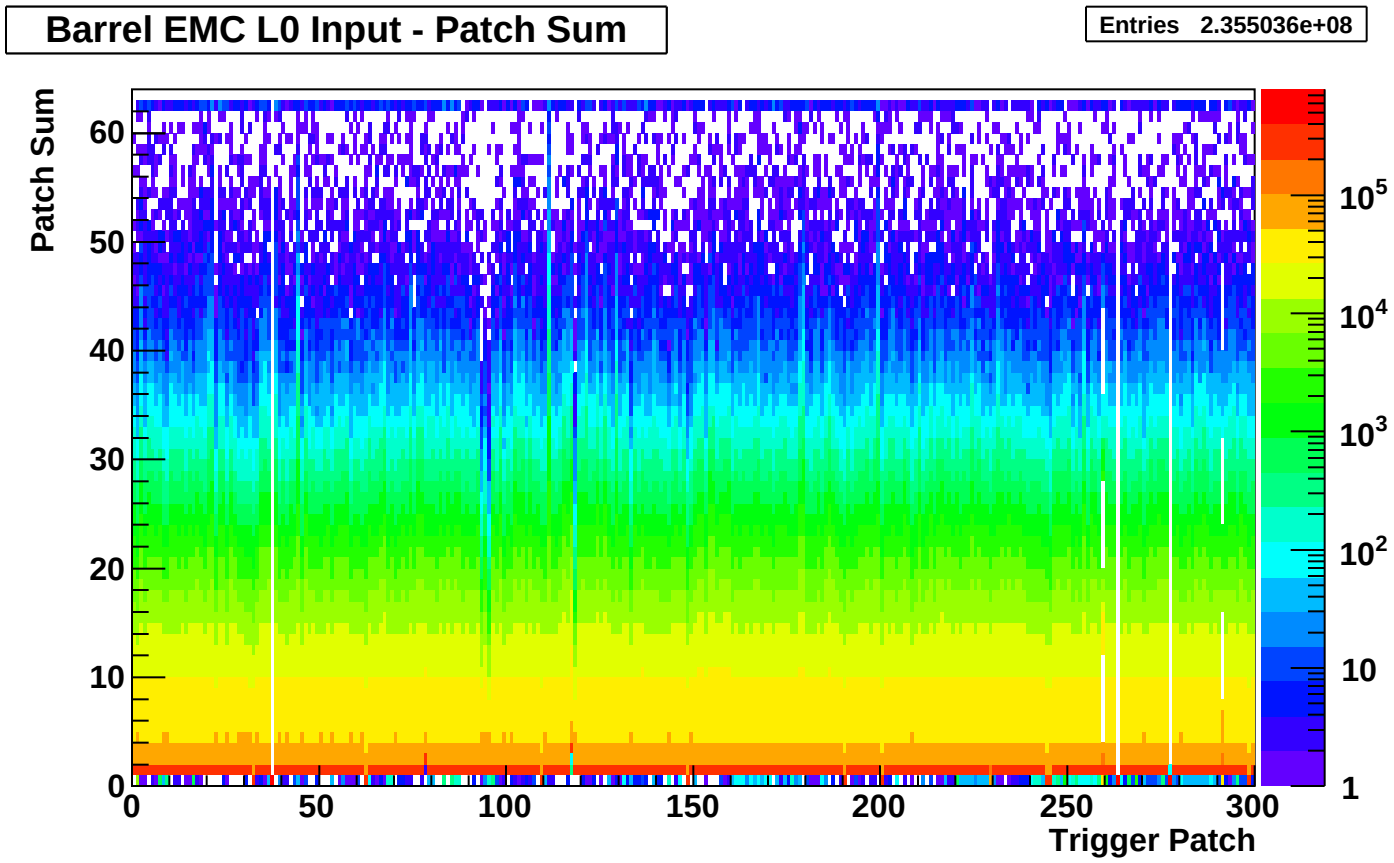

BunchId7Bit (BHT3)
Entries 19093
10^2
10
0
20
40
60
80
100
120

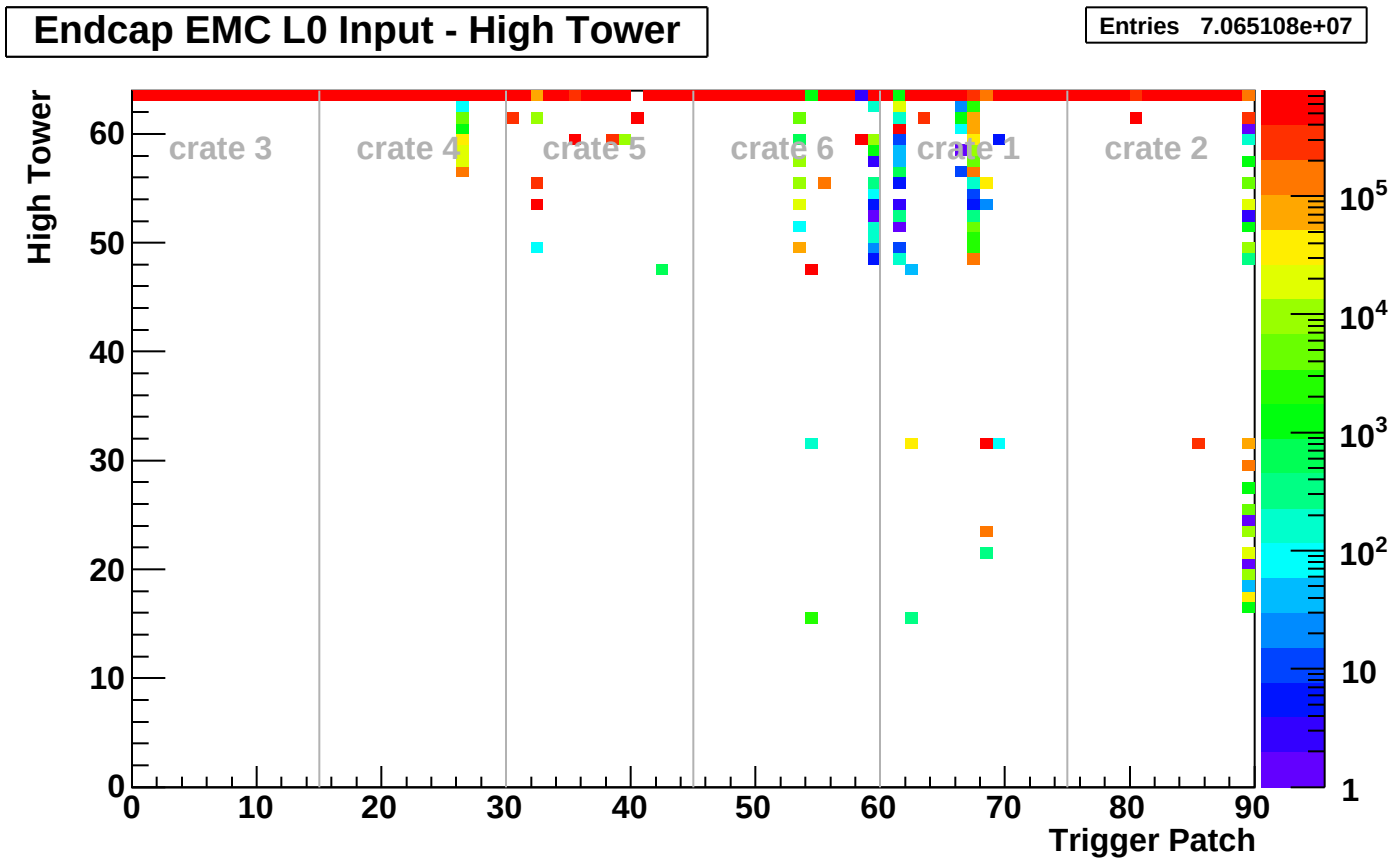

BunchId7Bit (all events)
Entries 785012
10^4
10^3
10^2
0
20
40
60
80
100
120

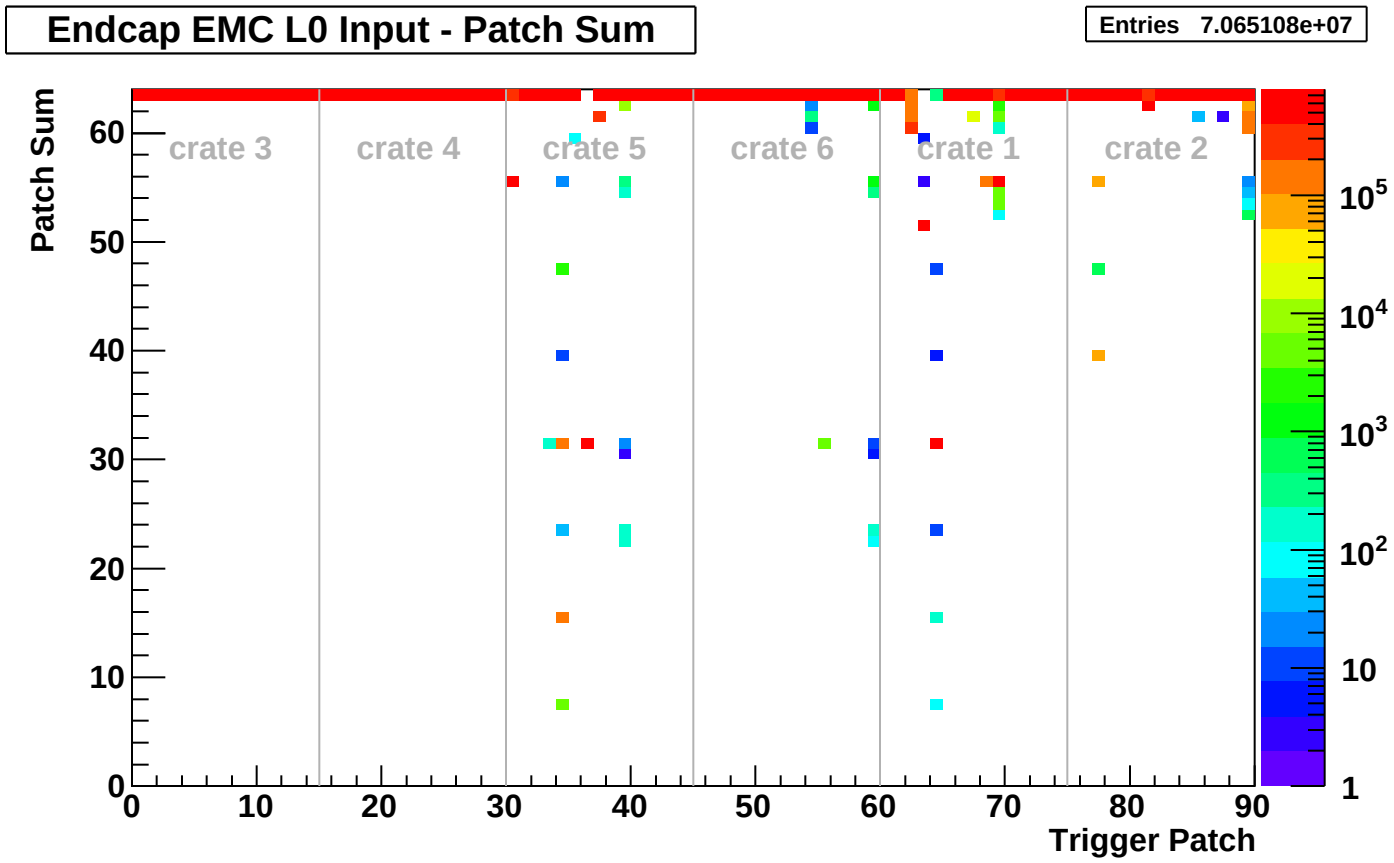

BunchId7Bit (JP1)
Entries 785012
10^4
10^3
10^2
0
20
40
60
80
100
120

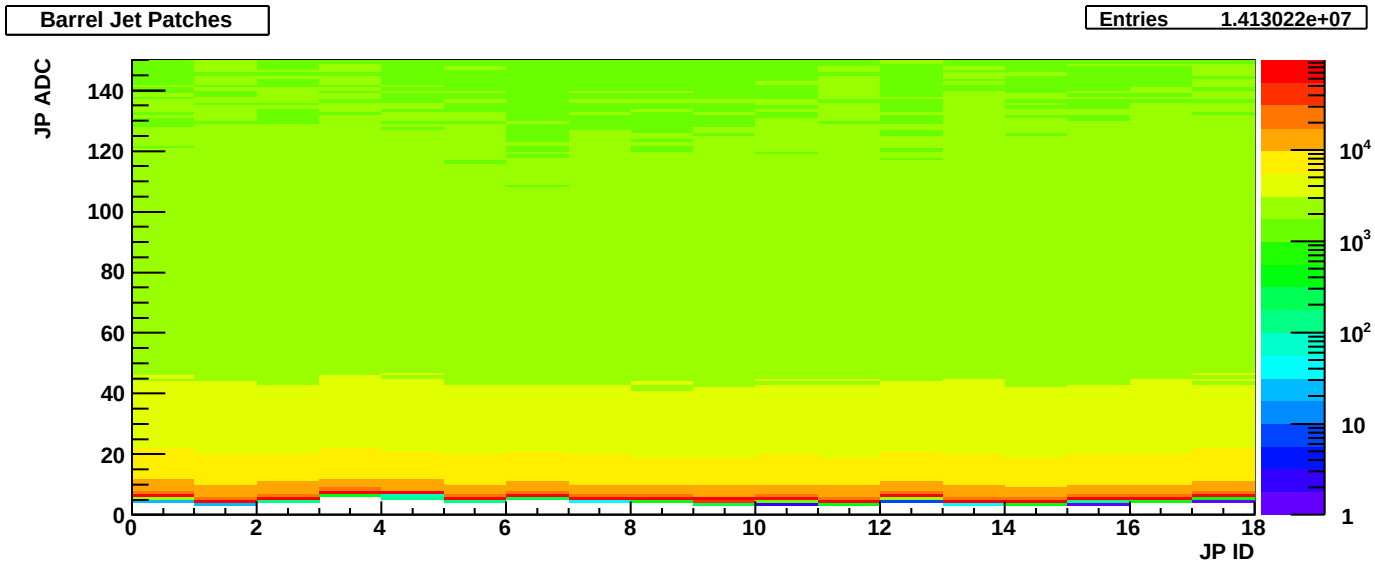

BunchId7Bit (JP2||AJP)
Entries 785012
10^4
10^3
10^2
0
20
40
60
80
100
120

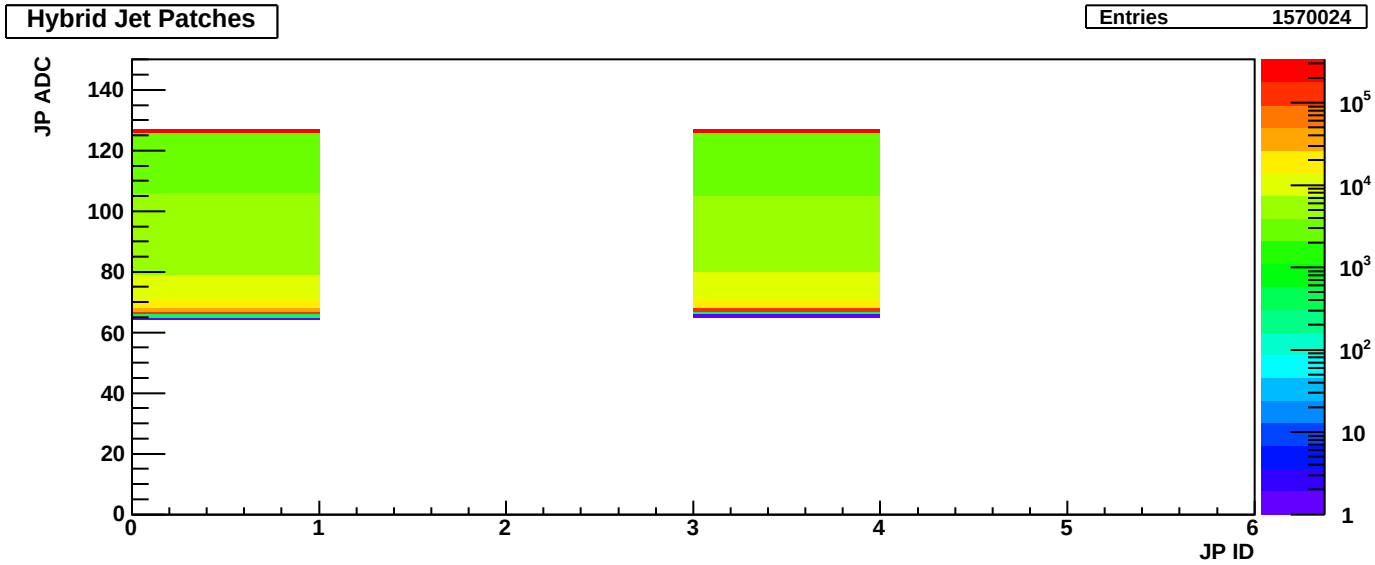

Barrel EMC L0 Input - High Tower
Entries 2.355036e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

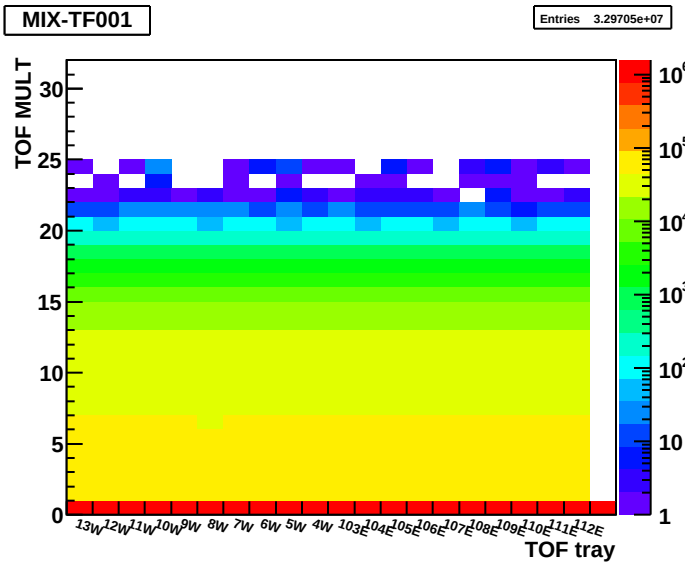

Barrel EMC L0 Input - Patch Sum
Entries 2.355036e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

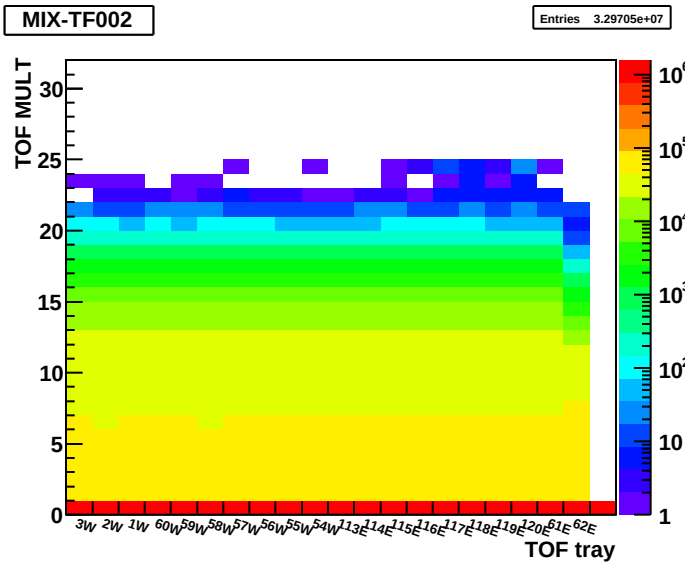

Endcap EMC L0 Input - High Tower
Entries 7.065108e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

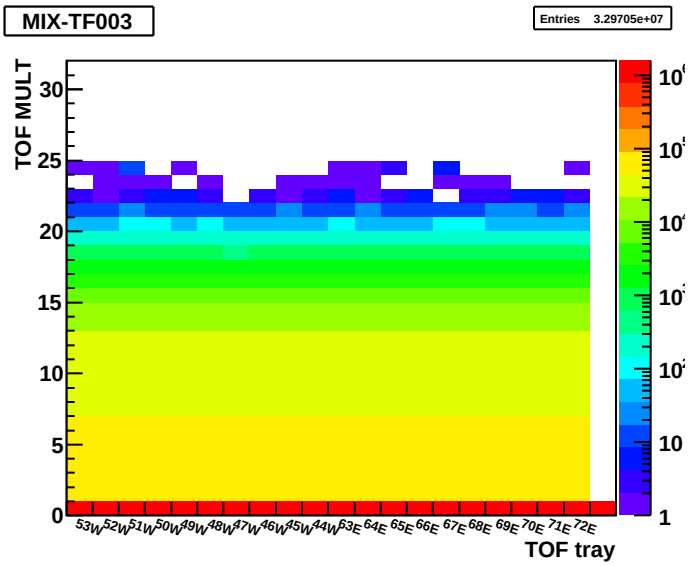

Endcap EMC L0 Input - Patch Sum
Entries 7.065108e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

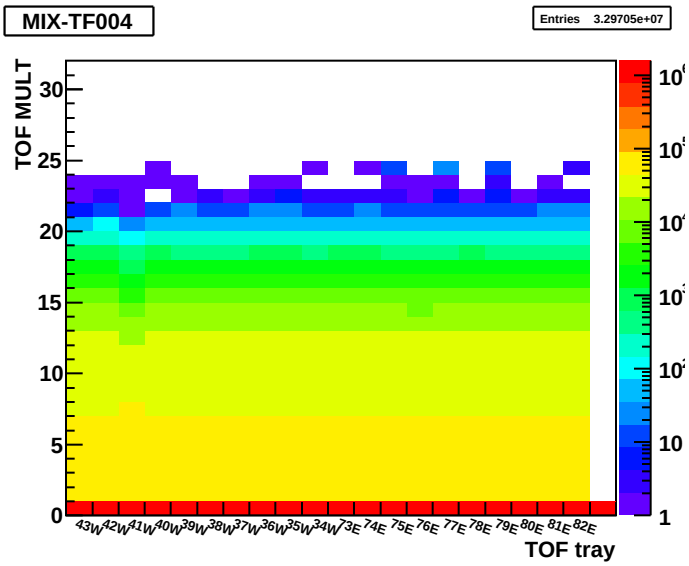

Barrel Jet Patches
Entries 1.413022e+07
JP ADC
JP ID

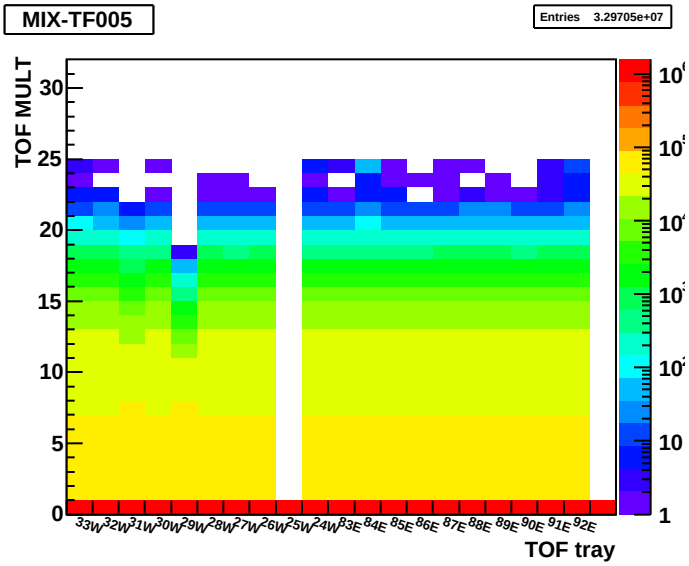

Hybrid Jet Patches
Entries 1570024
JP ADC
JP ID

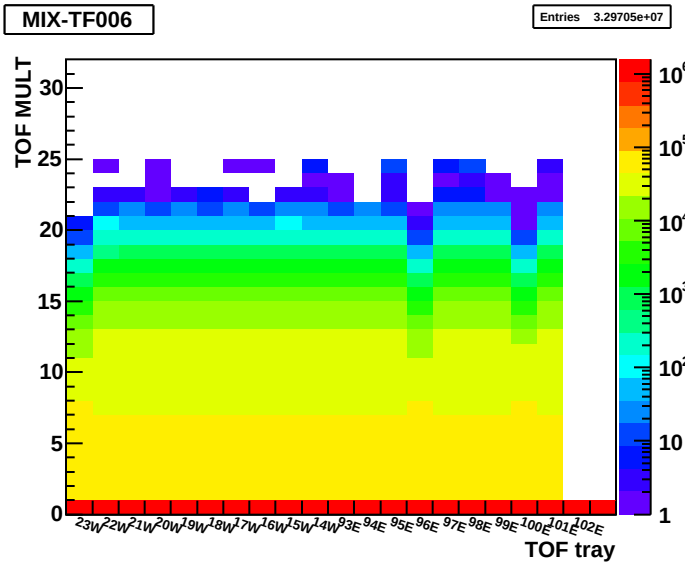

MIX-TF001
Entries 3.29705e+07
TOF MULT
TOF tray

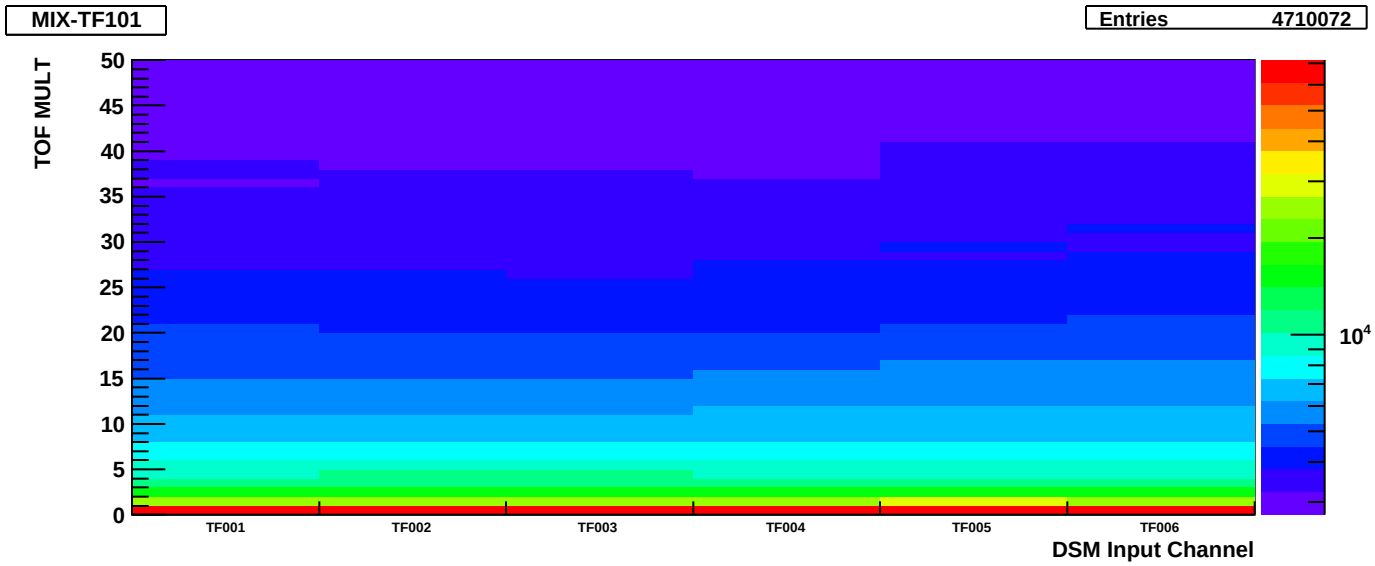

MIX-TF002
Entries 3.29705e+07
TOF MULT
TOF tray

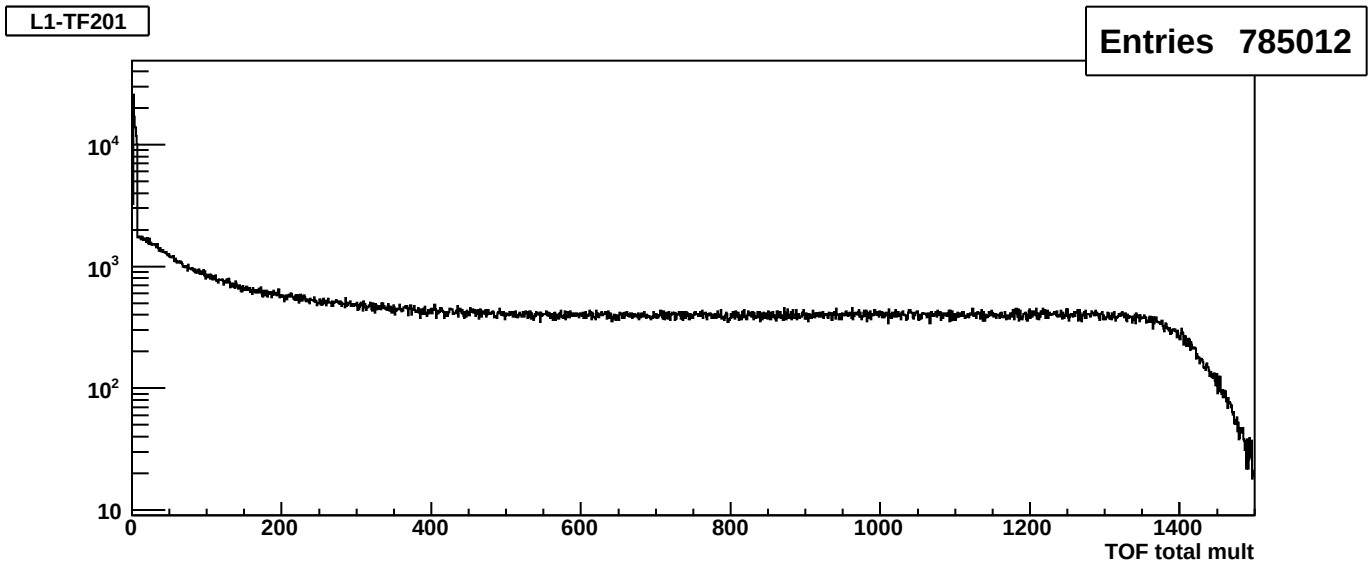

MIX-TF003
Entries 3.29705e+07
TOF MULT
TOF tray

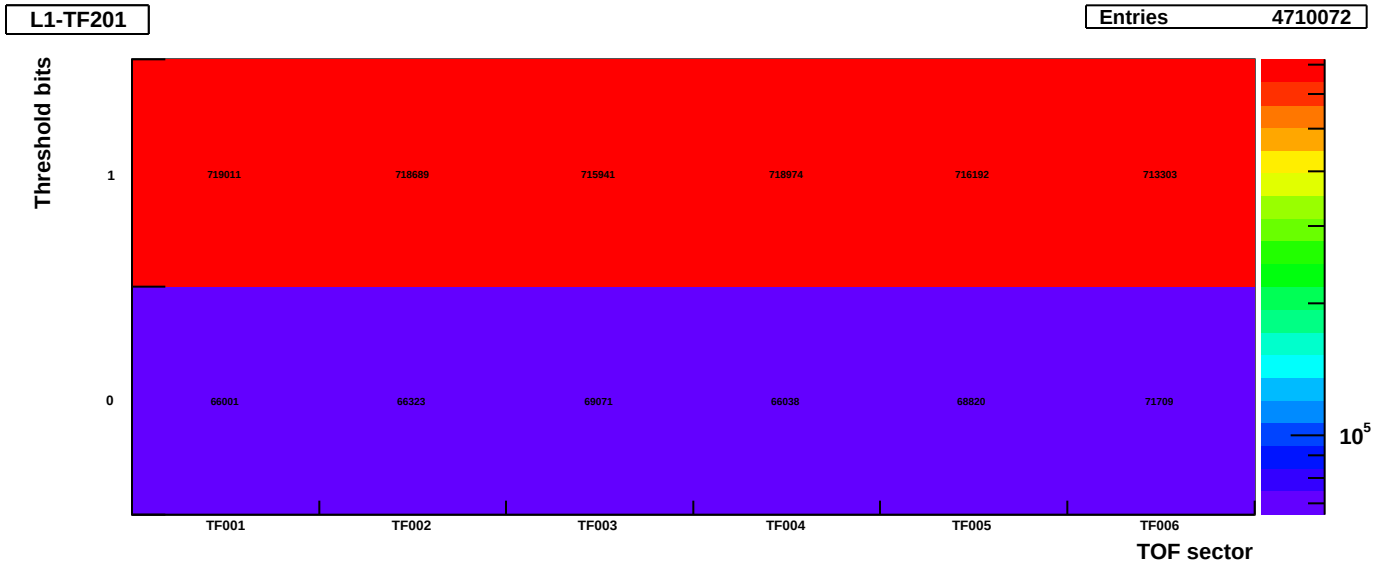

MIX-TF004
Entries 3.29705e+07
TOF MULT
TOF tray

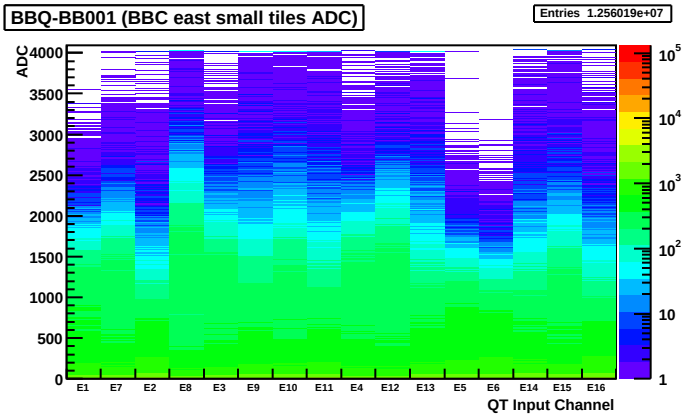

MIX-TF005
Entries 3.29705e+07
TOF MULT
TOF tray

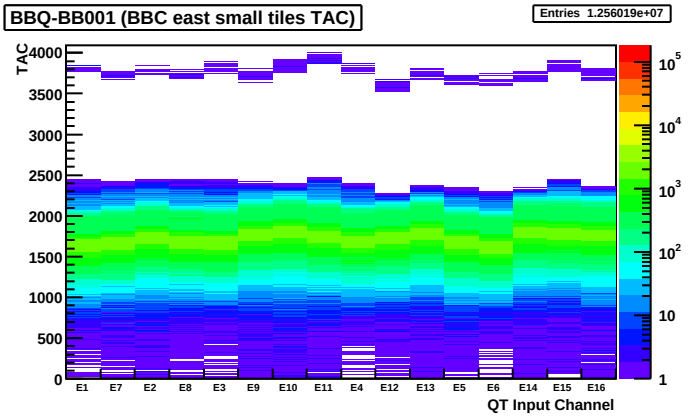

MIX-TF006
Entries 3.29705e+07
TOF MULT
TOF tray

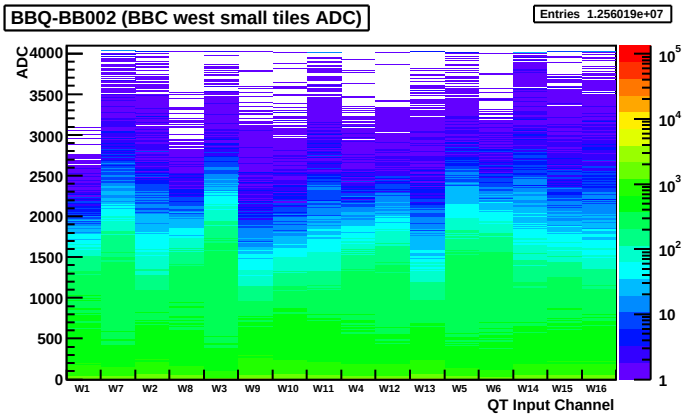

MIX-TF101
Entries 4710072
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

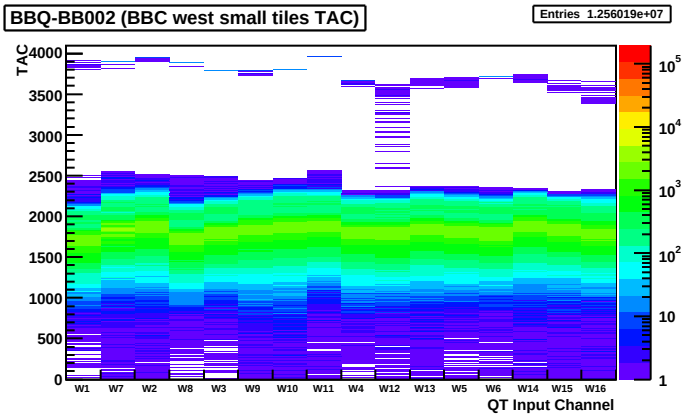

L1-TF201
Entries 785012
10^4
10^3
10^2
10
0
200
400
600
800
1000
1200
1400
TOF total mult

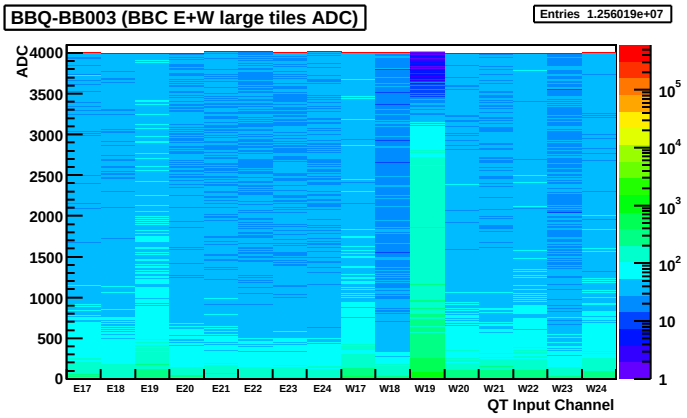

L1-TF201
Entries 4710072
Threshold bits
1
0
719011
718689
715941
718974
716192
713303
66001
66323
69071
66038
68820
71709
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

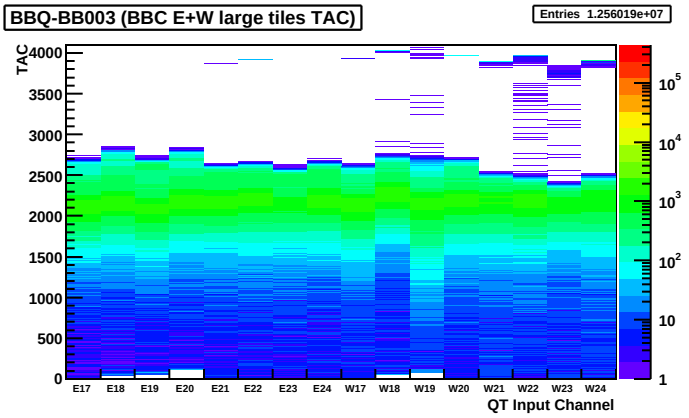

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.256019e+07
ADC
QT Input Channel

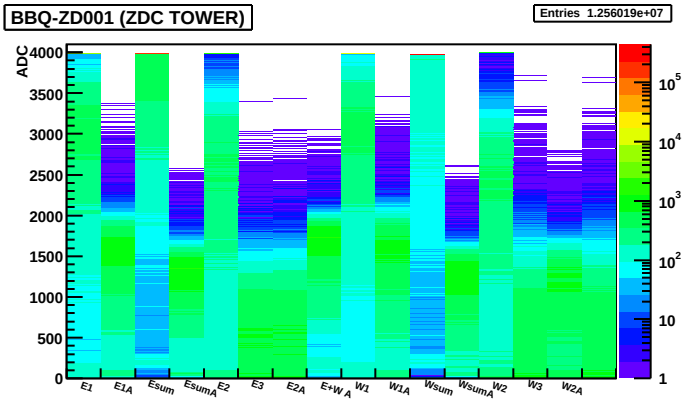

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.256019e+07
TAC
QT Input Channel

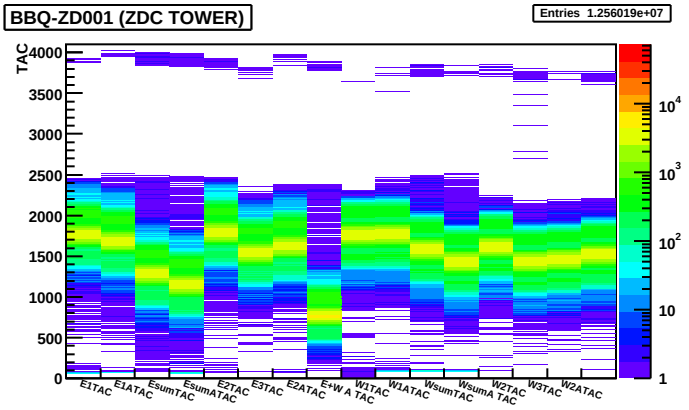

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.256019e+07
ADC
QT Input Channel

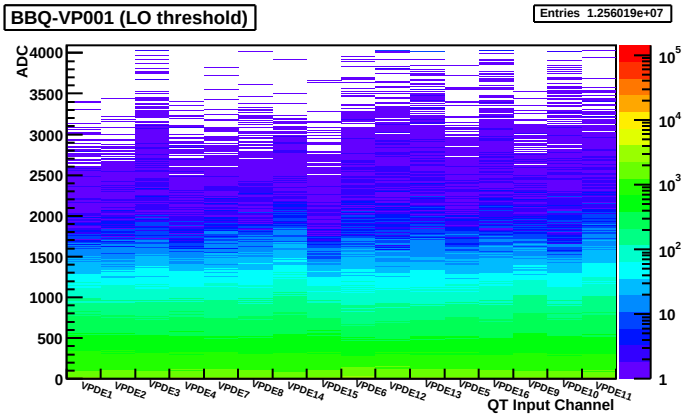

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.256019e+07
TAC
QT Input Channel

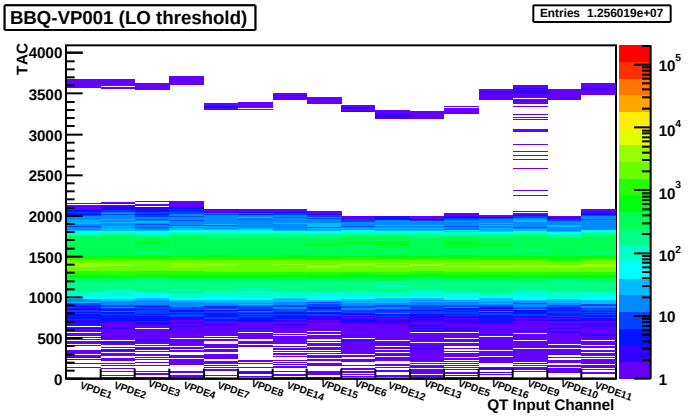

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.256019e+07
ADC
QT Input Channel

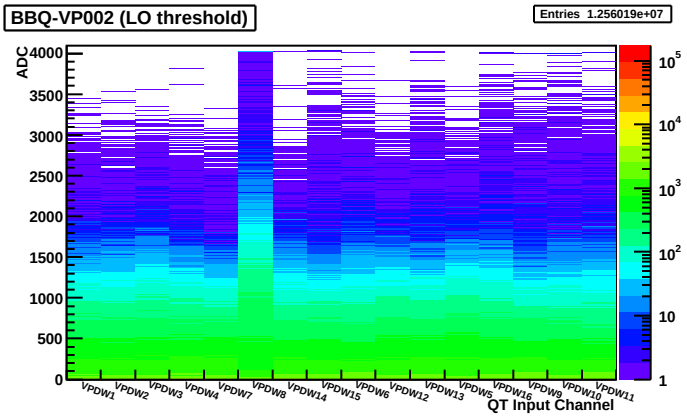

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.256019e+07
TAC
QT Input Channel

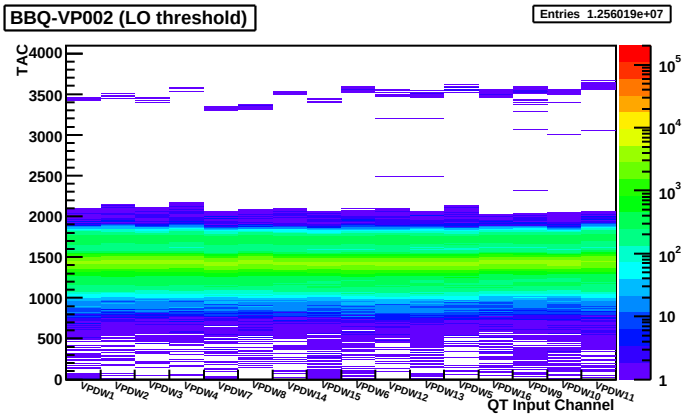

BBQ-ZD001 (ZDC TOWER)
Entries 1.256019e+07
ADC

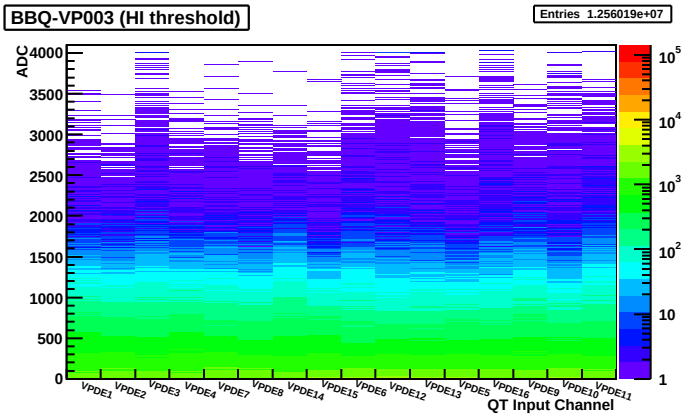

BBQ-ZD001 (ZDC TOWER)
Entries 1.256019e+07
TAC

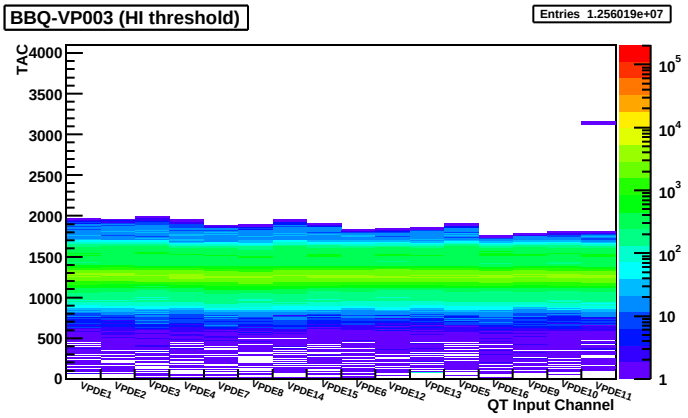

BBQ-VP001 (LO threshold)
Entries 1.256019e+07
ADC
QT Input Channel

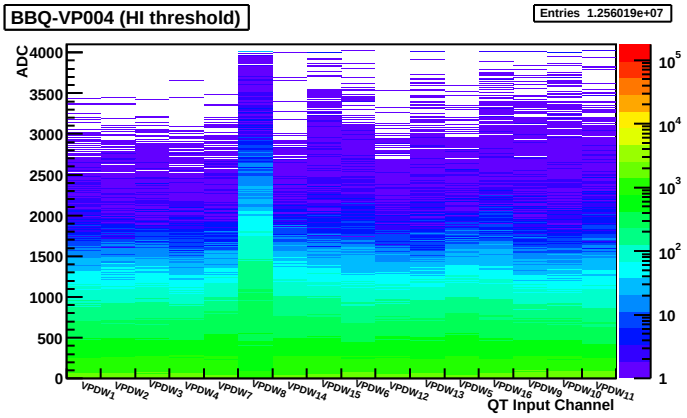

BBQ-VP001 (LO threshold)
Entries 1.256019e+07
TAC
QT Input Channel

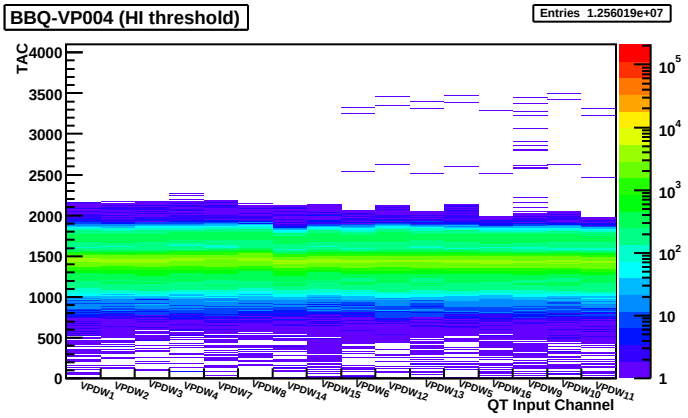

BBQ-VP002 (LO threshold)
Entries 1.256019e+07
ADC
QT Input Channel

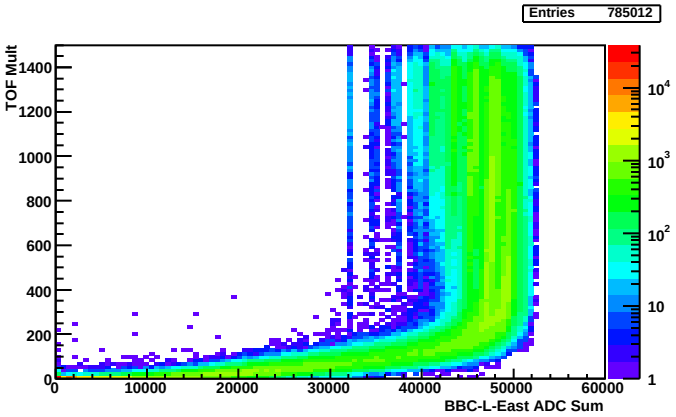

BBQ-VP002 (LO threshold)
Entries 1.256019e+07
TAC
QT Input Channel

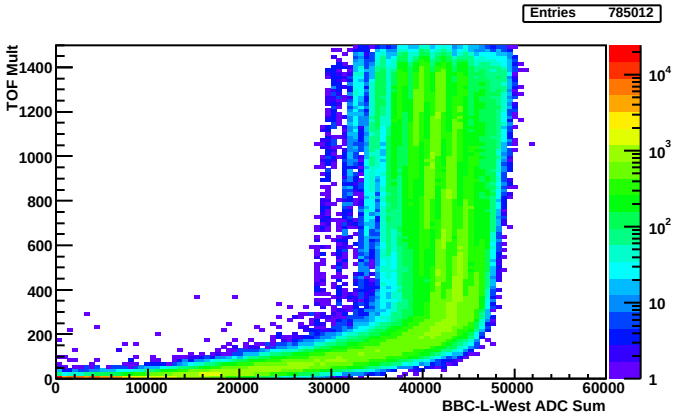

BBQ-VP003 (HI threshold)
Entries 1.256019e+07
ADC
QT Input Channel

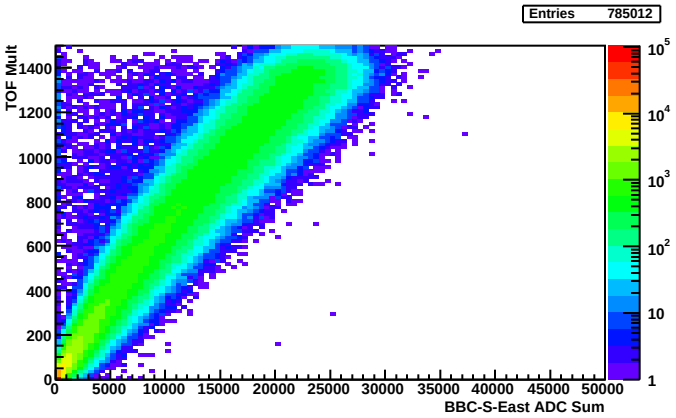

BBQ-VP003 (HI threshold)
Entries 1.256019e+07
TAC
QT Input Channel

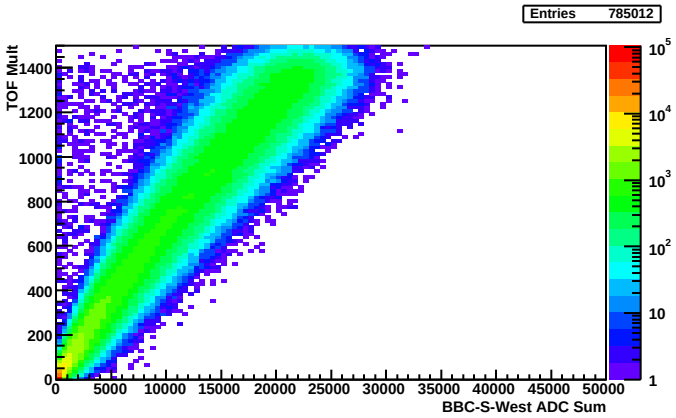

BBQ-VP004 (HI threshold)
Entries 1.256019e+07
ADC
QT Input Channel

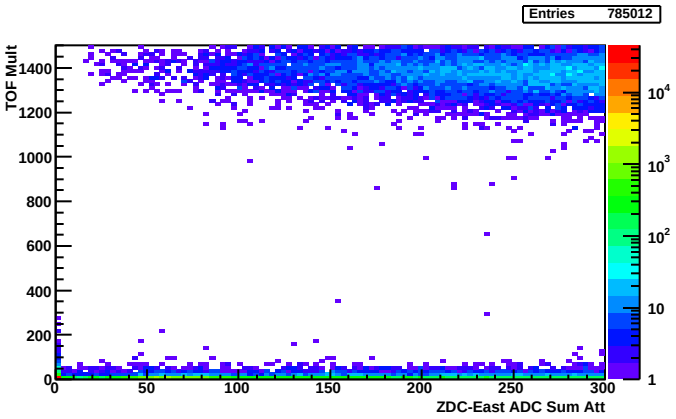

BBQ-VP004 (HI threshold)
Entries 1.256019e+07
TAC
QT Input Channel

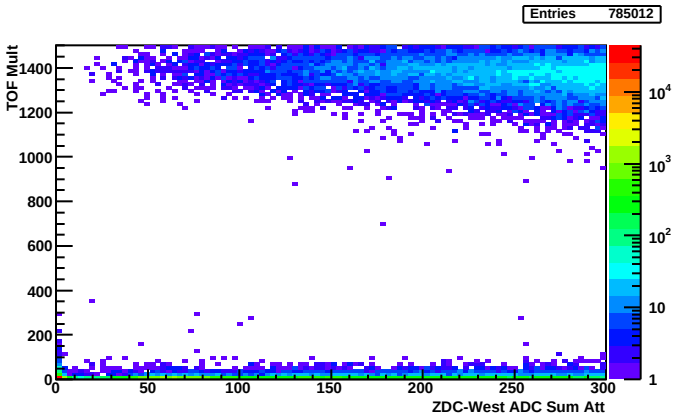

Entries 785012
TOF Mult
BBC-L-East ADC Sum

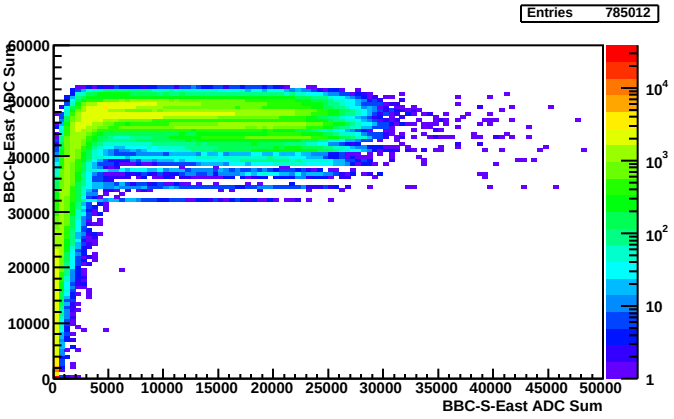

Entries 785012
TOF Mult
BBC-L-West ADC Sum

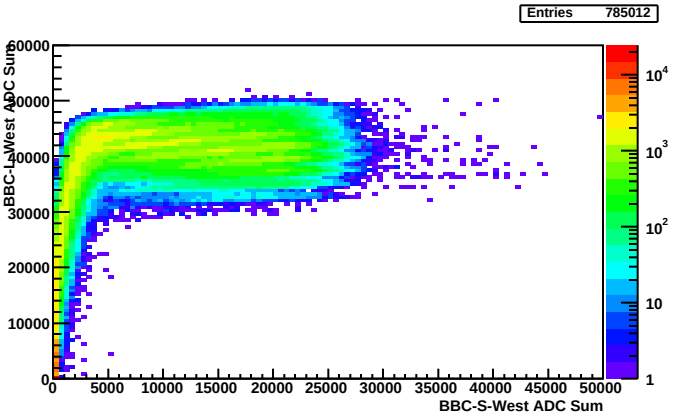

Entries 785012
TOF Mult
BBC-S-East ADC Sum

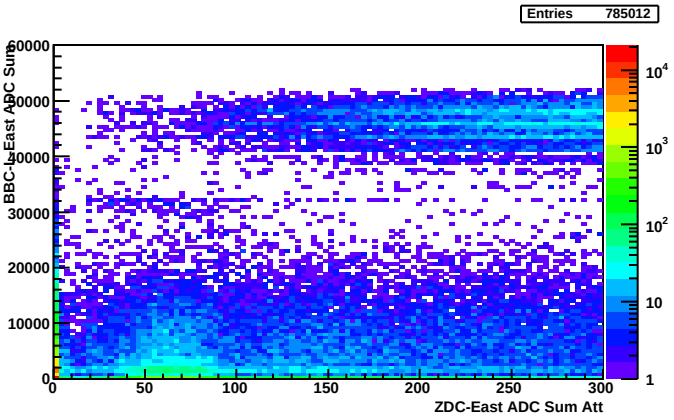

Entries 785012
TOF Mult
BBC-S-West ADC Sum

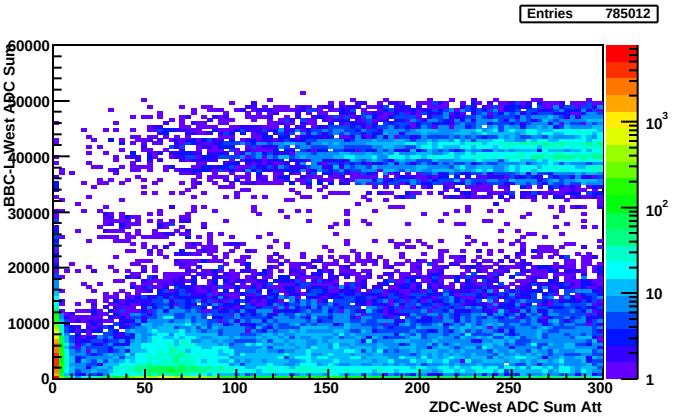

Entries 785012
TOF Mult
ZDC-East ADC Sum Att

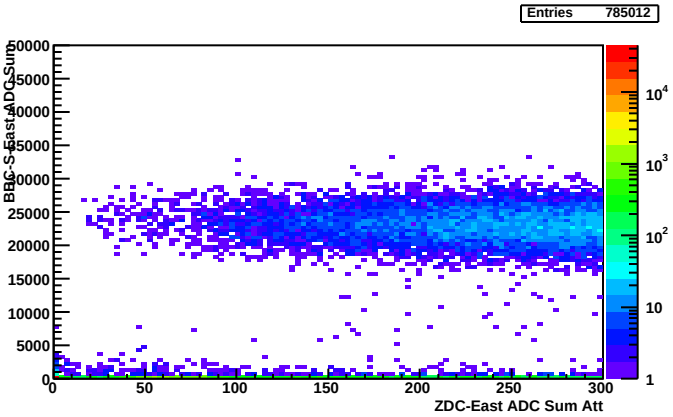

Entries 785012
TOF Mult
ZDC-West ADC Sum Att

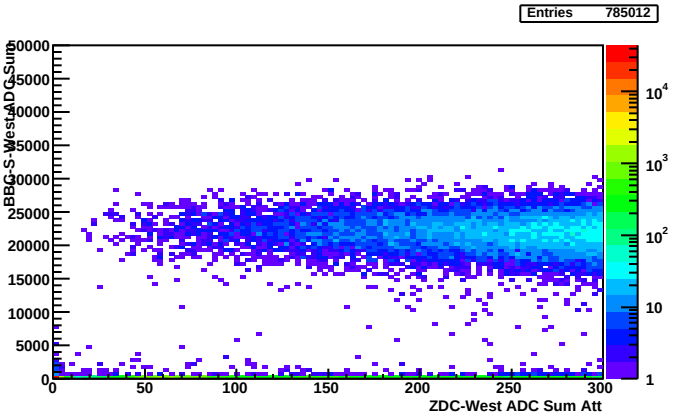

Entries 785012
BBC-L-East ADC Sum
BBC-S-East ADC Sum

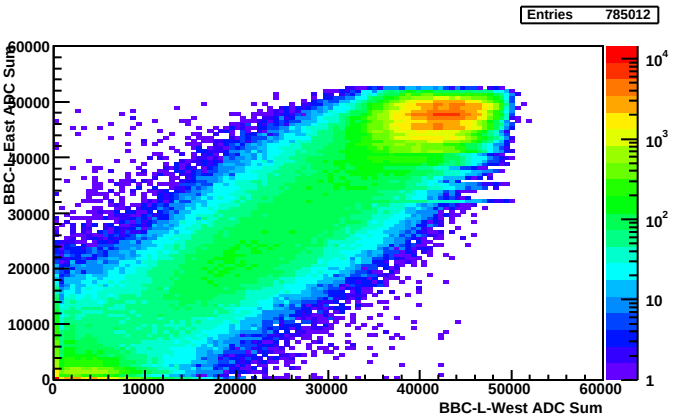

Entries 785012
BBC-L-West ADC Sum
BBC-S-West ADC Sum

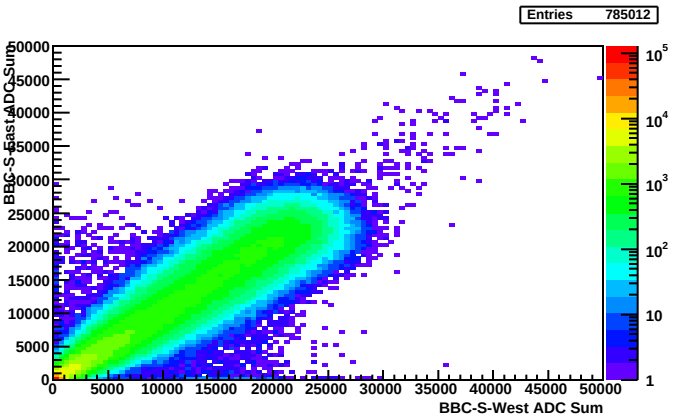

Entries 785012
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

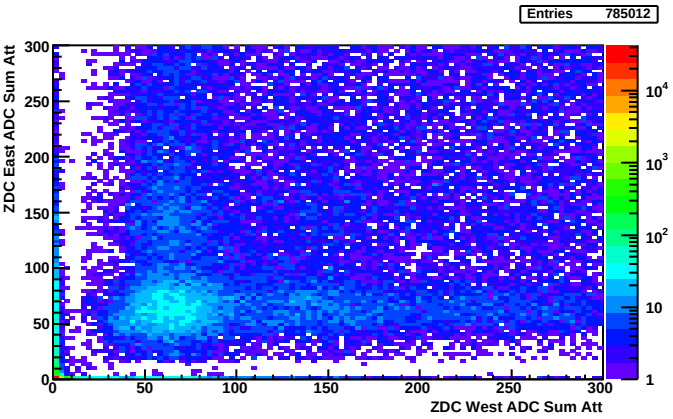

Entries 785012
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

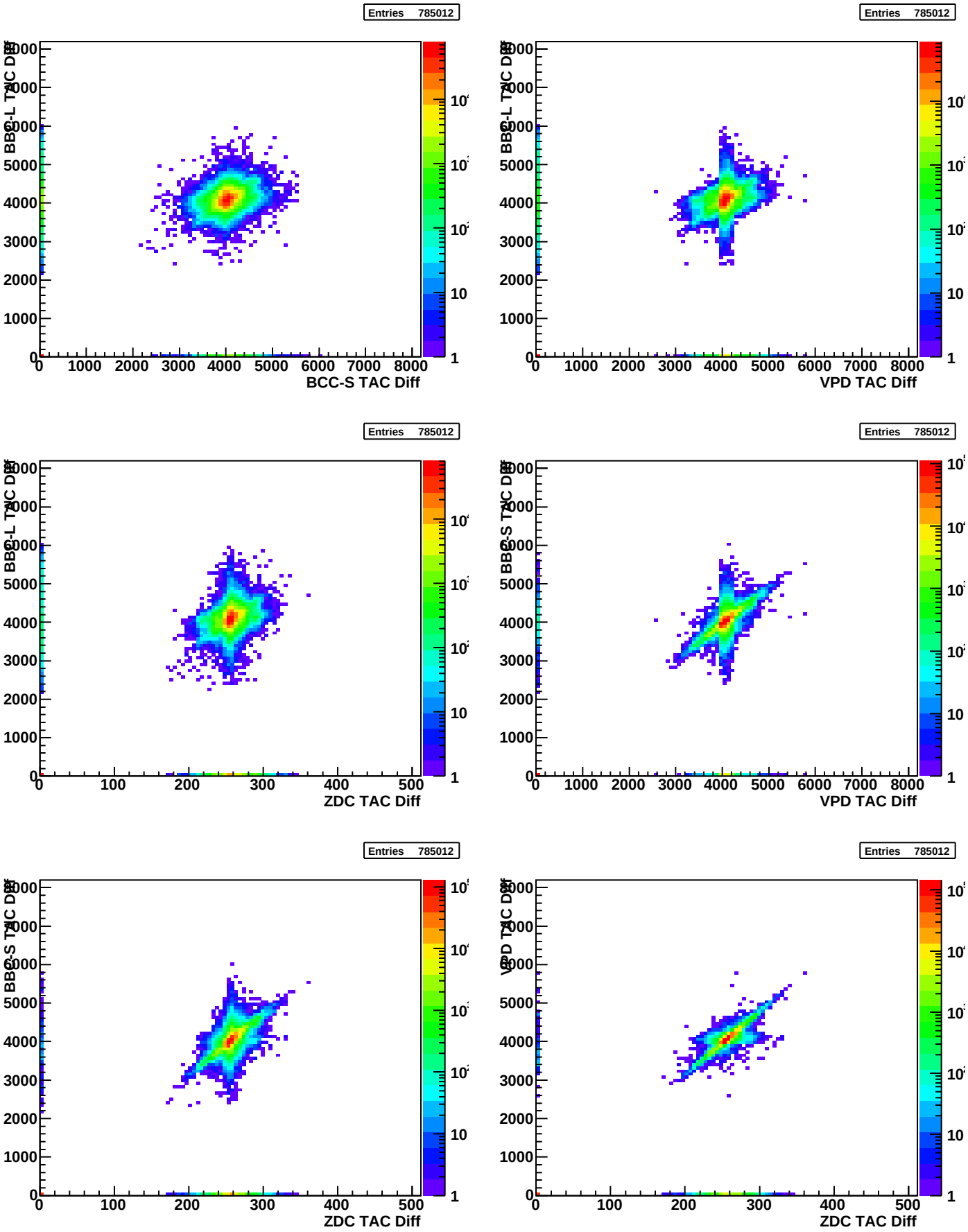

Entries 785012
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

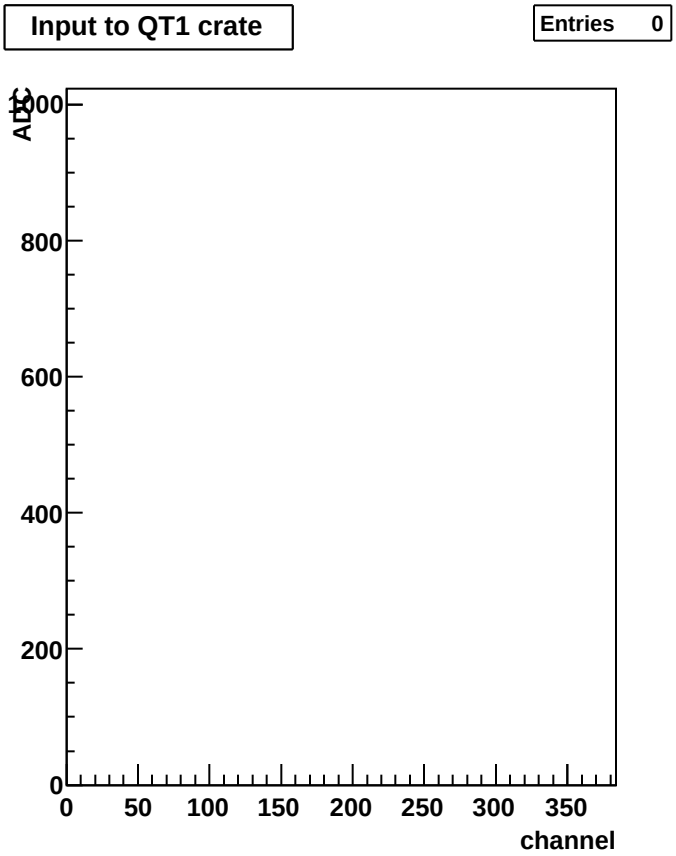

Entries 785012
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

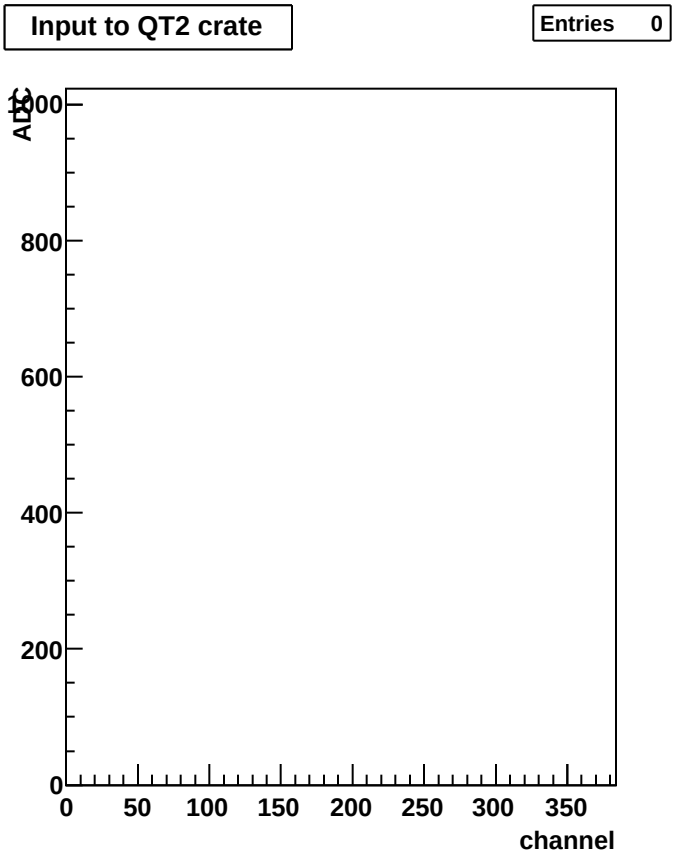

Entries 785012
BBC-L-East ADC Sum
BBC-L-West ADC Sum

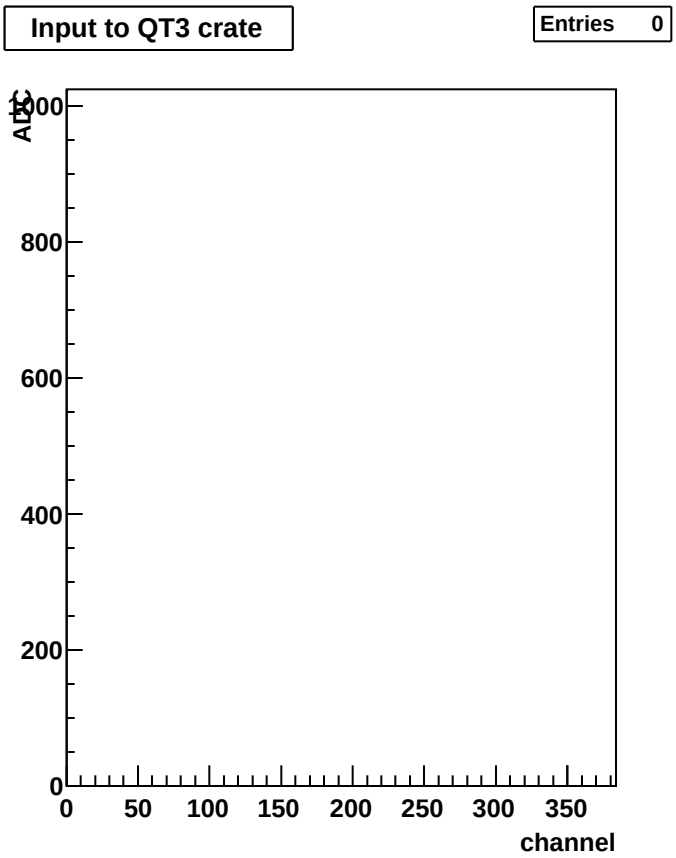

Entries 785012
BBC-S-East ADC Sum
BBC-S-West ADC Sum

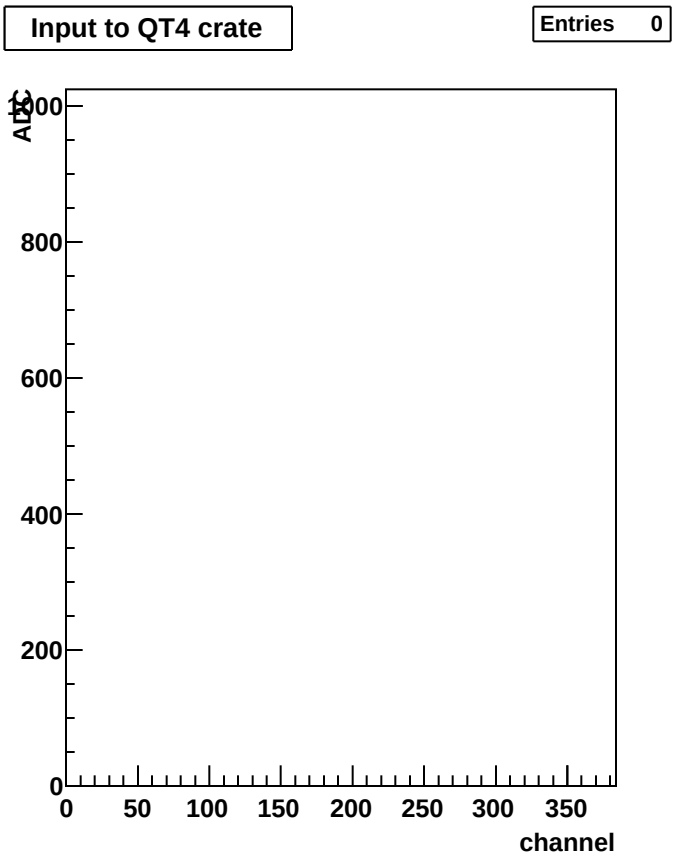

Entries 785012
ZDC East ADC Sum Att
ZDC West ADC Sum Att

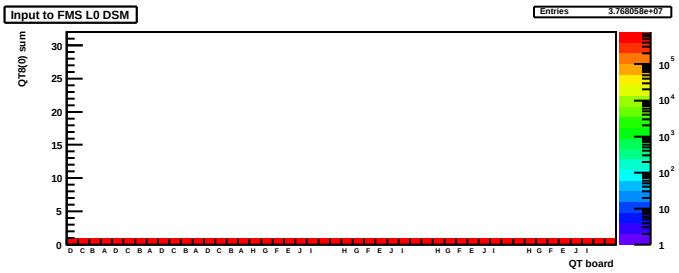

Entries 785012
BBC-L TAC Diff
BCC-S TAC Diff
Entries 785012
BBC-L TAC Diff
VPD TAC Diff
Entries 785012
BBC-L TAC Diff
ZDC TAC Diff
Entries 785012
BBC-S TAC Diff
VPD TAC Diff
Entries 785012
BBC-S TAC Diff
ZDC TAC Diff
Entries 785012
VPD TAC Diff
ZDC TAC Diff

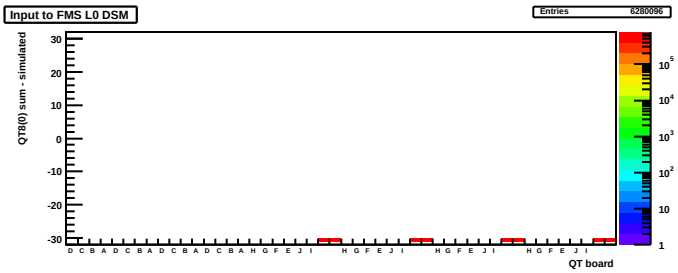

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

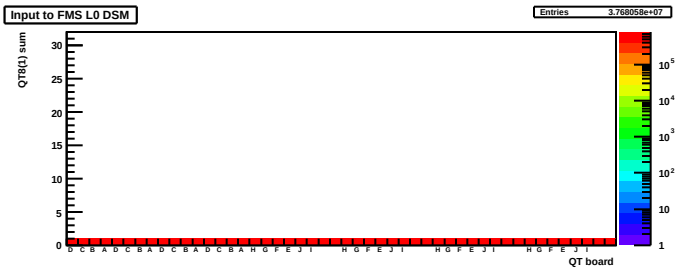

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

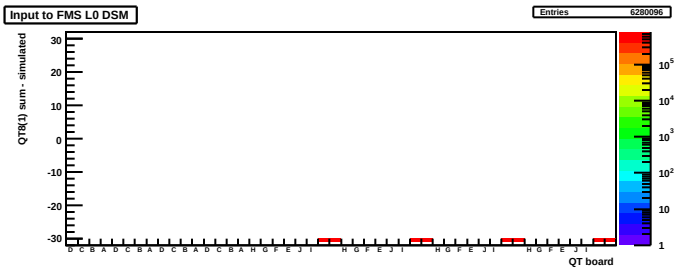

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

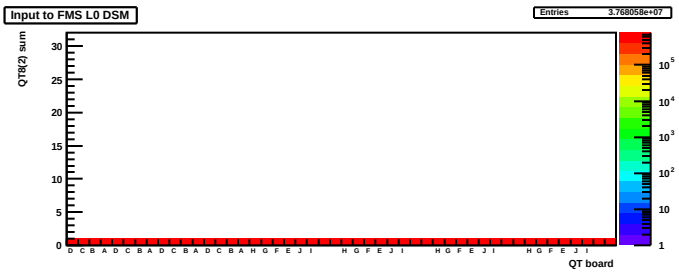

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

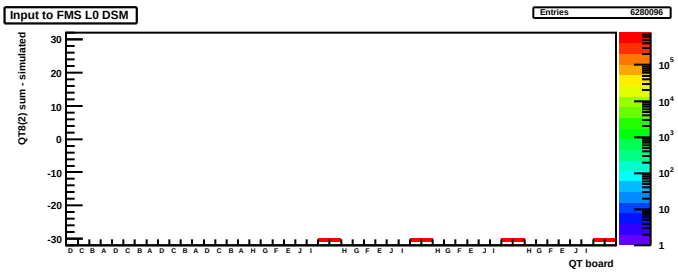

Input to FMS L0 DSM
Entries 3.768058e+07
QT8(0) sum
QT board

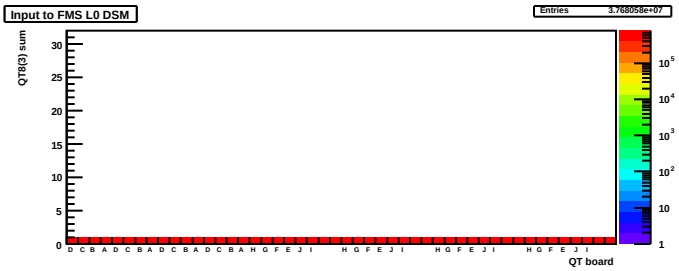

Input to FMS L0 DSM
Entries 6280096
QT8(0) sum - simulated
QT board

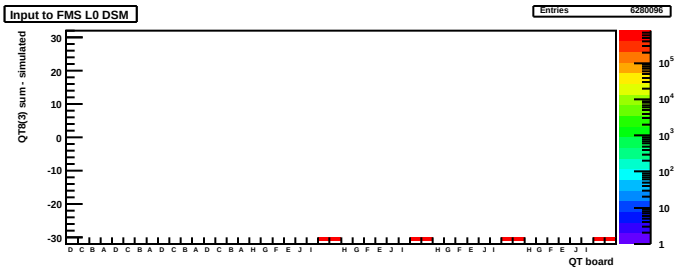

Input to FMS L0 DSM
Entries 3.768058e+07
QT8(1) sum
QT board

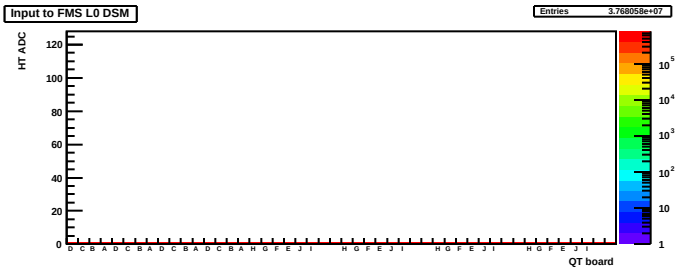

Input to FMS L0 DSM
Entries 6280096
QT8(1) sum - simulated
QT board

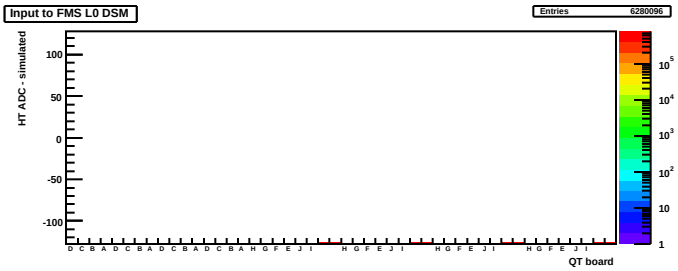

Input to FMS L0 DSM
Entries 3.768058e+07
QT8(2) sum
QT board

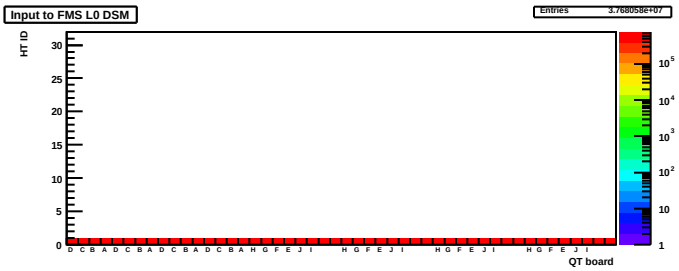

Input to FMS L0 DSM
Entries 6280096
QT8(2) sum - simulated
QT board

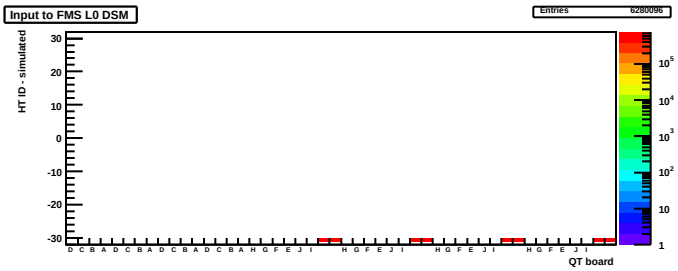

Input to FMS L0 DSM
Entries 3.768058e+07
QT8(3) sum
QT board

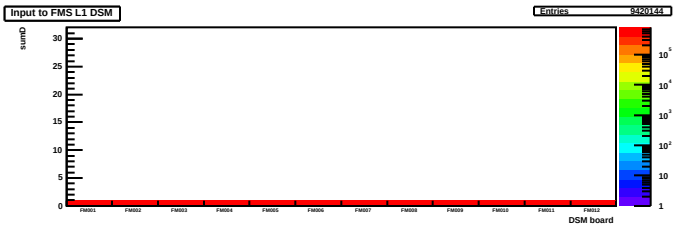

Input to FMS L0 DSM
Entries 6280096
QT8(3) sum - simulated
QT board

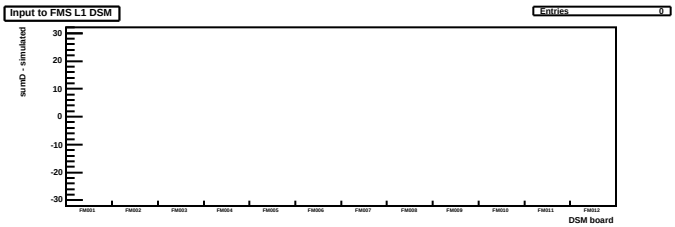

Input to FMS L0 DSM
Entries 3.768058e+07
HT ADC
QT board

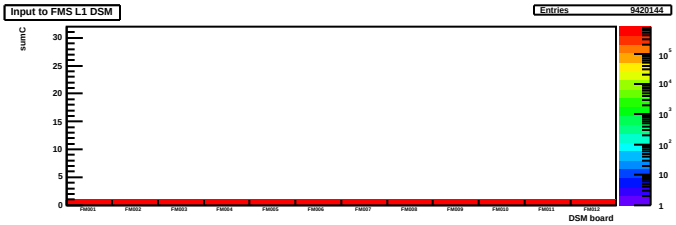

Input to FMS L0 DSM
Entries 6280096
HT ADC - simulated
QT board

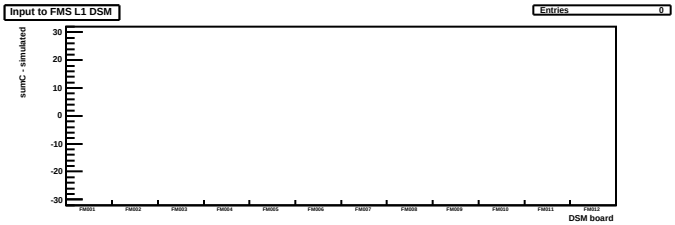

Input to FMS L0 DSM
Entries 3.768058e+07
HT ID
QT board

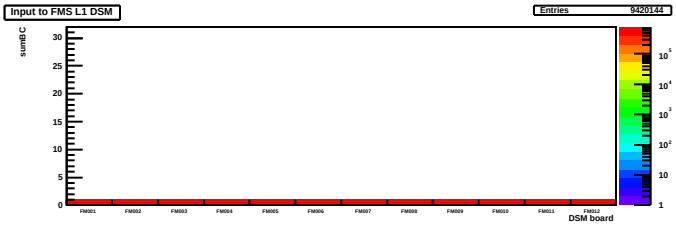

Input to FMS L0 DSM
Entries 6280096
HT ID - simulated
QT board

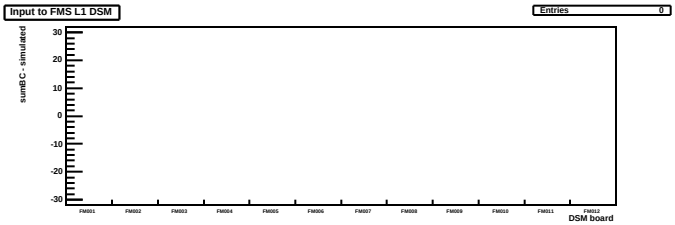

Input to FMS L1 DSM
Entries 9420144
sumD
DSM board

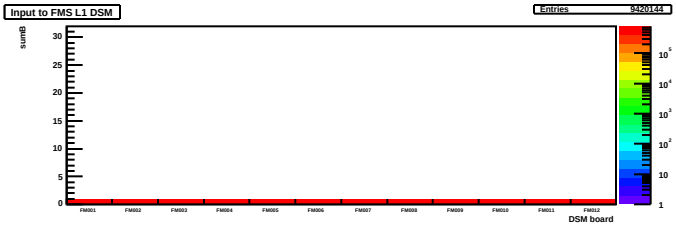

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

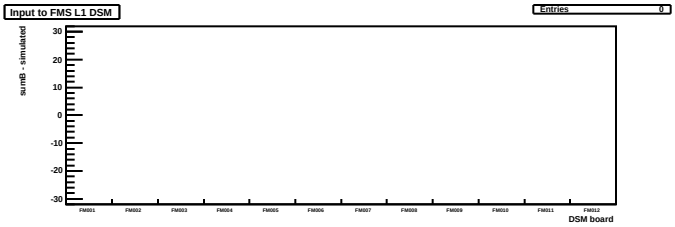

Input to FMS L1 DSM
Entries 9420144
sumC
DSM board

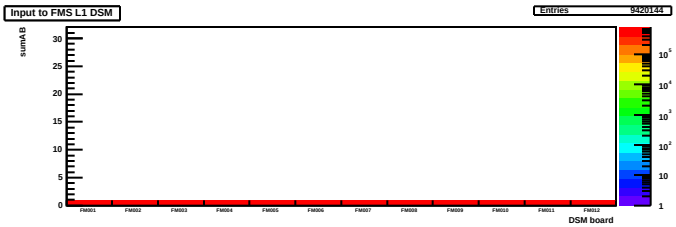

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

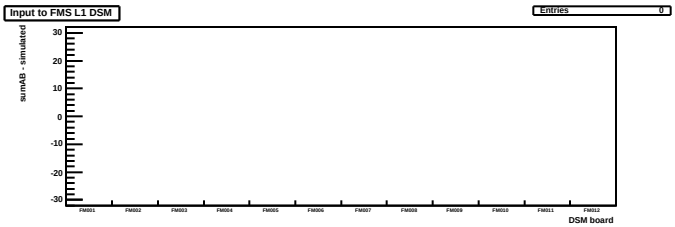

Input to FMS L1 DSM
Entries 9420144
sumBC
DSM board

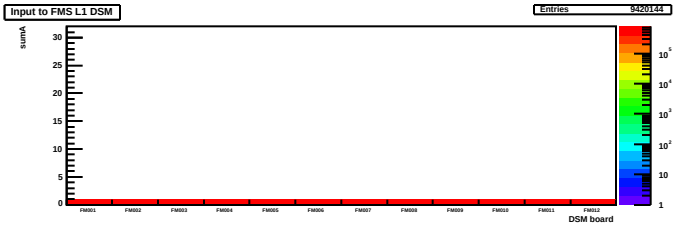

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

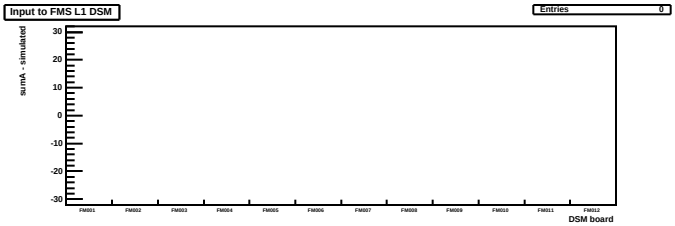

Input to FMS L1 DSM
Entries 9420144
sumB
DSM board

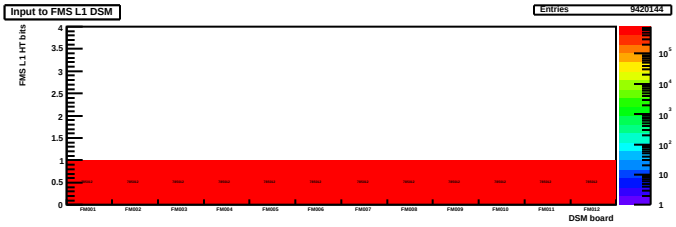

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

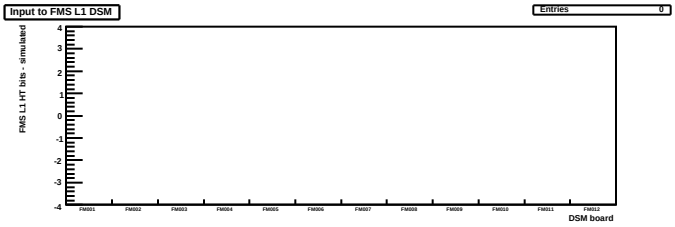

Input to FMS L1 DSM
Entries 9420144
sumAB
DSM board

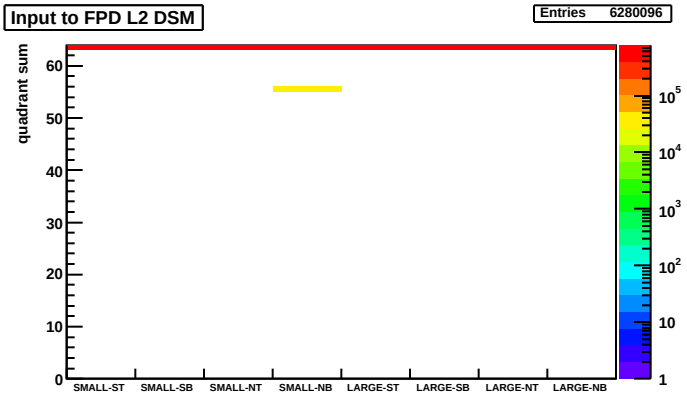

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

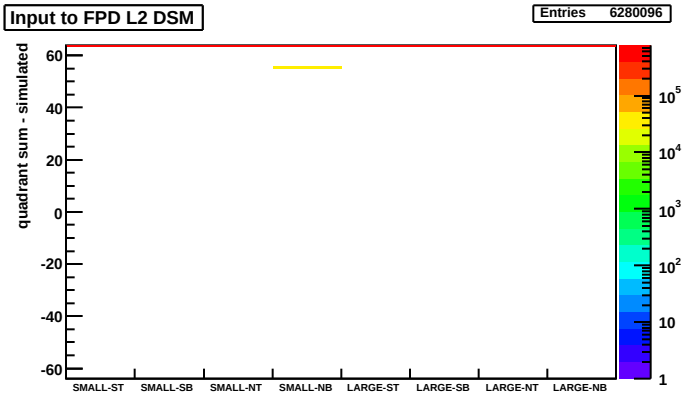

Input to FMS L1 DSM
Entries 9420144
sumA
DSM board

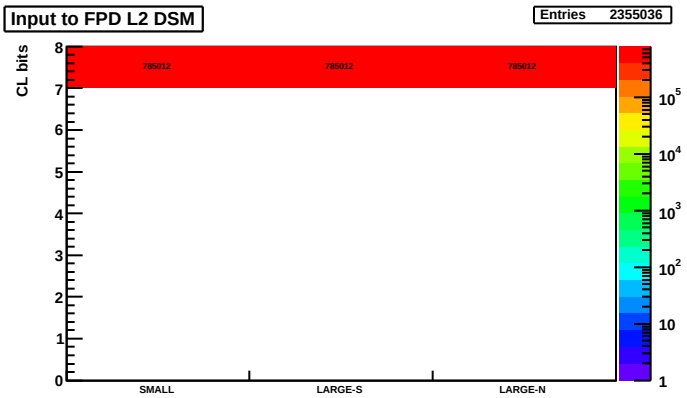

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

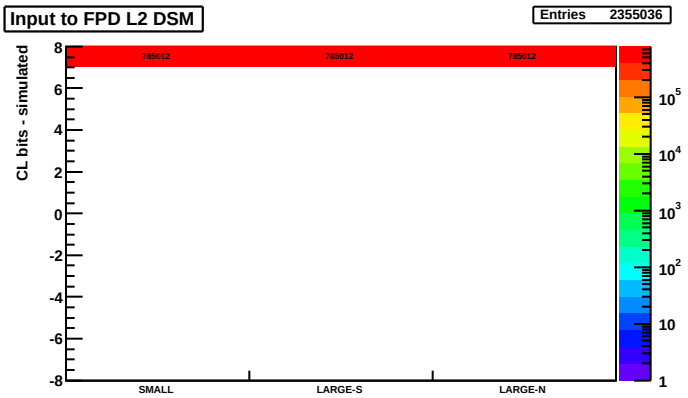

Input to FMS L1 DSM
Entries 9420144
FMS L1 HT bits
DSM board

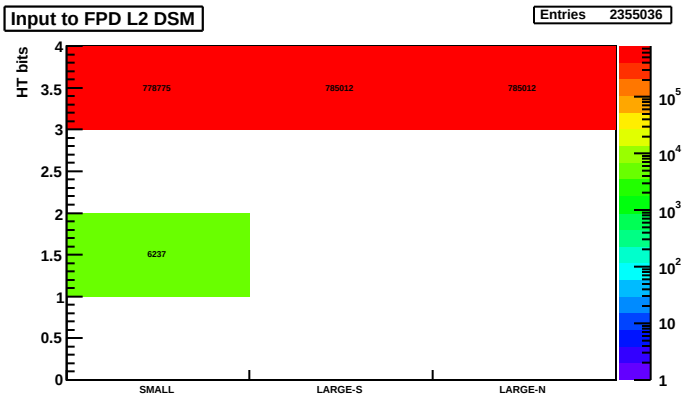

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

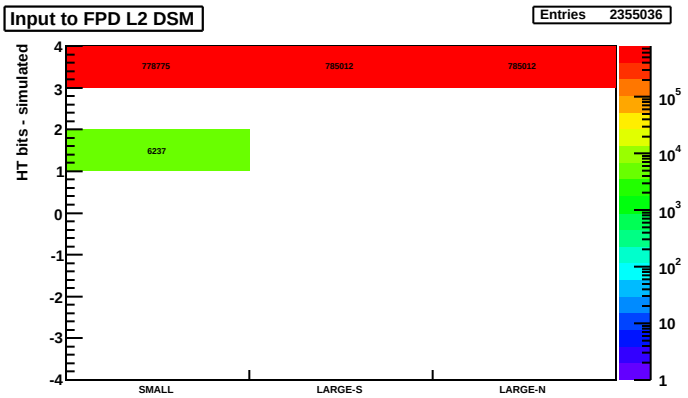

Input to FPD L2 DSM
Entries 6280096
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

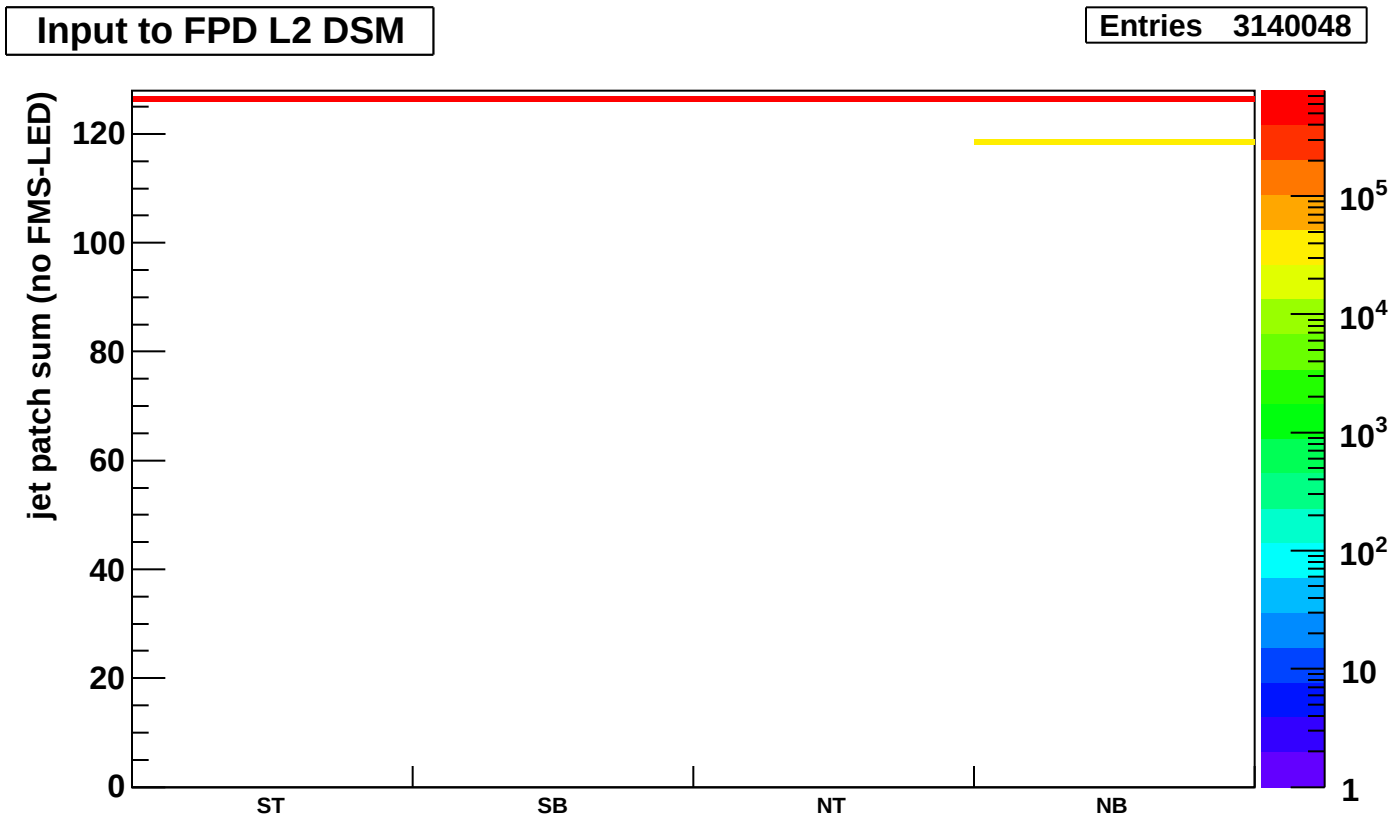

Input to FPD L2 DSM
Entries 6280096
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

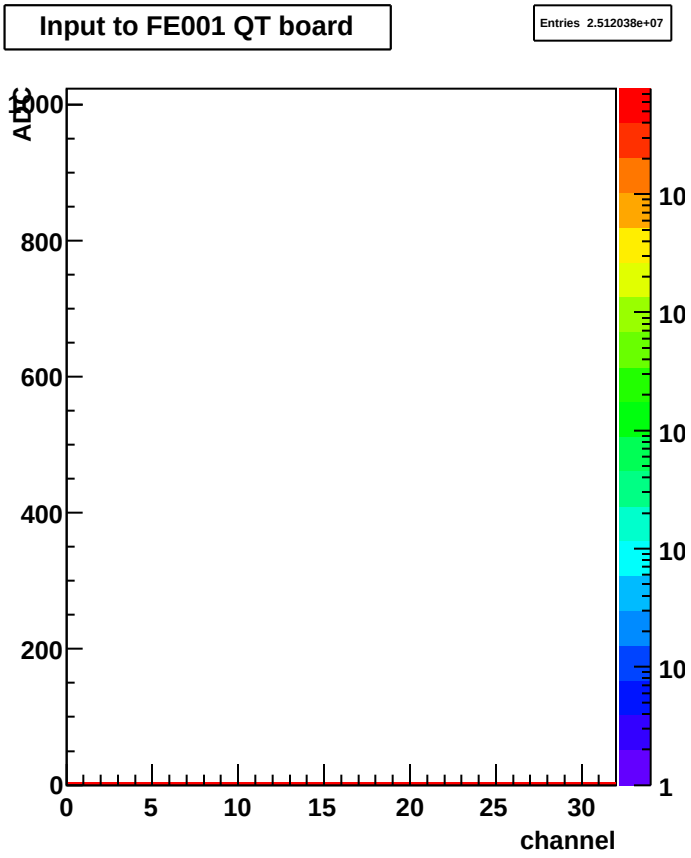

Input to FPD L2 DSM
Entries 2355036
CL bits
785012
785012
785012
SMALL
LARGE-S
LARGE-N

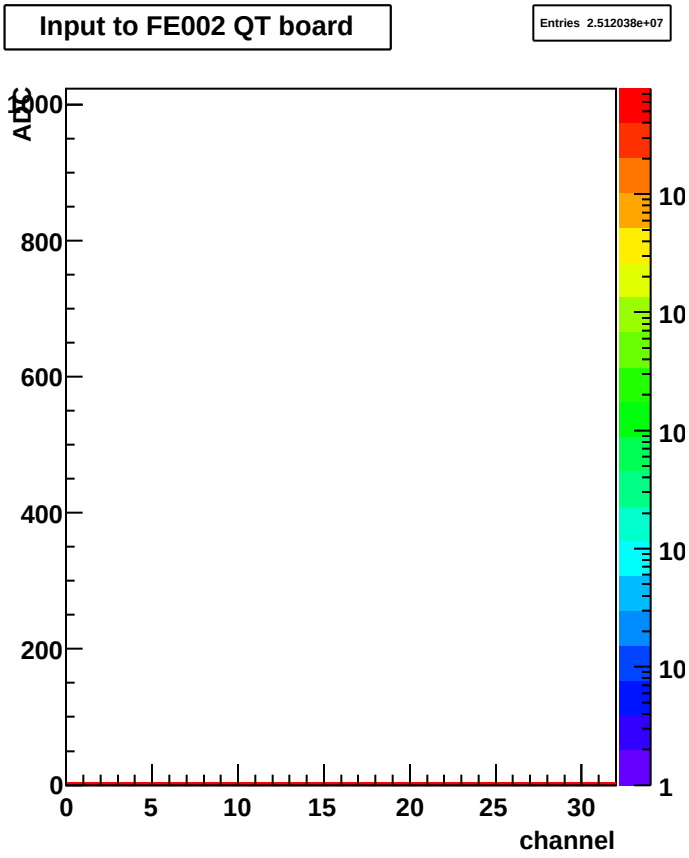

Input to FPD L2 DSM
Entries 2355036
CL bits - simulated
785012
785012
785012
SMALL
LARGE-S
LARGE-N

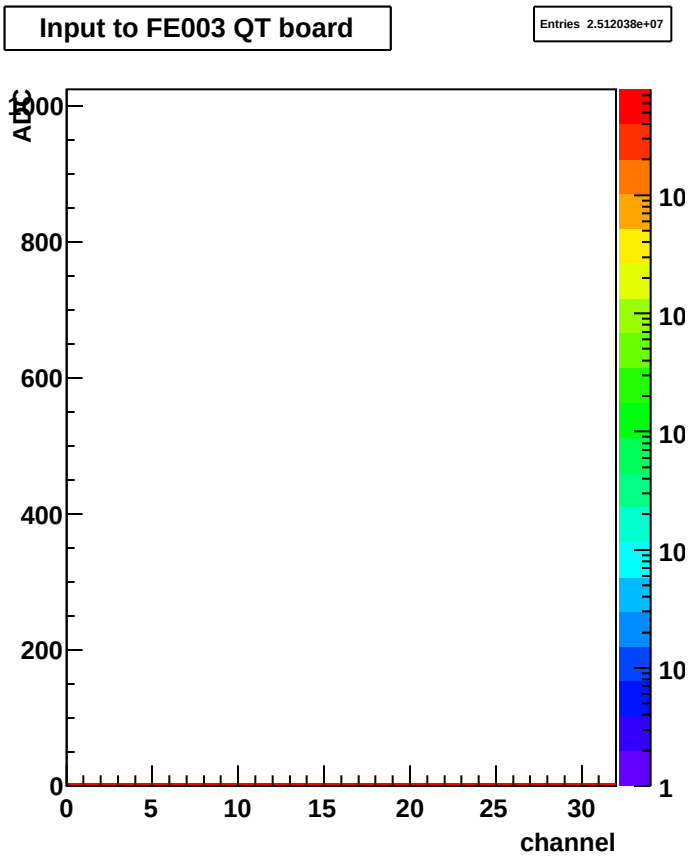

Input to FPD L2 DSM
Entries 2355036
HT bits
778775
785012
785012
6237
SMALL
LARGE-S
LARGE-N

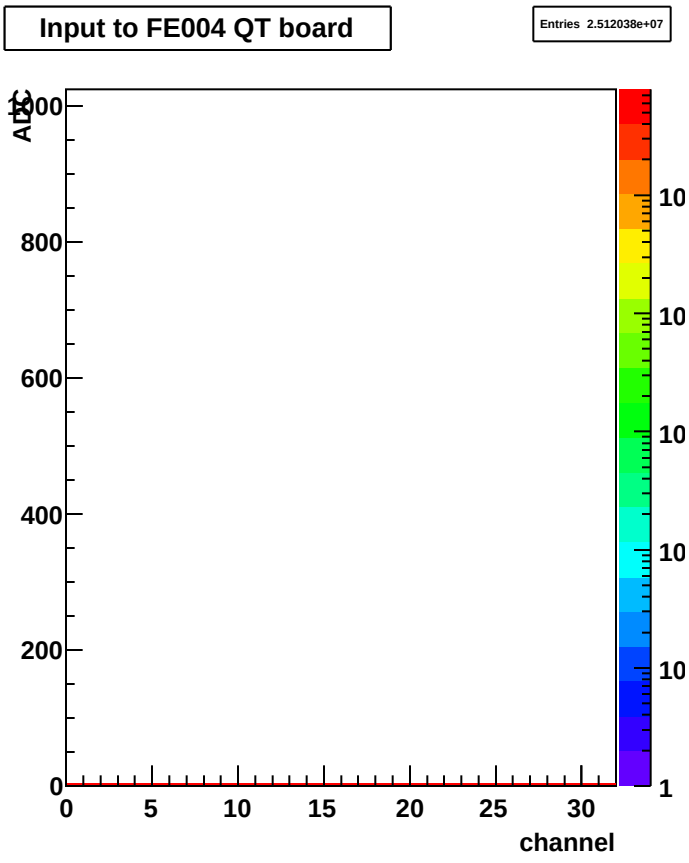

Input to FPD L2 DSM
Entries 2355036
HT bits - simulated
778775
785012
785012
6237
SMALL
LARGE-S
LARGE-N


Input to FPD L2 DSM
Entries 3140048
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

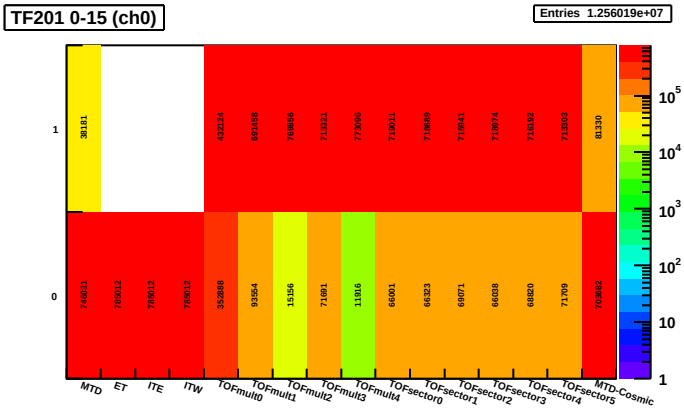

Input to FE001 QT board
Entries 2.512038e+07
ADC
channel


Input to FE002 QT board
Entries 2.512038e+07
ADC
channel

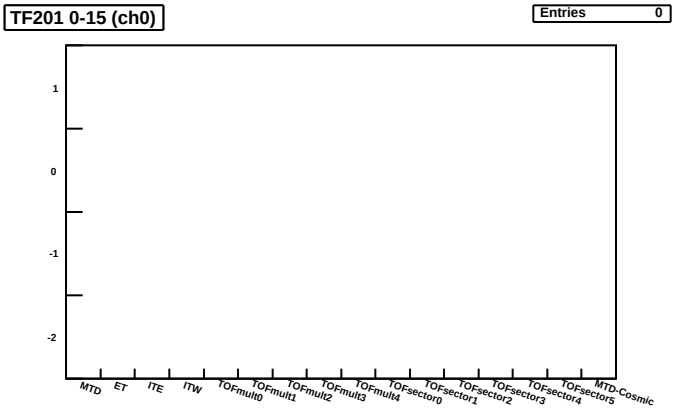

Input to FE003 QT board
Entries 2.512038e+07
ADC
channel


Input to FE004 QT board
Entries 2.512038e+07
ADC
channel

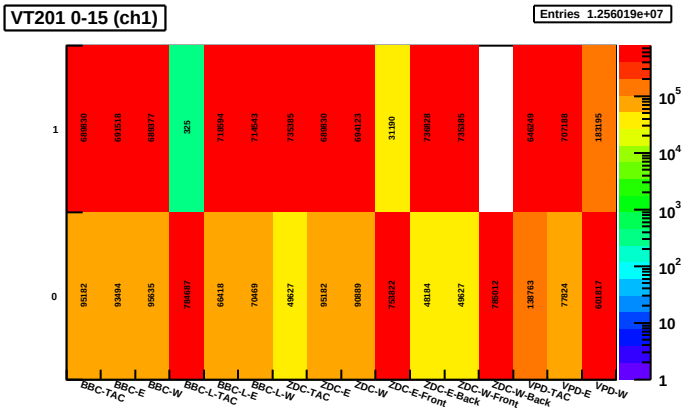
TF201 0-15 (ch0)



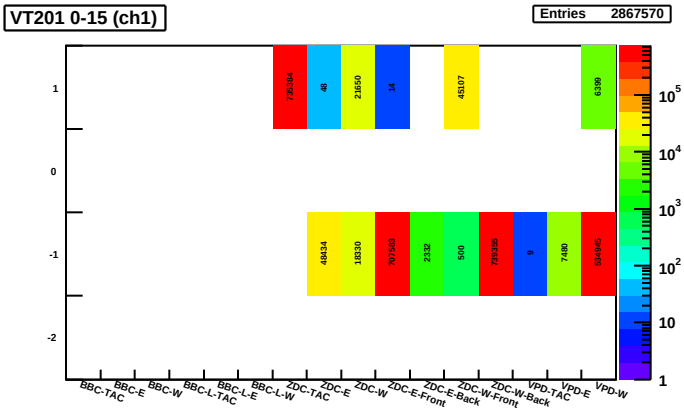
TF201 0-15 (ch0)



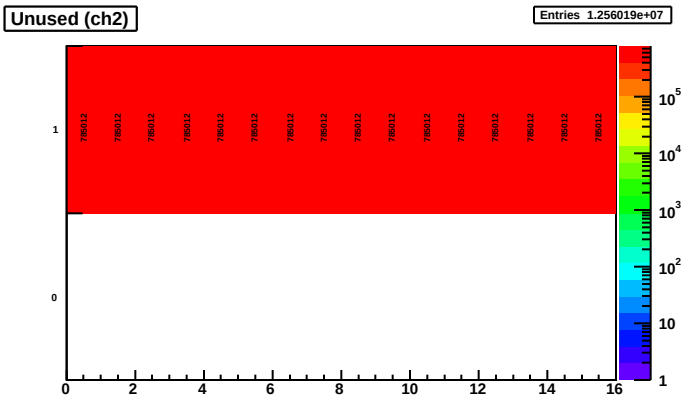
VT201 0-15 (ch1)

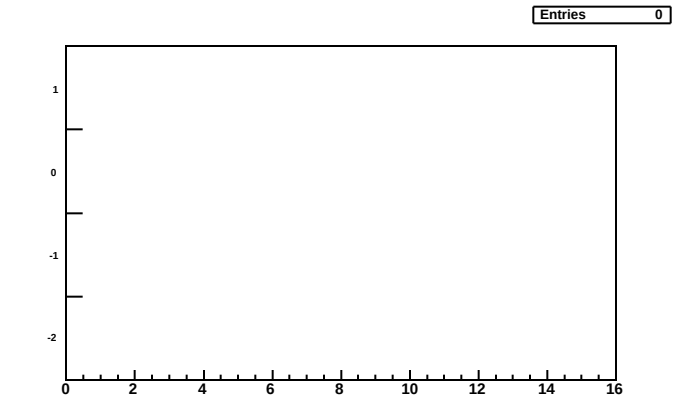


VT201 0-15 (ch1)

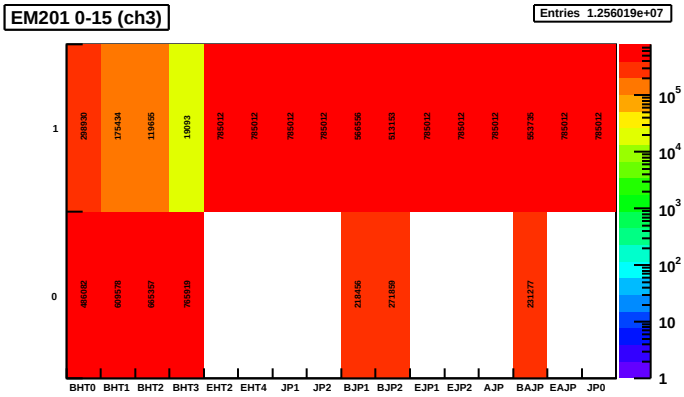


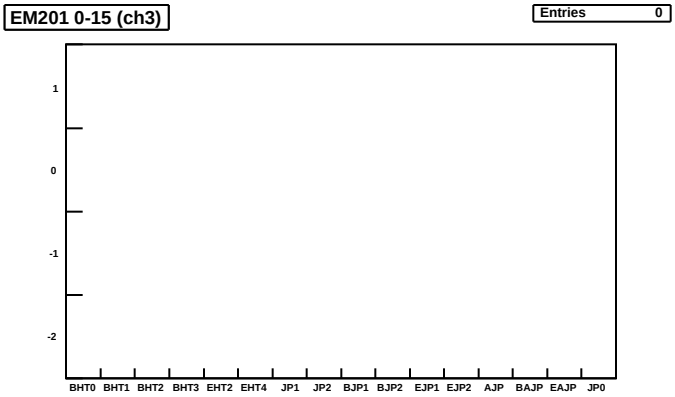
Unused (ch2)



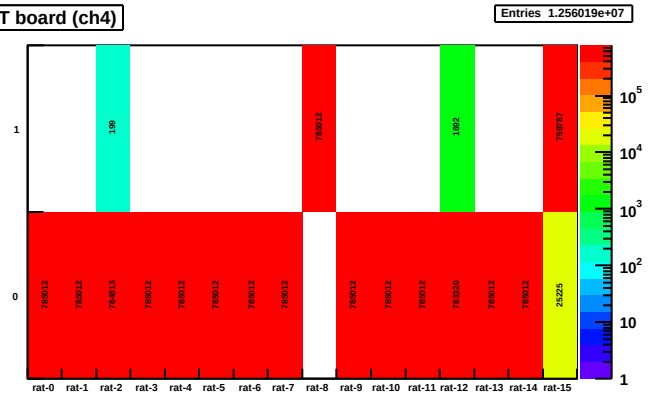


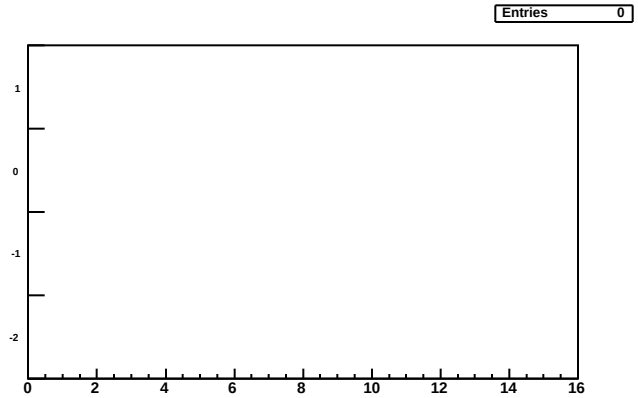
EM201 0-15 (ch3)



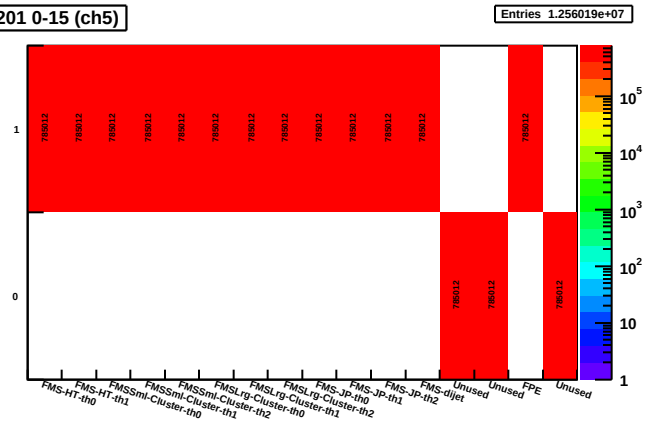


RAT board (ch4)

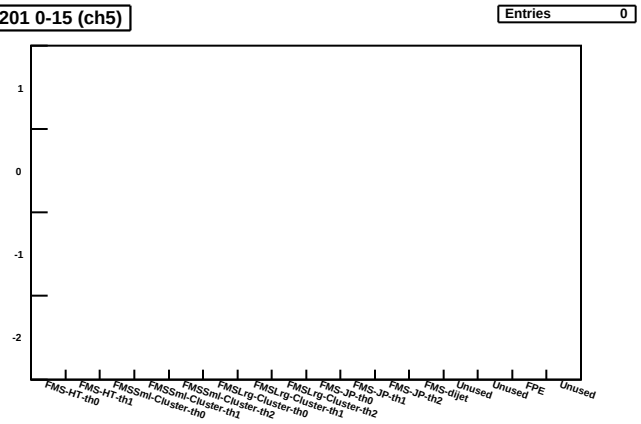




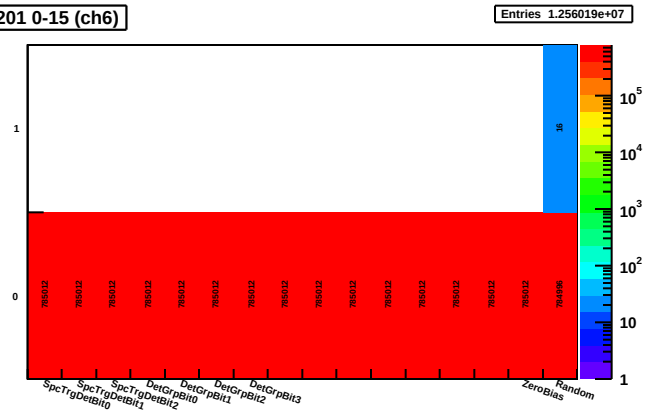
FP201 0-15 (ch5)

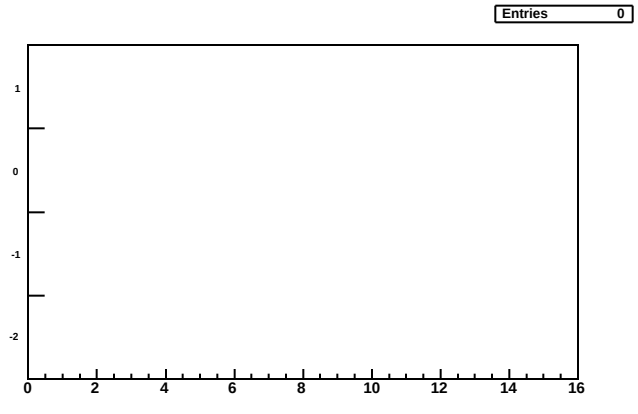
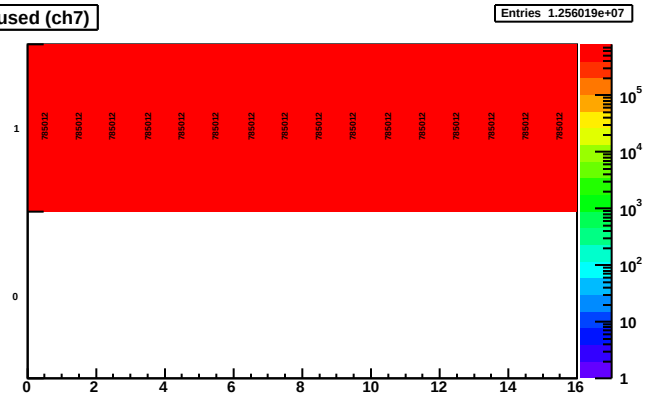
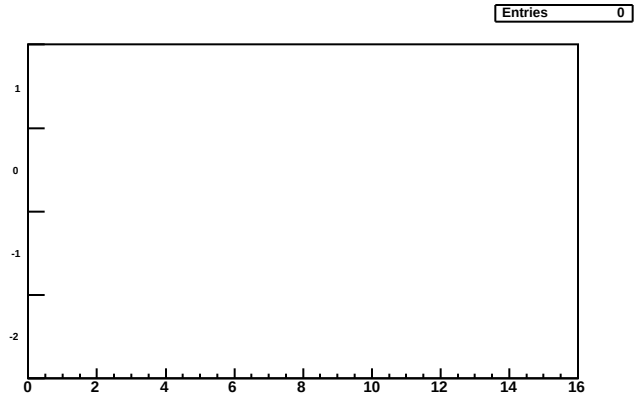


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)