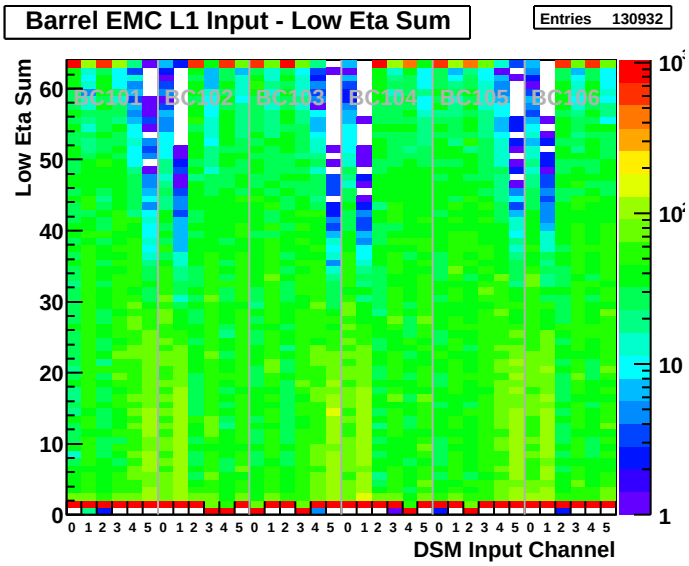
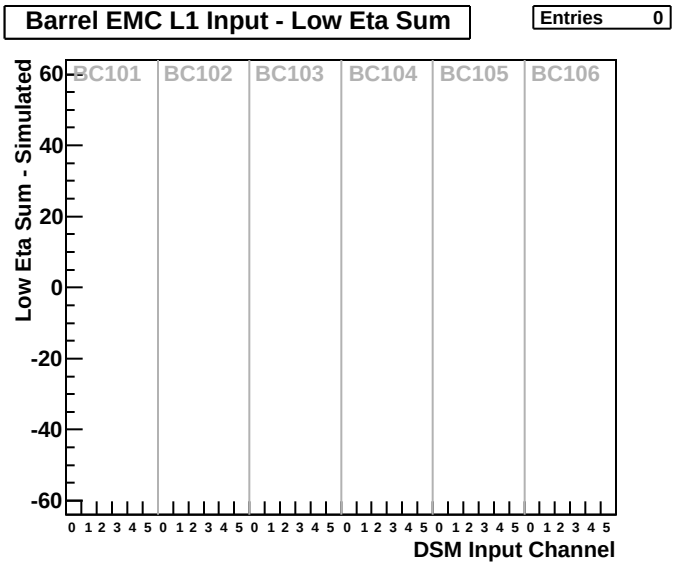
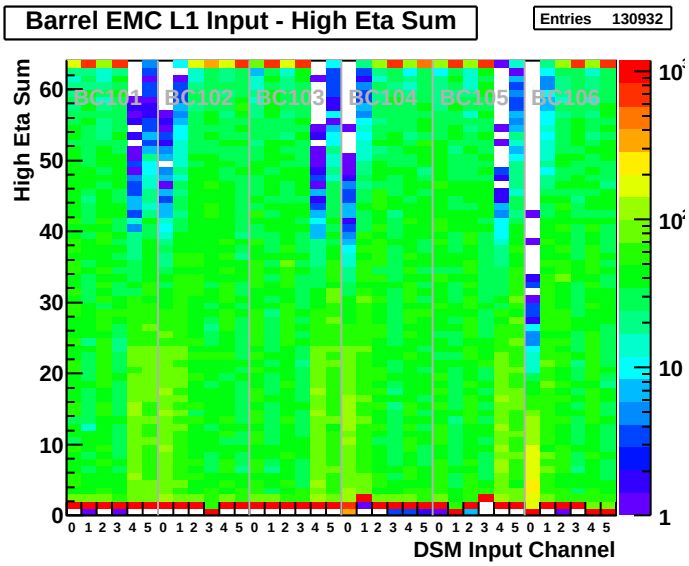
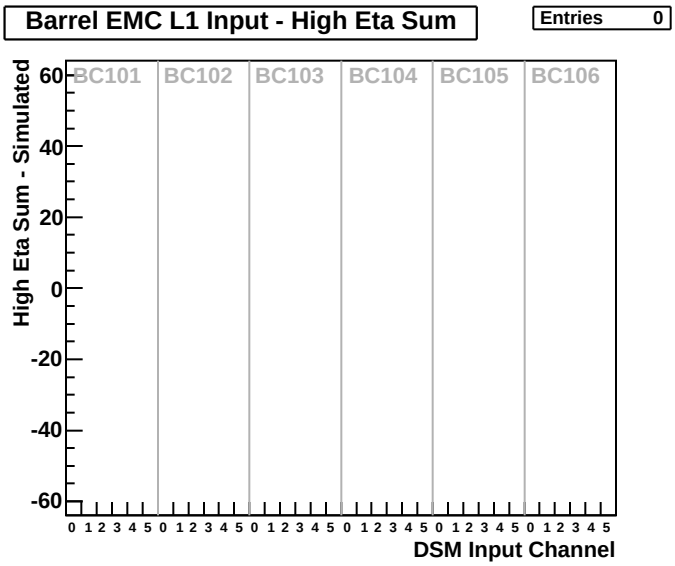
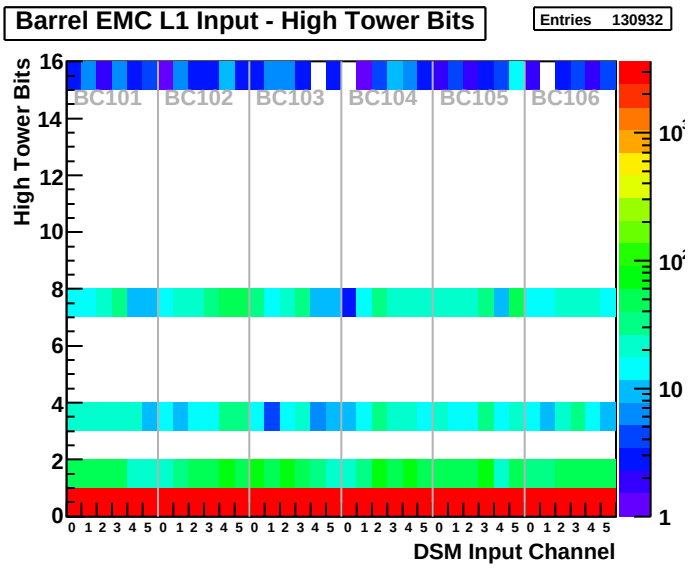
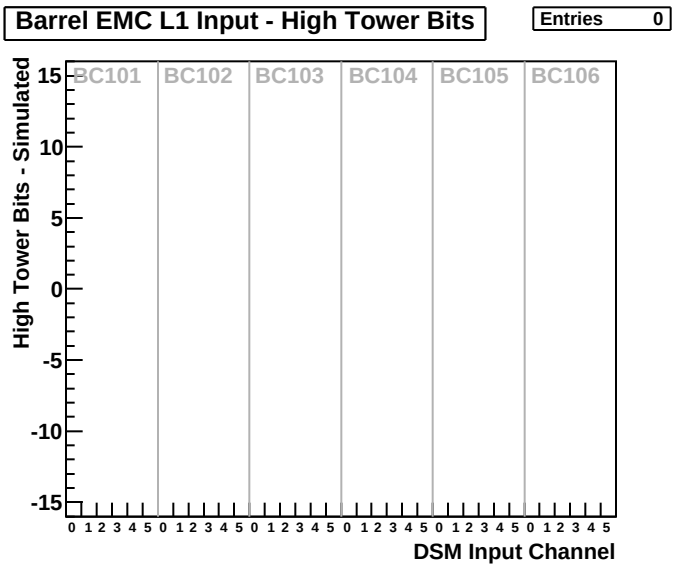

Barrel EMC L1 Input - Low Eta Sum
Entries 130932
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

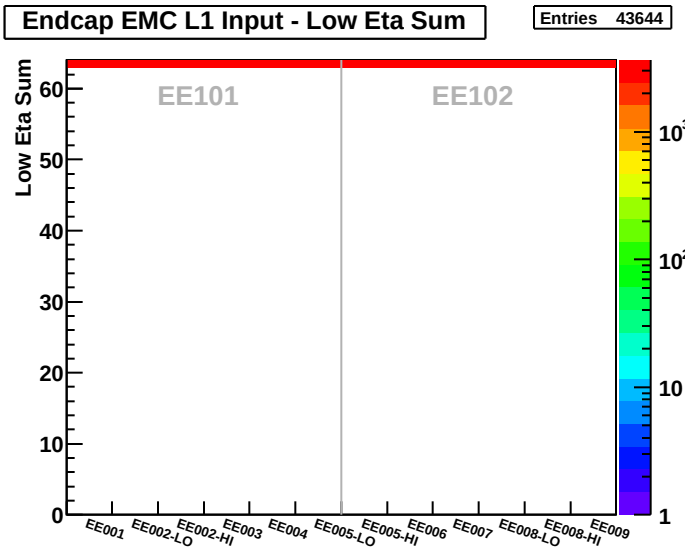

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

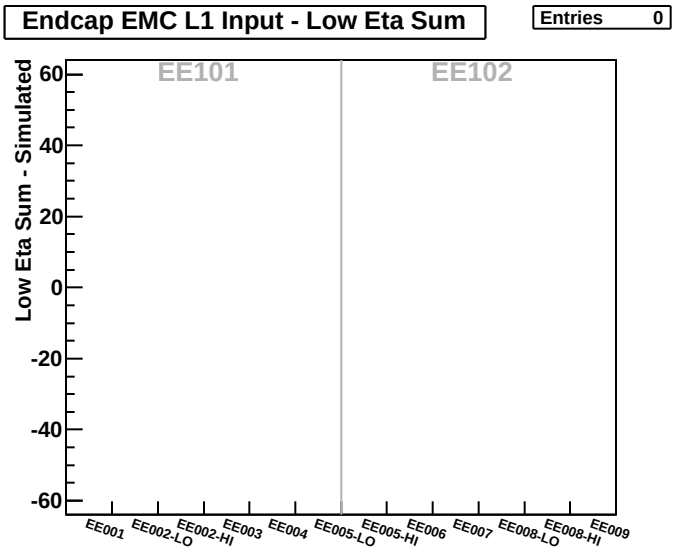

Barrel EMC L1 Input - High Eta Sum
Entries 130932
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

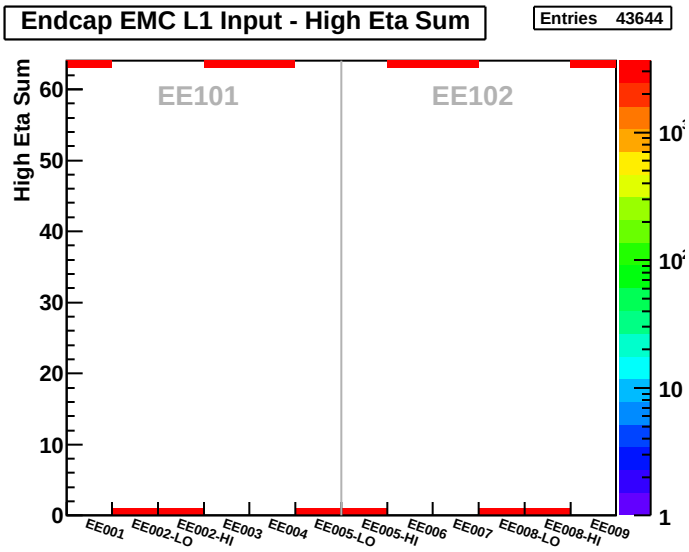

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

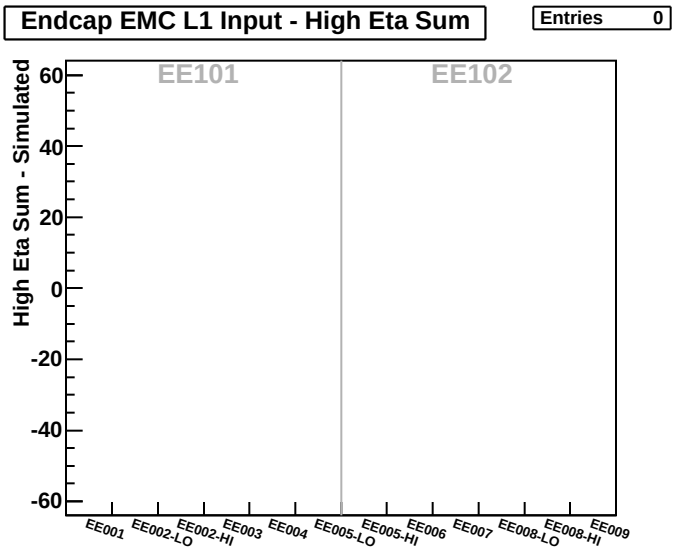

Barrel EMC L1 Input - High Tower Bits
Entries 130932
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

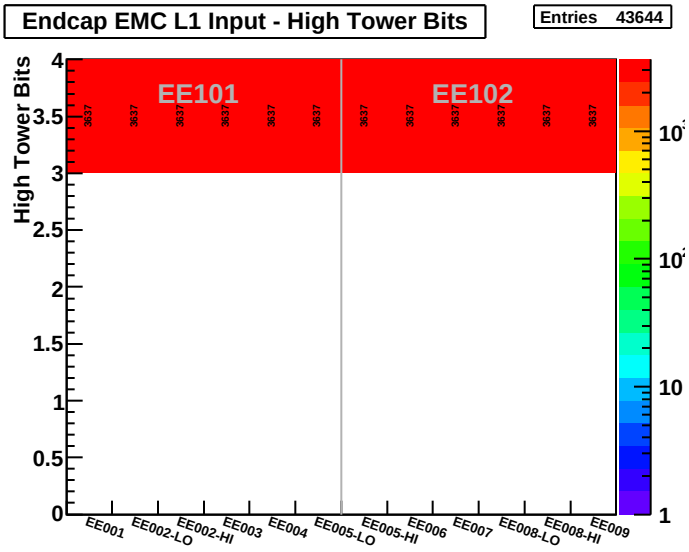

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

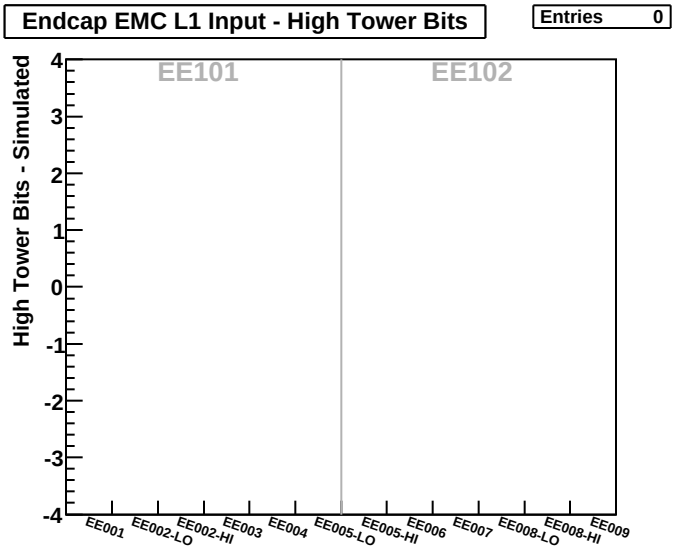

Endcap EMC L1 Input - Low Eta Sum
Entries 43644
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

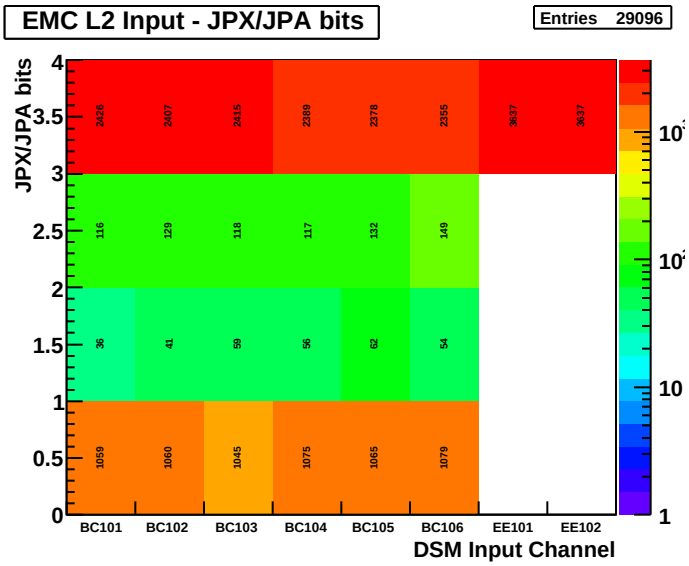

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

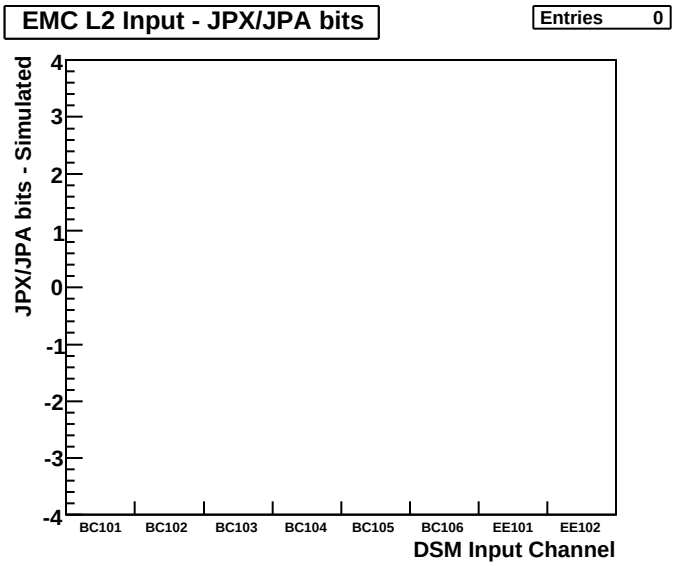

Endcap EMC L1 Input - High Eta Sum
Entries 43644
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

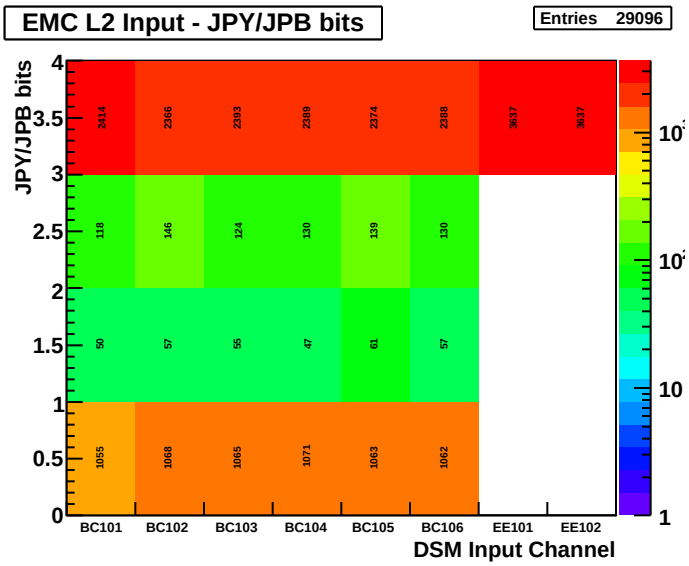

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

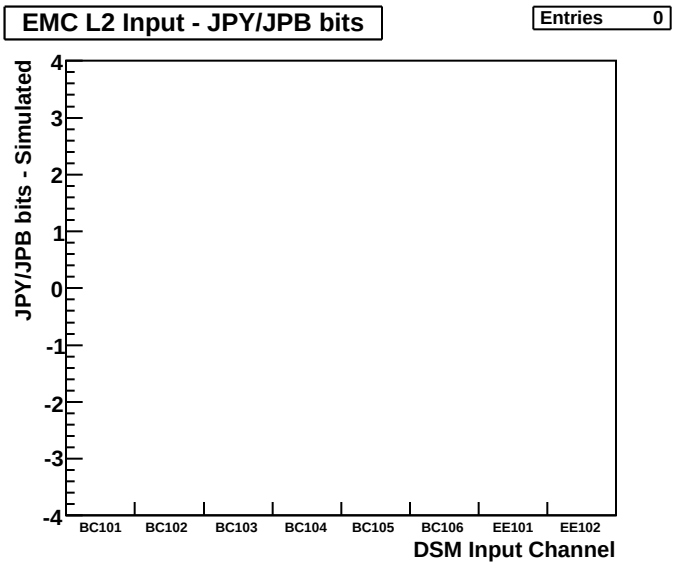

Endcap EMC L1 Input - High Tower Bits
Entries 43644
High Tower Bits
EE101
EE102
3637
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

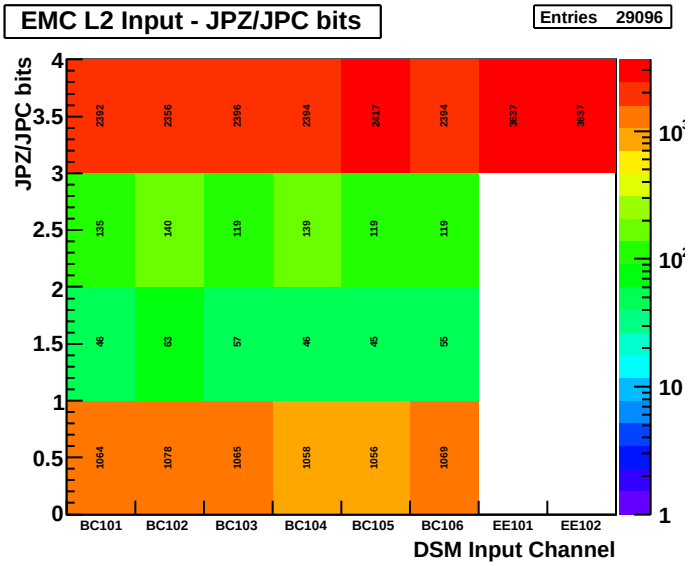

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

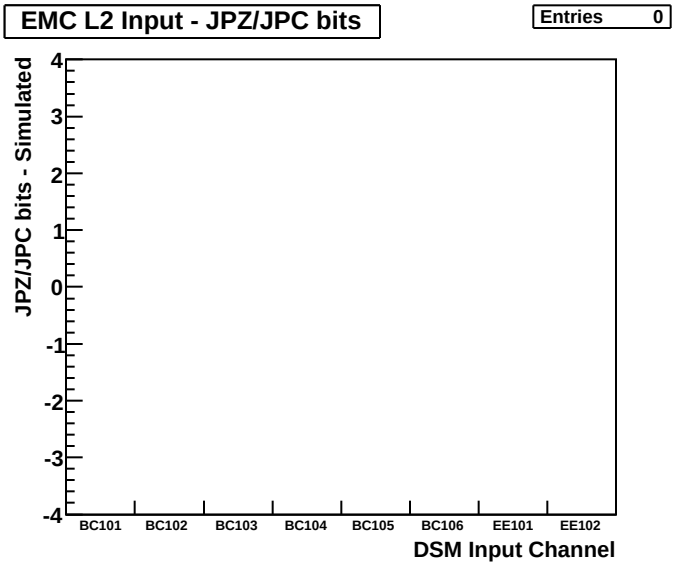

EMC L2 Input - JPX/JPA bits
Entries 29096
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2426 2407 2415 2389 2378 2355 3637 3637
116 129 118 117 132 149
36 41 59 56 62 54
1059 1060 1045 1075 1065 1079

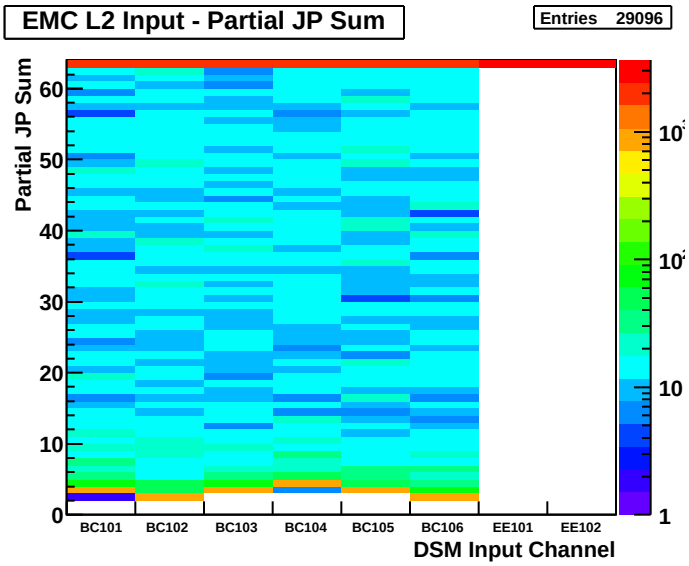

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

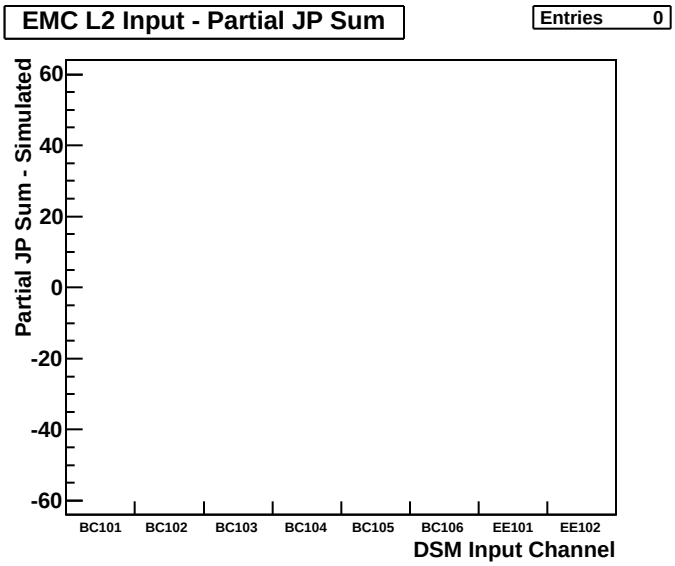

EMC L2 Input - JPY/JPB bits
Entries 29096
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2414 2366 2393 2389 2374 2388 3637 3637
118 146 124 130 139 130
50 57 55 47 61 57
1055 1068 1065 1071 1063 1062

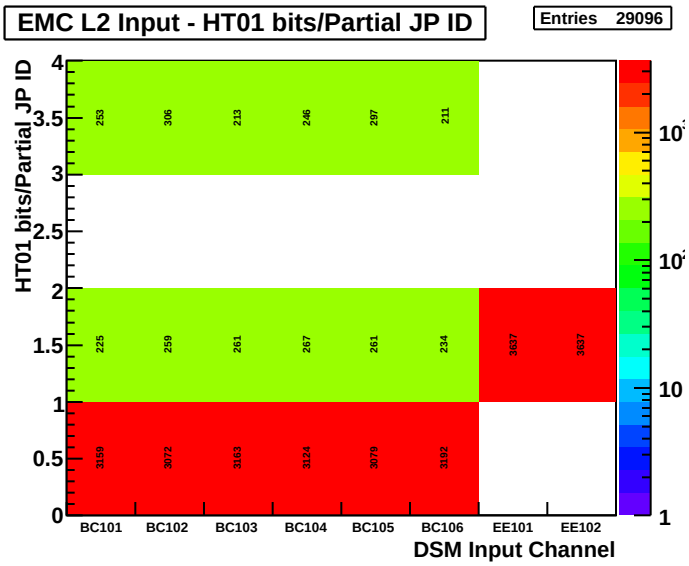

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

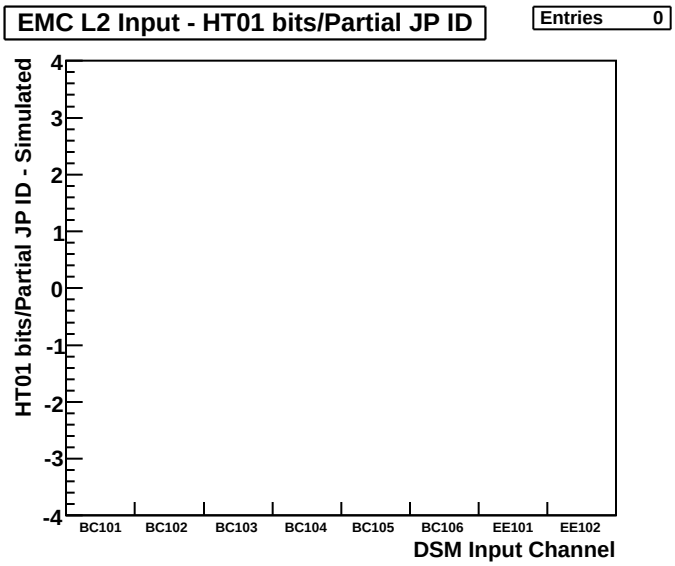

EMC L2 Input - JPZ/JPC bits
Entries 29096
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2392 2356 2306 2304 2417 2304 3637 3637
135 140 119 139 119 119
46 63 57 46 45 55
1064 1078 1065 1058 1056 1069

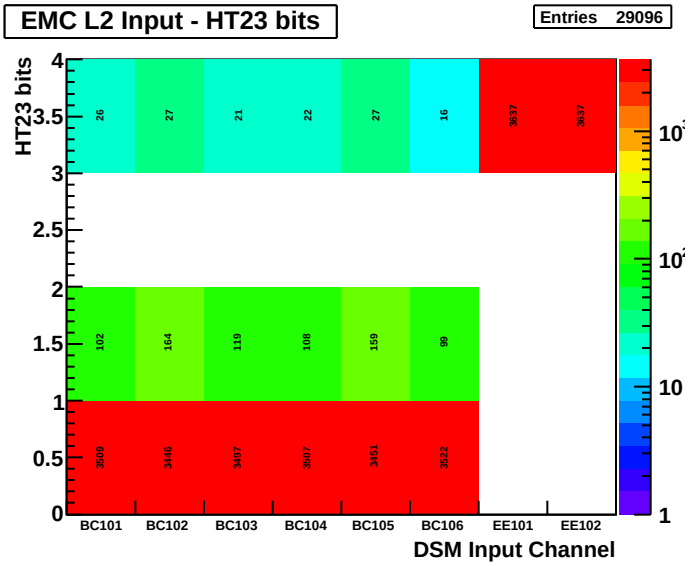

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

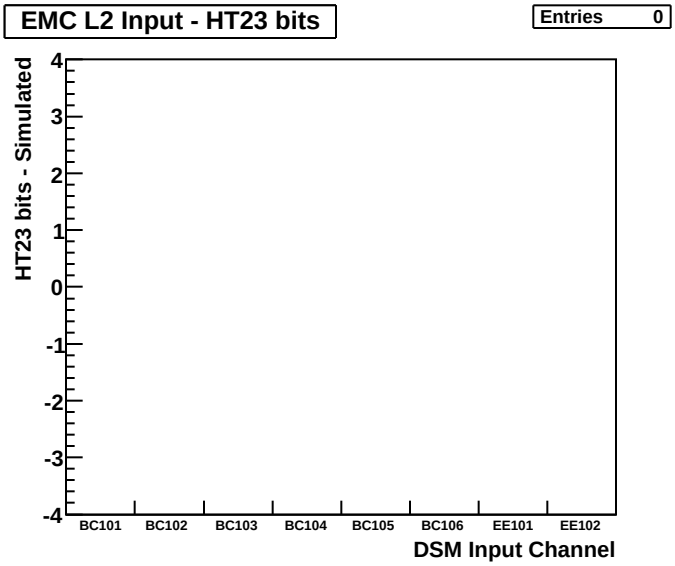

EMC L2 Input - Partial JP Sum
Entries 29096
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

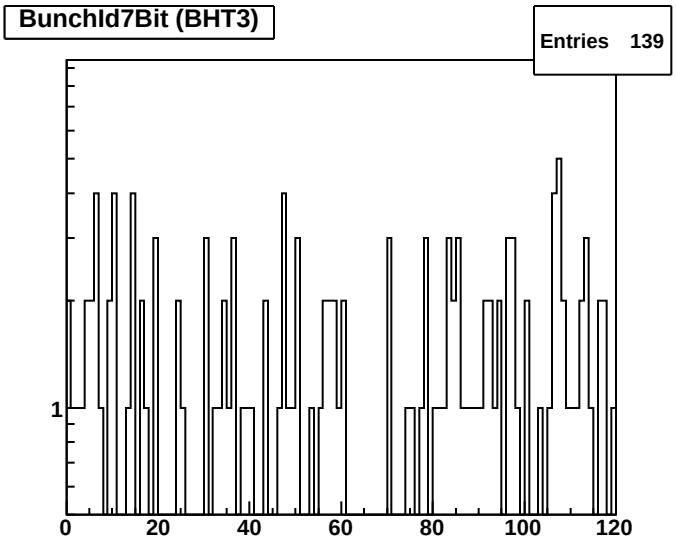

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

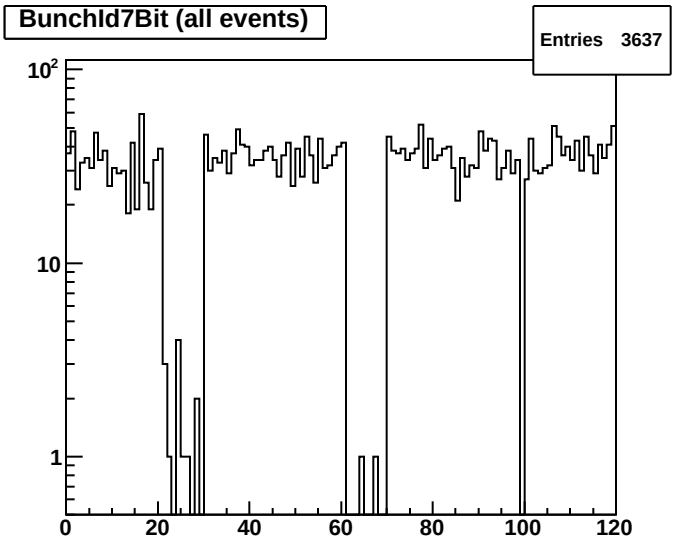

EMC L2 Input - HT01 bits/Partial JP ID
Entries 29096
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

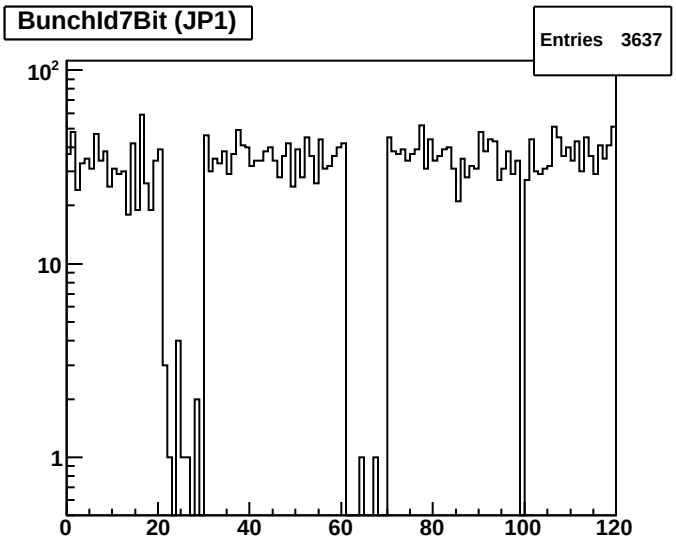

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

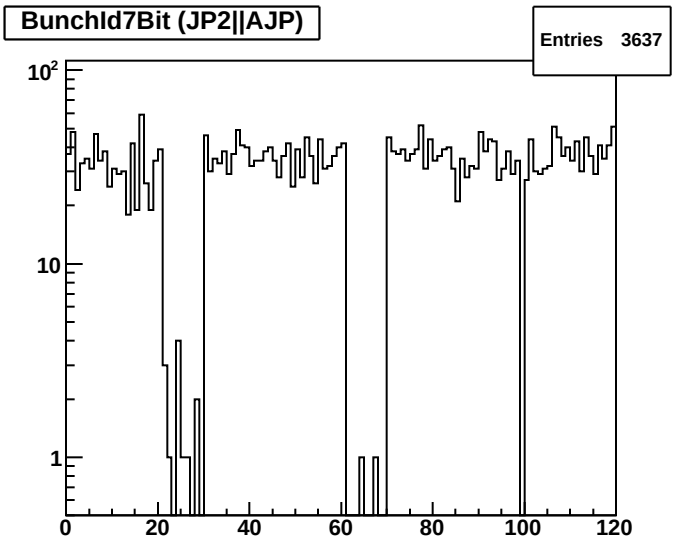

EMC L2 Input - HT23 bits
Entries 29096
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

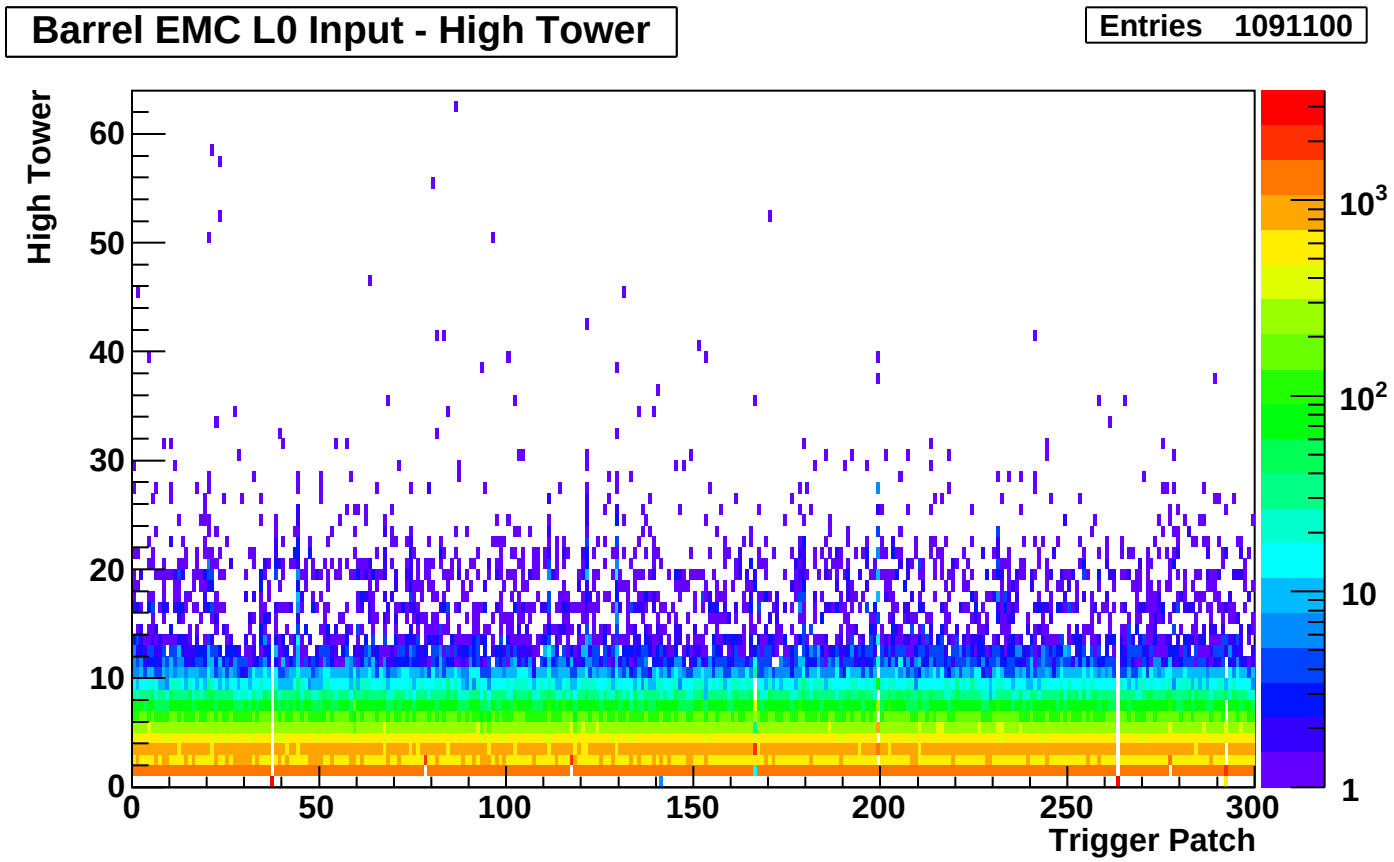

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

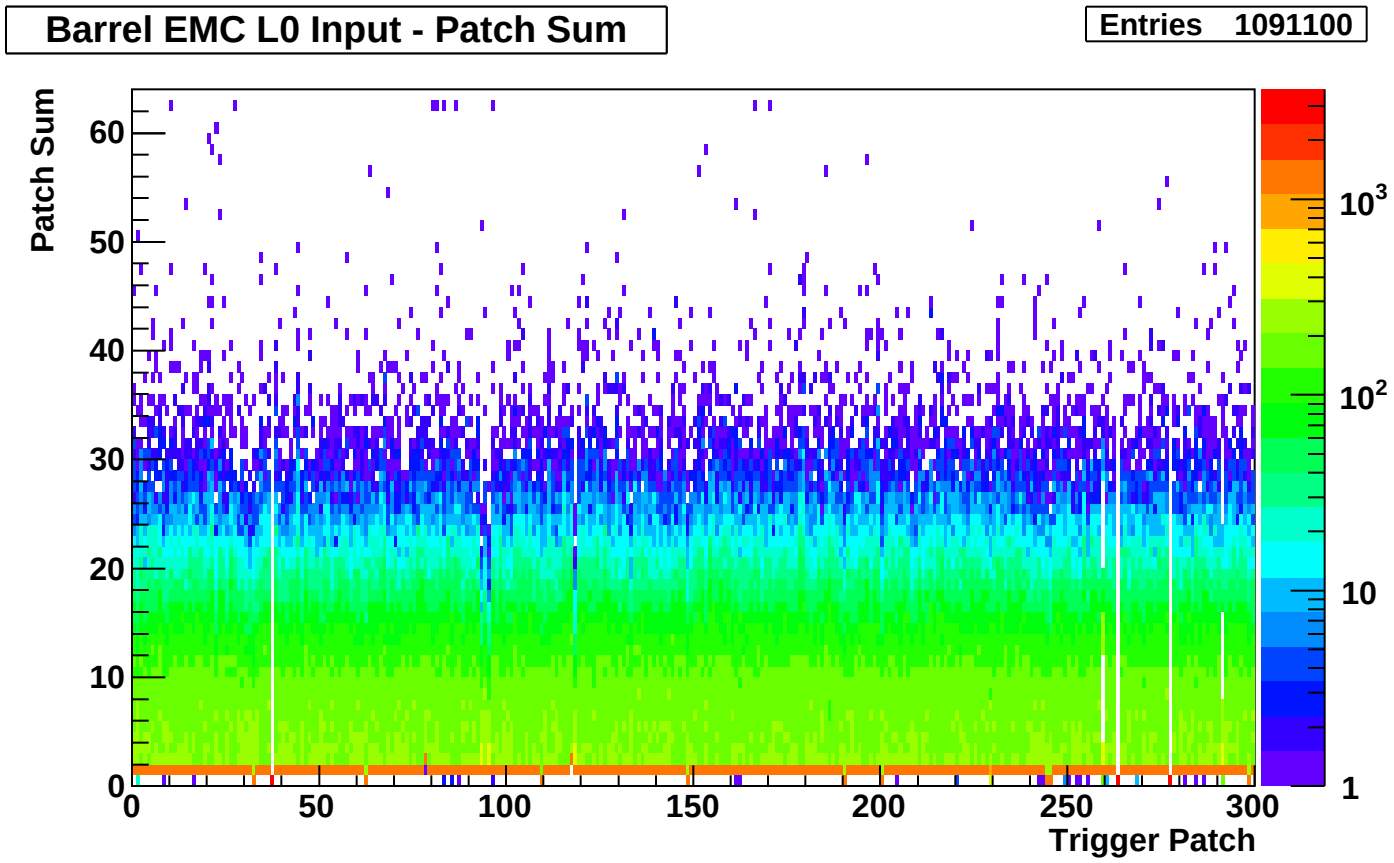

BunchId7Bit (BHT3)
Entries 139
1
0
20
40
60
80
100
120

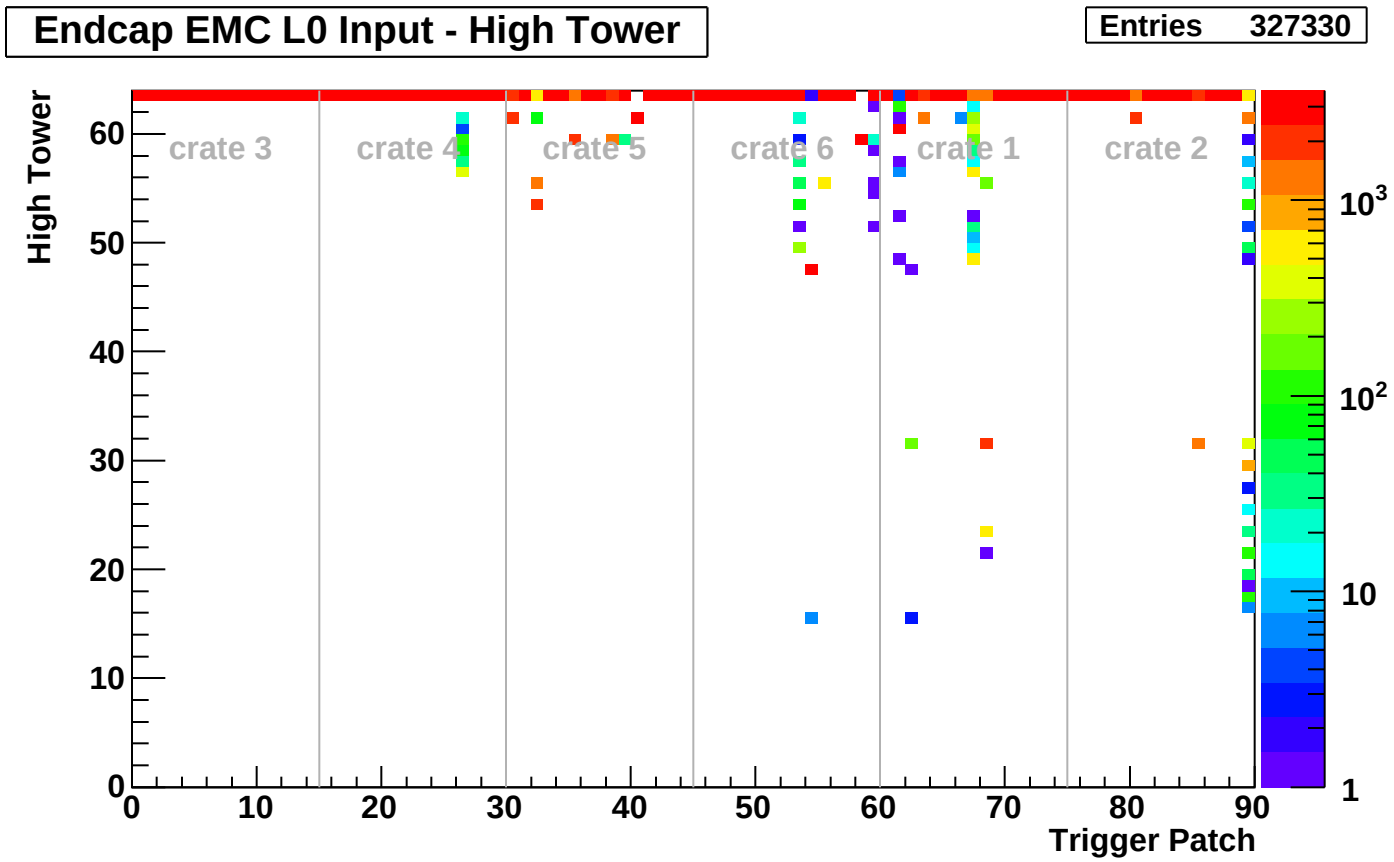

BunchId7Bit (all events)
Entries 3637
10^2
10
1
0
20
40
60
80
100
120

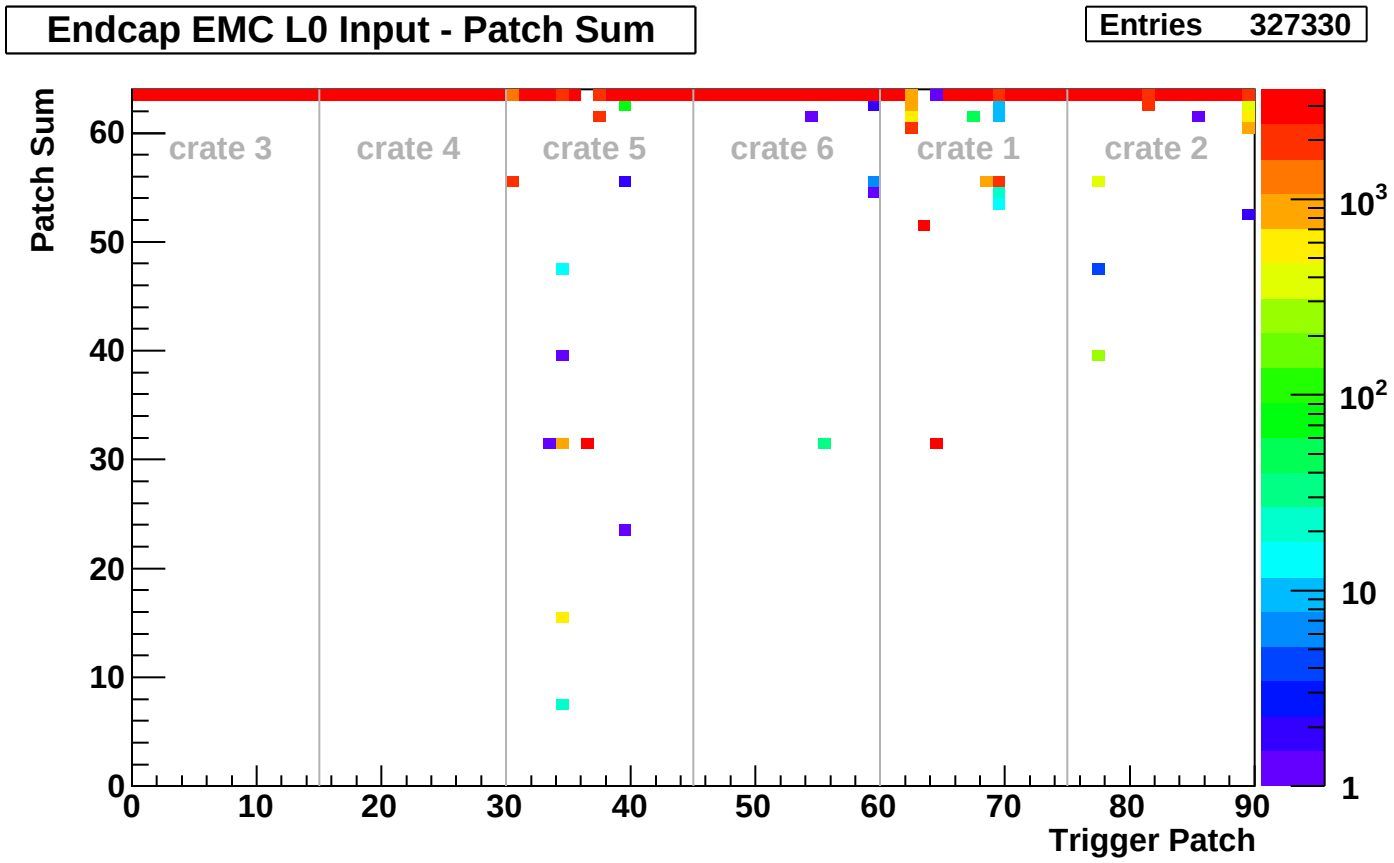

BunchId7Bit (JP1)
Entries 3637
10^2
10
1
0
20
40
60
80
100
120

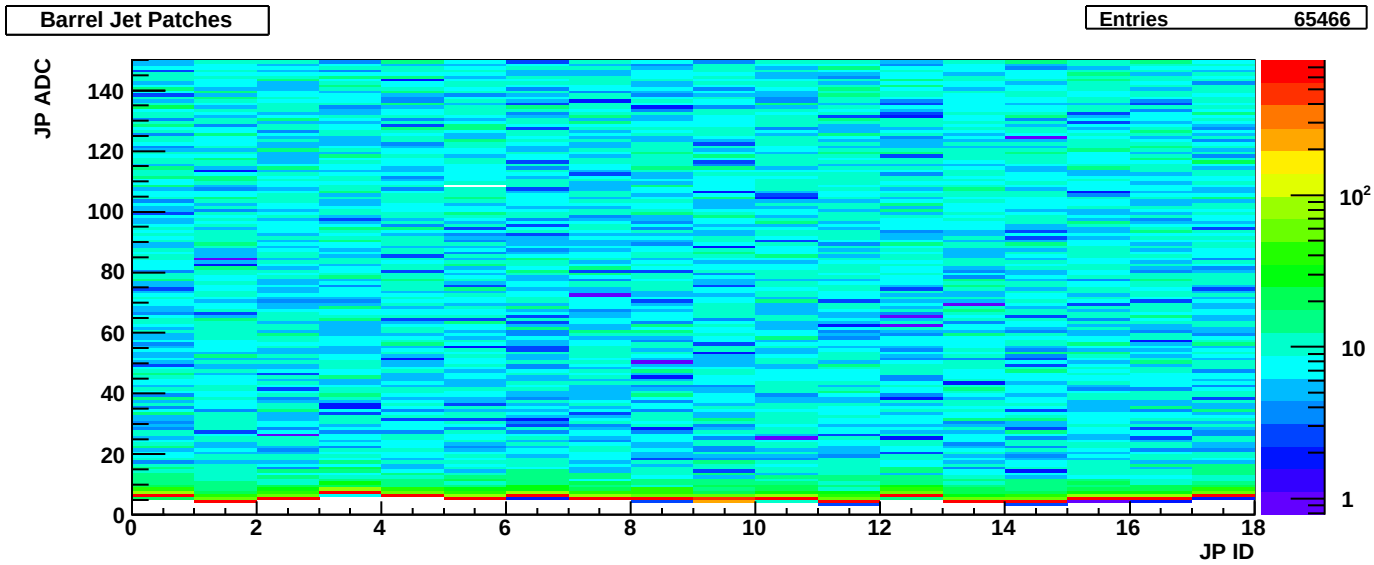

BunchId7Bit (JP2||AJP)
Entries 3637
10^2
10
1
0
20
40
60
80
100
120

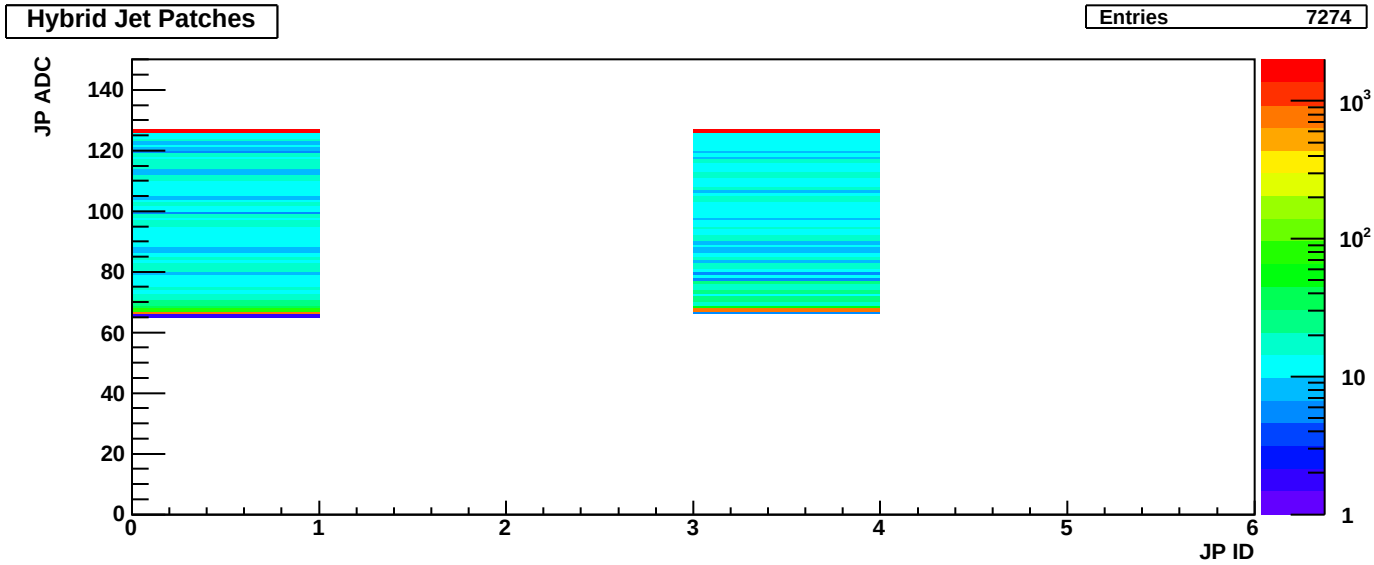

Barrel EMC L0 Input - High Tower
Entries 1091100
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3

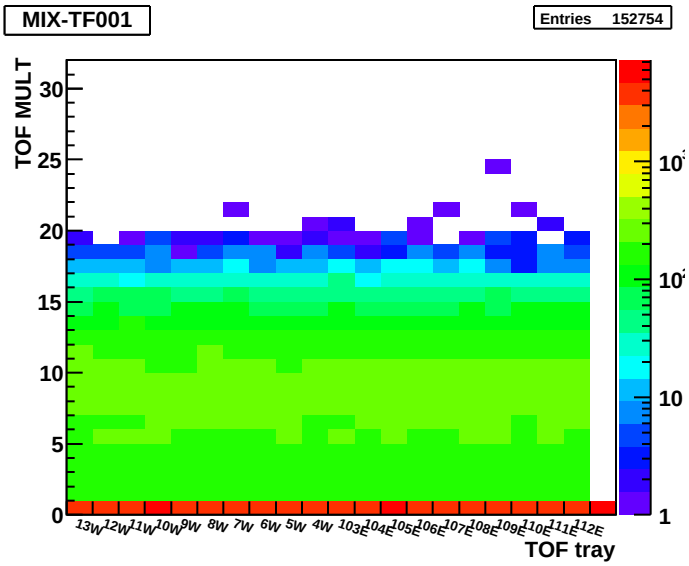

Barrel EMC L0 Input - Patch Sum
Entries 1091100
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3

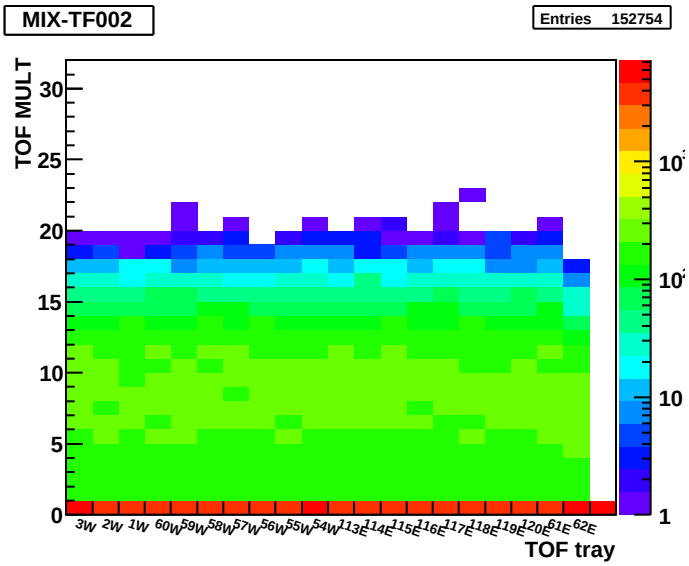

Endcap EMC L0 Input - High Tower
Entries 327330
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

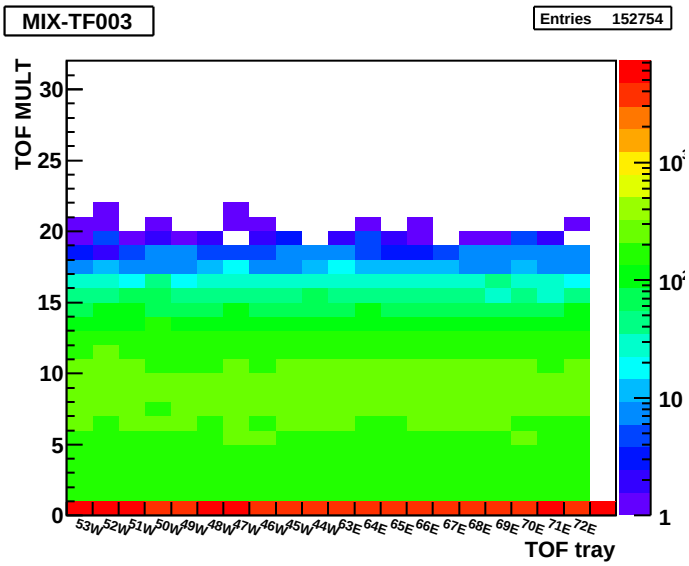

Endcap EMC L0 Input - Patch Sum
Entries 327330
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

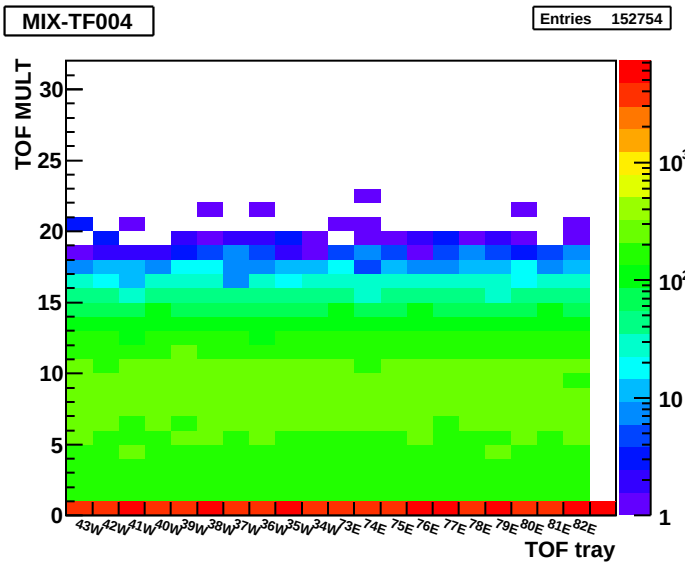

Barrel Jet Patches
Entries 65466
JP ADC
JP ID

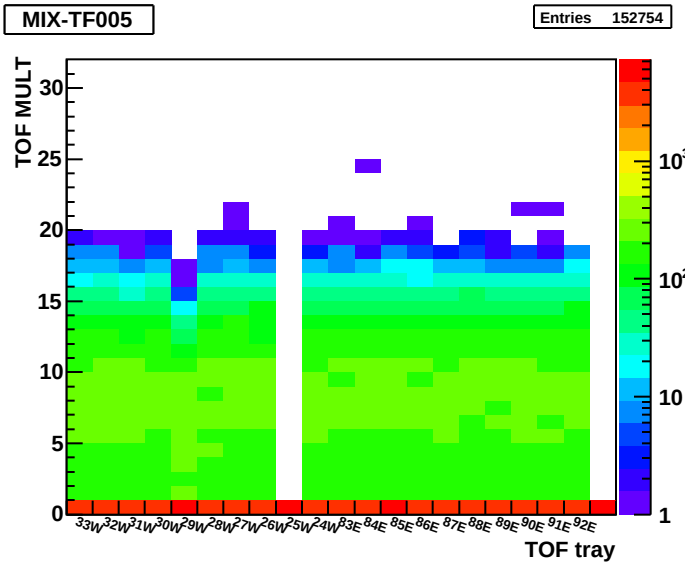

Hybrid Jet Patches
Entries 7274
JP ADC
JP ID

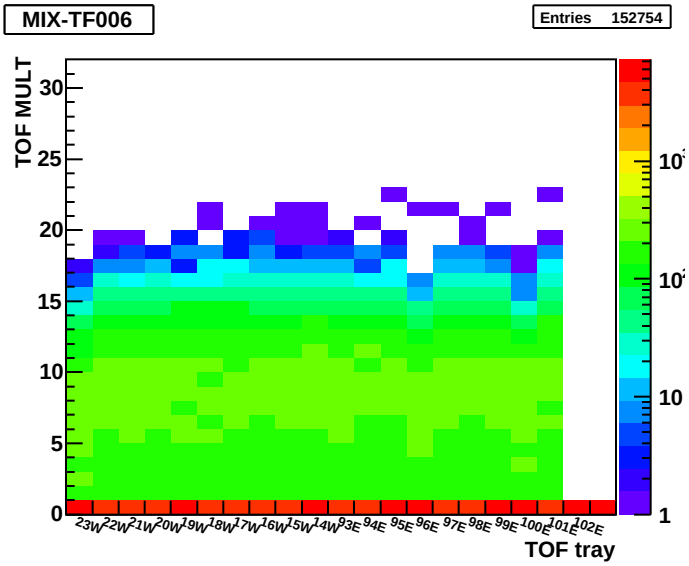

MIX-TF001
Entries 152754
TOF MULT
TOF tray

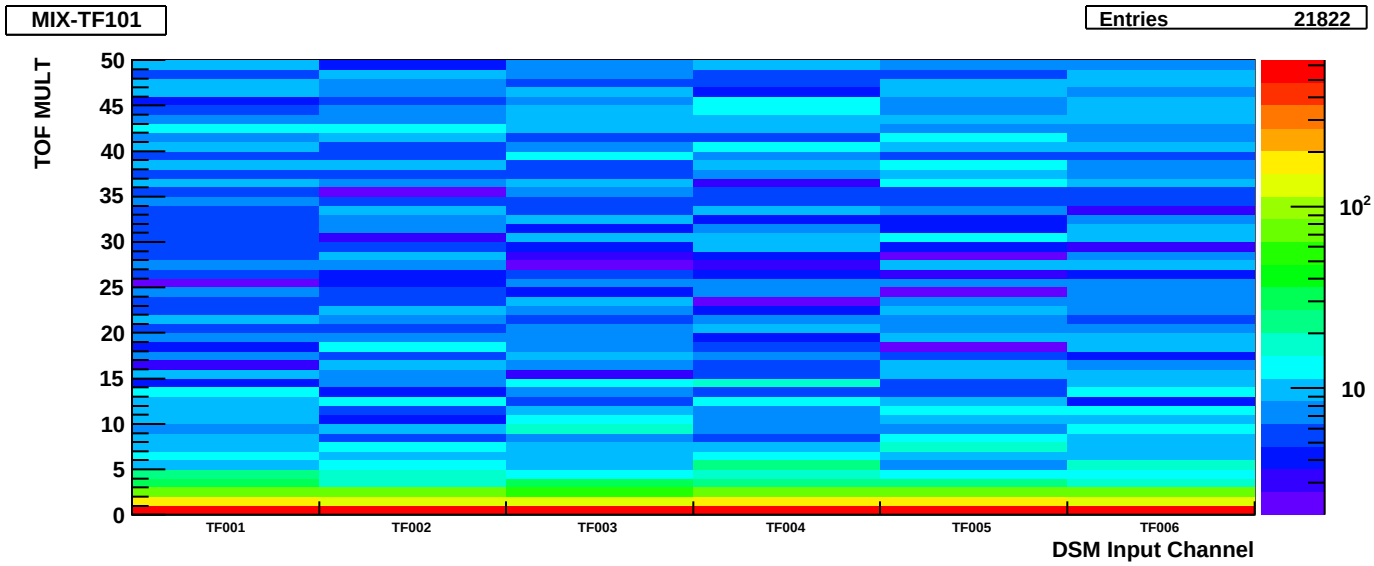

MIX-TF002
Entries 152754
TOF MULT
TOF tray

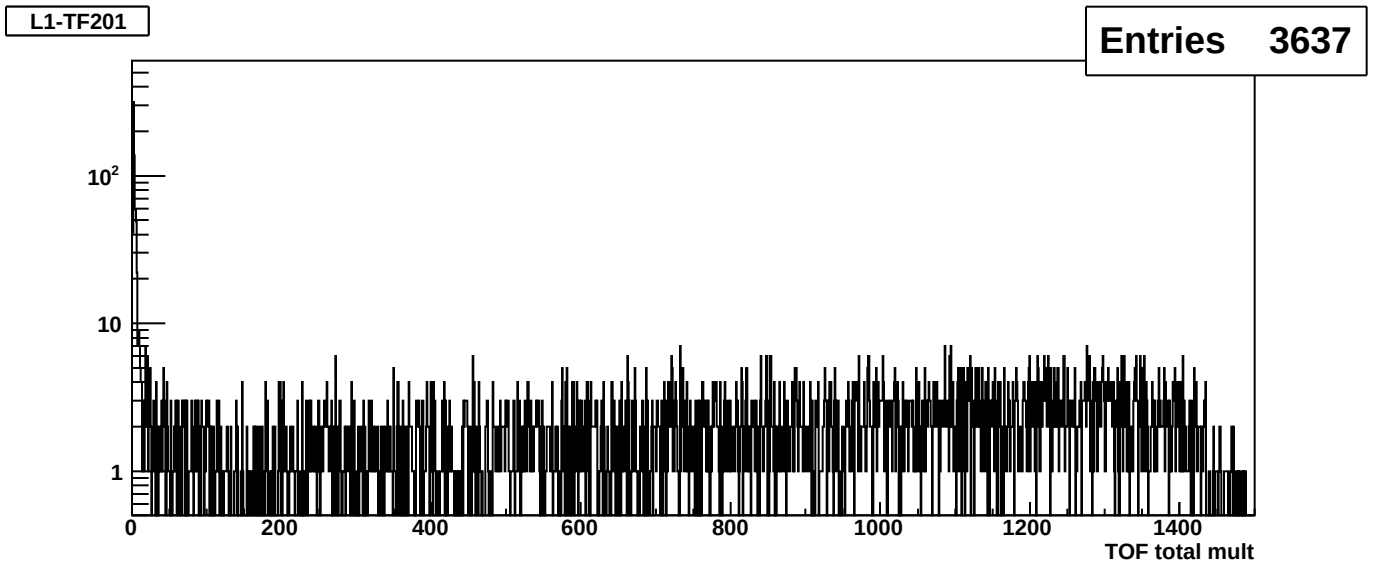

MIX-TF003
Entries 152754
TOF MULT
TOF tray

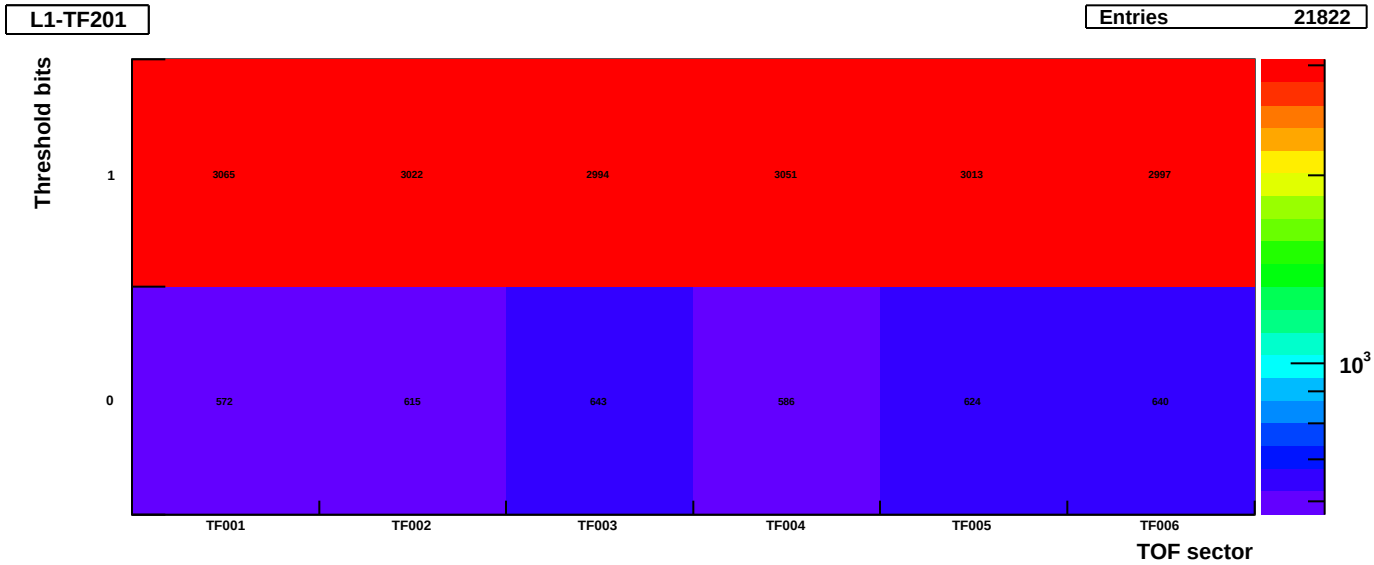

MIX-TF004
Entries 152754
TOF MULT
TOF tray

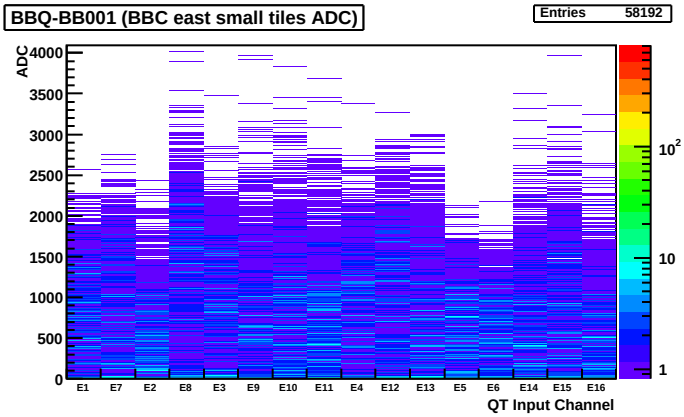

MIX-TF005
Entries 152754
TOF MULT
TOF tray

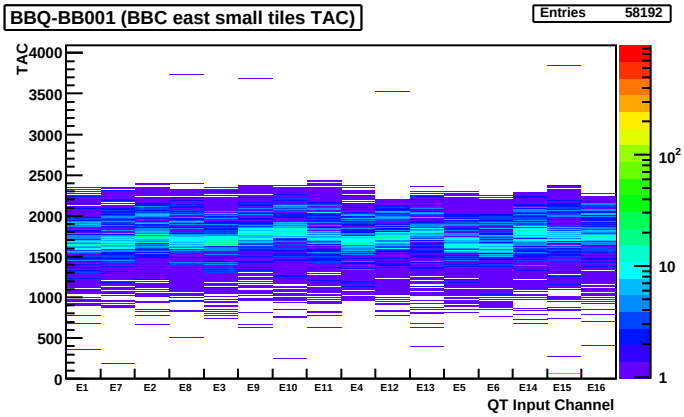

MIX-TF006
Entries 152754
TOF MULT
TOF tray

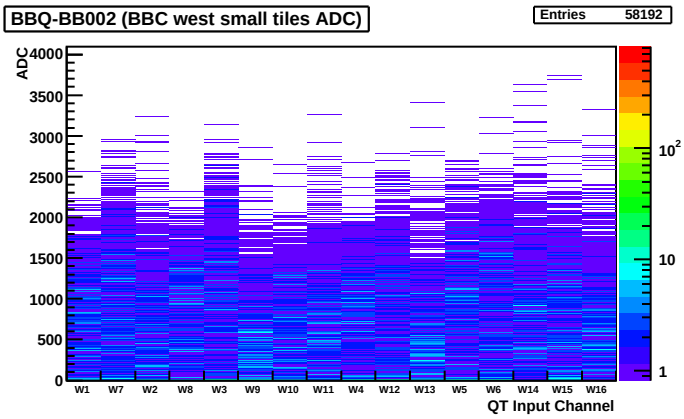

MIX-TF101
Entries 21822
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^2
10

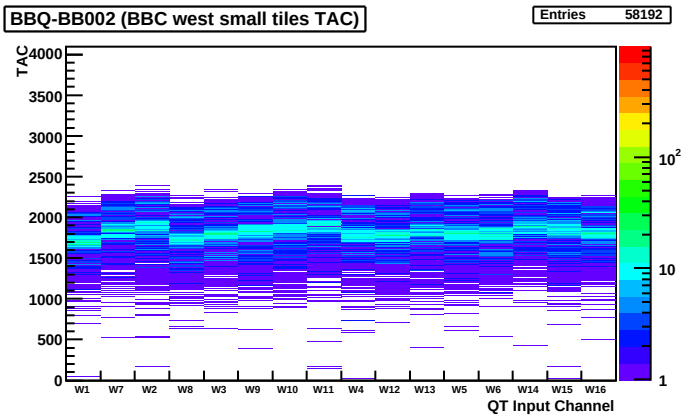

L1-TF201
Entries 3637
10^2
10
1
0
200
400
600
800
1000
1200
1400
TOF total mult

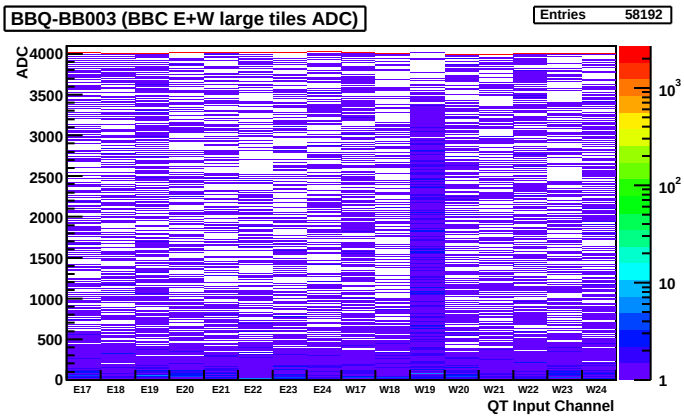

L1-TF201
Entries 21822
Threshold bits
1
0
3065
3022
2994
3051
3013
2997
572
615
643
586
624
640
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^3

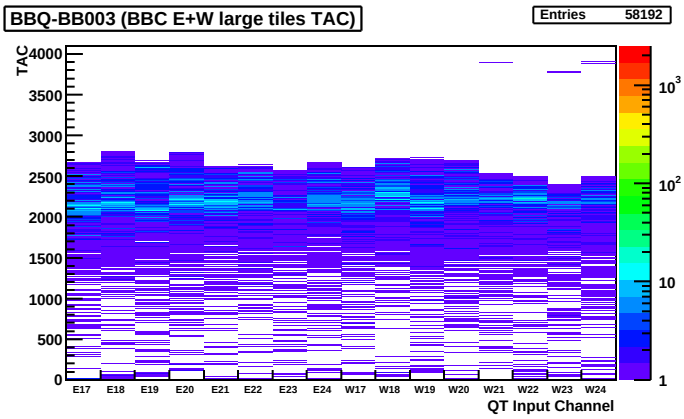

BBQ-BB001 (BBC east small tiles ADC)
Entries 58192
ADC
QT Input Channel

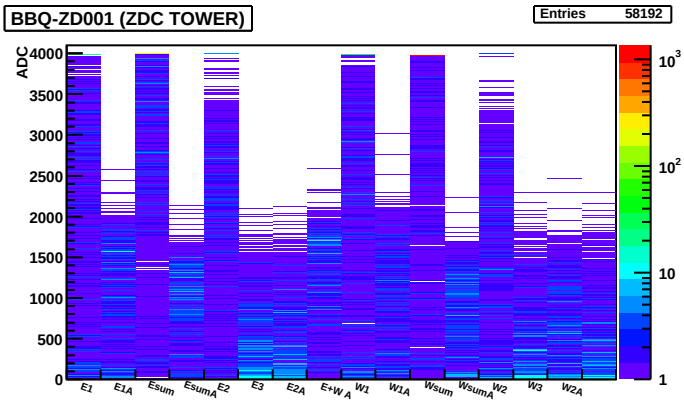

BBQ-BB001 (BBC east small tiles TAC)
Entries 58192
TAC
QT Input Channel

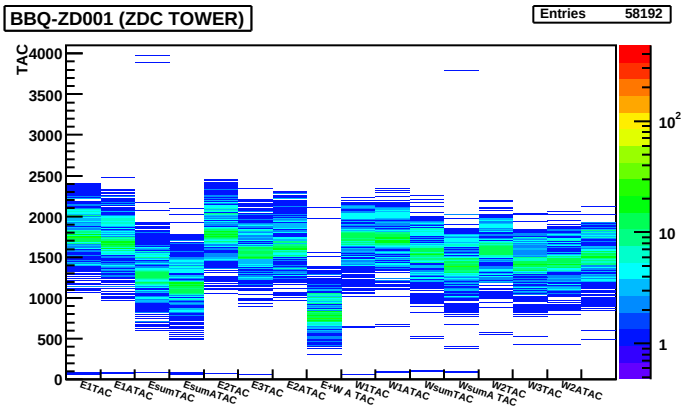

BBQ-BB002 (BBC west small tiles ADC)
Entries 58192
ADC
QT Input Channel

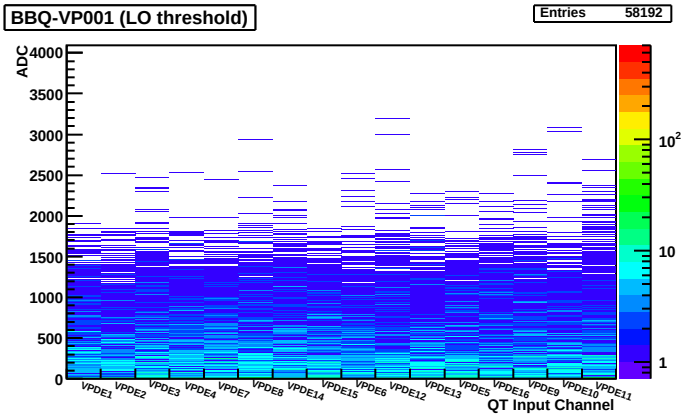

BBQ-BB002 (BBC west small tiles TAC)
Entries 58192
TAC
QT Input Channel

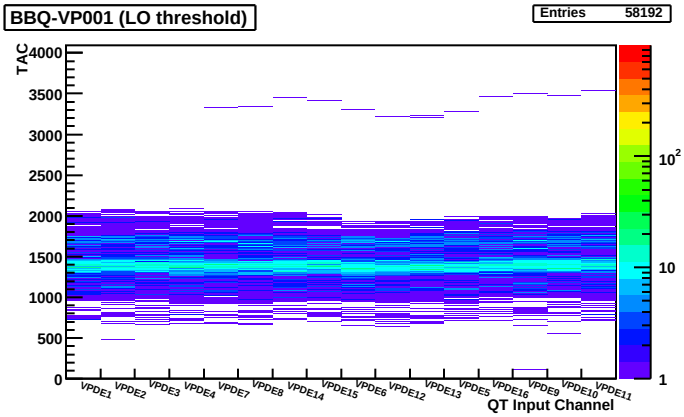

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 58192
ADC
QT Input Channel

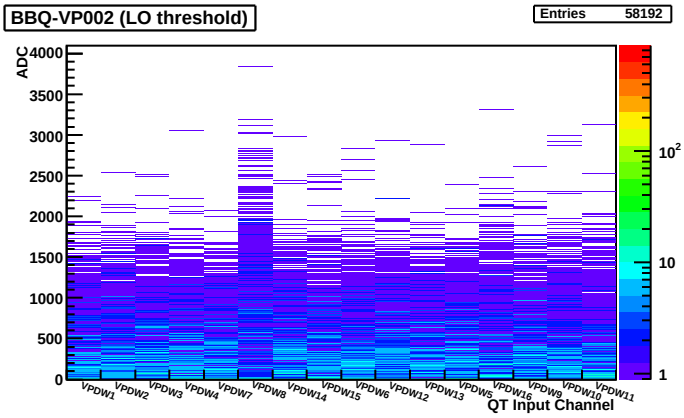

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 58192
TAC
QT Input Channel

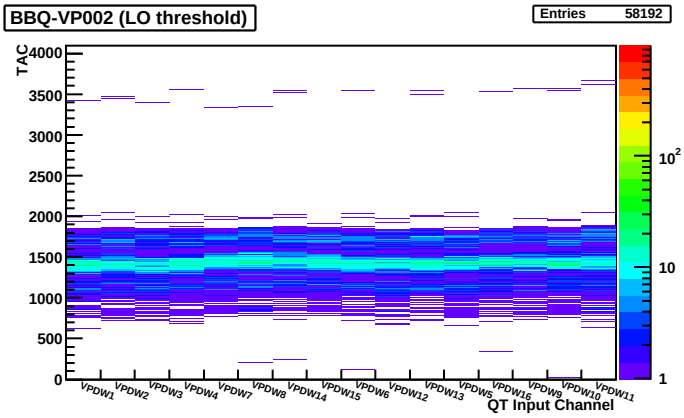

BBQ-ZD001 (ZDC TOWER)
Entries 58192
ADC

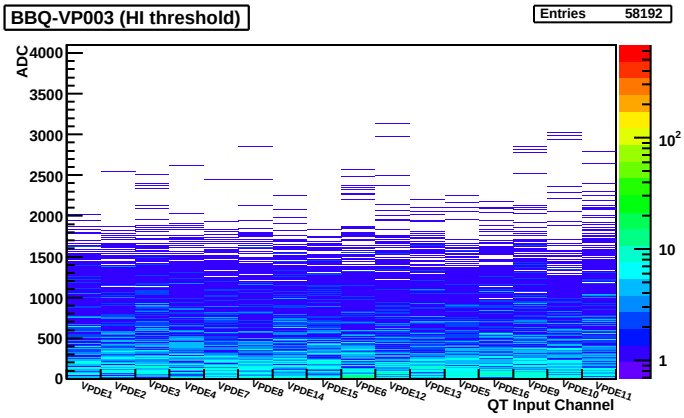

BBQ-ZD001 (ZDC TOWER)
Entries 58192
TAC

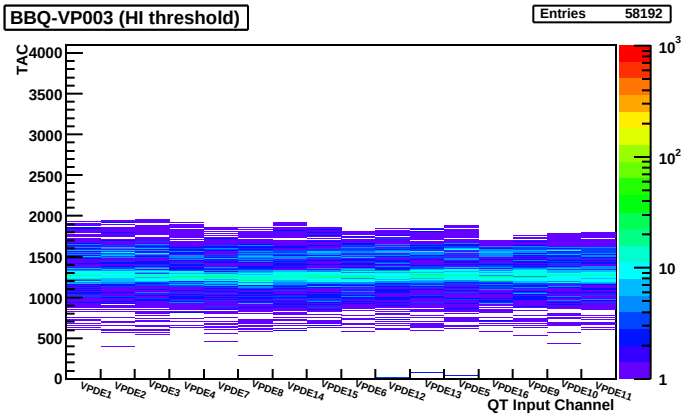

BBQ-VP001 (LO threshold)
Entries 58192
ADC
QT Input Channel

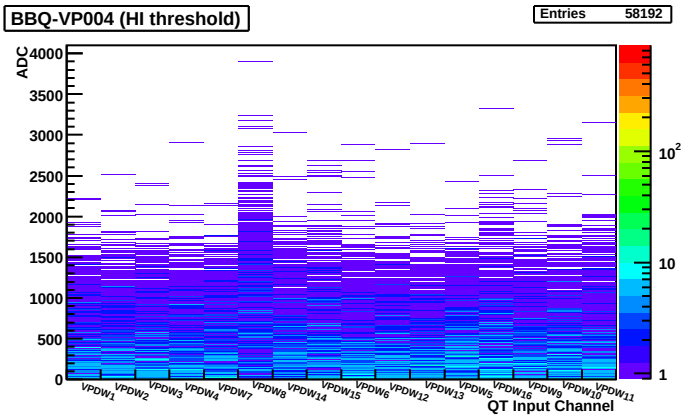

BBQ-VP001 (LO threshold)
Entries 58192
TAC
QT Input Channel

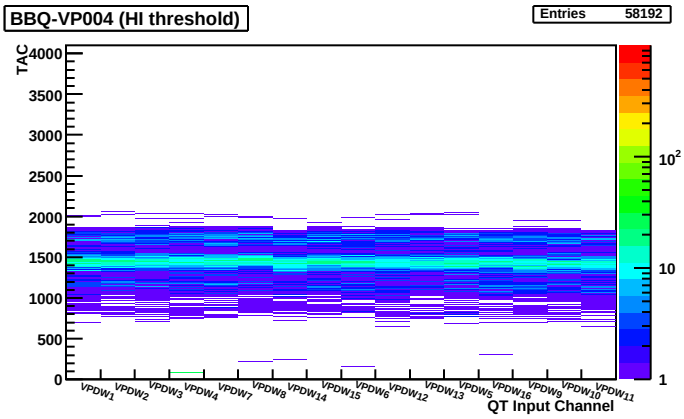

BBQ-VP002 (LO threshold)
Entries 58192
ADC
QT Input Channel

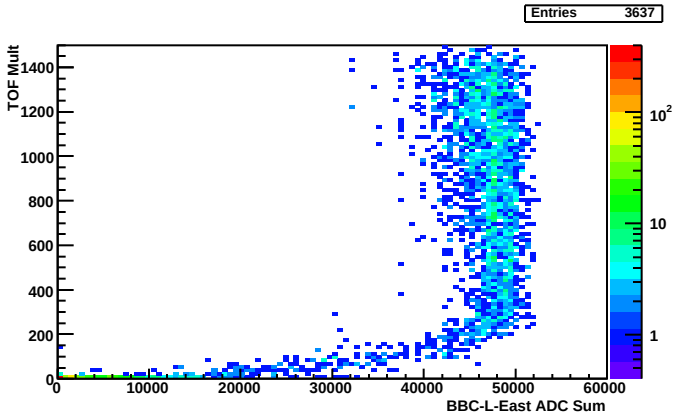

BBQ-VP002 (LO threshold)
Entries 58192
TAC
QT Input Channel

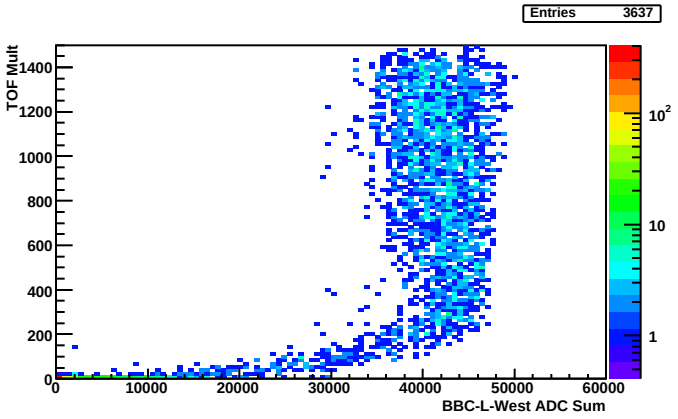

BBQ-VP003 (HI threshold)
Entries 58192
ADC
QT Input Channel

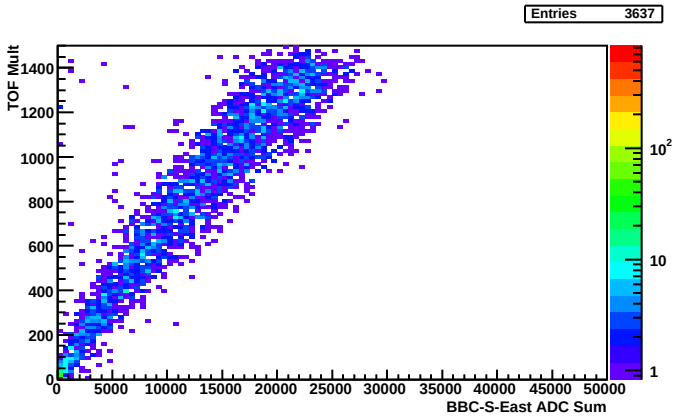

BBQ-VP003 (HI threshold)
Entries 58192
TAC
QT Input Channel

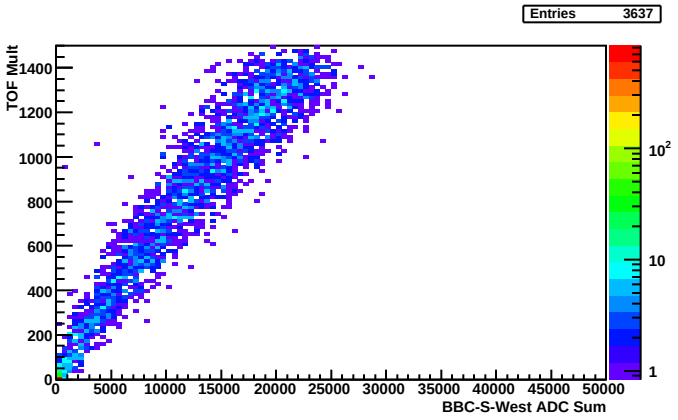

BBQ-VP004 (HI threshold)
Entries 58192
ADC
QT Input Channel

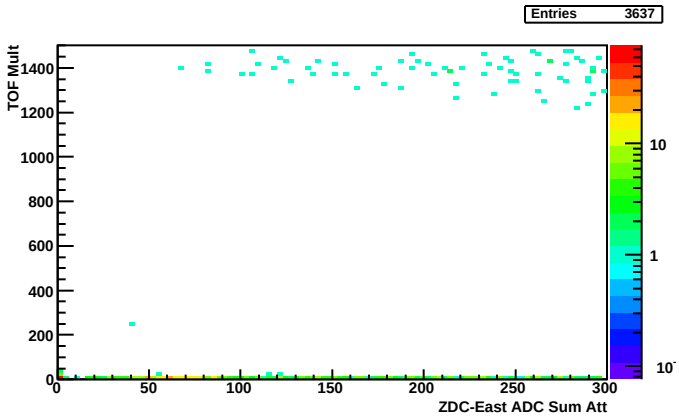

BBQ-VP004 (HI threshold)
Entries 58192
TAC
QT Input Channel

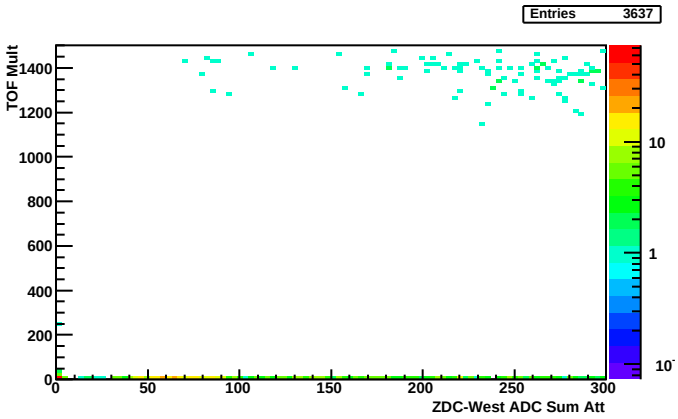

Entries 3637
TOF Mult
BBC-L-East ADC Sum

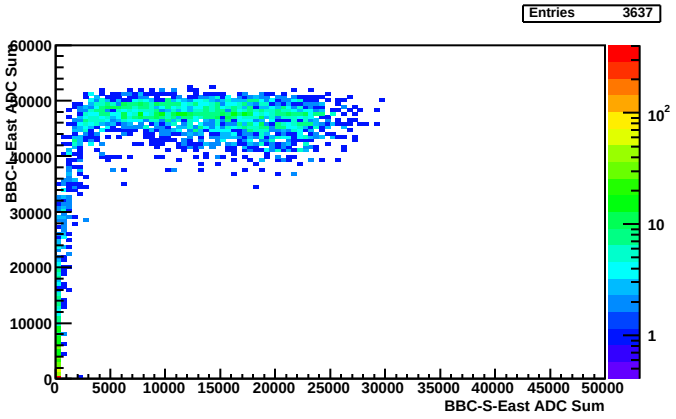

Entries 3637
TOF Mult
BBC-L-West ADC Sum

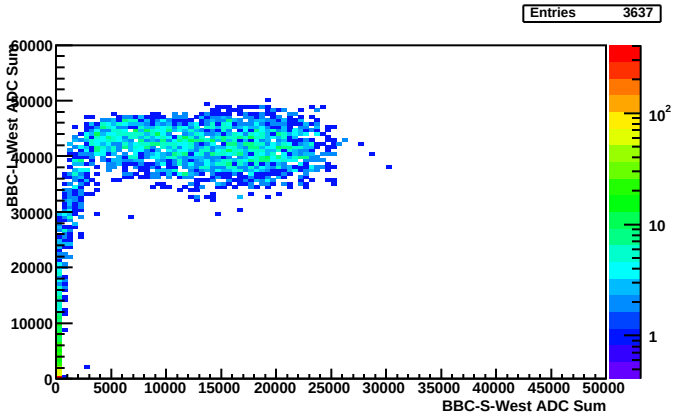

Entries 3637
TOF Mult
BBC-S-East ADC Sum

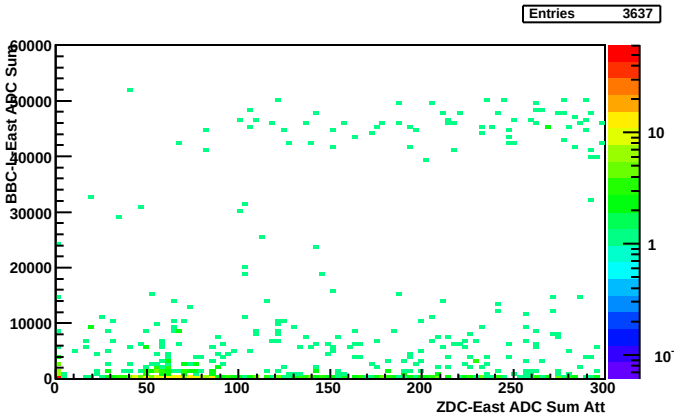

Entries 3637
TOF Mult
BBC-S-West ADC Sum

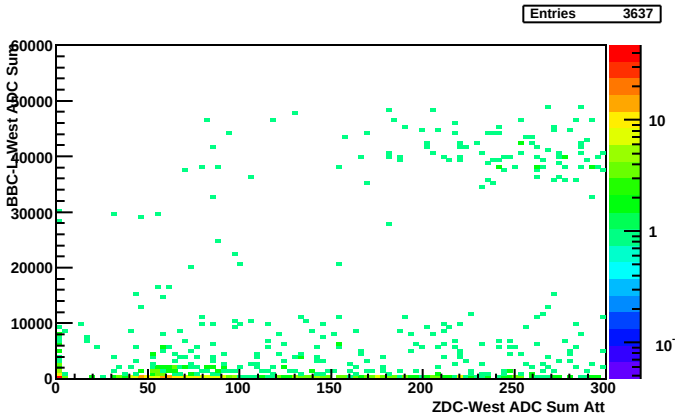

Entries 3637
TOF Mult
ZDC-East ADC Sum Att

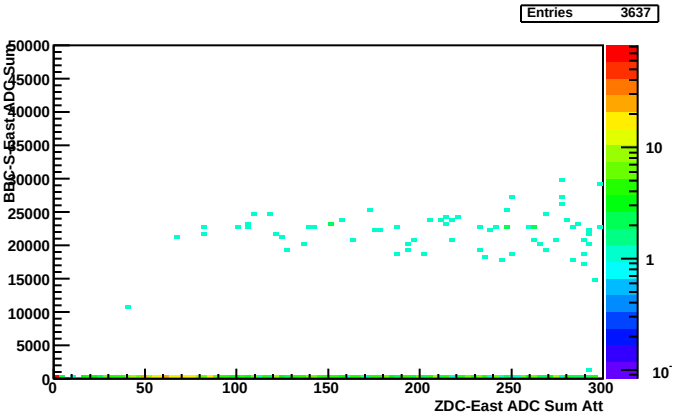

Entries 3637
TOF Mult
ZDC-West ADC Sum Att

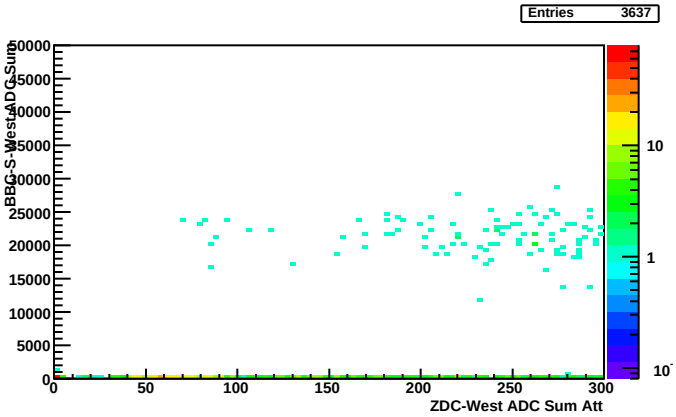

Entries 3637
BBC-L-East ADC Sum
BBC-S-East ADC Sum

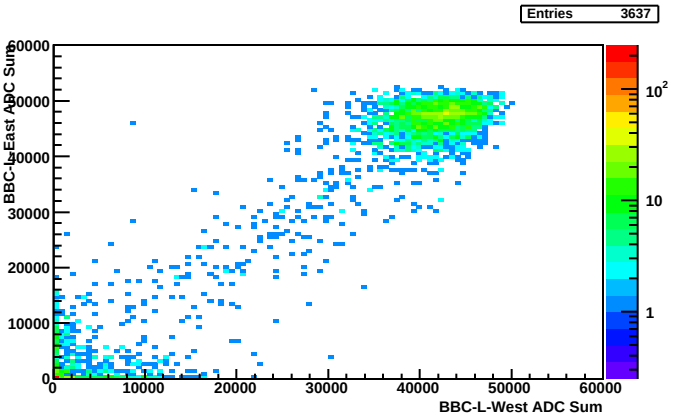

Entries 3637
BBC-L-West ADC Sum
BBC-S-West ADC Sum

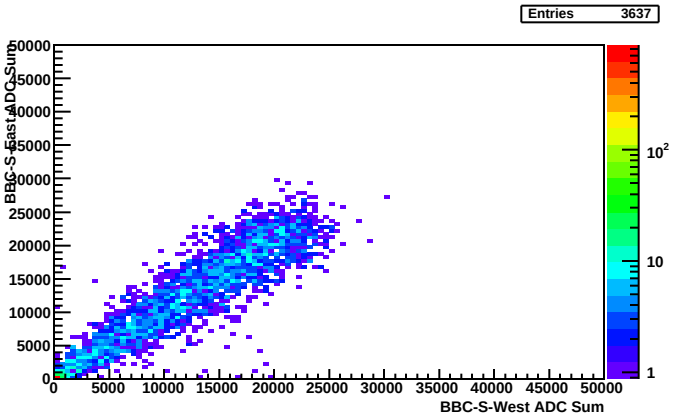

Entries 3637
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

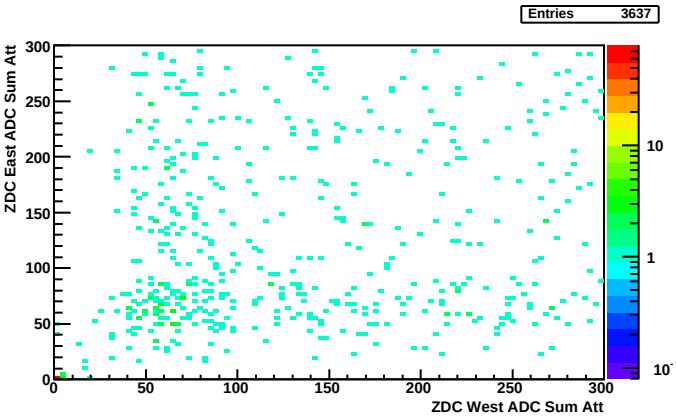

Entries 3637
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

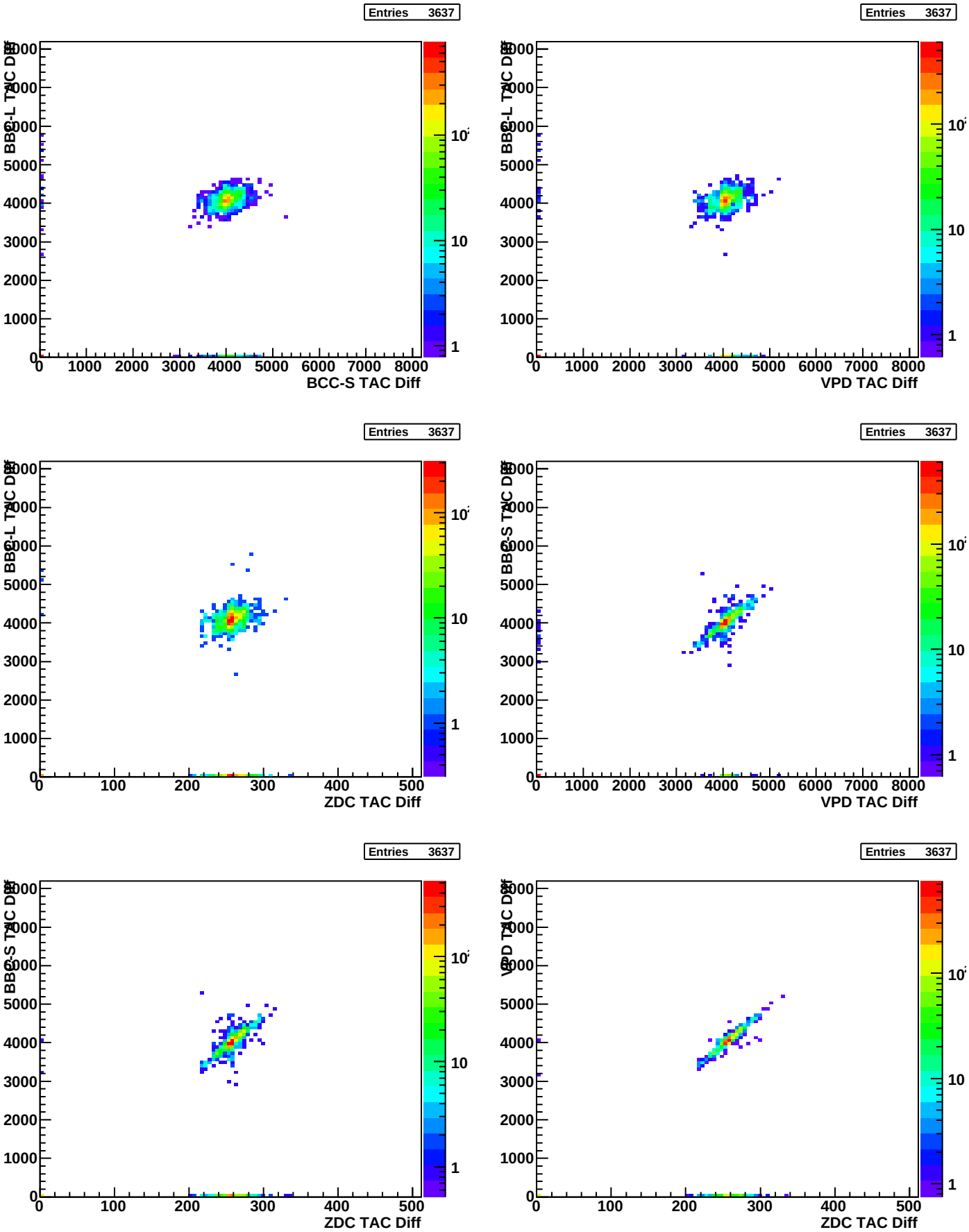

Entries 3637
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

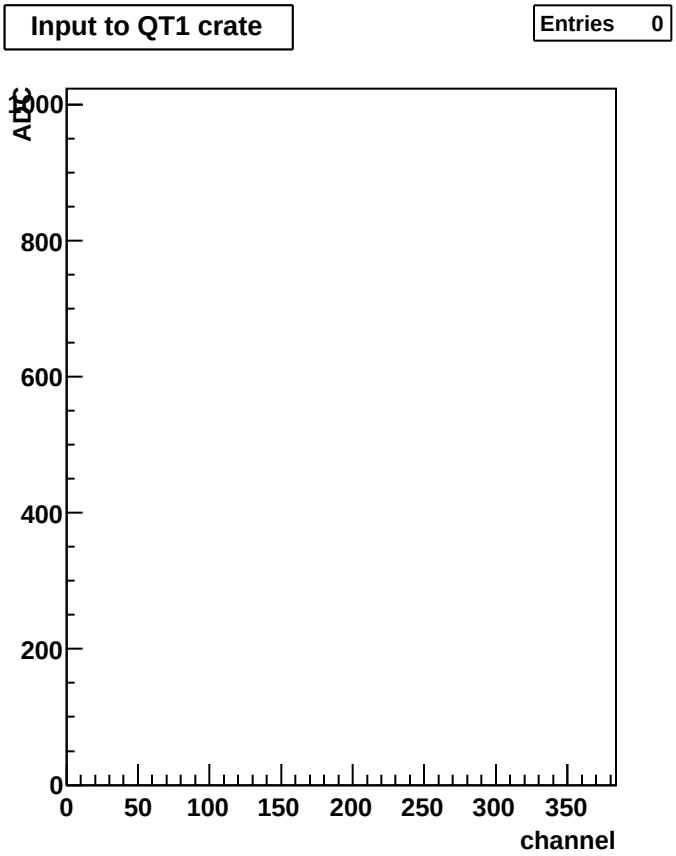

Entries 3637
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

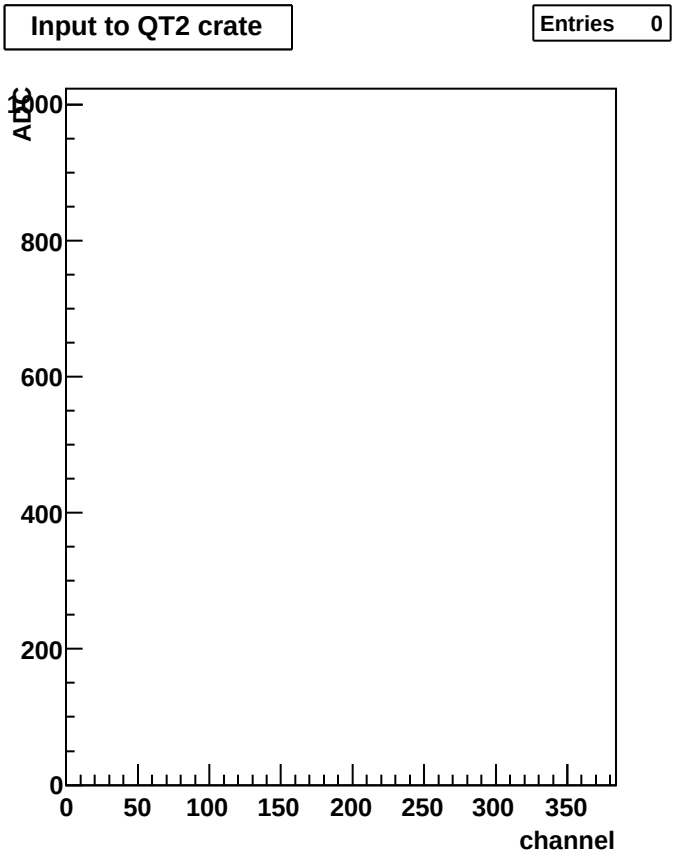

Entries 3637
BBC-L-East ADC Sum
BBC-L-West ADC Sum

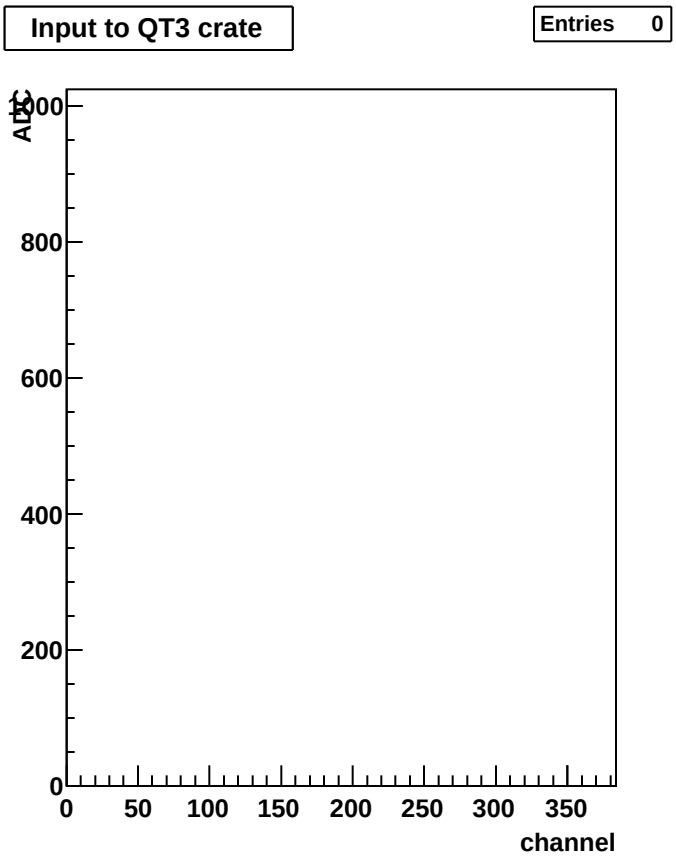

Entries 3637
BBC-S-East ADC Sum
BBC-S-West ADC Sum

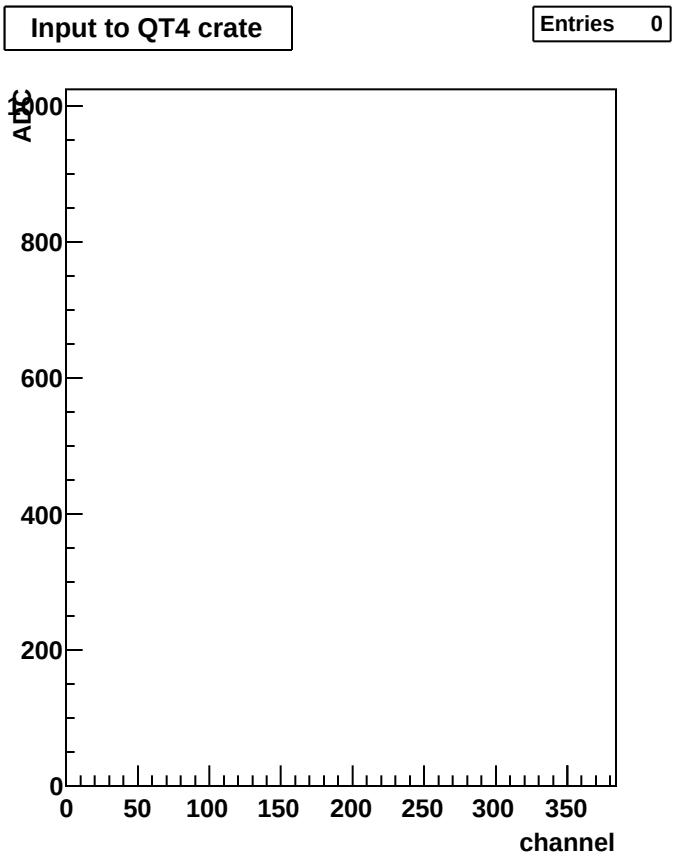

Entries 3637
ZDC East ADC Sum Att
ZDC West ADC Sum Att

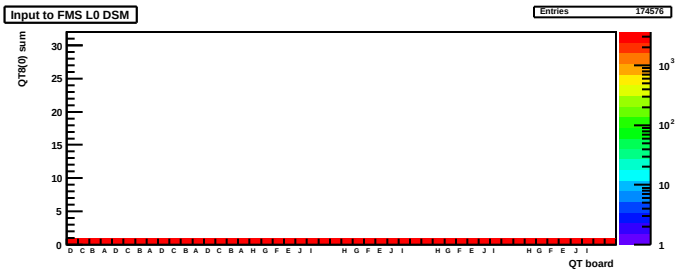

Entries 3637
BBC-L TAC Diff
BCC-S TAC Diff
Entries 3637
BBC-L TAC Diff
VPD TAC Diff
Entries 3637
BBC-L TAC Diff
ZDC TAC Diff
Entries 3637
BBC-S TAC Diff
VPD TAC Diff
Entries 3637
BBC-S TAC Diff
ZDC TAC Diff
Entries 3637
VPD TAC Diff
ZDC TAC Diff

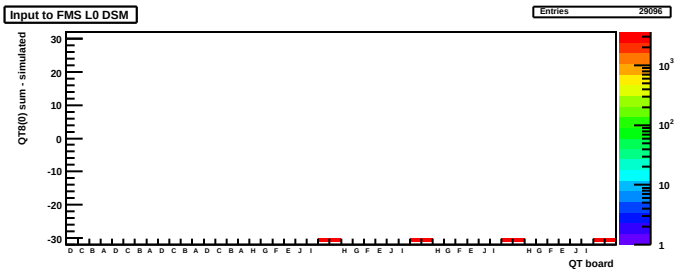

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

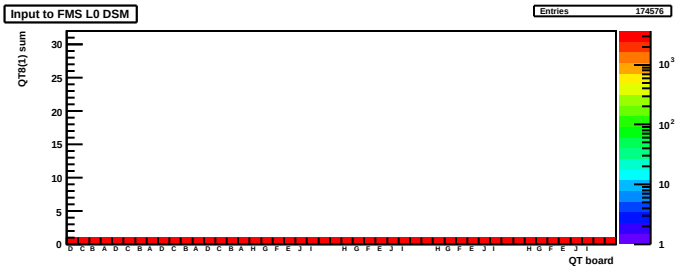

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

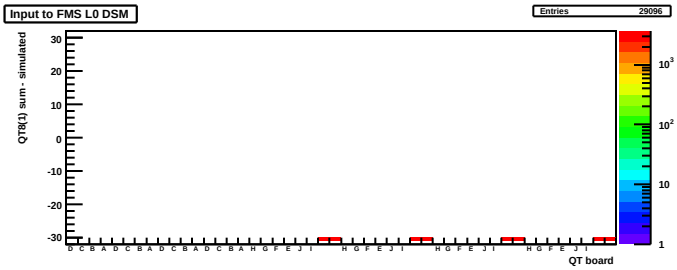

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

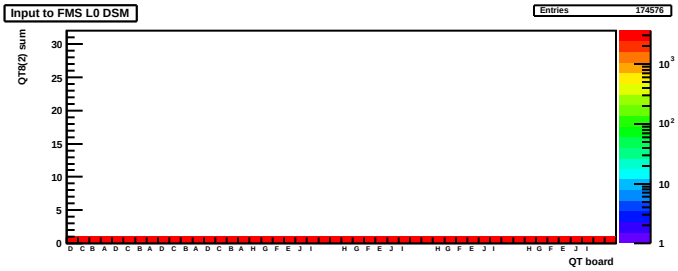

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

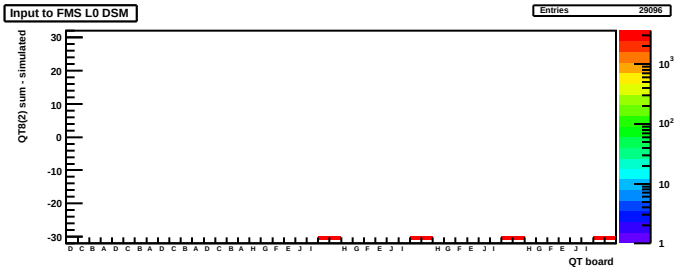

Input to FMS L0 DSM
Entries 174576
QT8(0) sum
QT board

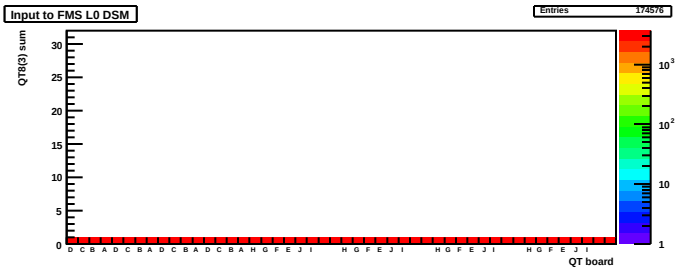

Input to FMS L0 DSM
Entries 29096
QT8(0) sum - simulated
QT board

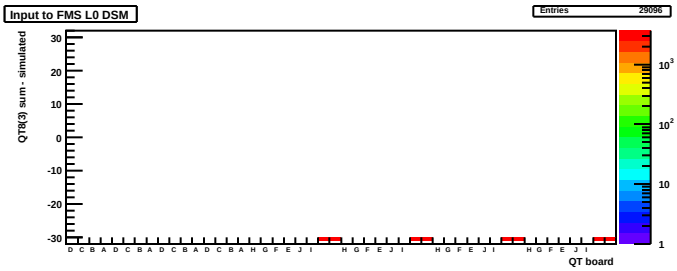

Input to FMS L0 DSM
Entries 174576
QT8(1) sum
QT board

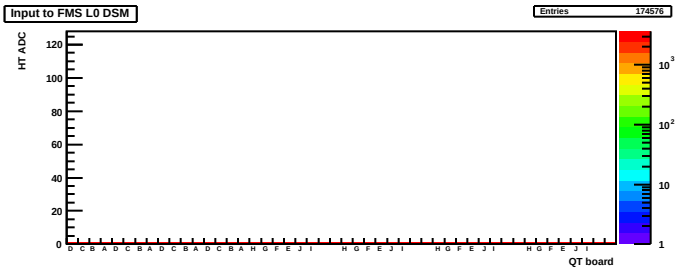

Input to FMS L0 DSM
Entries 29096
QT8(1) sum - simulated
QT board

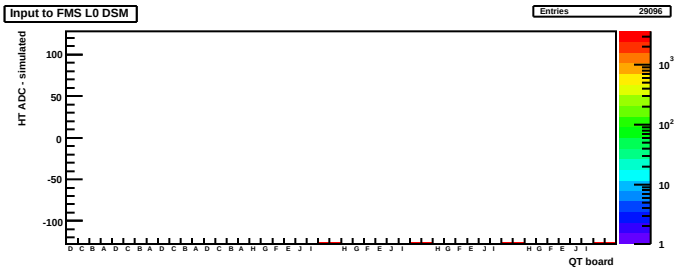

Input to FMS L0 DSM
Entries 174576
QT8(2) sum
QT board

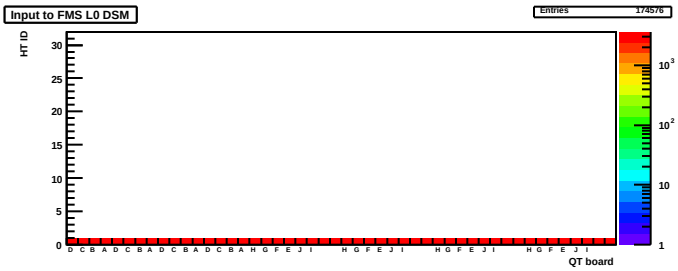

Input to FMS L0 DSM
Entries 29096
QT8(2) sum - simulated
QT board

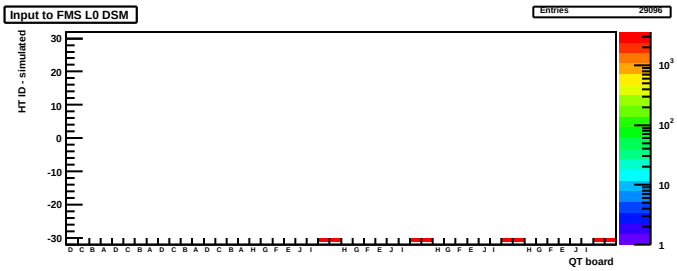

Input to FMS L0 DSM
Entries 174576
QT8(3) sum
QT board

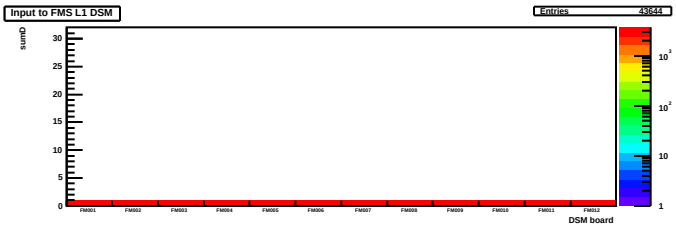

Input to FMS L0 DSM
Entries 29096
QT8(3) sum - simulated
QT board

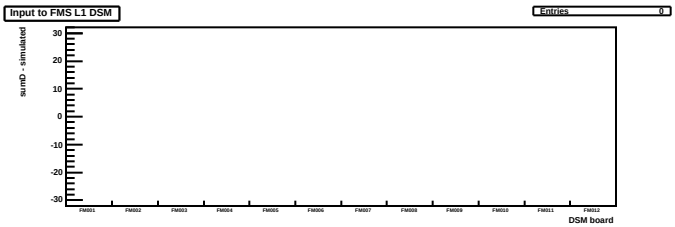

Input to FMS L0 DSM
Entries 174576
HT ADC
QT board

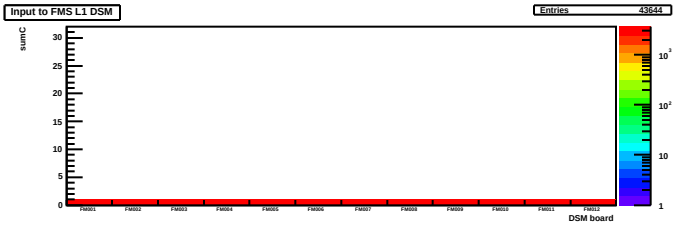

Input to FMS L0 DSM
Entries 29096
HT ADC - simulated
QT board

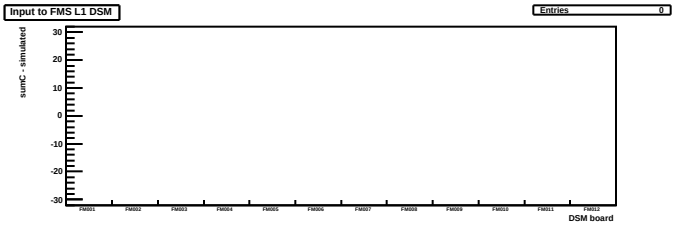

Input to FMS L0 DSM
Entries 174576
HT ID
QT board

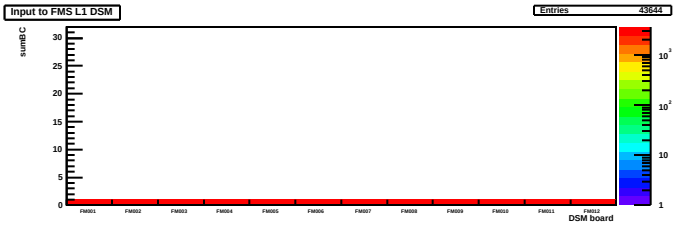

Input to FMS L0 DSM
Entries 29096
HT ID - simulated
QT board

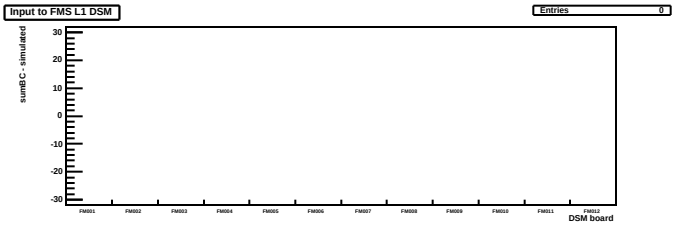

Input to FMS L1 DSM
Entries 43644
sumD
DSM board

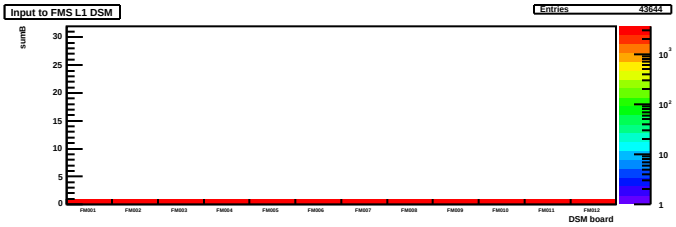

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

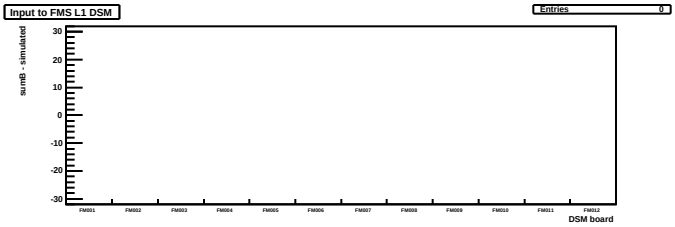

Input to FMS L1 DSM
Entries 43644
sumC
DSM board

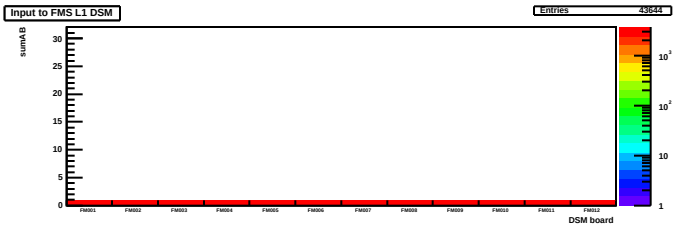

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

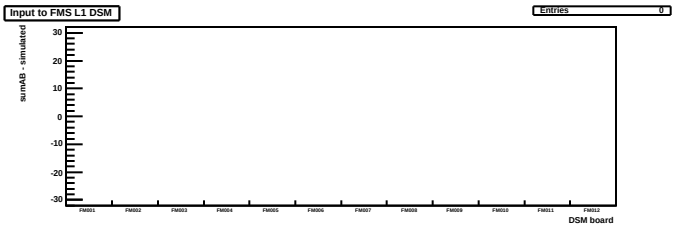

Input to FMS L1 DSM
Entries 43644
sumBC
DSM board

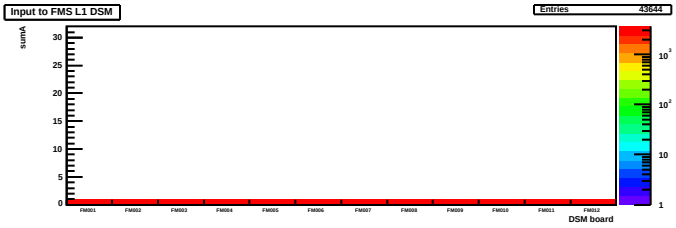

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

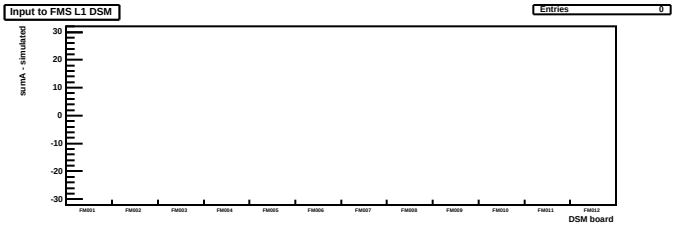

Input to FMS L1 DSM
Entries 43644
sumB
DSM board

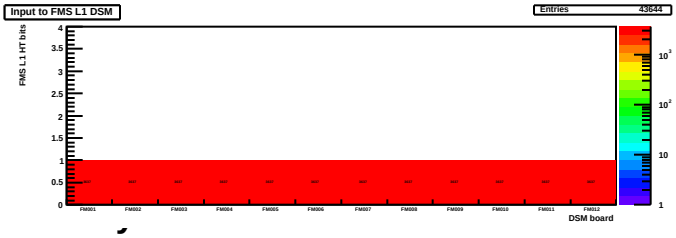

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

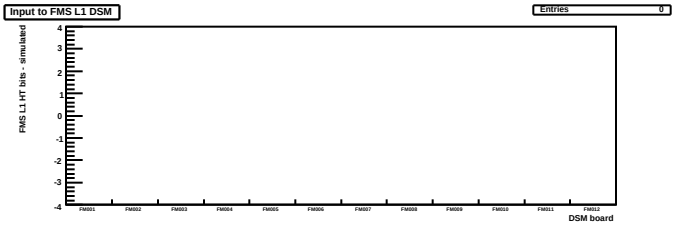

Input to FMS L1 DSM
Entries 43644
sumAB
DSM board

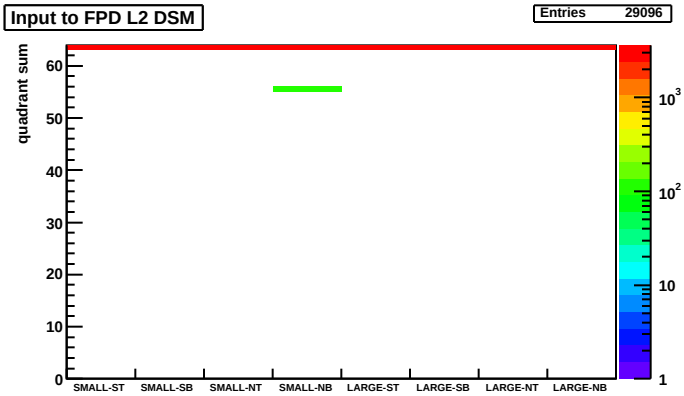

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

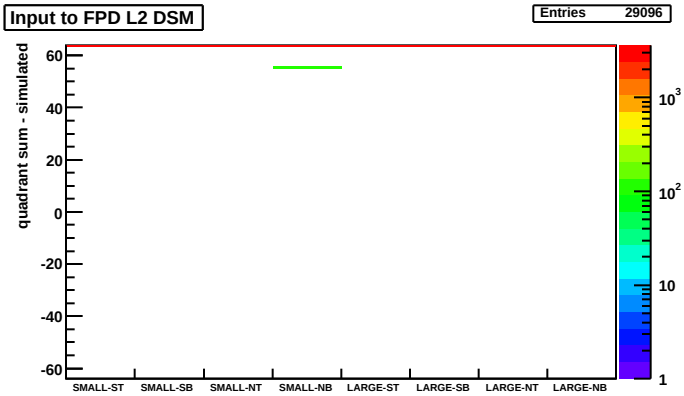

Input to FMS L1 DSM
Entries 43644
sumA
DSM board

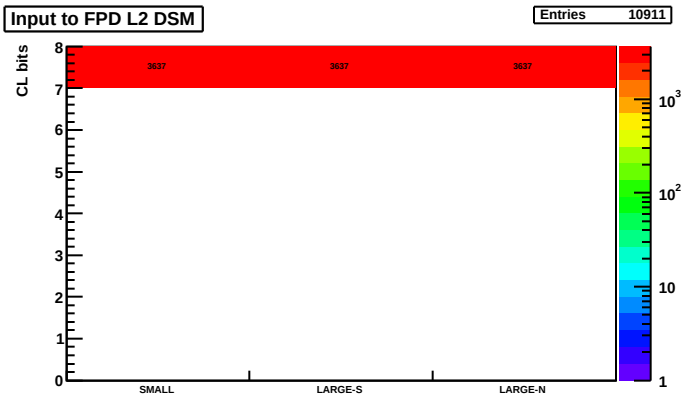

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

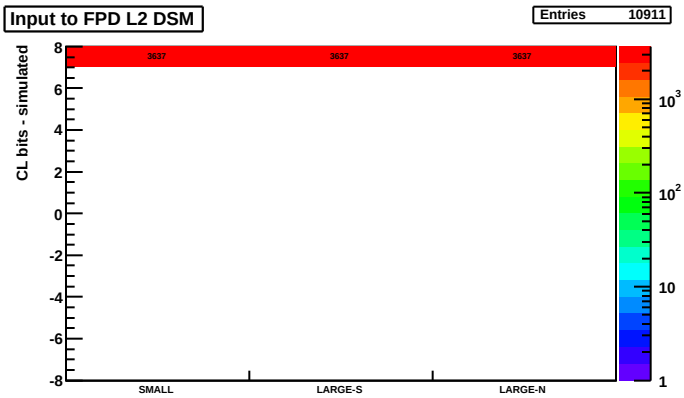

Input to FMS L1 DSM
Entries 43644
FMS L1 HT bits
DSM board

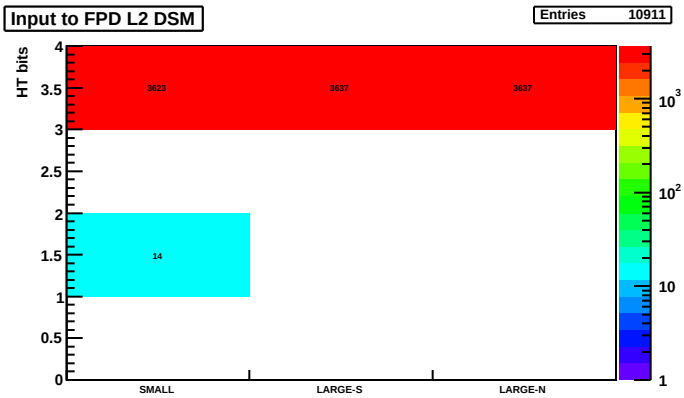

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

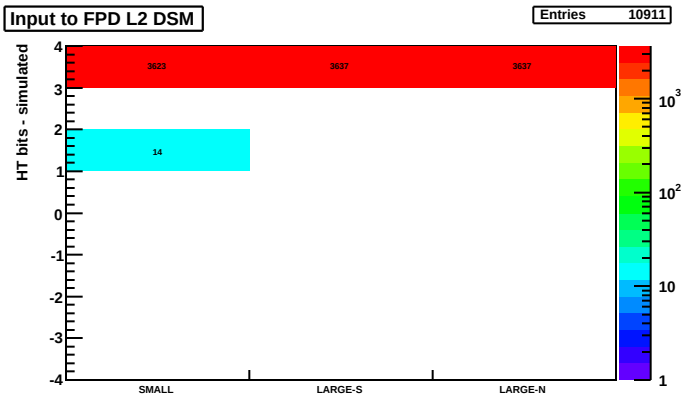

Input to FPD L2 DSM
Entries 29096
quadrant sum
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

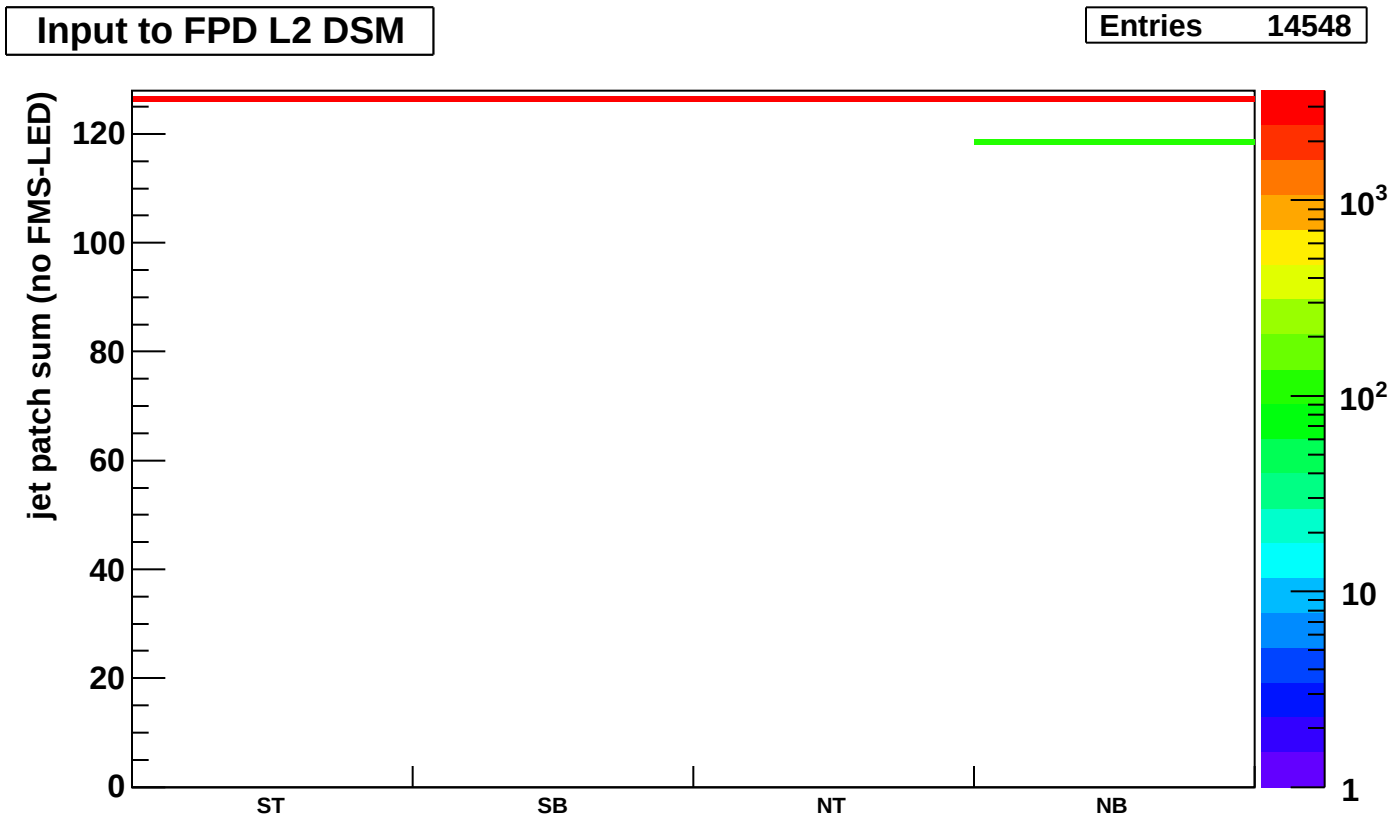

Input to FPD L2 DSM
Entries 29096
quadrant sum - simulated
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

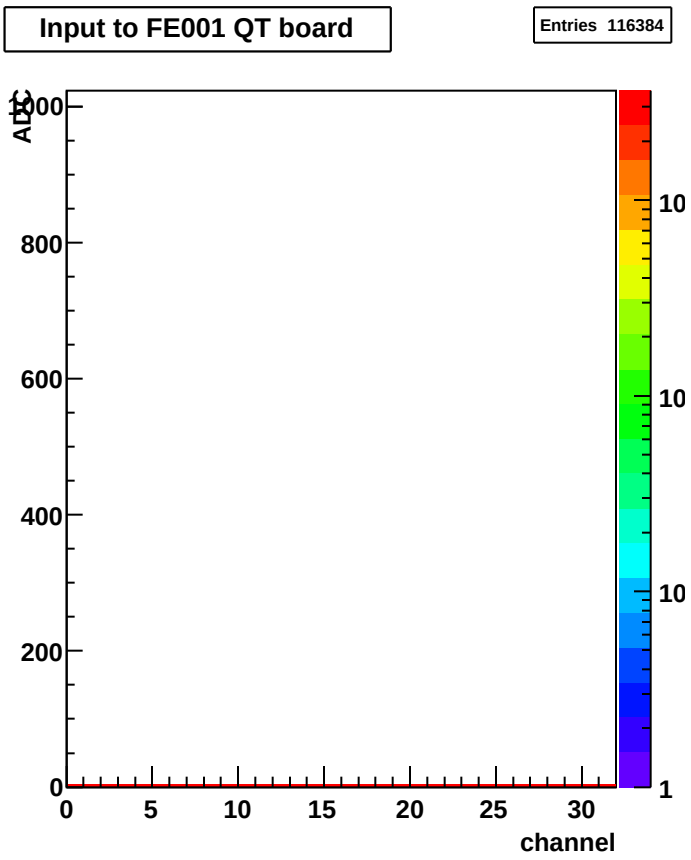

Input to FPD L2 DSM
Entries 10911
CL bits
3637 3637 3637
SMALL LARGE-S LARGE-N

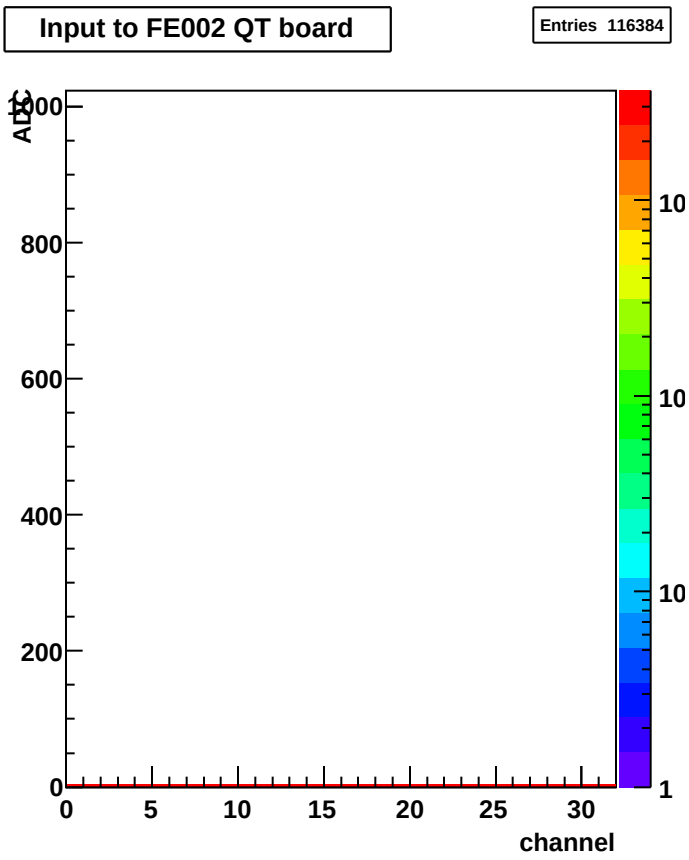

Input to FPD L2 DSM
Entries 10911
CL bits - simulated
3637 3637 3637
SMALL LARGE-S LARGE-N

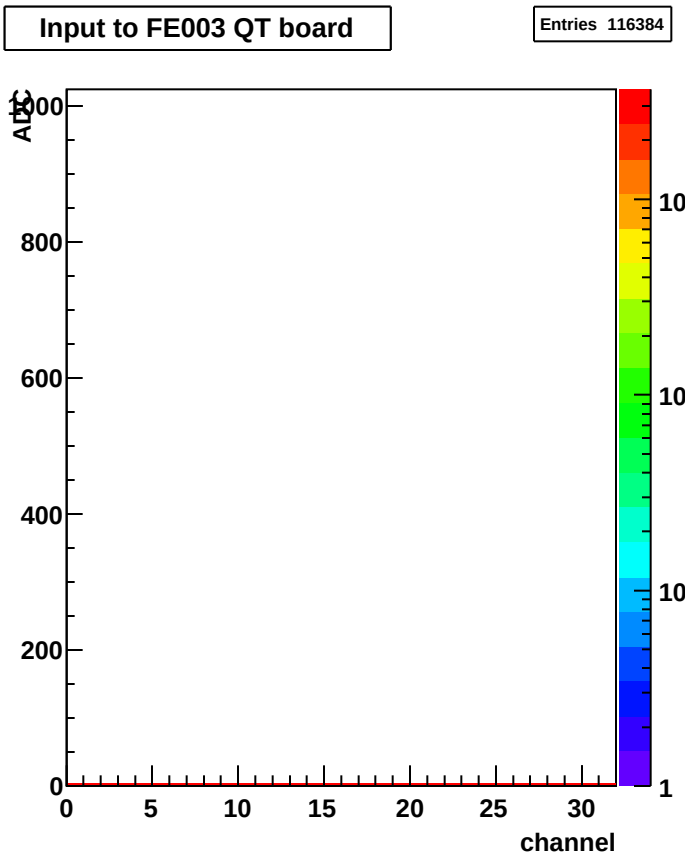

Input to FPD L2 DSM
Entries 10911
HT bits
3623 3637 3637
14
SMALL LARGE-S LARGE-N

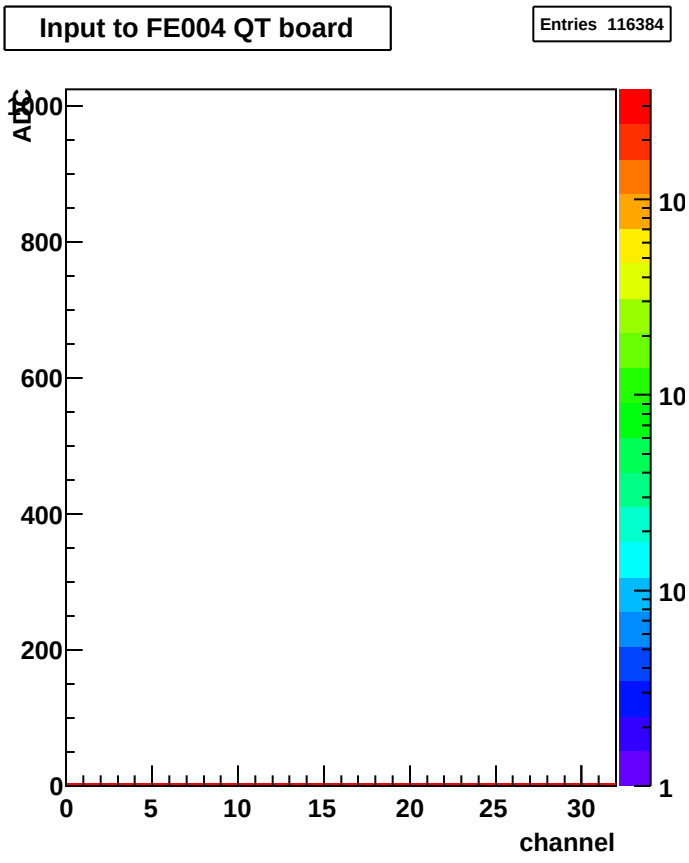

Input to FPD L2 DSM
Entries 10911
HT bits - simulated
3623 3637 3637
14
SMALL LARGE-S LARGE-N

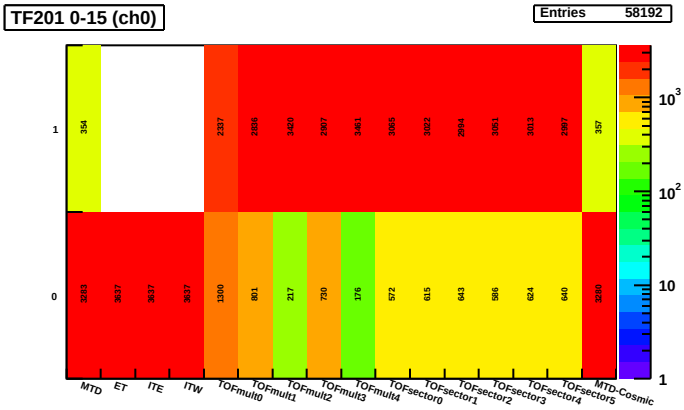

Input to FPD L2 DSM
Entries 14548
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3

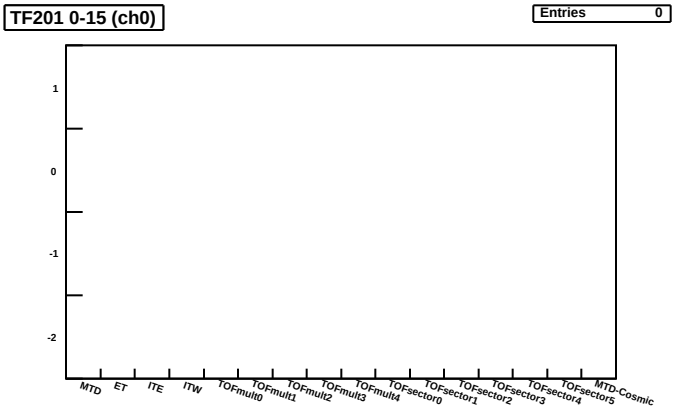

Input to FE001 QT board
Entries 116384
ADC
channel

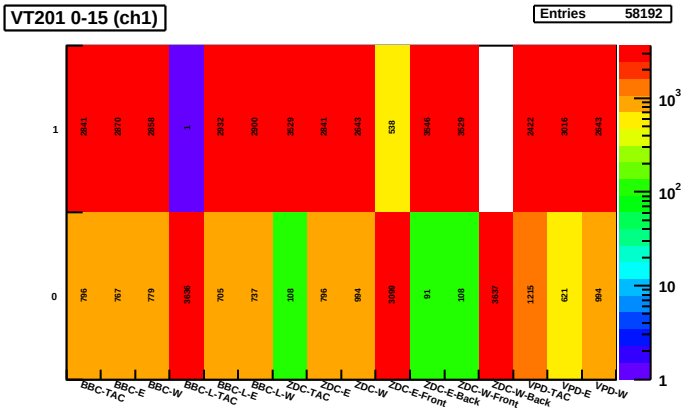

Input to FE002 QT board
Entries 116384
ADC
channel

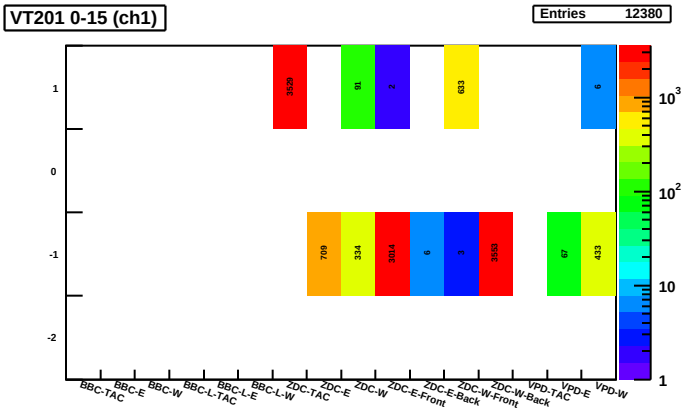

Input to FE003 QT board
Entries 116384
ADC
channel

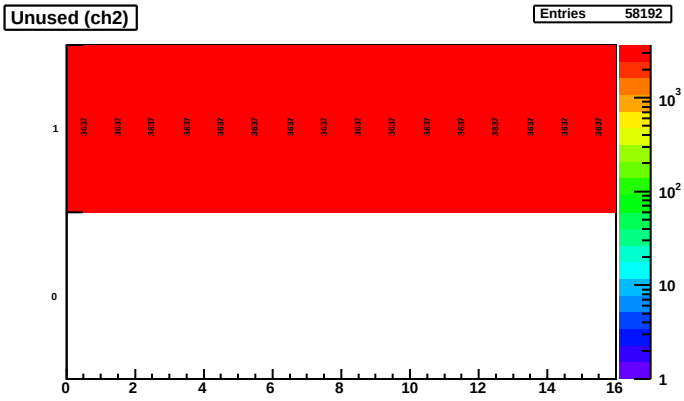

Input to FE004 QT board
Entries 116384
ADC
channel

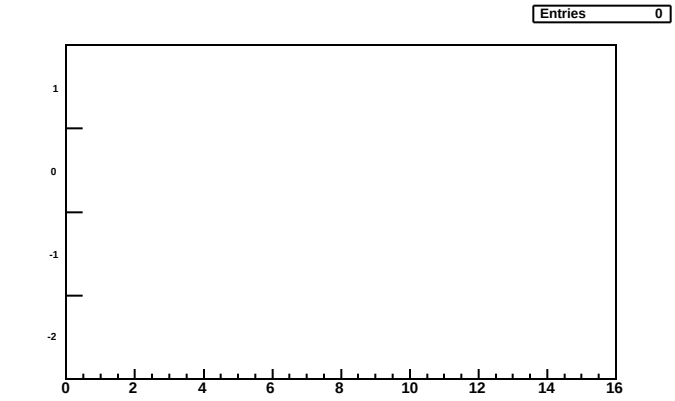

TF201 0-15 (ch0)
Entries 58192
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

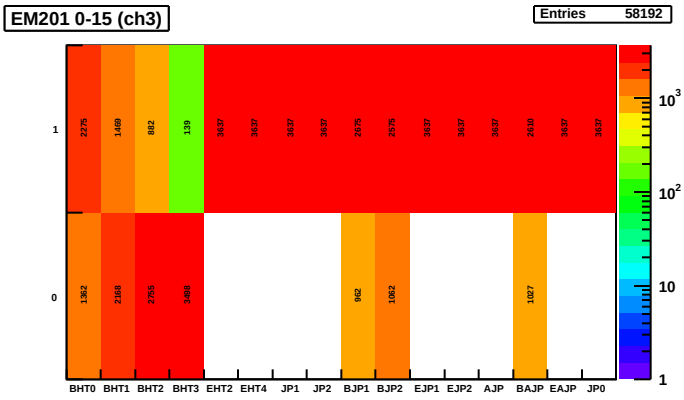

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

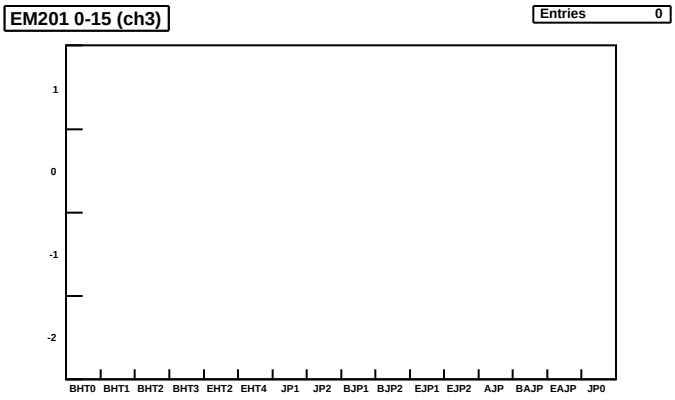

VT201 0-15 (ch1)
Entries 58192
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 12380
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

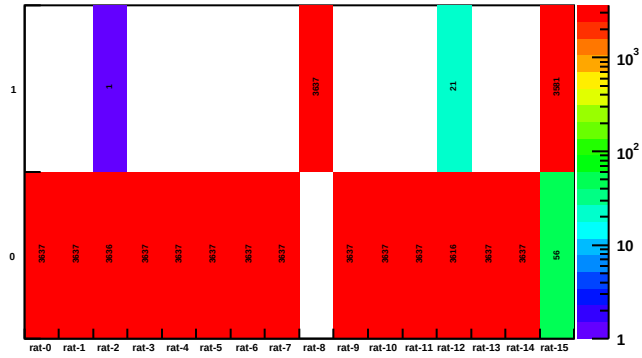

Unused (ch2)
Entries 58192

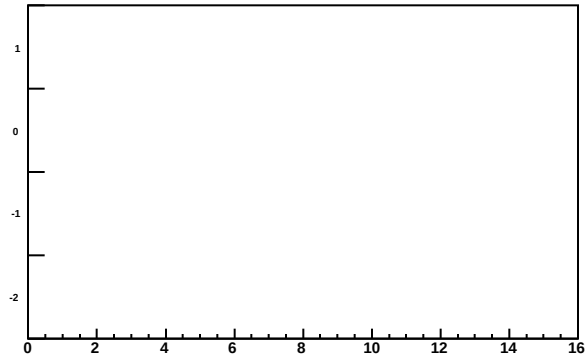

Entries 0


EM201 0-15 (ch3)
Entries 58192
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

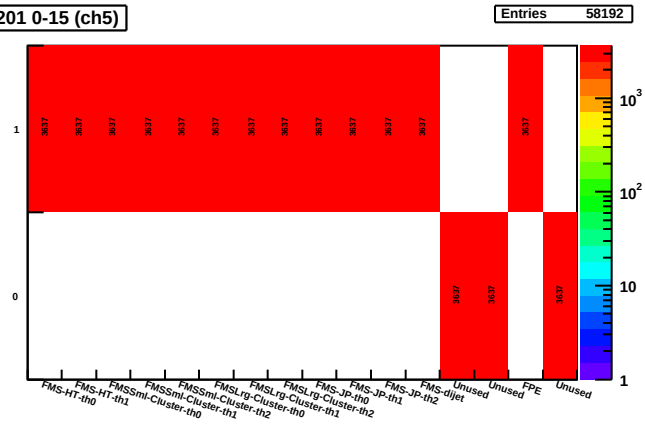

EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

RAT board (ch4)

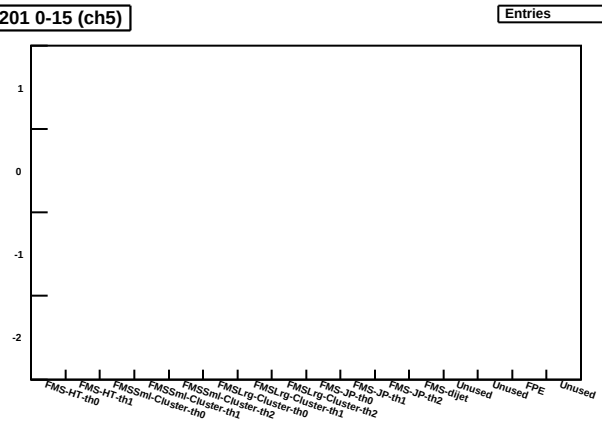




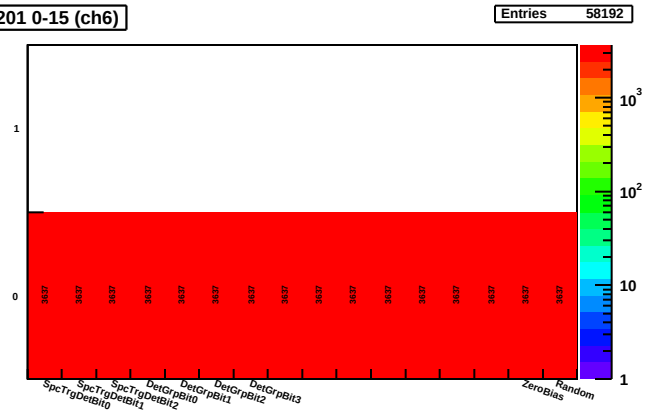
FP201 0-15 (ch5)

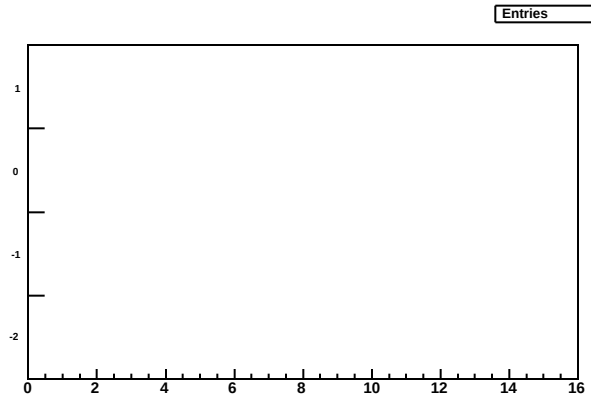
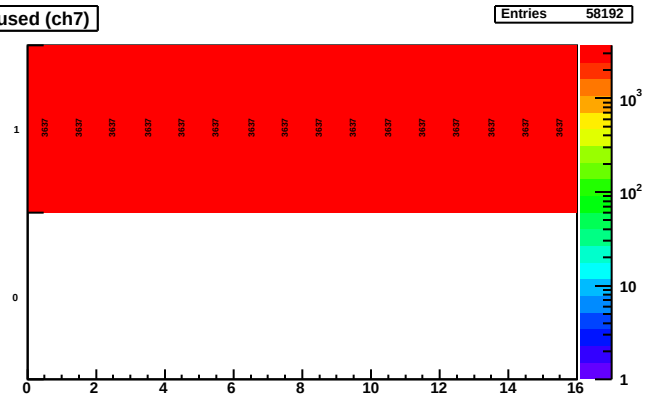
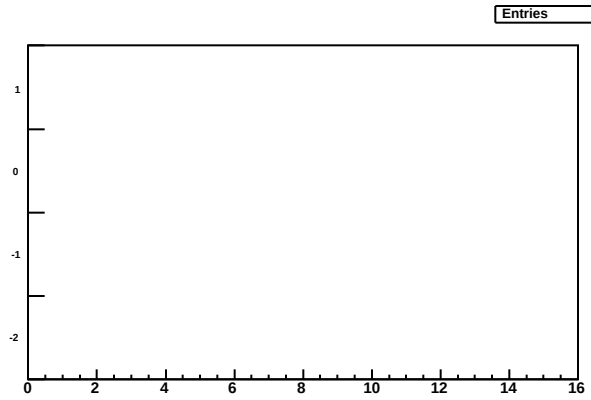


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)