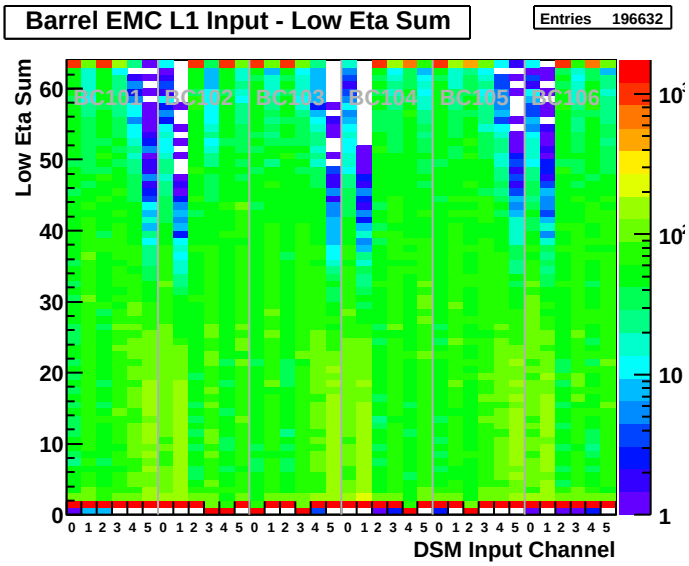
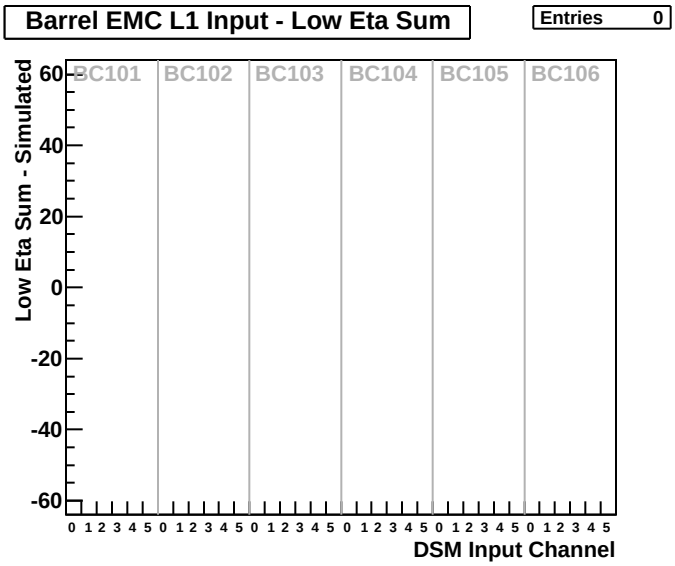
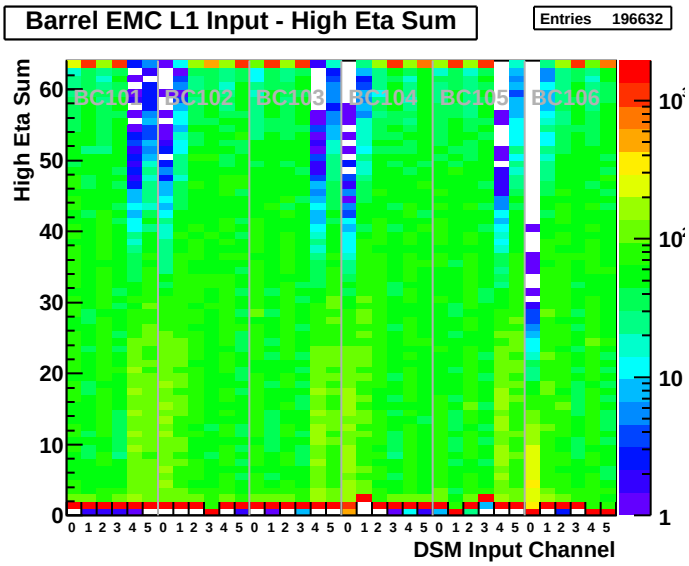
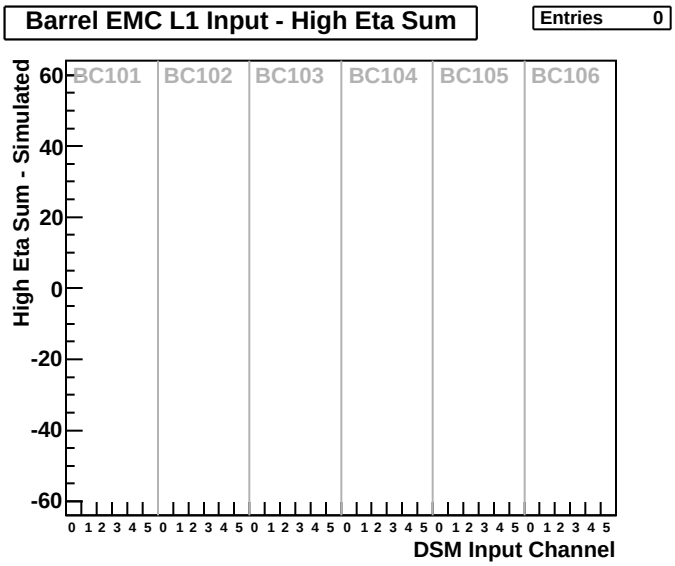
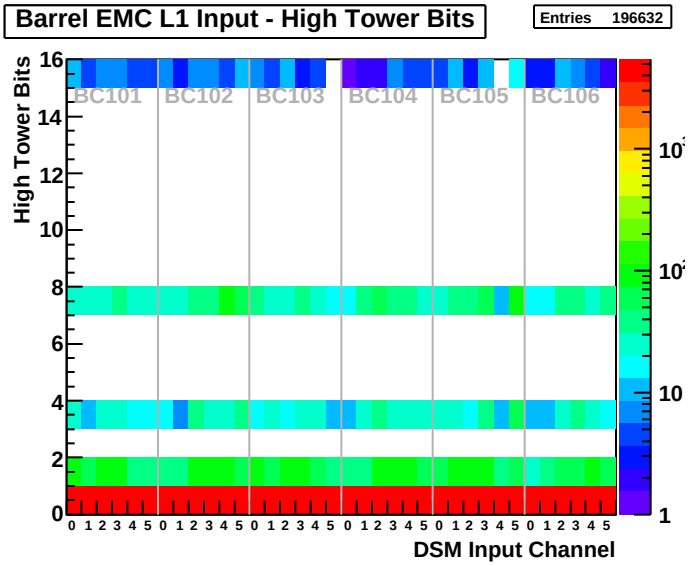
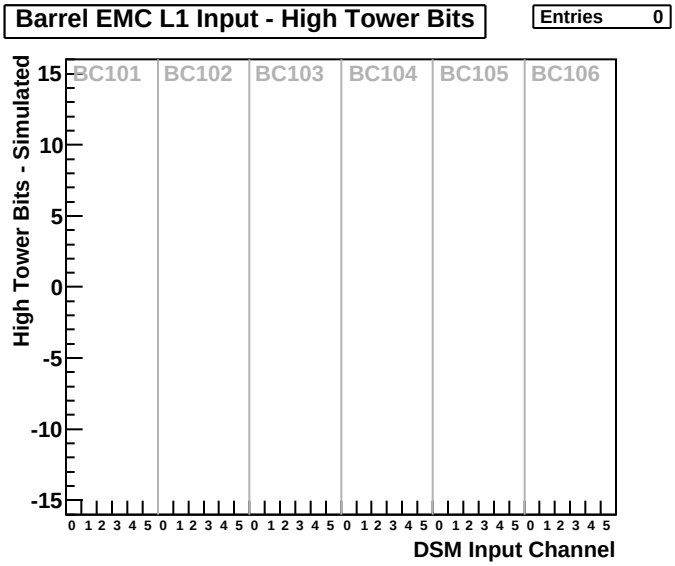

Barrel EMC L1 Input - Low Eta Sum
Entries 196632
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

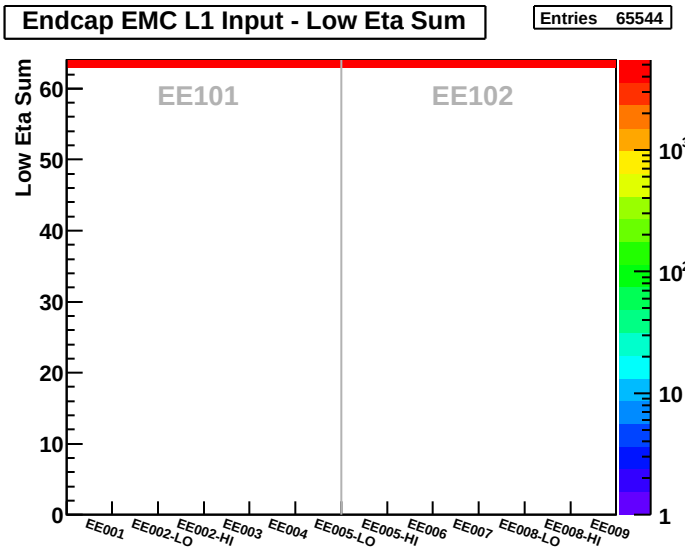

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

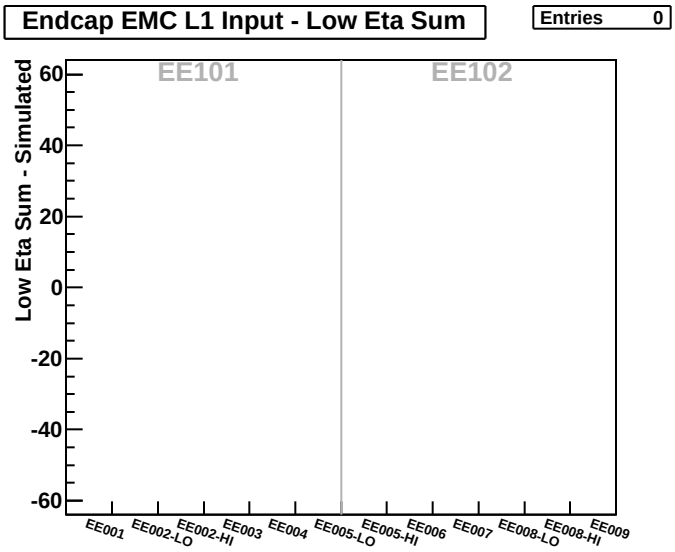

Barrel EMC L1 Input - High Eta Sum
Entries 196632
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

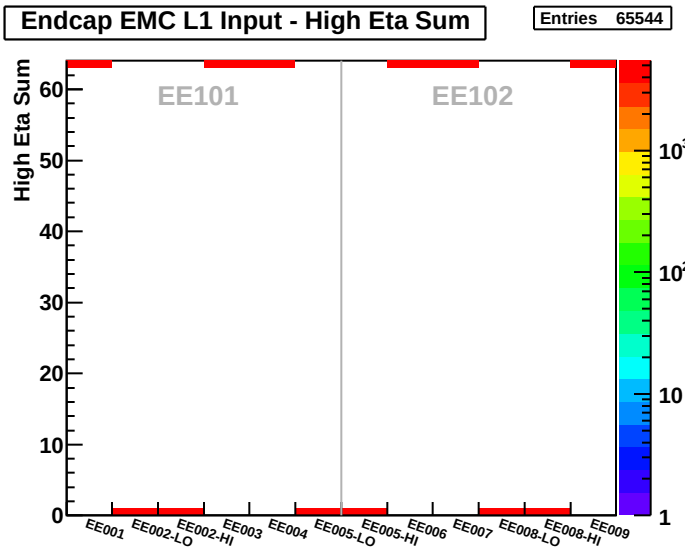

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

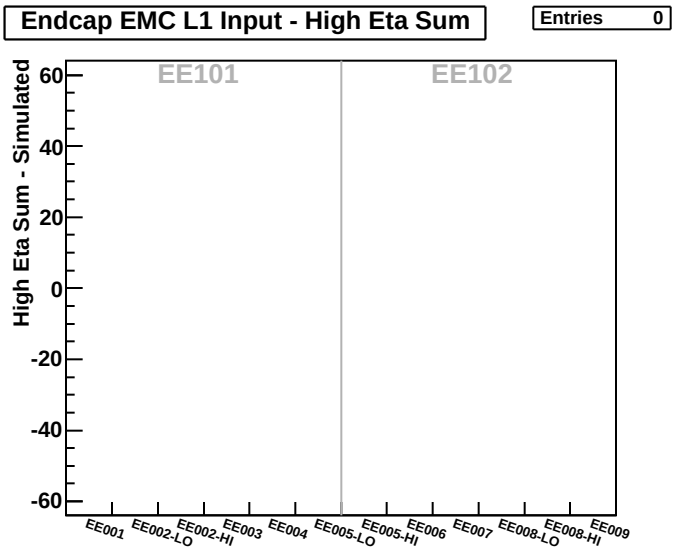

Barrel EMC L1 Input - High Tower Bits
Entries 196632
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

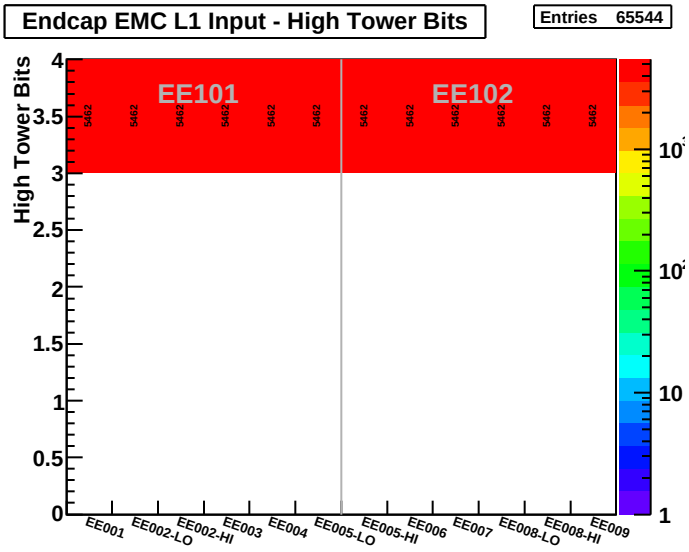

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

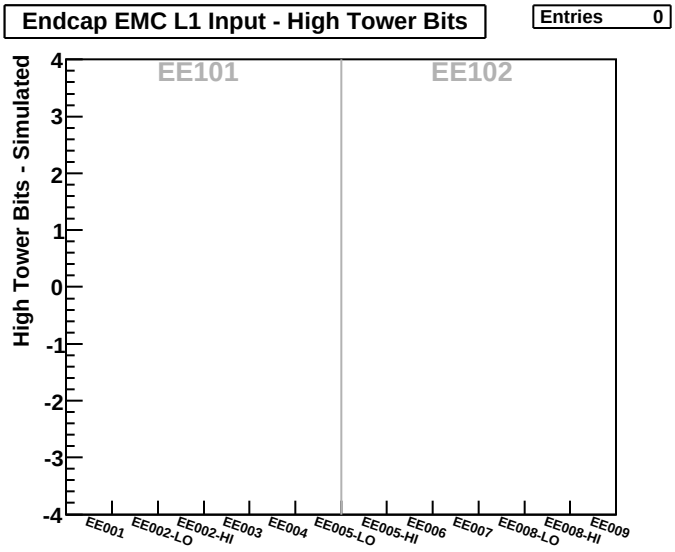

Endcap EMC L1 Input - Low Eta Sum
Entries 65544
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

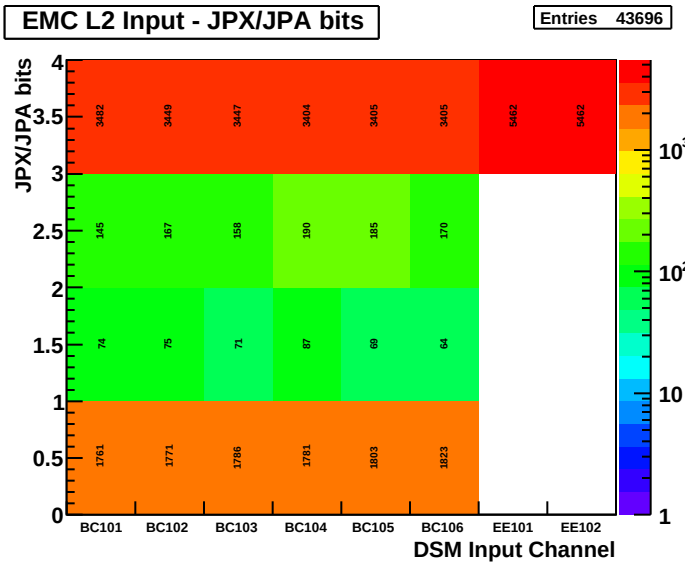

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

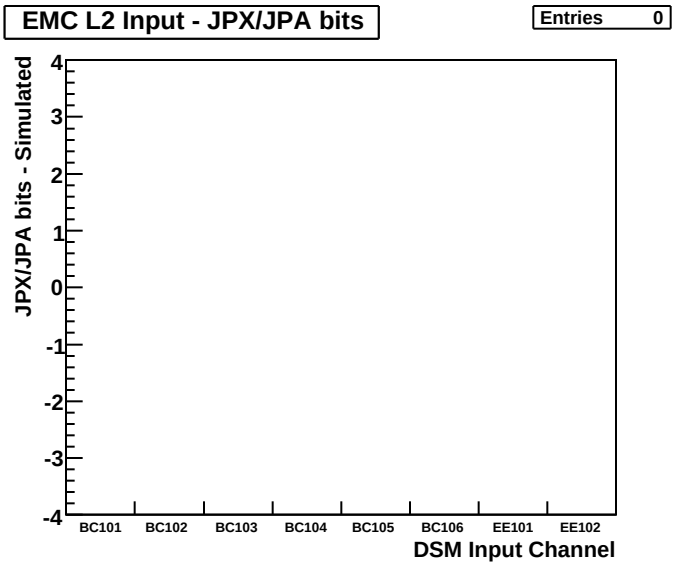

Endcap EMC L1 Input - High Eta Sum
Entries 65544
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

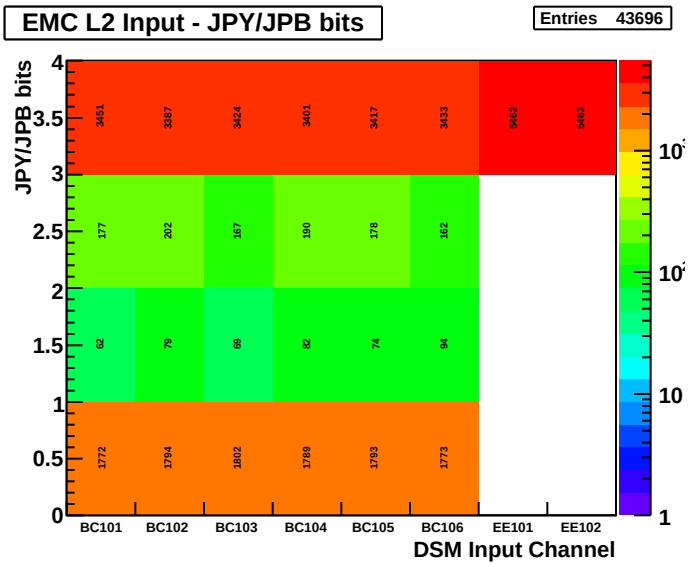

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

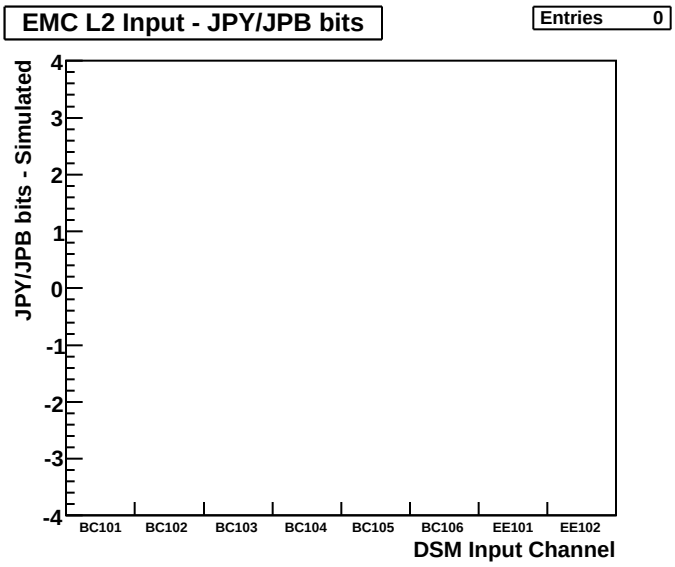

Endcap EMC L1 Input - High Tower Bits
Entries 65544
High Tower Bits
EE101
EE102
5462
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

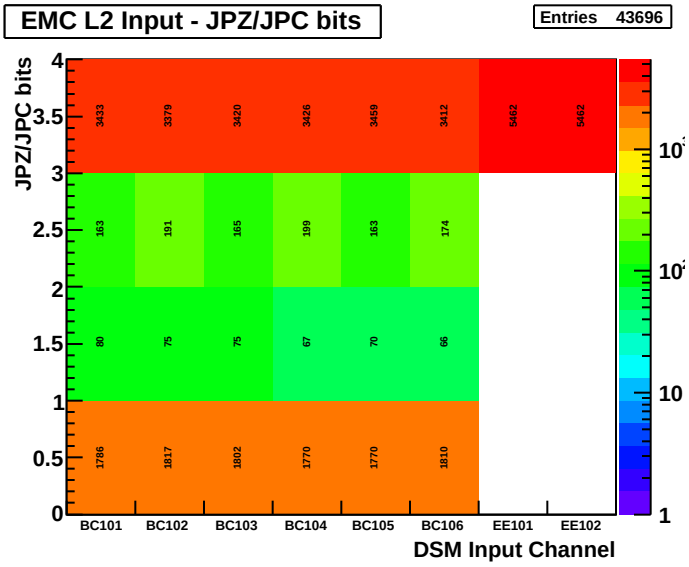

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

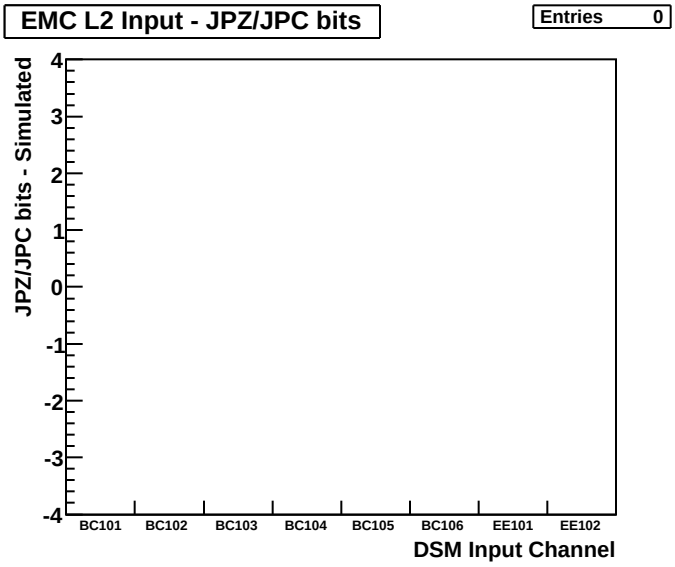

EMC L2 Input - JPX/JPA bits
Entries 43696
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3482 3449 3447 3404 3405 3405 5462 5462
145 167 158 190 185 170
74 75 71 87 69 64
1761 1771 1786 1781 1803 1823

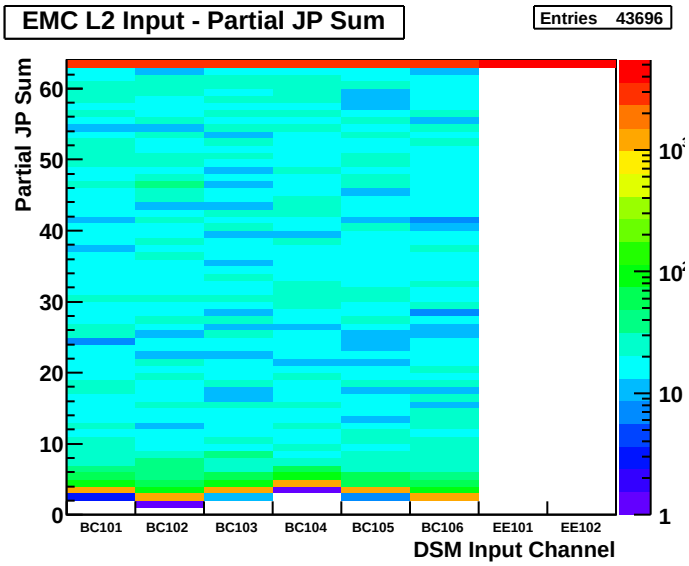

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

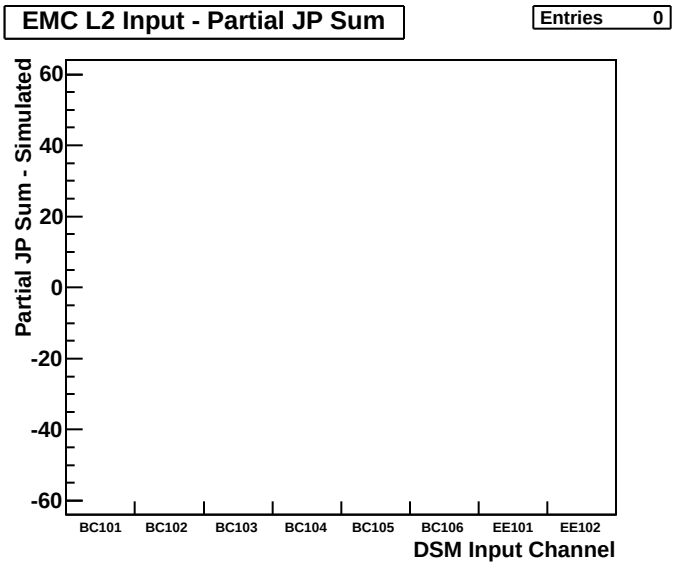

EMC L2 Input - JPY/JPB bits
Entries 43696
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3451 3387 3424 3401 3417 3433 5462 5462
177 202 167 190 178 162
62 79 69 82 74 94
1772 1794 1802 1789 1793 1773

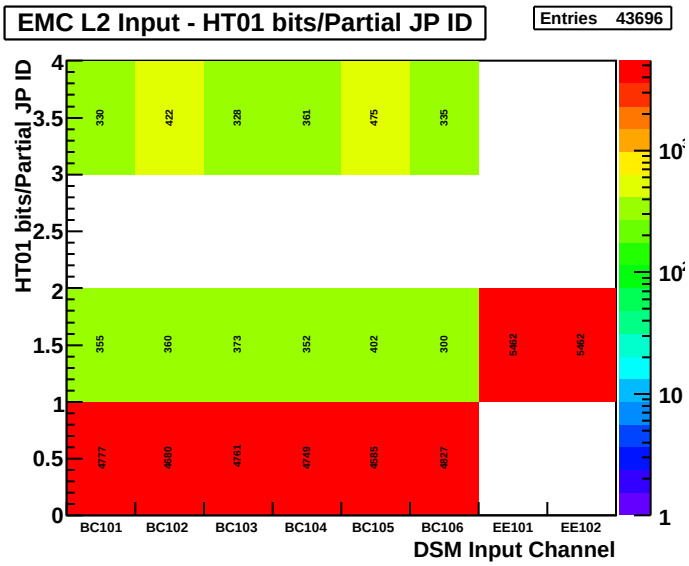

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

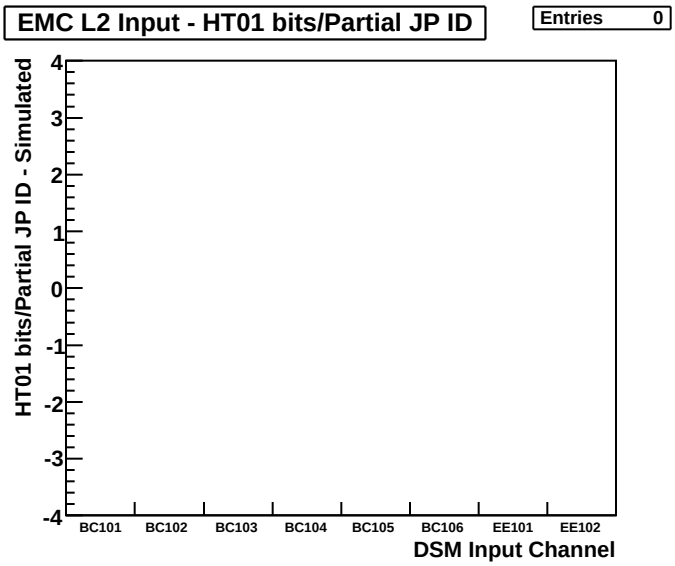

EMC L2 Input - JPZ/JPC bits
Entries 43696
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3433 3379 3420 3426 3459 3412 5462 5462
153 191 165 199 163 174
80 75 75 67 70 66
1786 1817 1802 1770 1770 1810

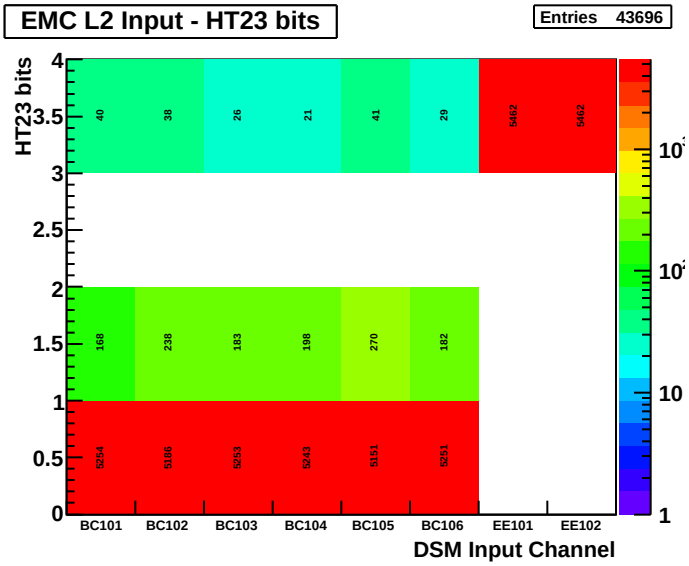

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

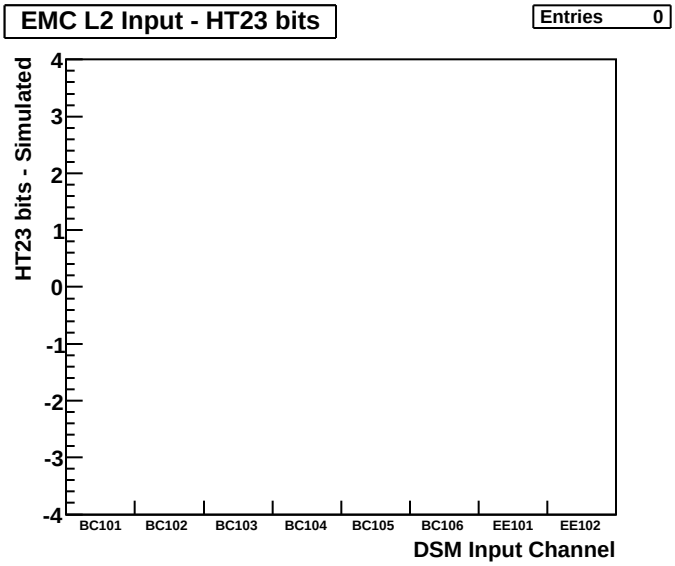

EMC L2 Input - Partial JP Sum
Entries 43696
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

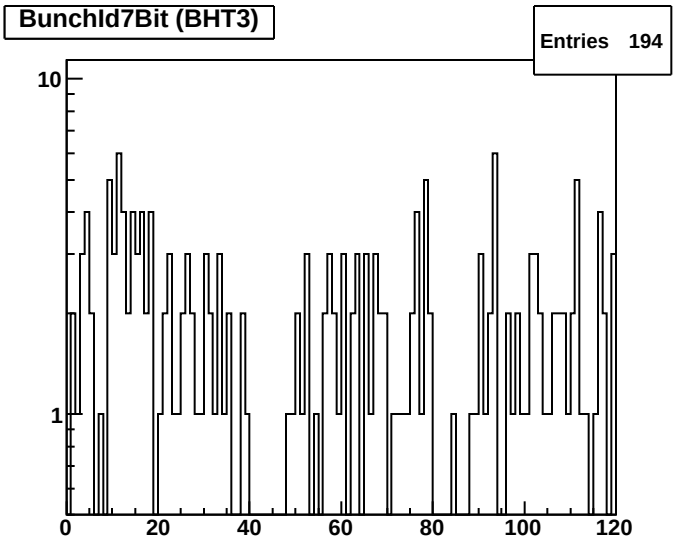

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

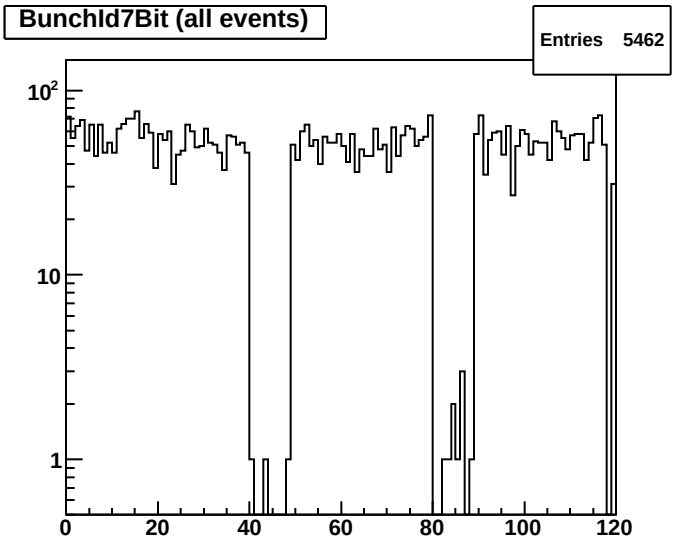

EMC L2 Input - HT01 bits/Partial JP ID
Entries 43696
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

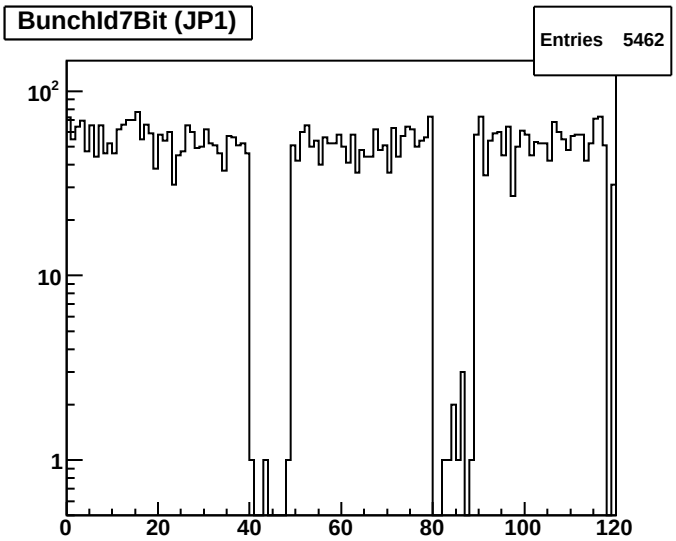

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

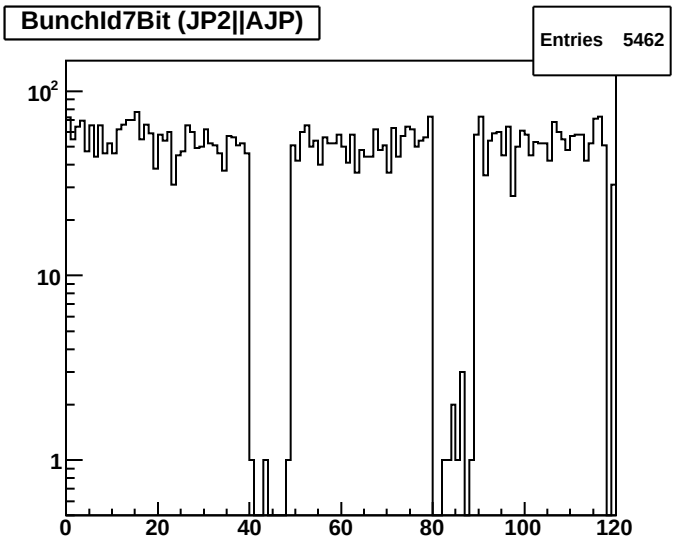

EMC L2 Input - HT23 bits
Entries 43696
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

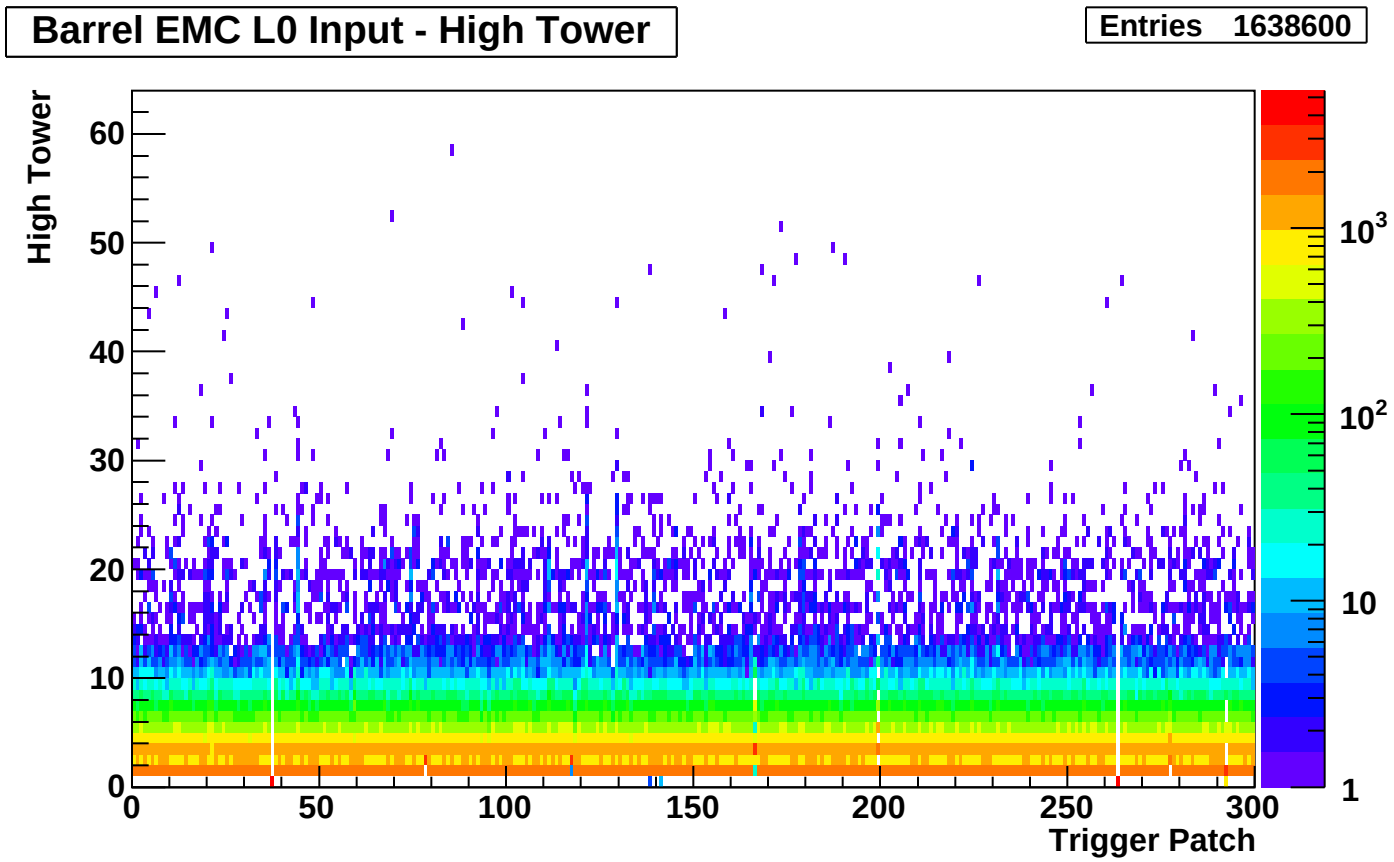

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

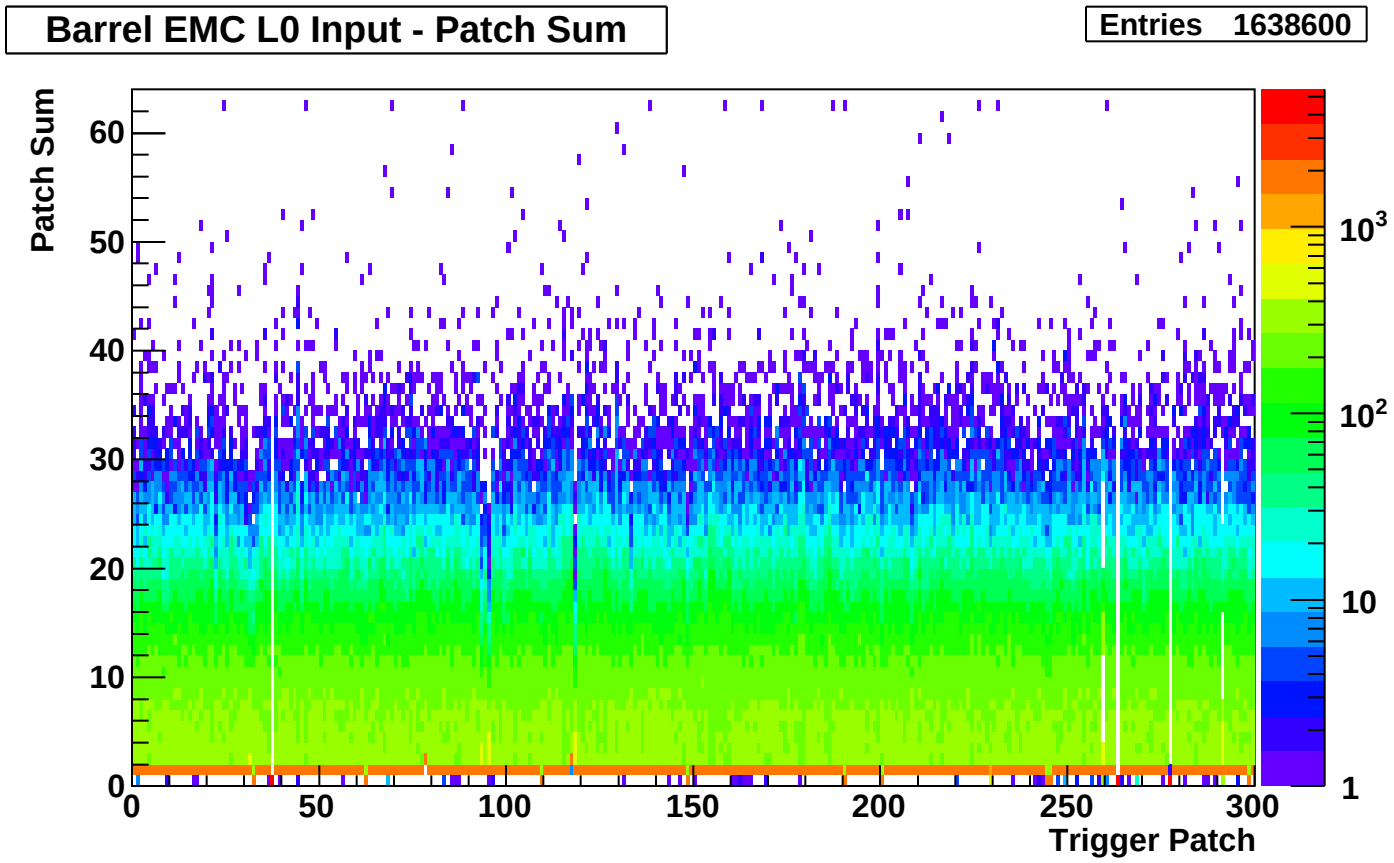

BunchId7Bit (BHT3)
Entries 194
10
1
0
20
40
60
80
100
120

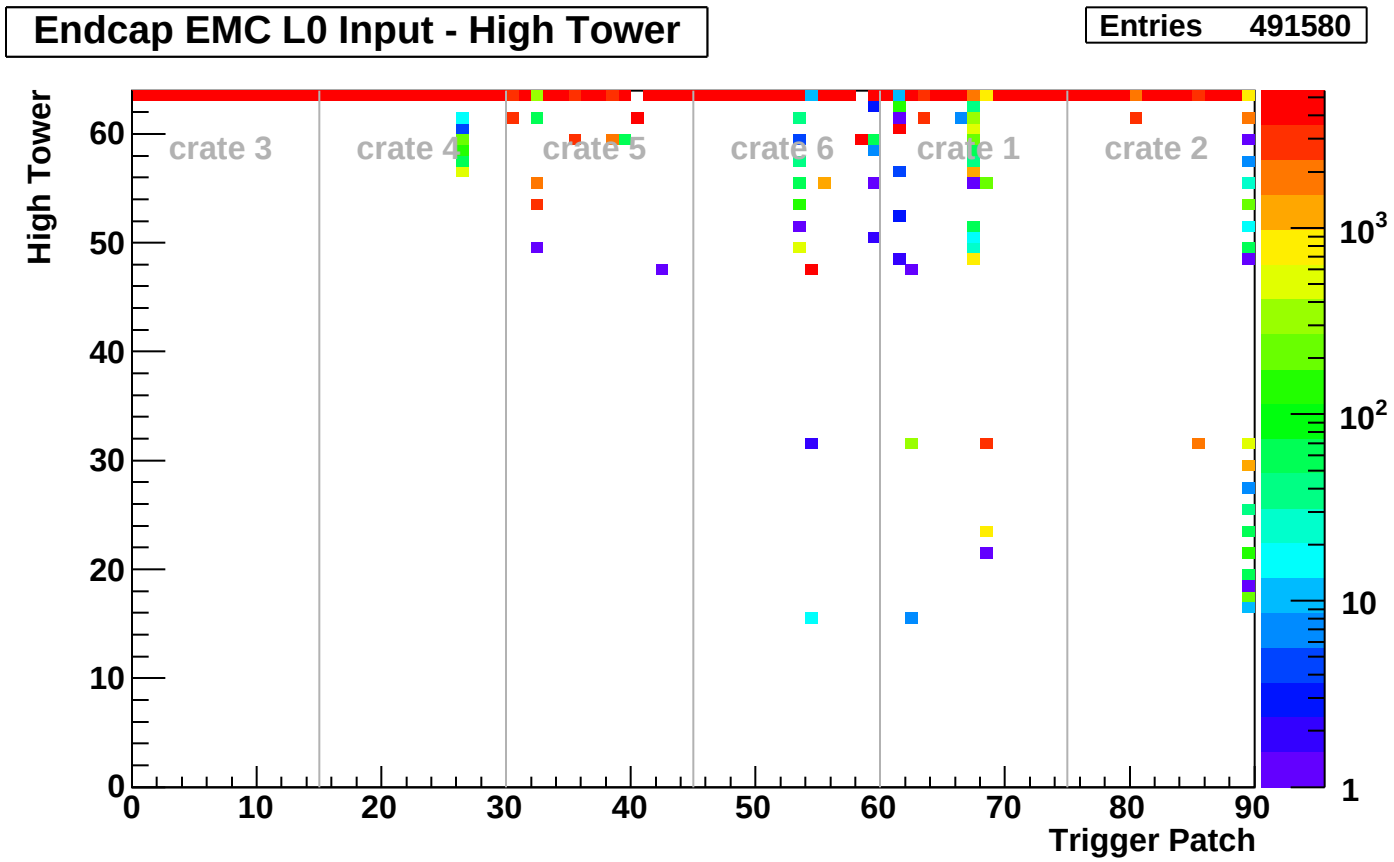

BunchId7Bit (all events)
Entries 5462
10^2
10
1
0
20
40
60
80
100
120

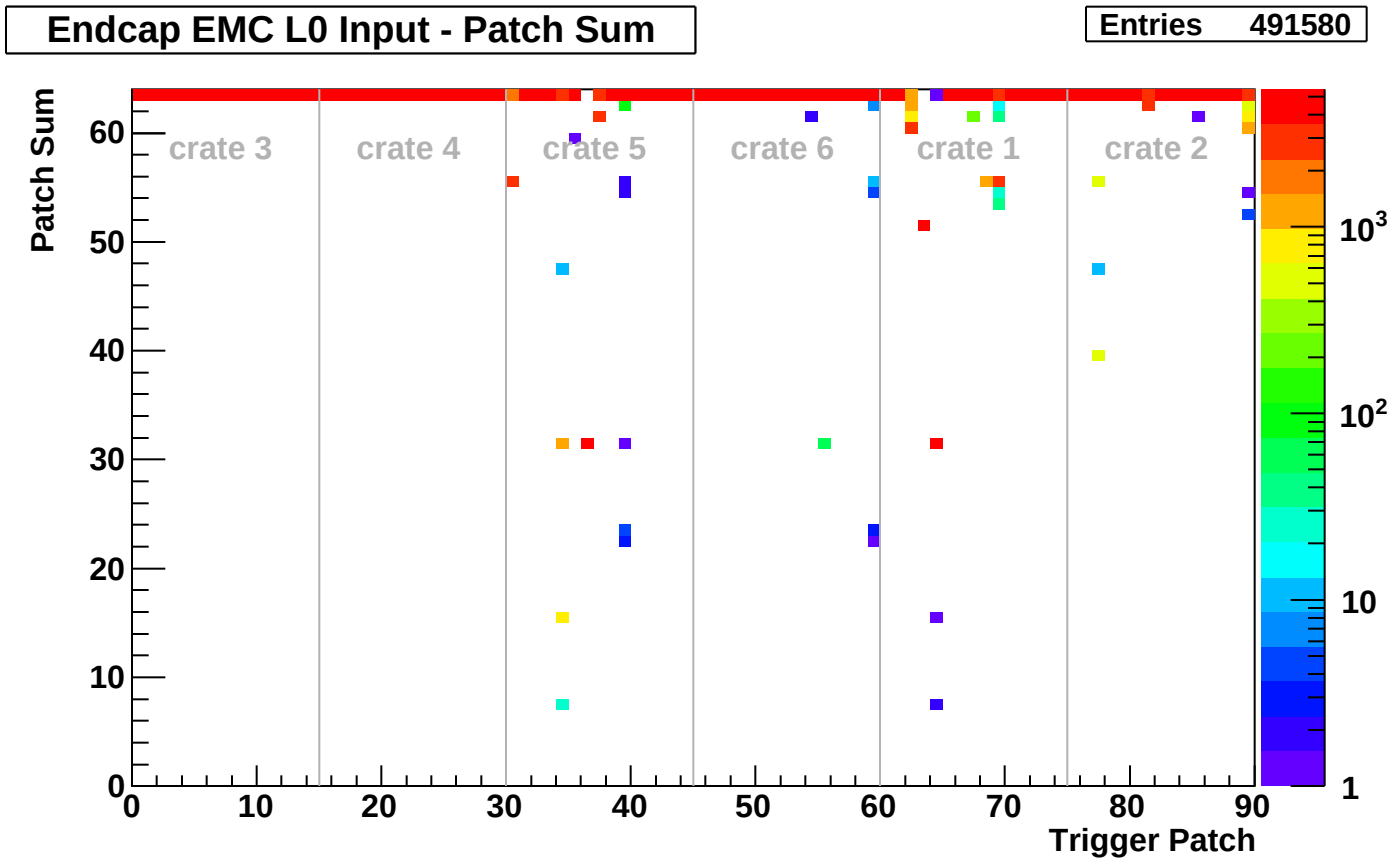

BunchId7Bit (JP1)
Entries 5462
10^2
10
1
0
20
40
60
80
100
120

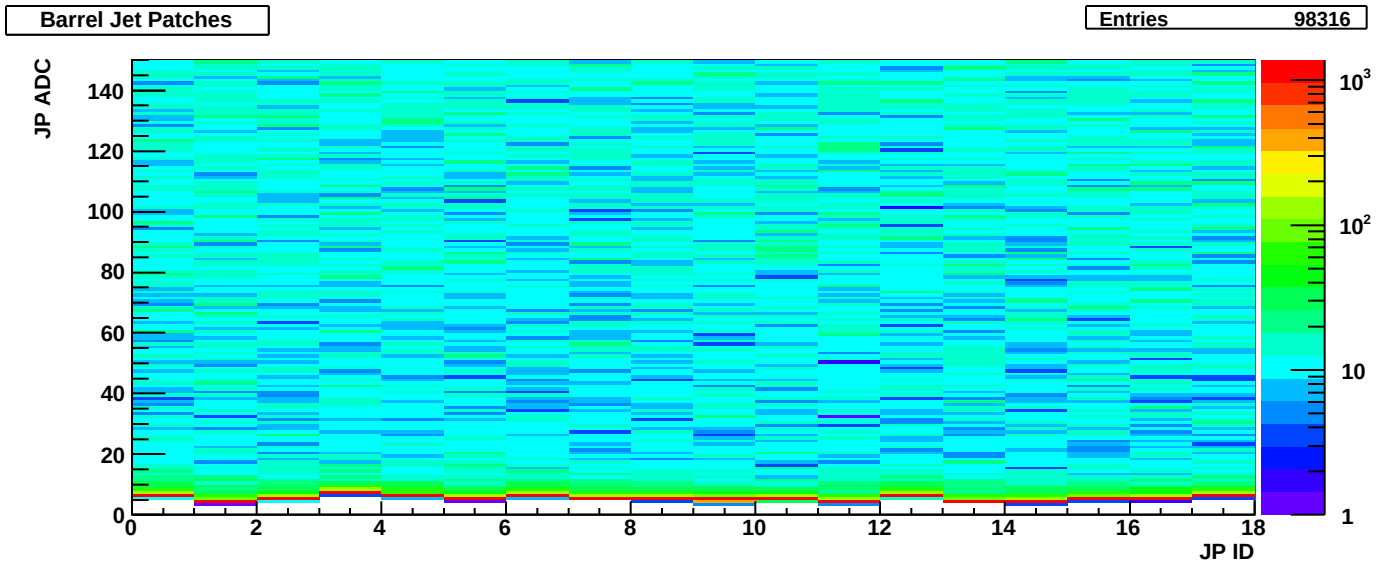

BunchId7Bit (JP2||AJP)
Entries 5462
10^2
10
1
0
20
40
60
80
100
120

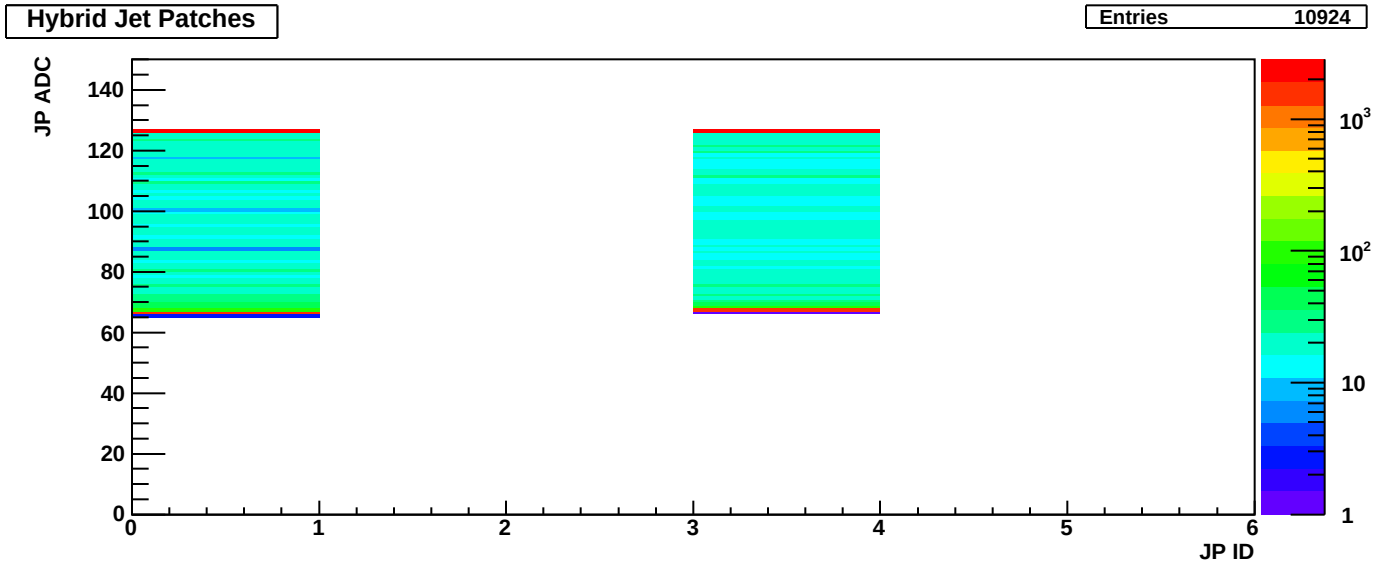

Barrel EMC L0 Input - High Tower
Entries 1638600
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3

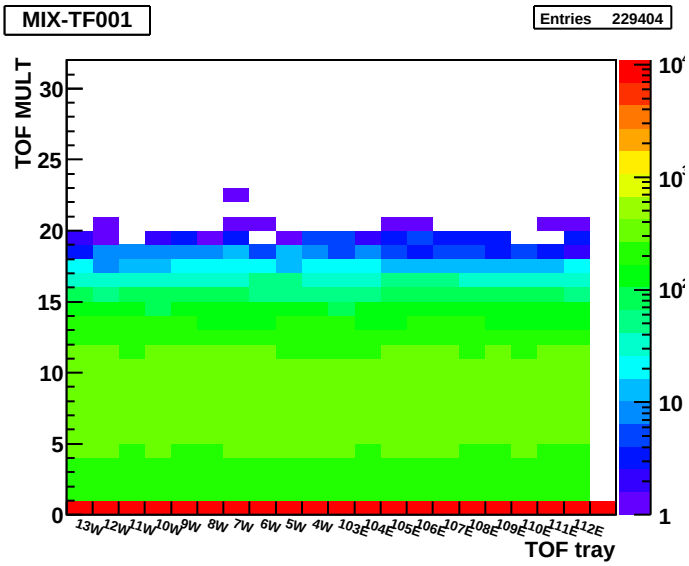

Barrel EMC L0 Input - Patch Sum
Entries 1638600
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3

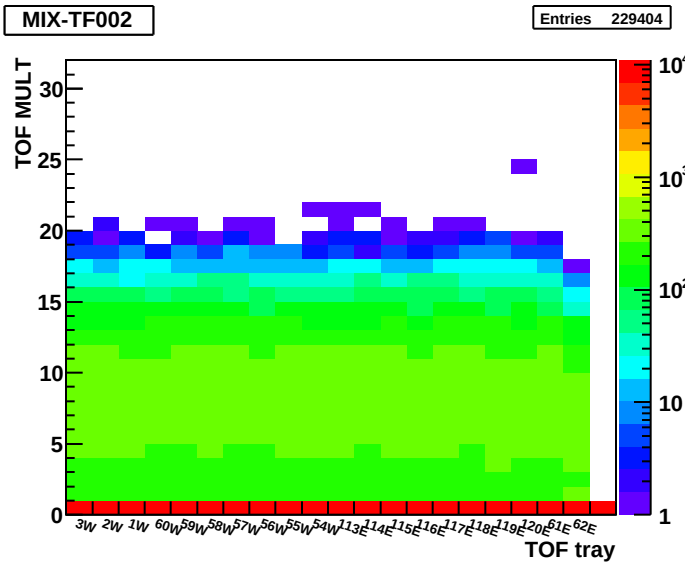

Endcap EMC L0 Input - High Tower
Entries 491580
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

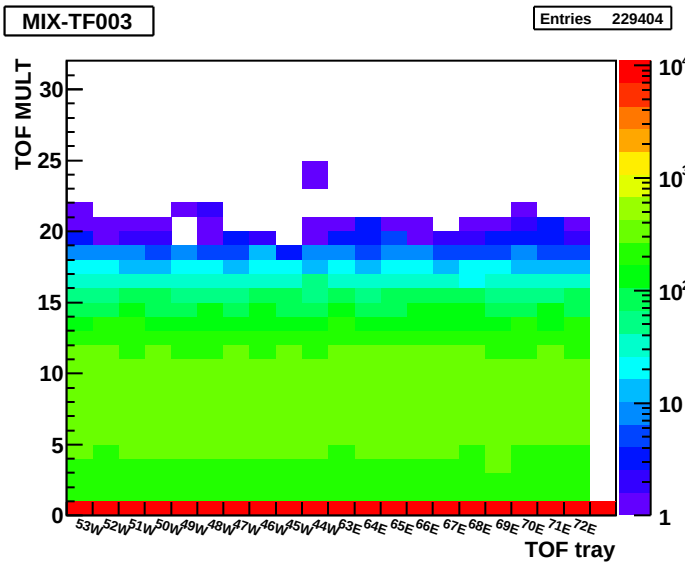

Endcap EMC L0 Input - Patch Sum
Entries 491580
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

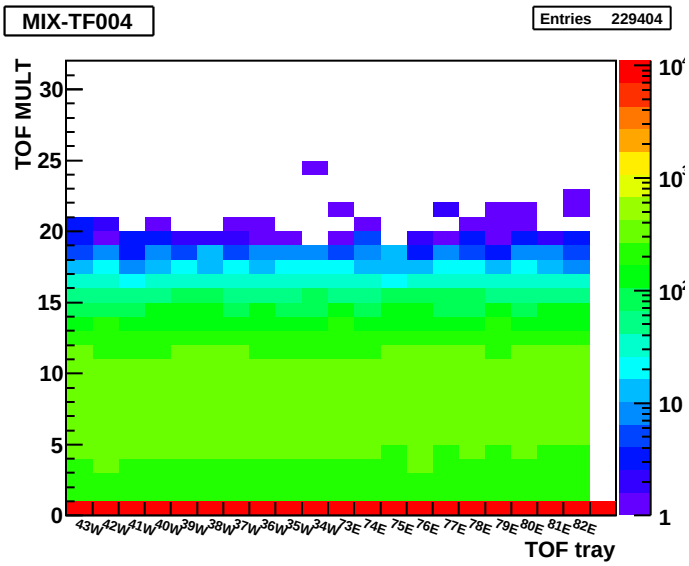

Barrel Jet Patches
Entries 98316
JP ADC
JP ID

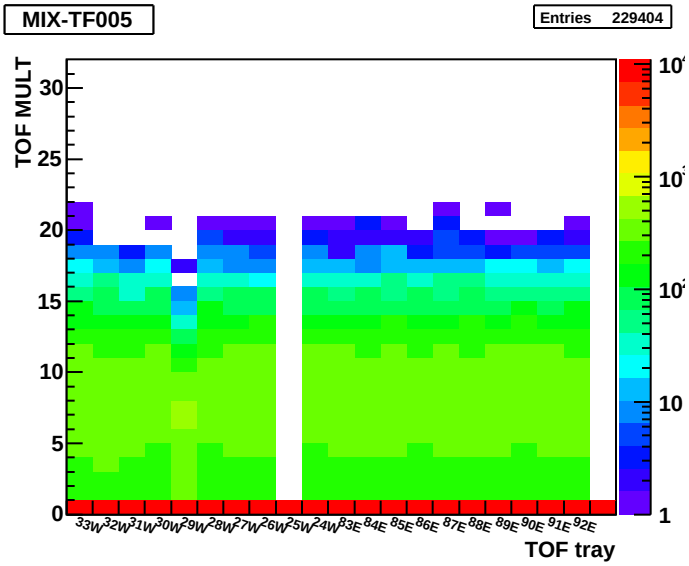

Hybrid Jet Patches
Entries 10924
JP ADC
JP ID

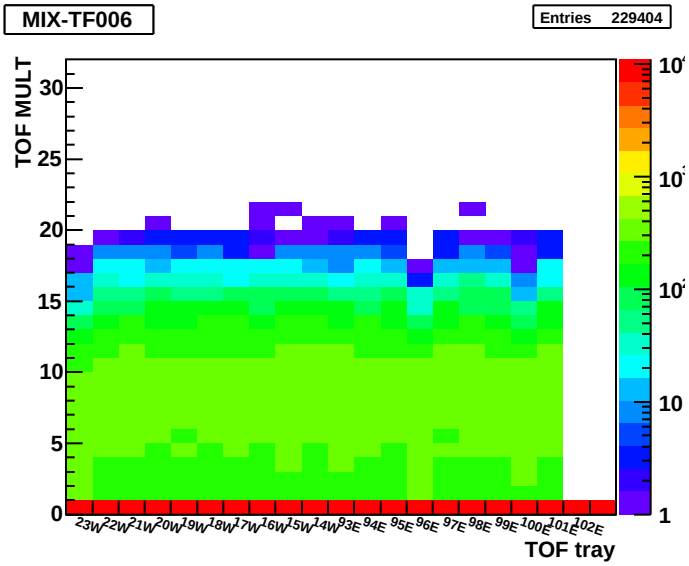

MIX-TF001
Entries 229404
TOF MULT
TOF tray

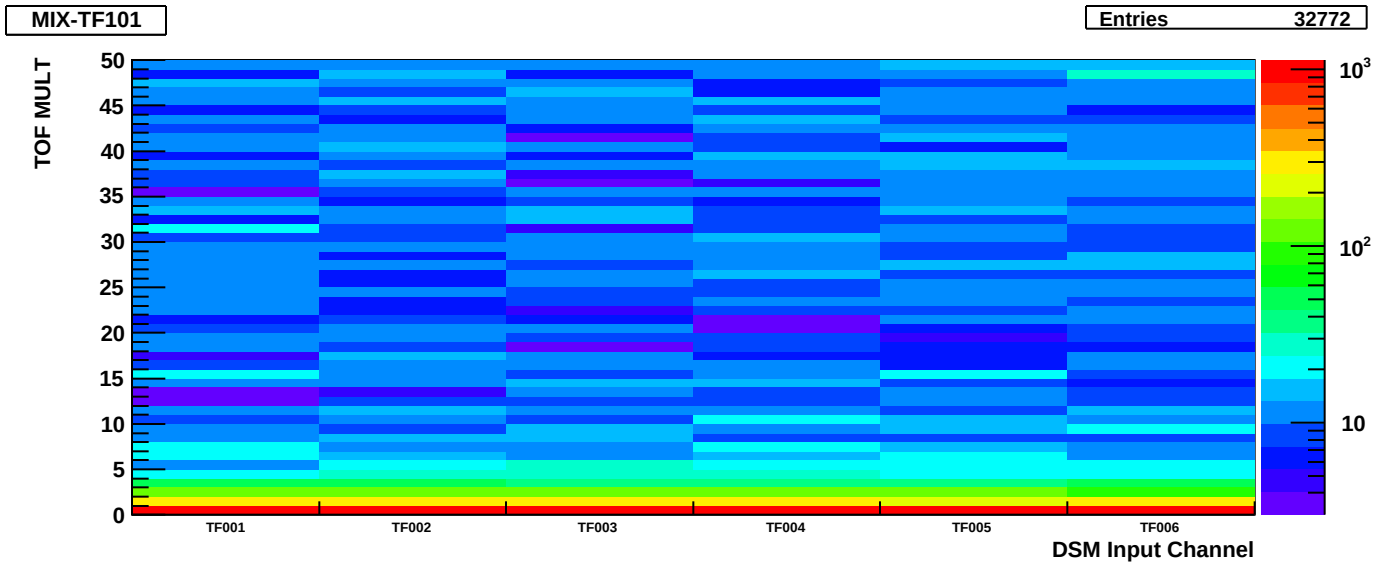

MIX-TF002
Entries 229404
TOF MULT
TOF tray

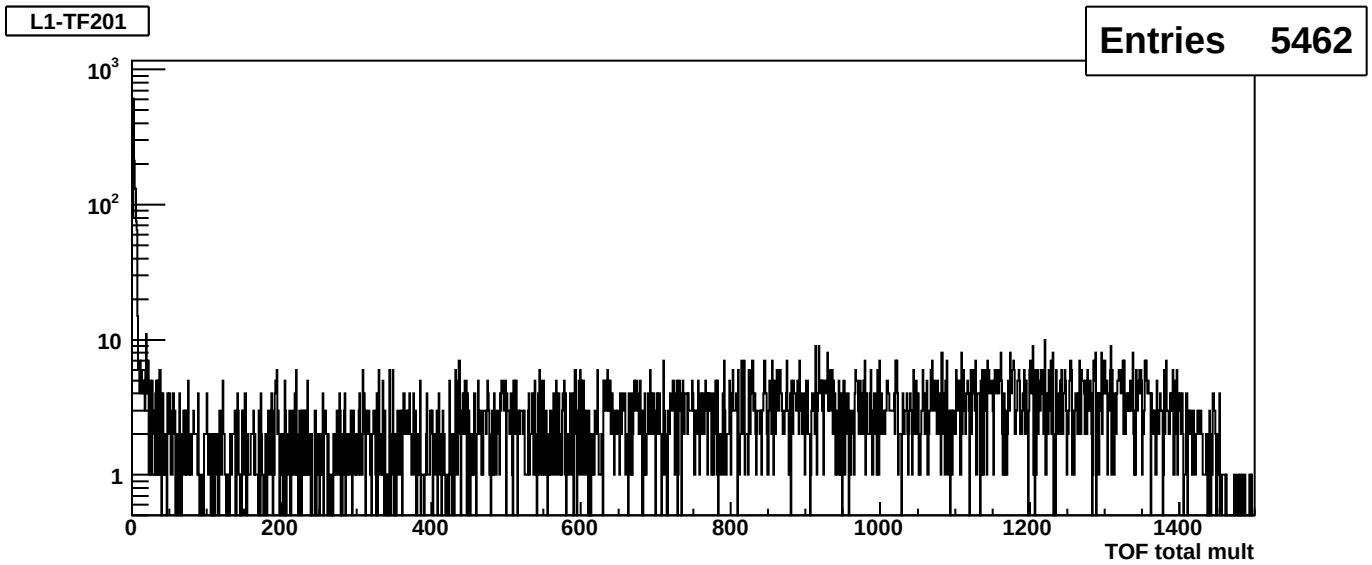

MIX-TF003
Entries 229404
TOF MULT
TOF tray

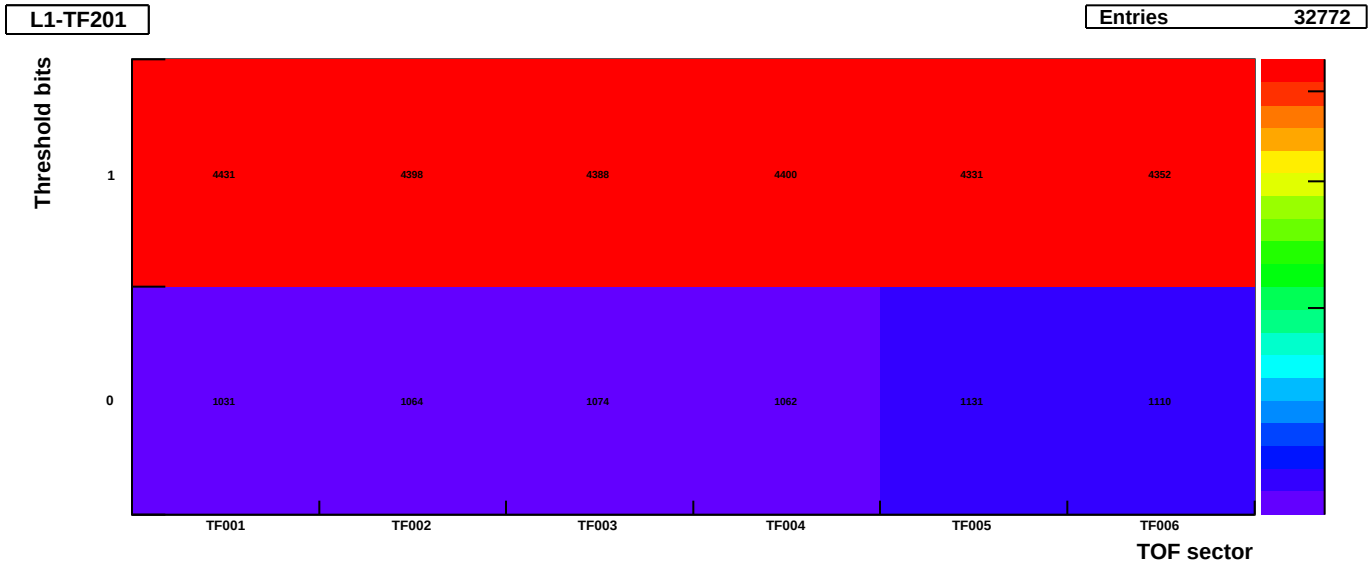

MIX-TF004
Entries 229404
TOF MULT
TOF tray

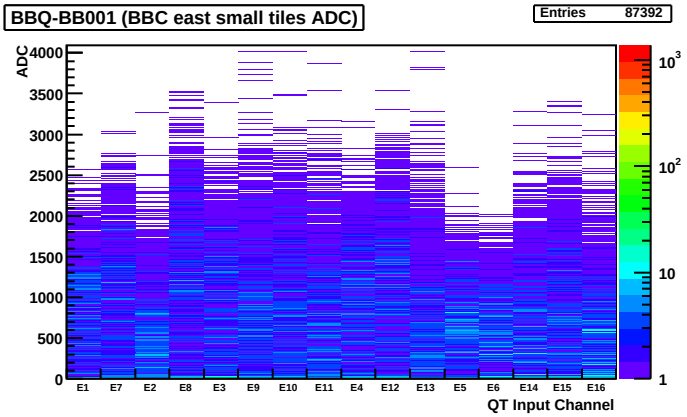

MIX-TF005
Entries 229404
TOF MULT
TOF tray

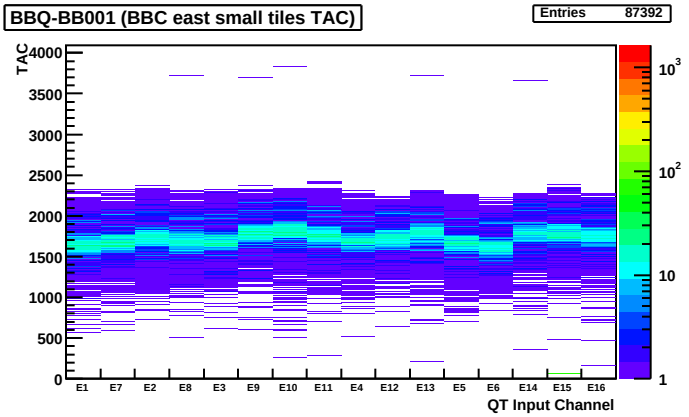

MIX-TF006
Entries 229404
TOF MULT
TOF tray

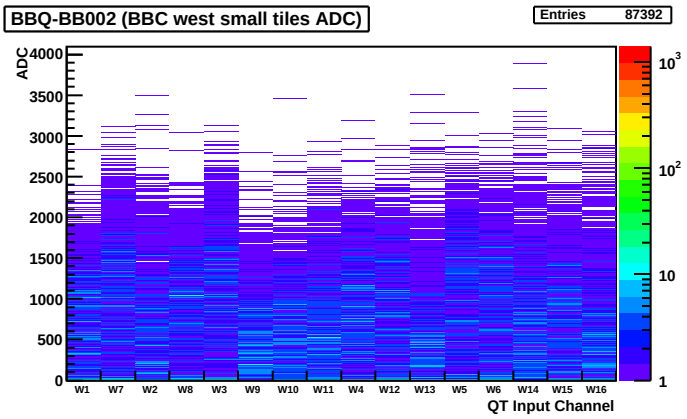

MIX-TF101
Entries 32772
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^3
10^2
10

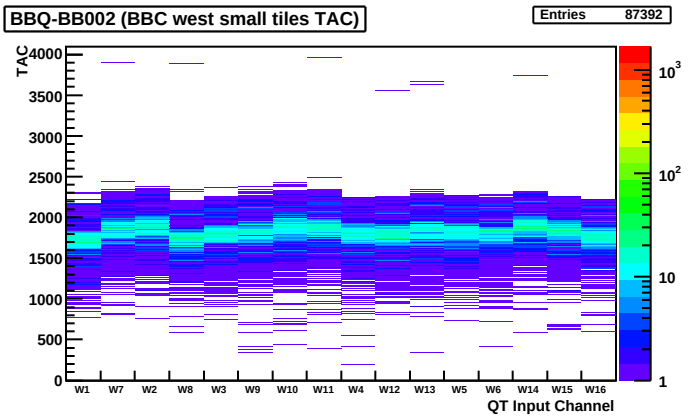

L1-TF201
Entries 5462
10^3
10^2
10
1
0
200
400
600
800
1000
1200
1400
TOF total mult

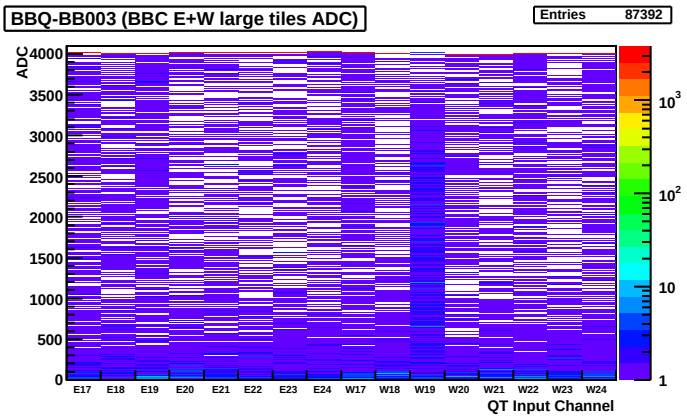

L1-TF201
Entries 32772
Threshold bits
1
0
4431
4398
4388
4400
4331
4352
1031
1064
1074
1062
1131
1110
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

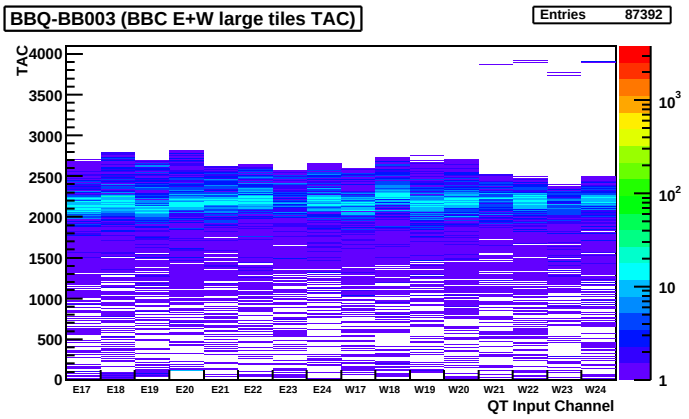

BBQ-BB001 (BBC east small tiles ADC)
Entries 87392
ADC
QT Input Channel

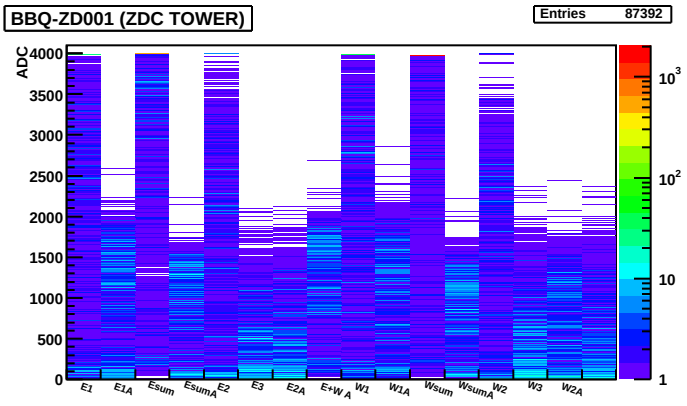

BBQ-BB001 (BBC east small tiles TAC)
Entries 87392
TAC
QT Input Channel

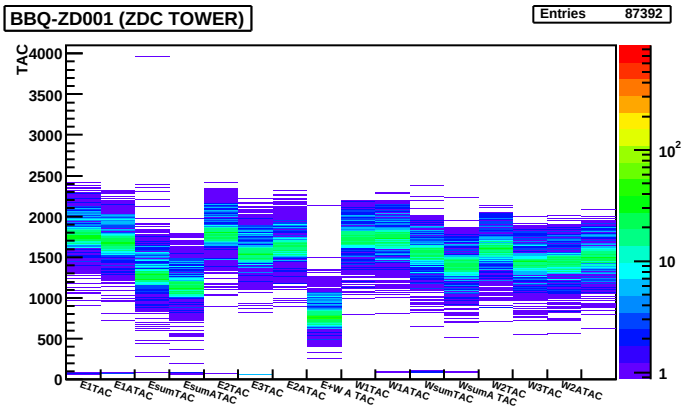

BBQ-BB002 (BBC west small tiles ADC)
Entries 87392
ADC
QT Input Channel

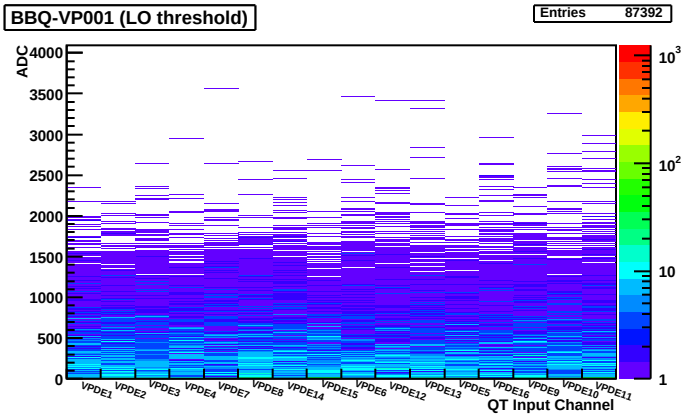

BBQ-BB002 (BBC west small tiles TAC)
Entries 87392
TAC
QT Input Channel

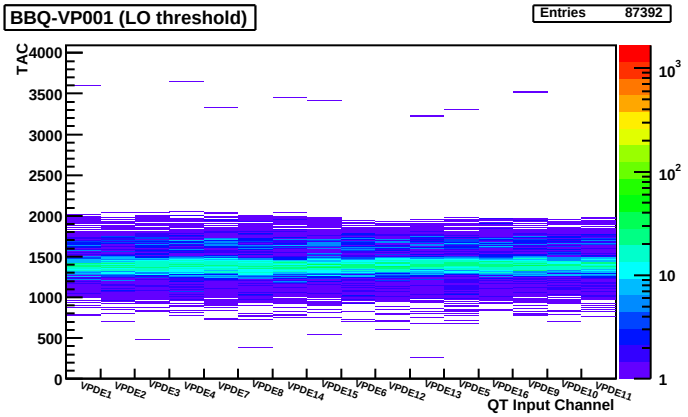

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 87392
ADC
QT Input Channel

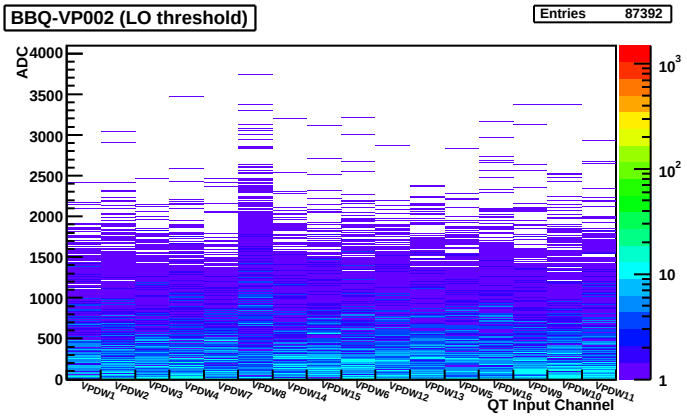

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 87392
TAC
QT Input Channel

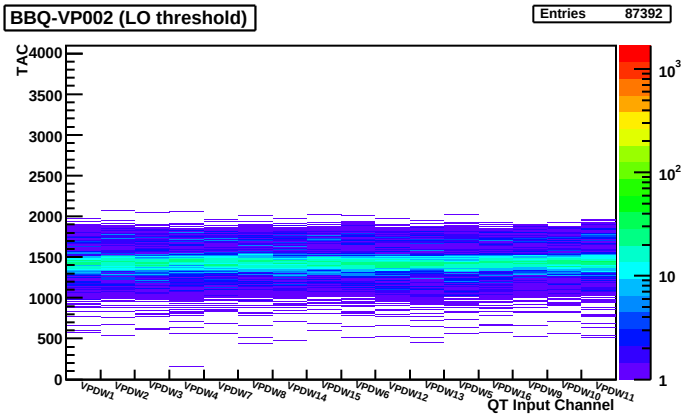

BBQ-ZD001 (ZDC TOWER)
Entries 87392
ADC

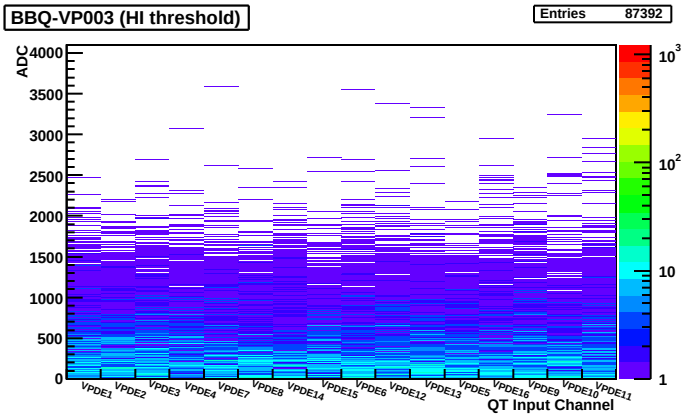

BBQ-ZD001 (ZDC TOWER)
Entries 87392
TAC

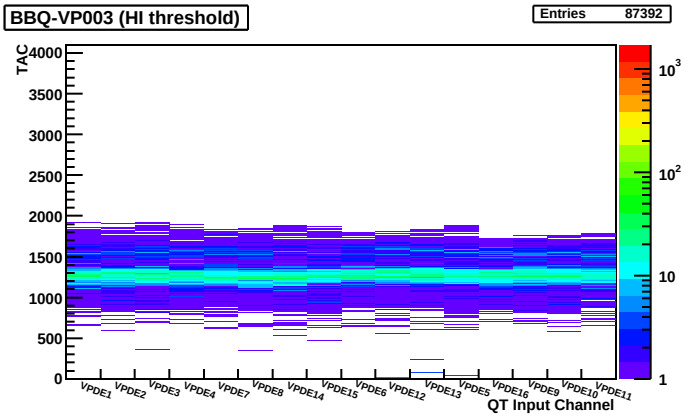

BBQ-VP001 (LO threshold)
Entries 87392
ADC
QT Input Channel

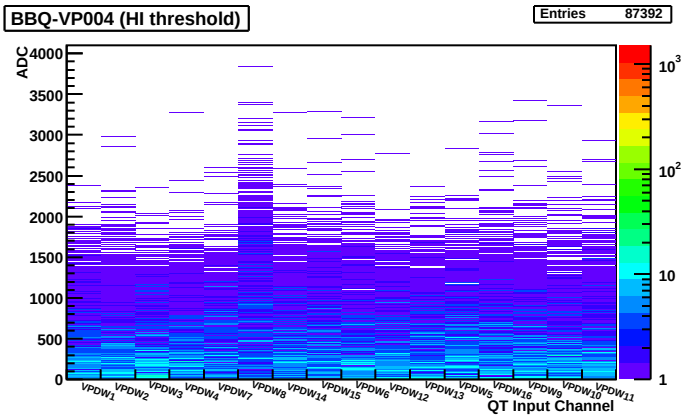

BBQ-VP001 (LO threshold)
Entries 87392
TAC
QT Input Channel

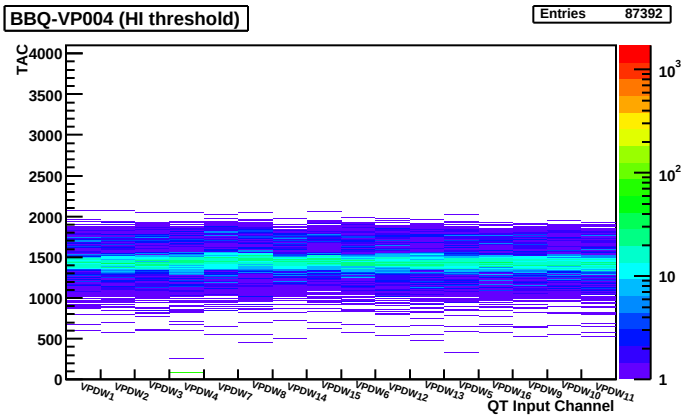

BBQ-VP002 (LO threshold)
Entries 87392
ADC
QT Input Channel

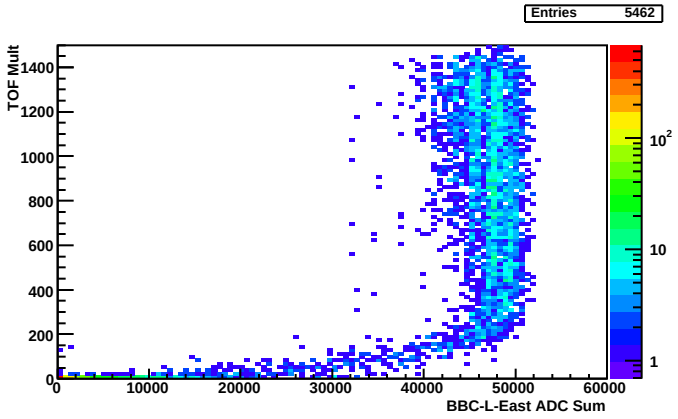

BBQ-VP002 (LO threshold)
Entries 87392
TAC
QT Input Channel

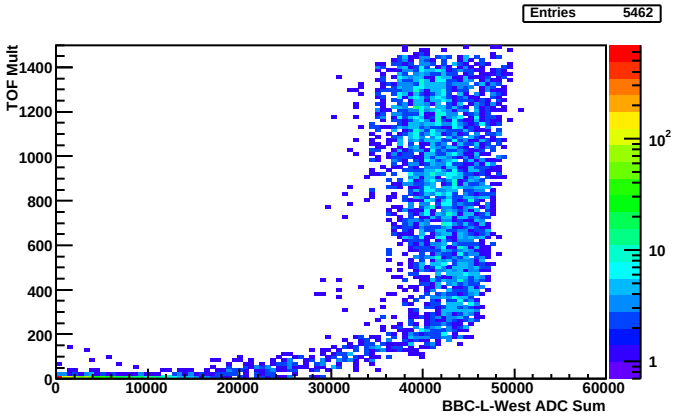

BBQ-VP003 (HI threshold)
Entries 87392
ADC
QT Input Channel

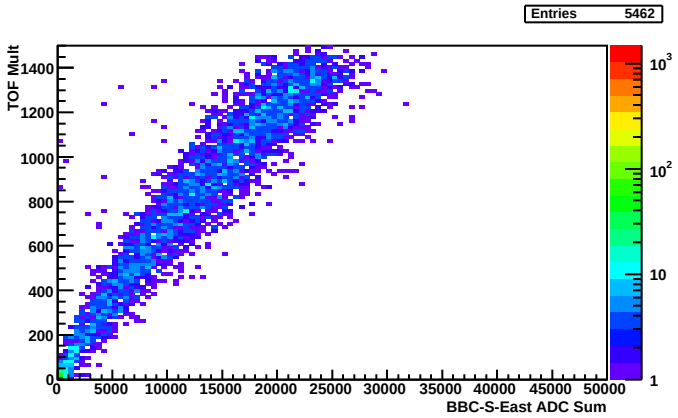

BBQ-VP003 (HI threshold)
Entries 87392
TAC
QT Input Channel

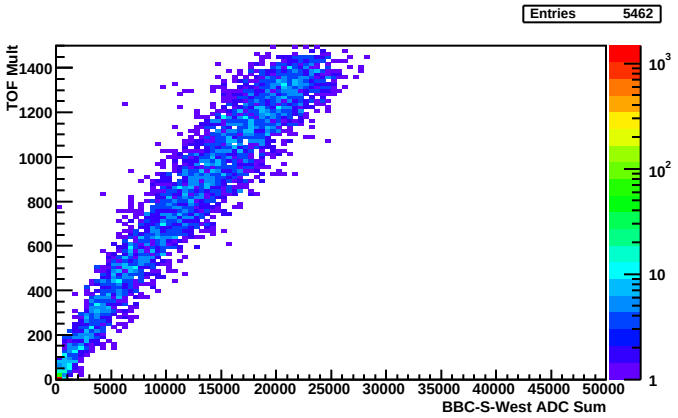

BBQ-VP004 (HI threshold)
Entries 87392
ADC
QT Input Channel

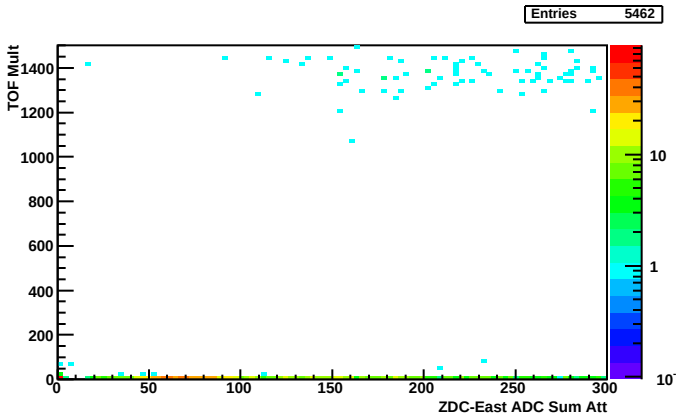

BBQ-VP004 (HI threshold)
Entries 87392
TAC
QT Input Channel

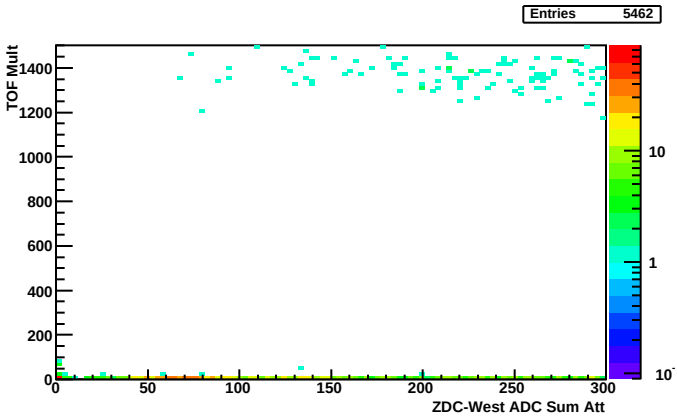

Entries 5462
TOF Mult
BBC-L-East ADC Sum

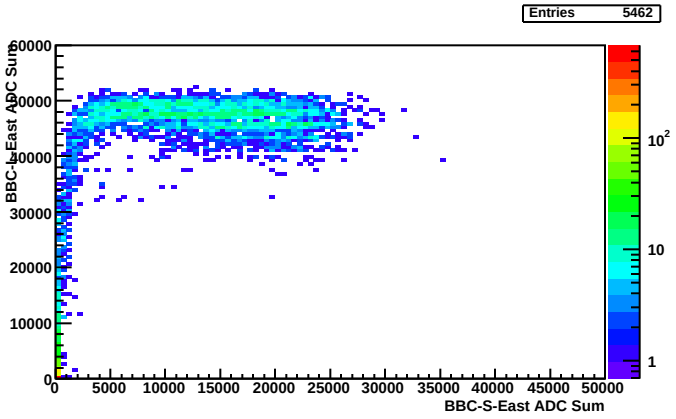

Entries 5462
TOF Mult
BBC-L-West ADC Sum

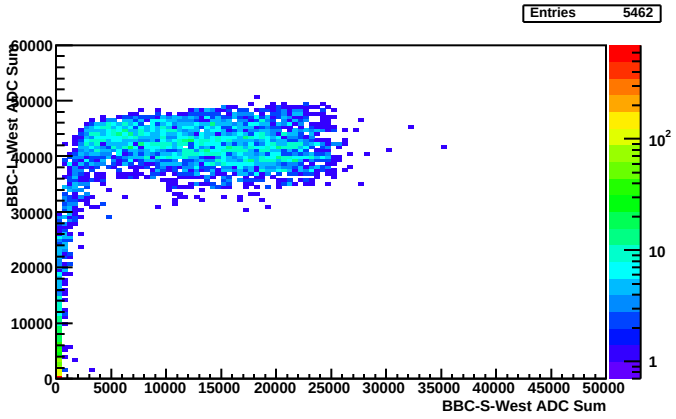

Entries 5462
TOF Mult
BBC-S-East ADC Sum

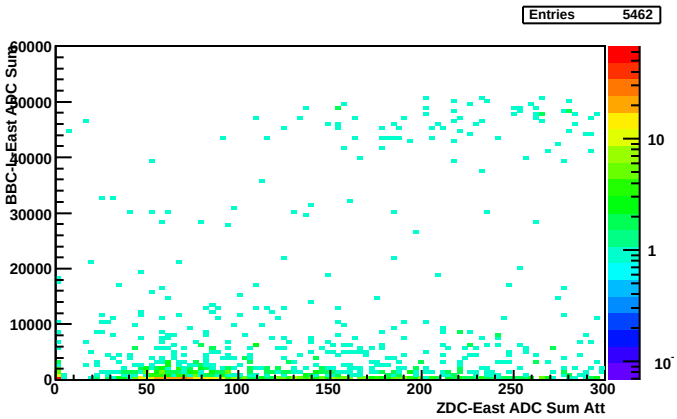

Entries 5462
TOF Mult
BBC-S-West ADC Sum

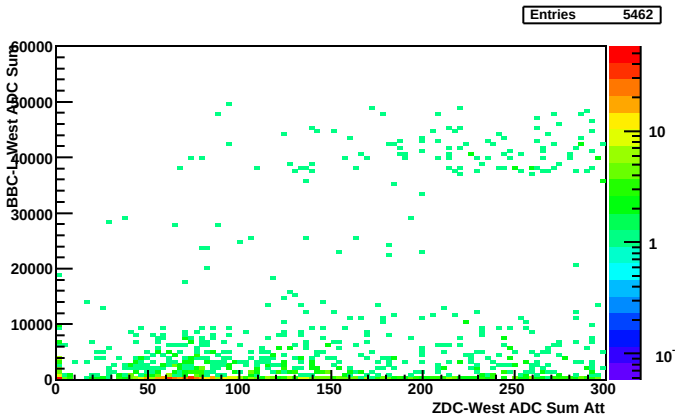

Entries 5462
TOF Mult
ZDC-East ADC Sum Att

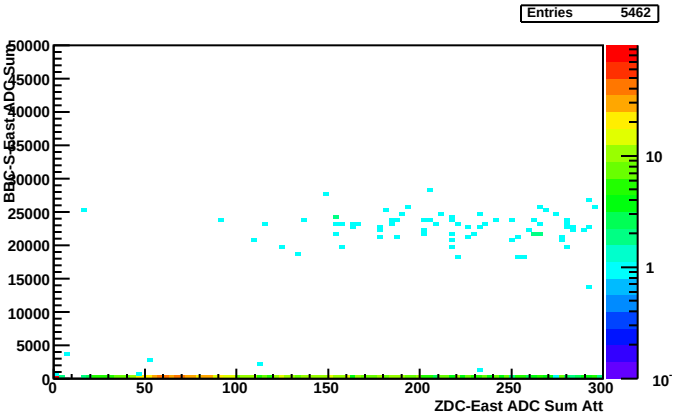

Entries 5462
TOF Mult
ZDC-West ADC Sum Att

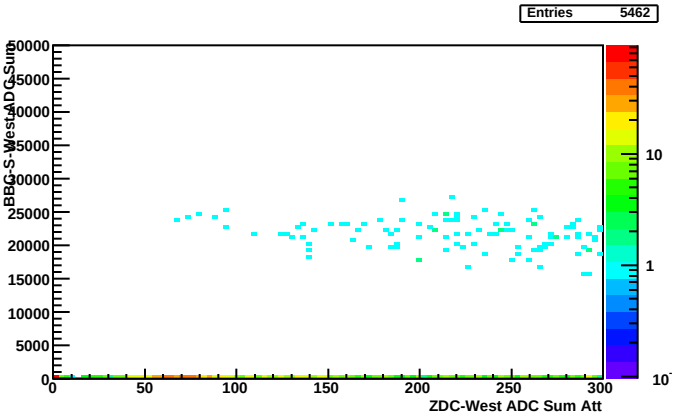

Entries 5462
BBC-L-East ADC Sum
BBC-S-East ADC Sum

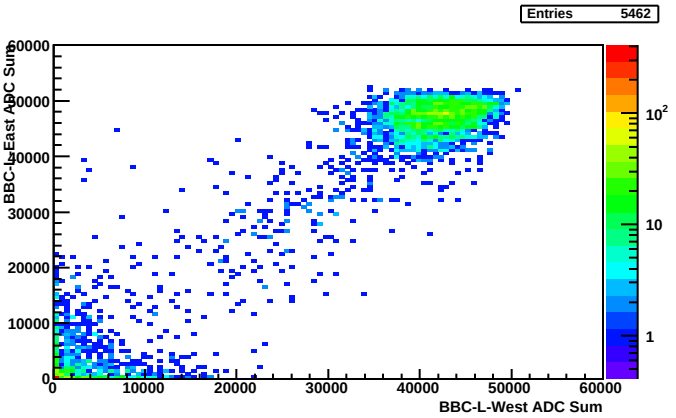

Entries 5462
BBC-L-West ADC Sum
BBC-S-West ADC Sum

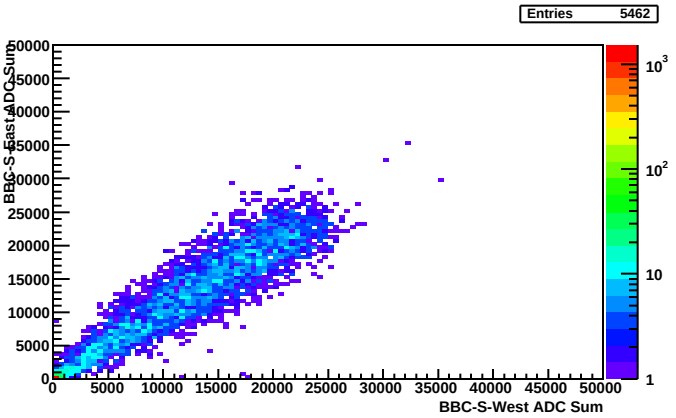

Entries 5462
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

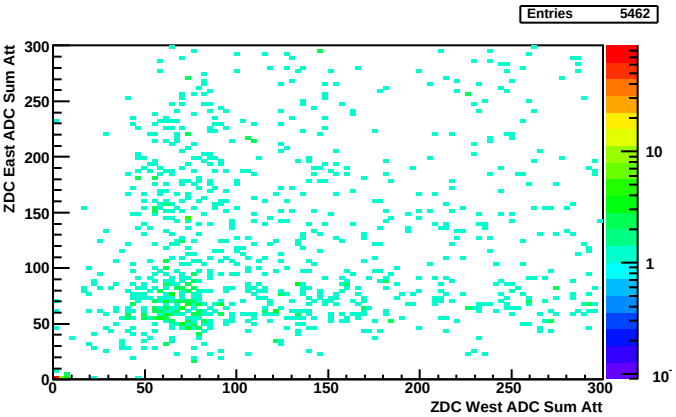

Entries 5462
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

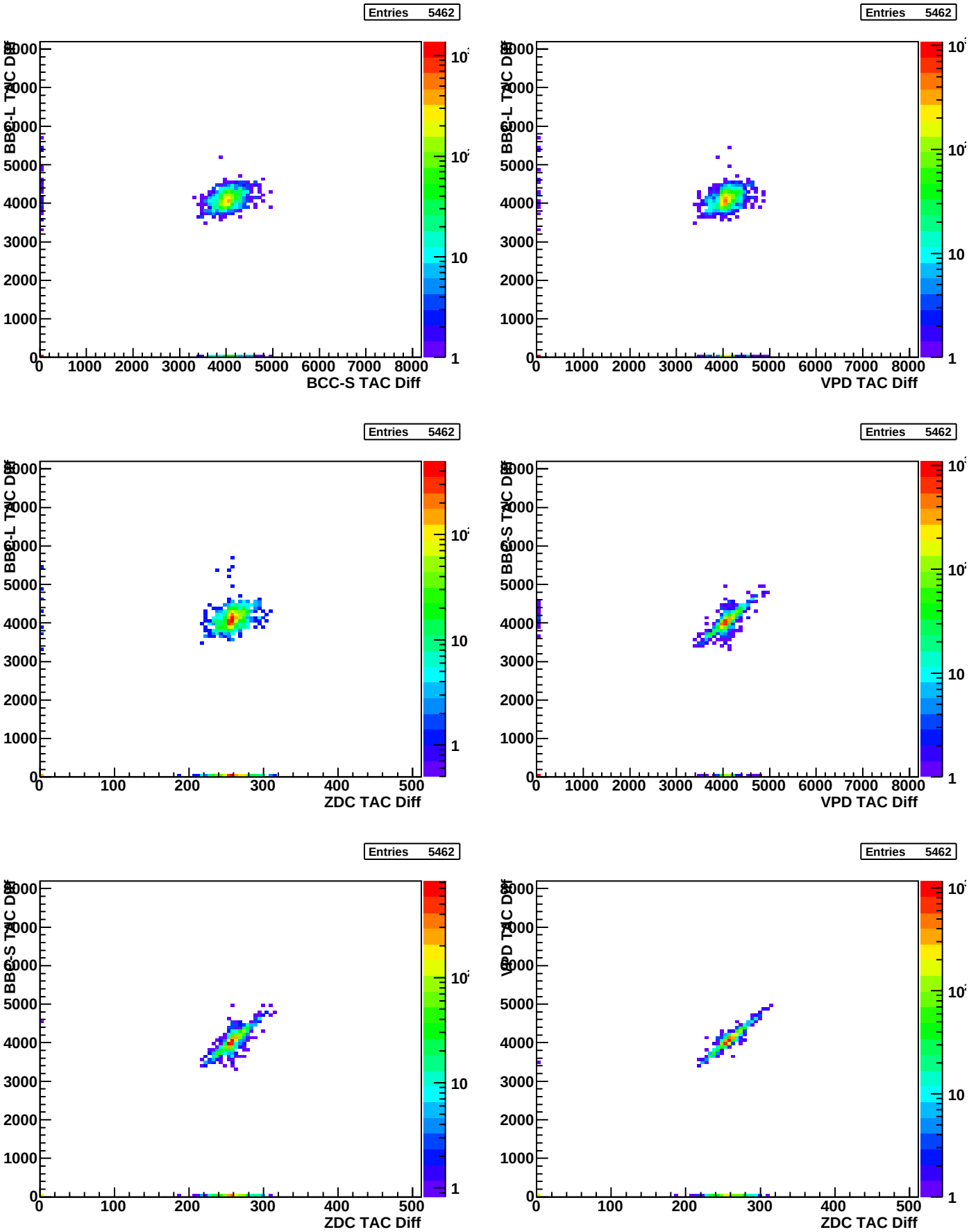

Entries 5462
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

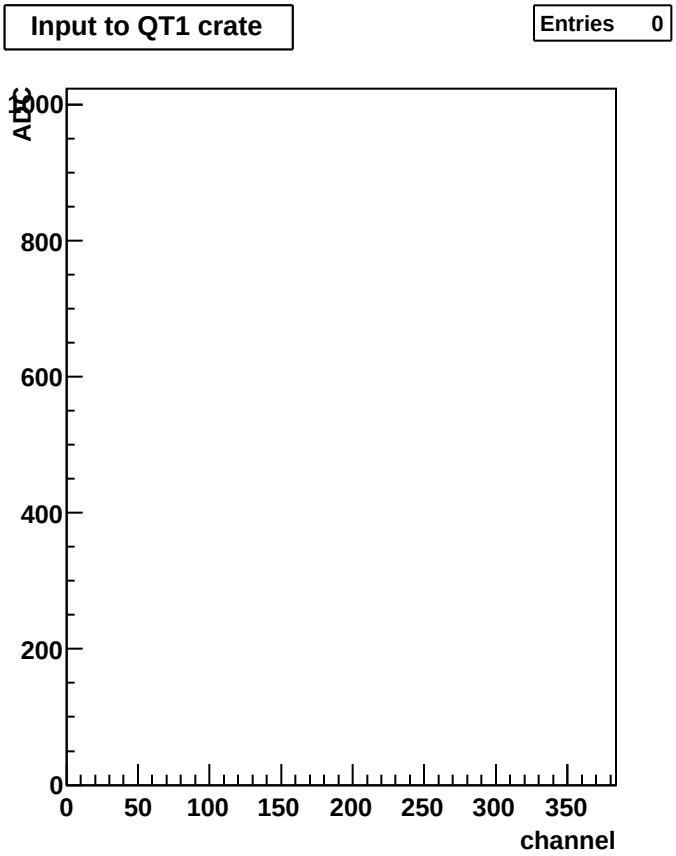

Entries 5462
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

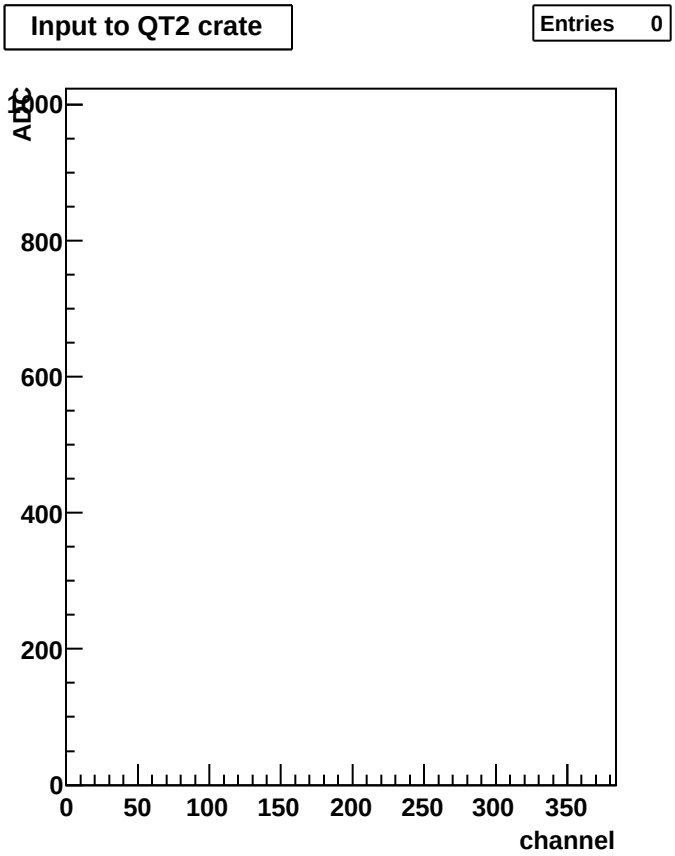

Entries 5462
BBC-L-East ADC Sum
BBC-L-West ADC Sum

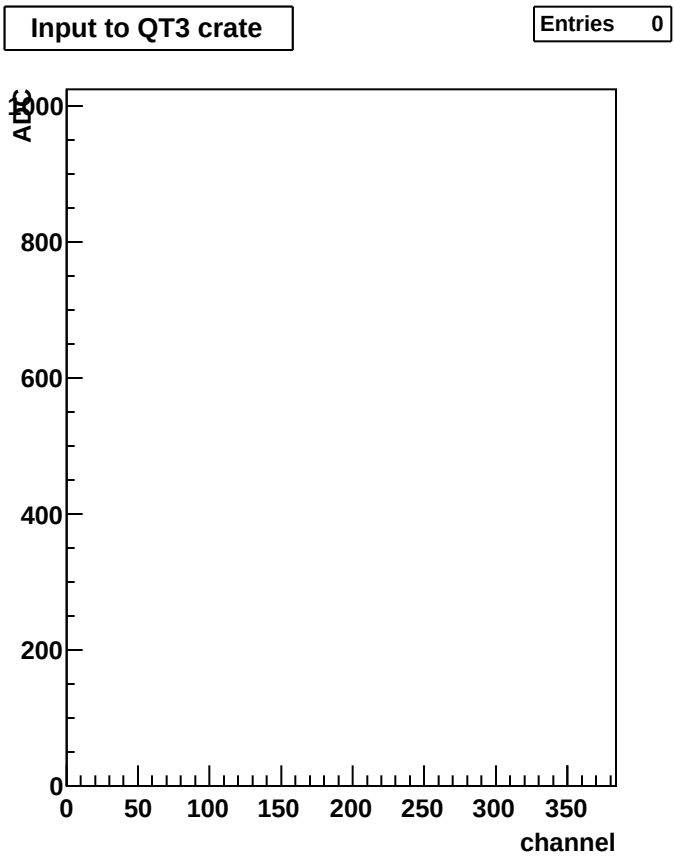

Entries 5462
BBC-S-East ADC Sum
BBC-S-West ADC Sum

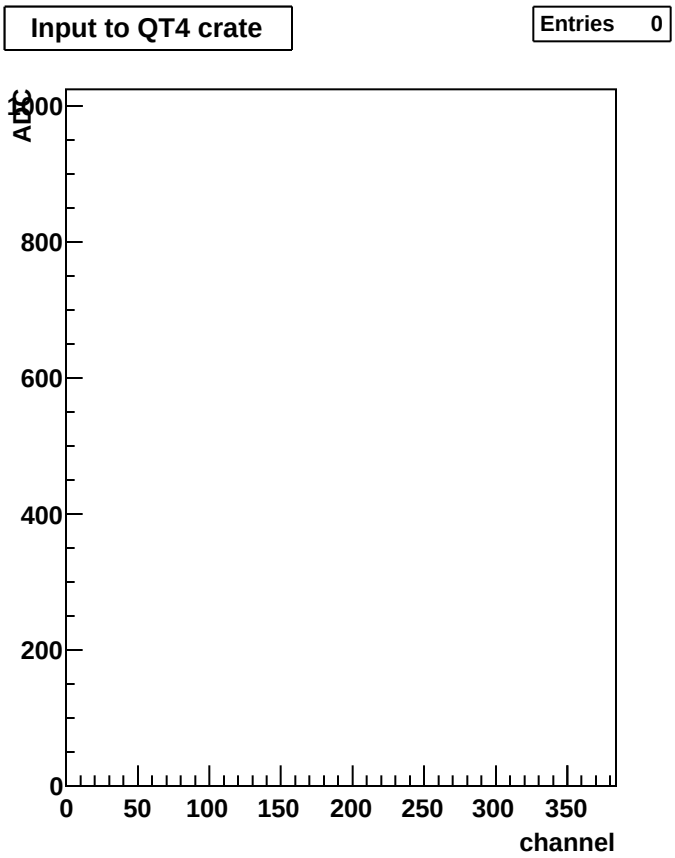

Entries 5462
ZDC East ADC Sum Att
ZDC West ADC Sum Att

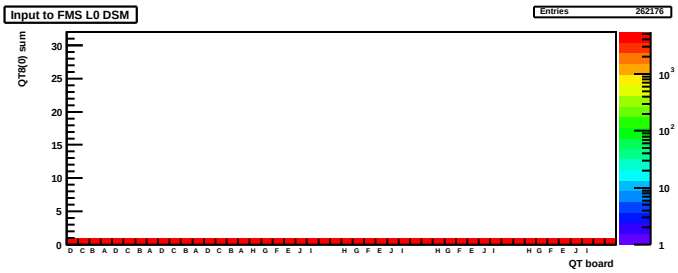

Entries 5462
BBC-L TAC Diff
BCC-S TAC Diff
VPD TAC Diff
ZDC TAC Diff
BBC-S TAC Diff

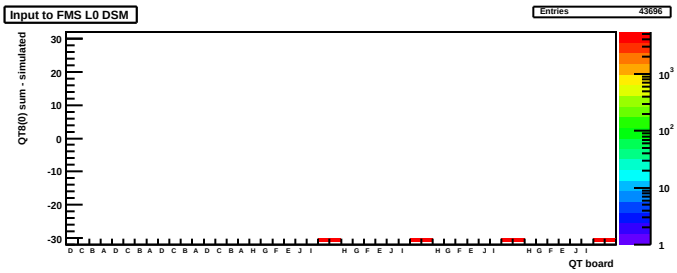

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

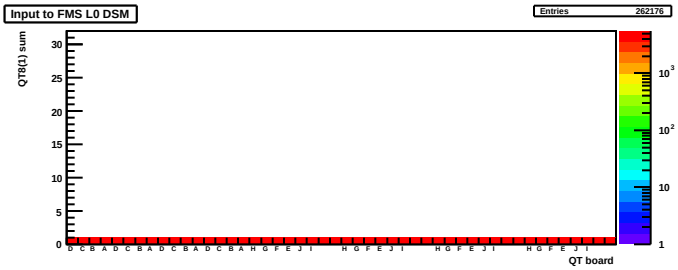

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

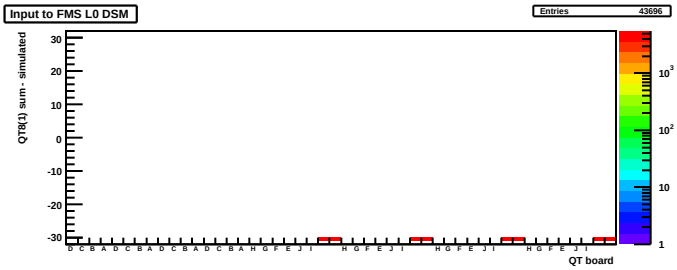

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

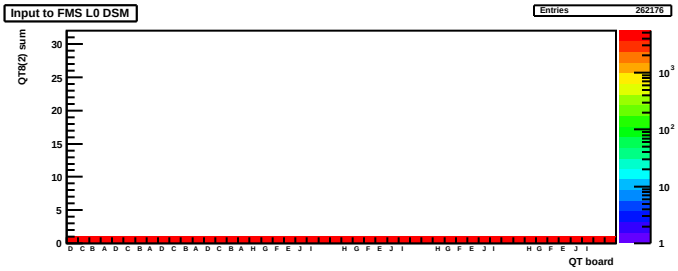

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

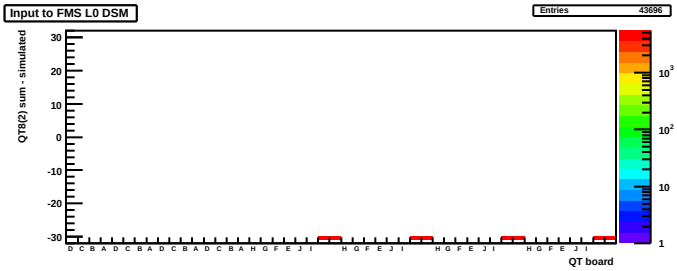

Input to FMS L0 DSM
Entries 262176
QT8(0) sum
QT board

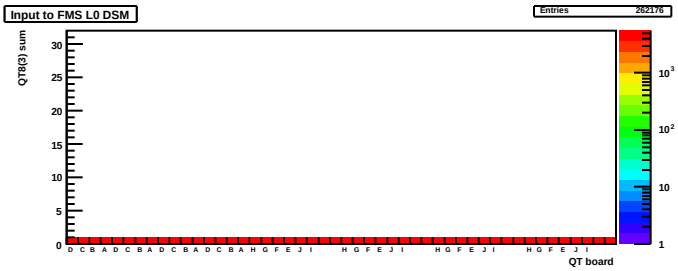

Input to FMS L0 DSM
Entries 43696
QT8(0) sum - simulated
QT board

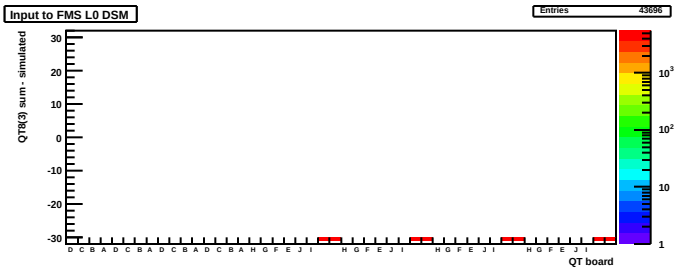

Input to FMS L0 DSM
Entries 262176
QT8(1) sum
QT board

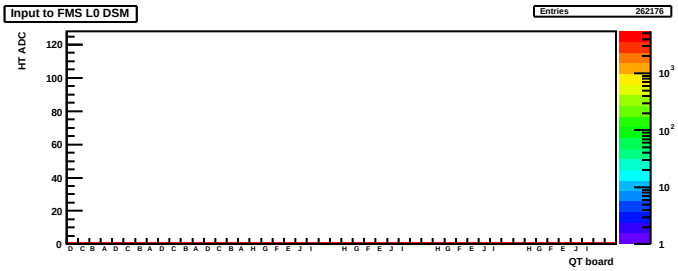

Input to FMS L0 DSM
Entries 43696
QT8(1) sum - simulated
QT board

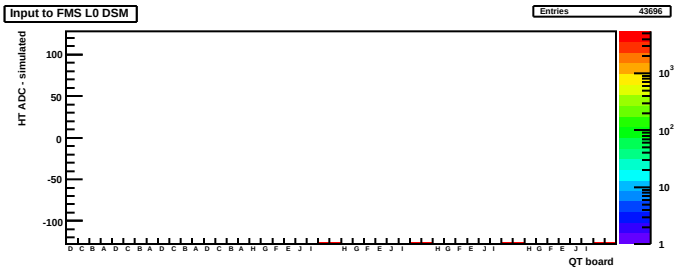

Input to FMS L0 DSM
Entries 262176
QT8(2) sum
QT board

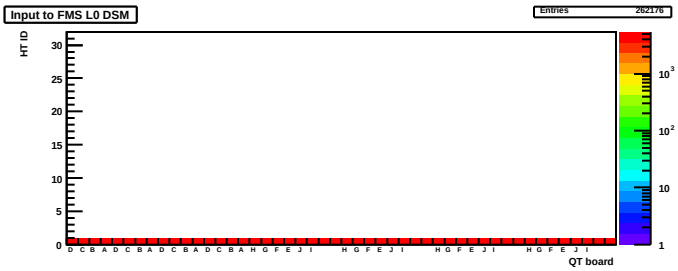

Input to FMS L0 DSM
Entries 43696
QT8(2) sum - simulated
QT board

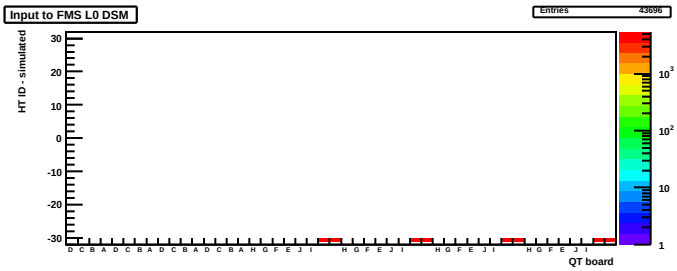

Input to FMS L0 DSM
Entries 262176
QT8(3) sum
QT board

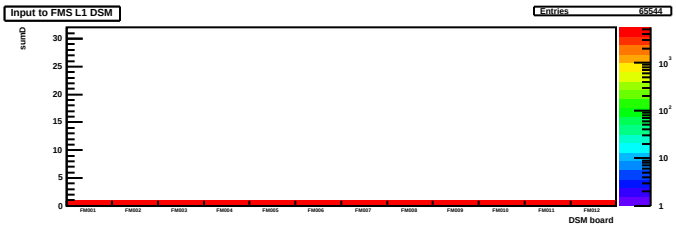

Input to FMS L0 DSM
Entries 43696
QT8(3) sum - simulated
QT board

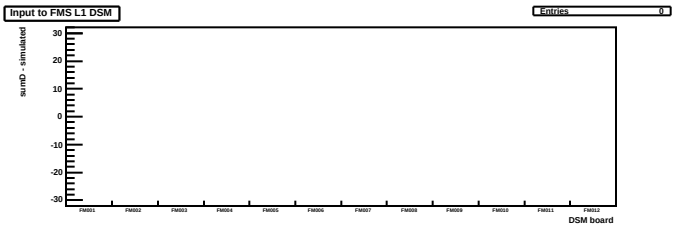

Input to FMS L0 DSM
Entries 262176
HT ADC
QT board

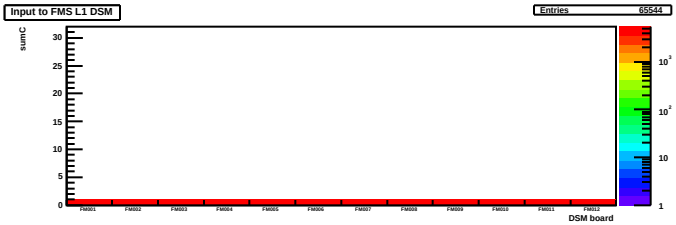

Input to FMS L0 DSM
Entries 43696
HT ADC - simulated
QT board

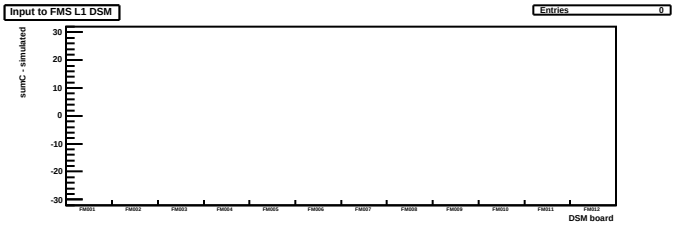

Input to FMS L0 DSM
Entries 262176
HT ID
QT board

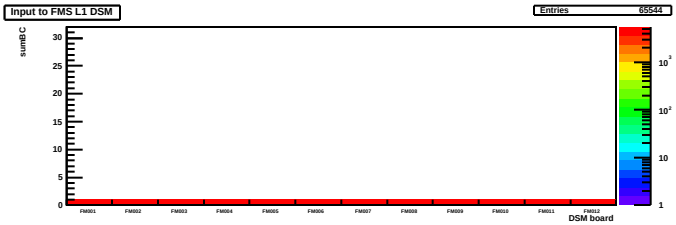

Input to FMS L0 DSM
Entries 43696
HT ID - simulated
QT board

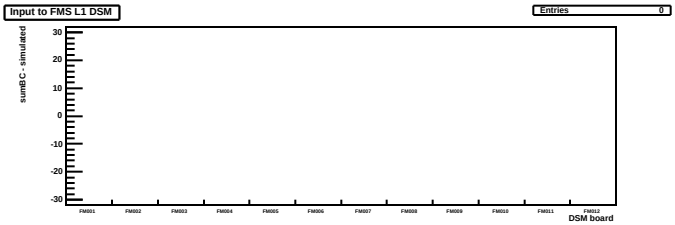

Input to FMS L1 DSM
Entries 65544
sumD
DSM board

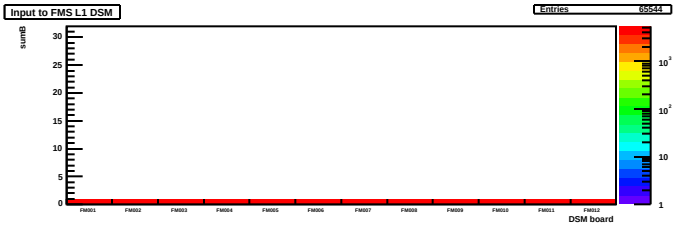

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

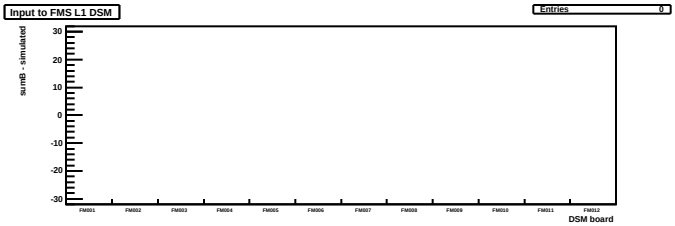

Input to FMS L1 DSM
Entries 65544
sumC
DSM board

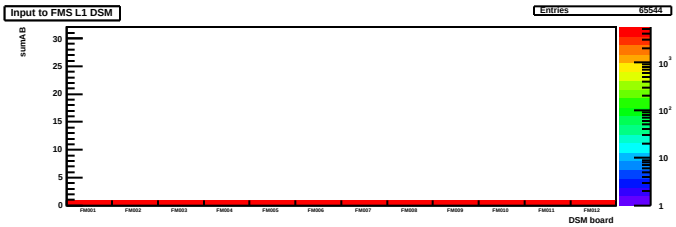

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

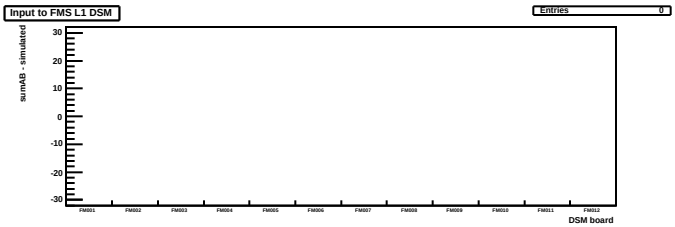

Input to FMS L1 DSM
Entries 65544
sumBC
DSM board

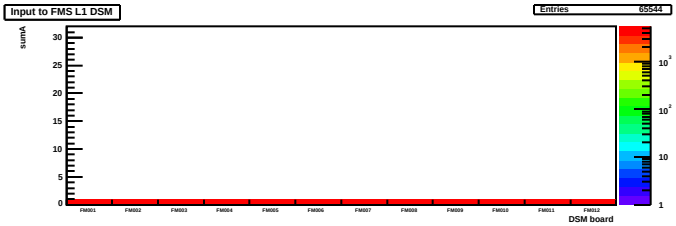

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

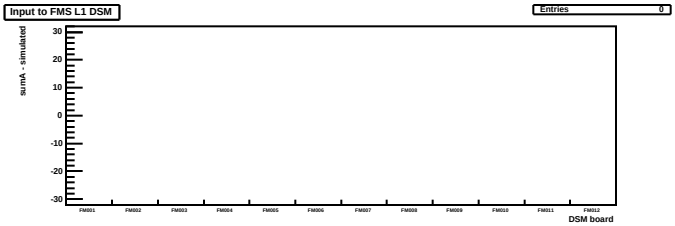

Input to FMS L1 DSM
Entries 65544
sumB
DSM board

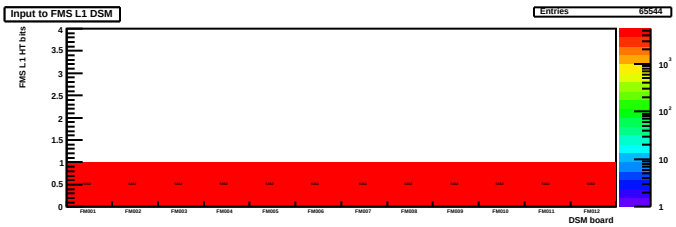

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

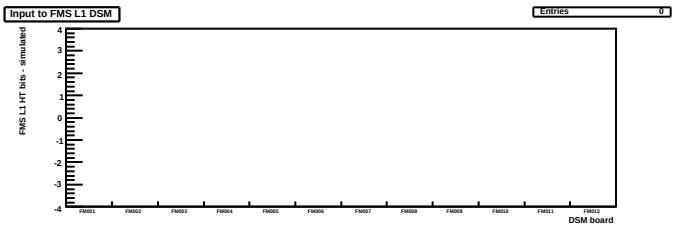

Input to FMS L1 DSM
Entries 65544
sumAB
DSM board

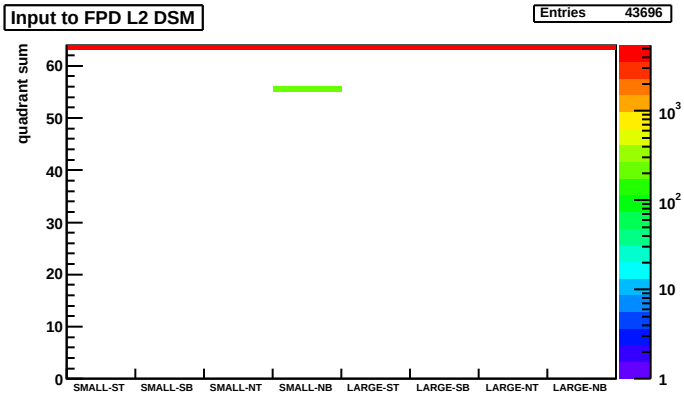

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

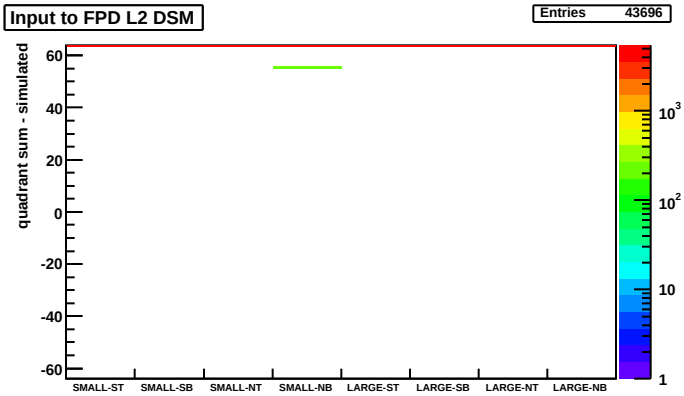

Input to FMS L1 DSM
Entries 65544
sumA
DSM board

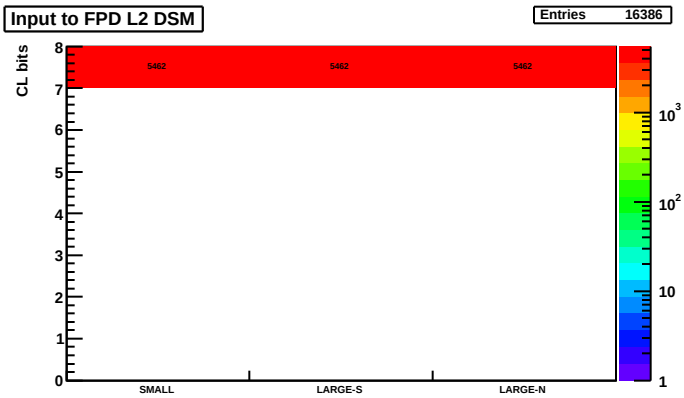

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

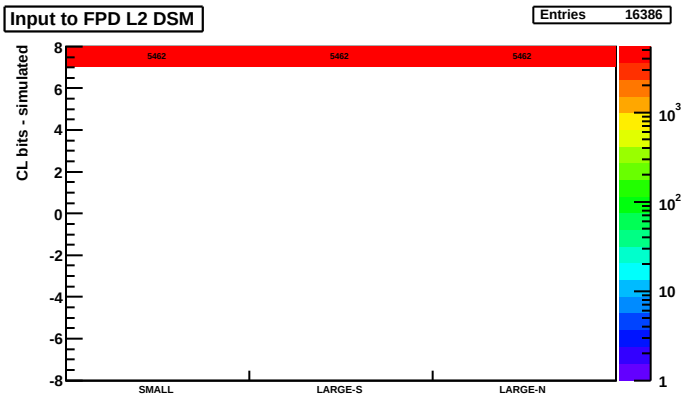

Input to FMS L1 DSM
Entries 65544
FMS L1 HT bits
DSM board

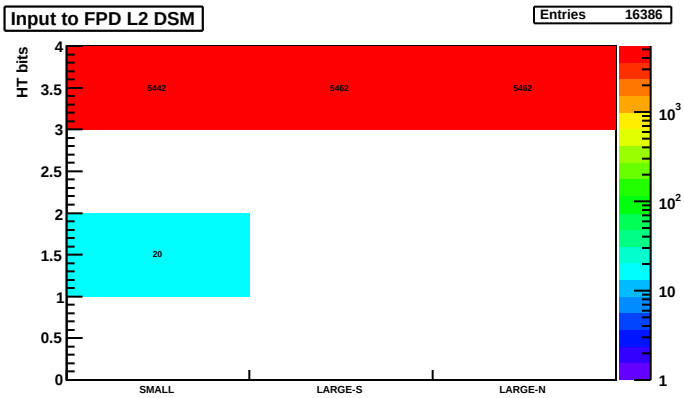

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

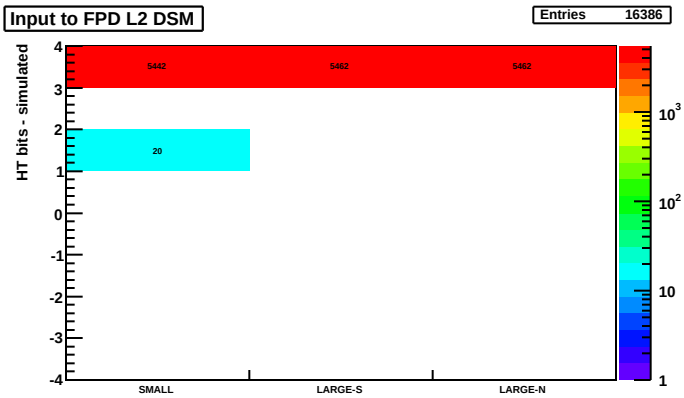

Input to FPD L2 DSM
Entries 43696
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

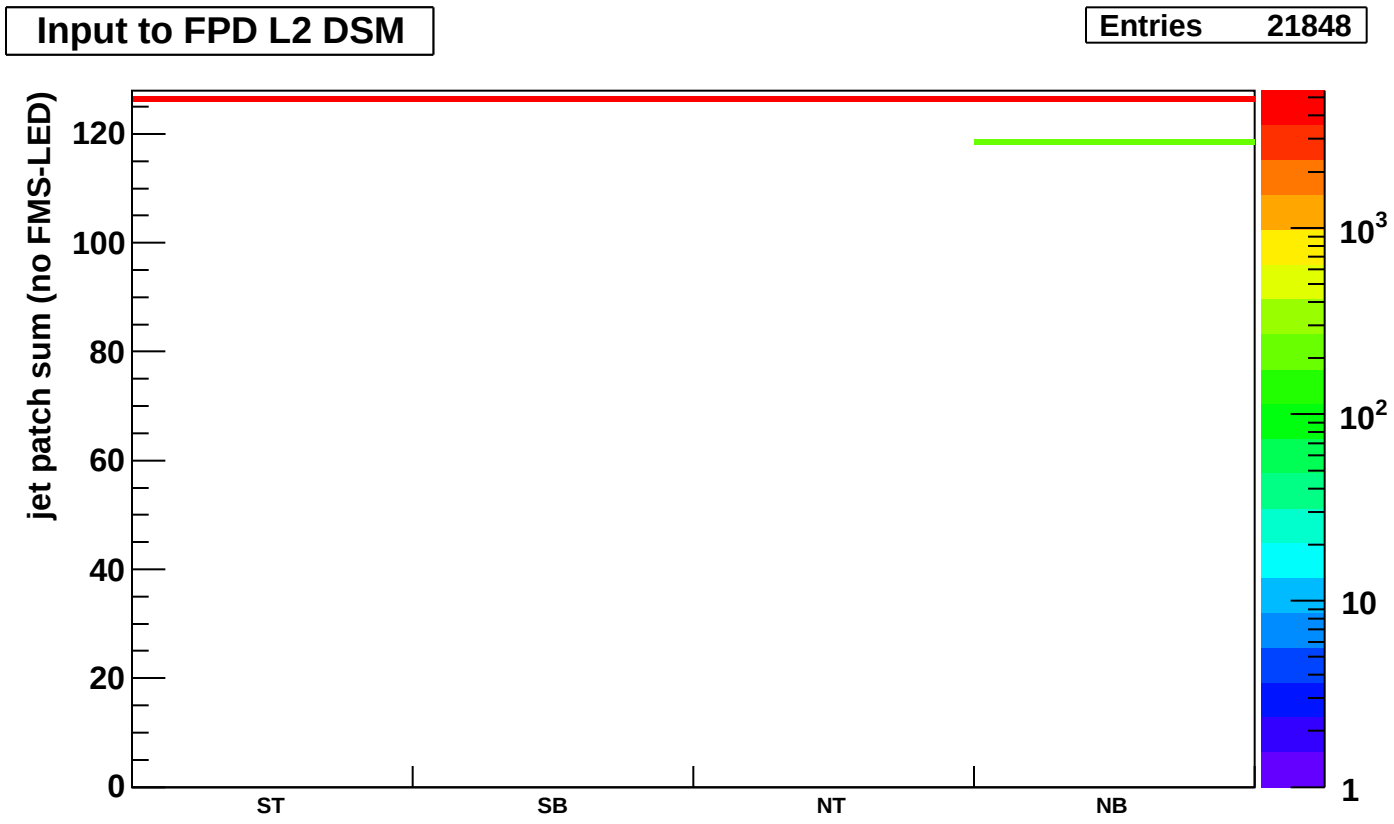

Input to FPD L2 DSM
Entries 43696
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

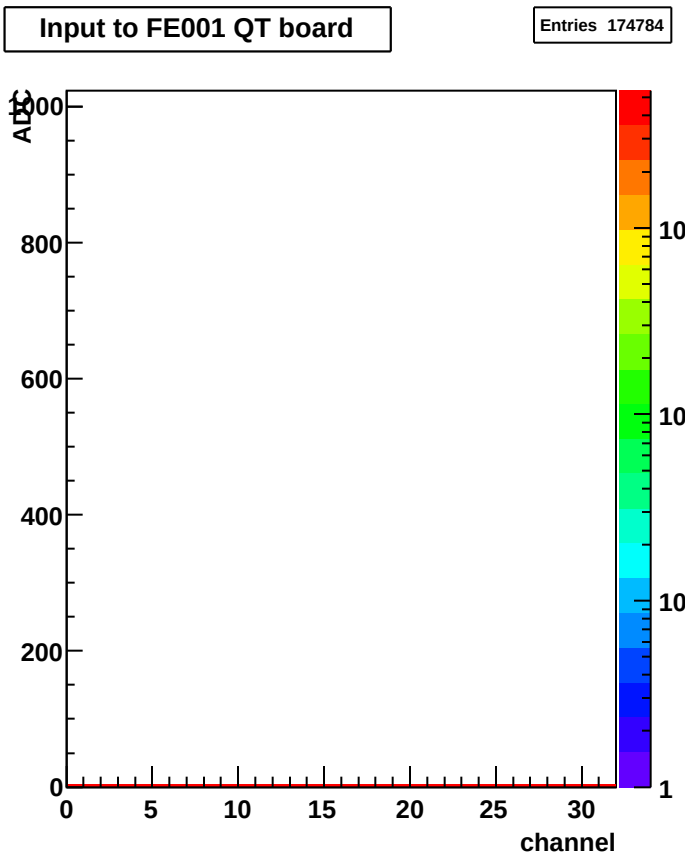

Input to FPD L2 DSM
Entries 16386
CL bits
5462
5462
5462
SMALL
LARGE-S
LARGE-N

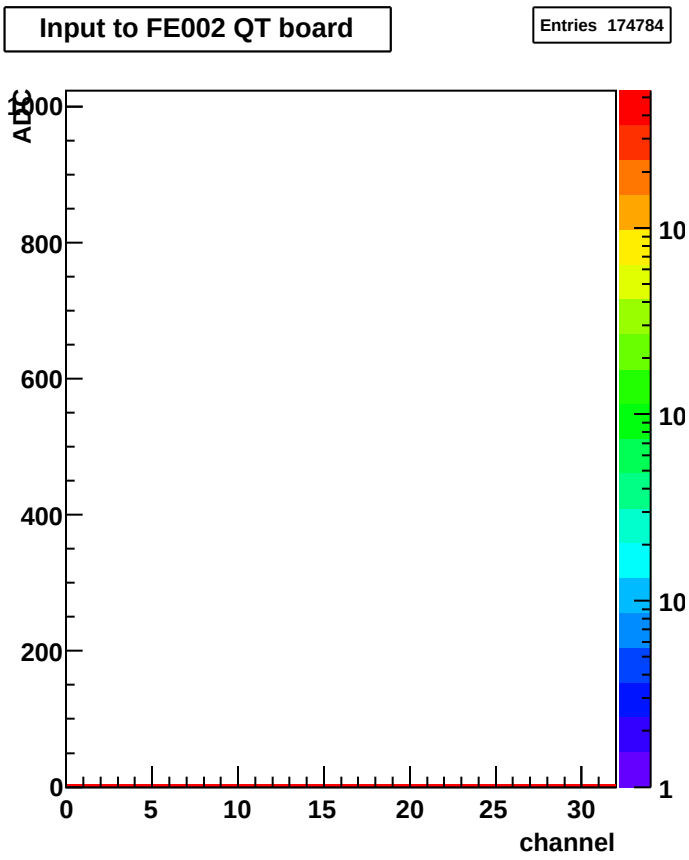

Input to FPD L2 DSM
Entries 16386
CL bits - simulated
5462
5462
5462
SMALL
LARGE-S
LARGE-N

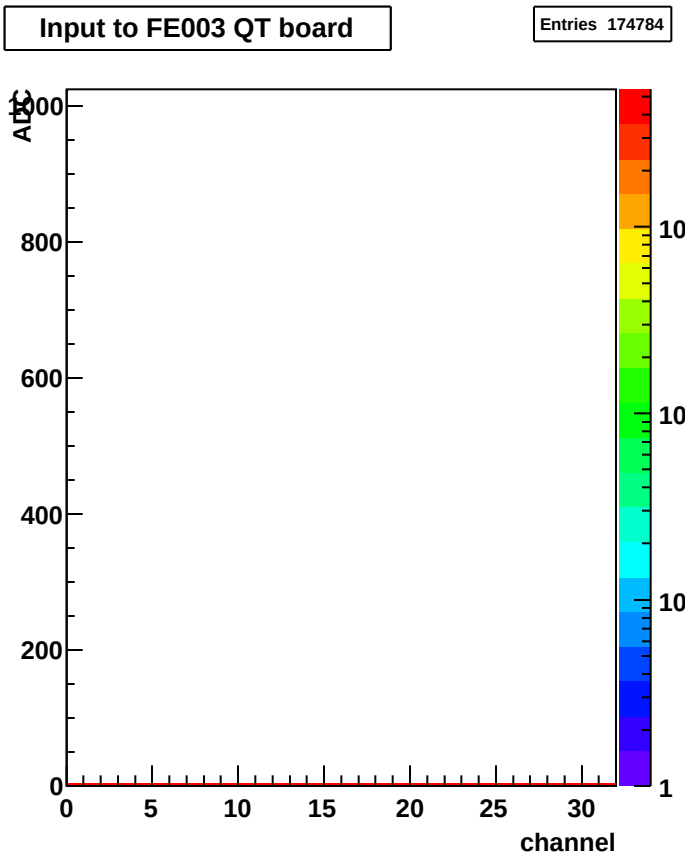

Input to FPD L2 DSM
Entries 16386
HT bits
5442
5462
5462
20
SMALL
LARGE-S
LARGE-N

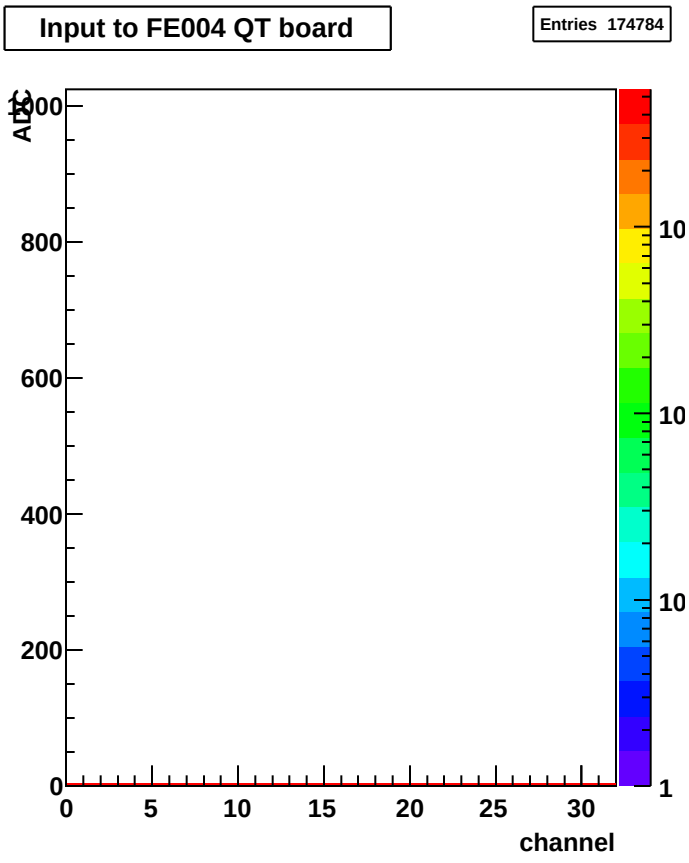

Input to FPD L2 DSM
Entries 16386
HT bits - simulated
5442
5462
5462
20
SMALL
LARGE-S
LARGE-N

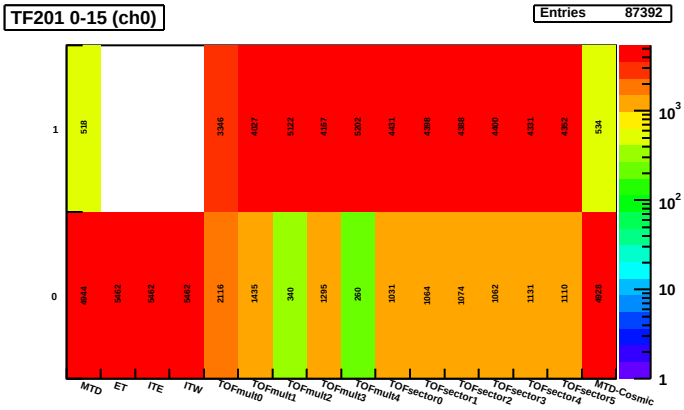

Input to FPD L2 DSM
Entries 21848
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3

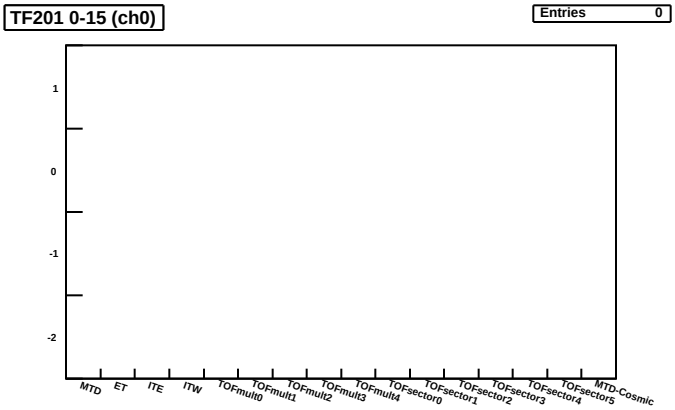

Input to FE001 QT board
Entries 174784
ADC
channel

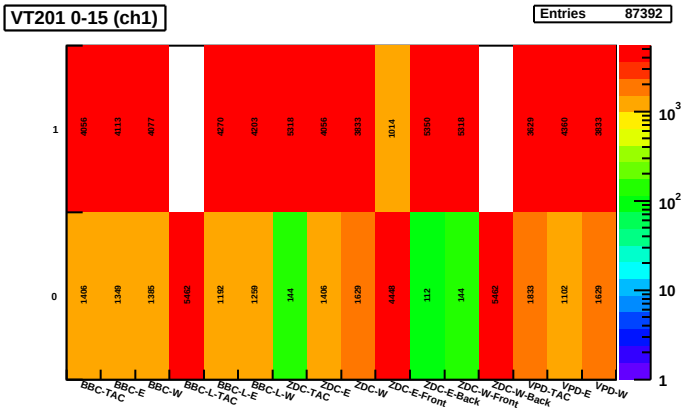

Input to FE002 QT board
Entries 174784
ADC
channel

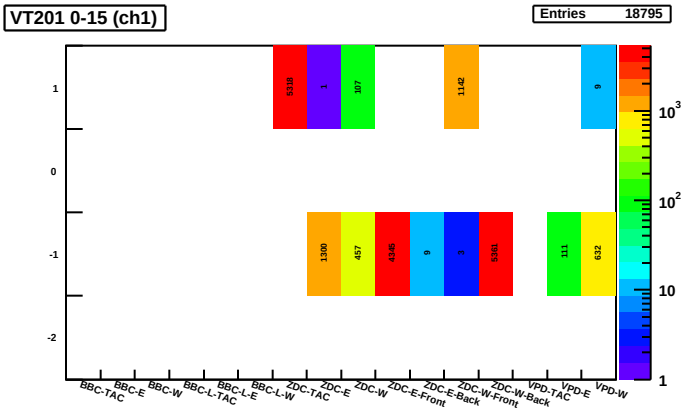

Input to FE003 QT board
Entries 174784
ADC
channel

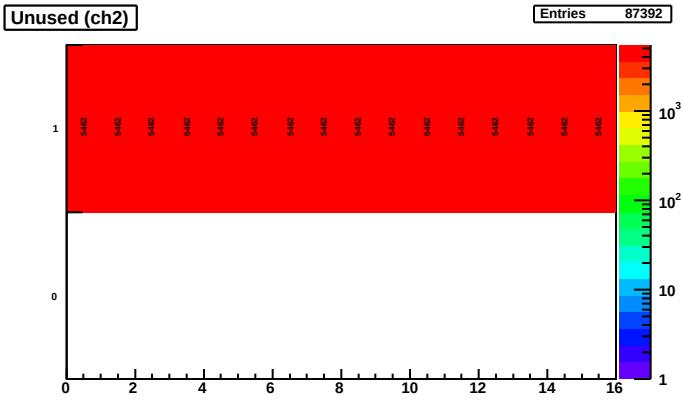

Input to FE004 QT board
Entries 174784
ADC
channel

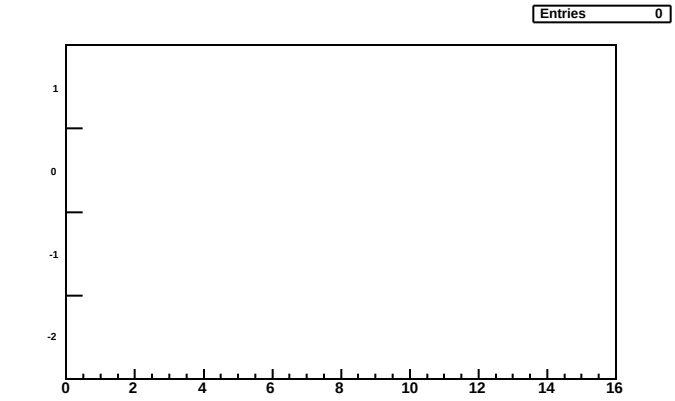

TF201 0-15 (ch0)
Entries 87392
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

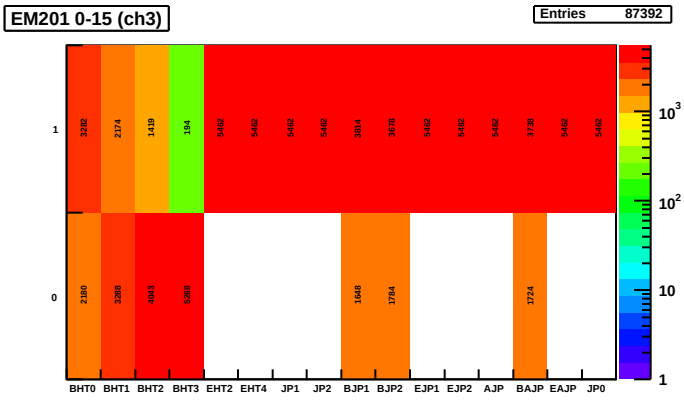

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

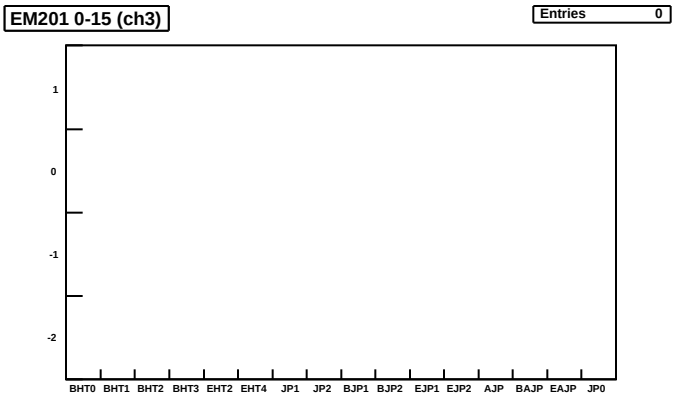

VT201 0-15 (ch1)
Entries 87392
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 18795
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

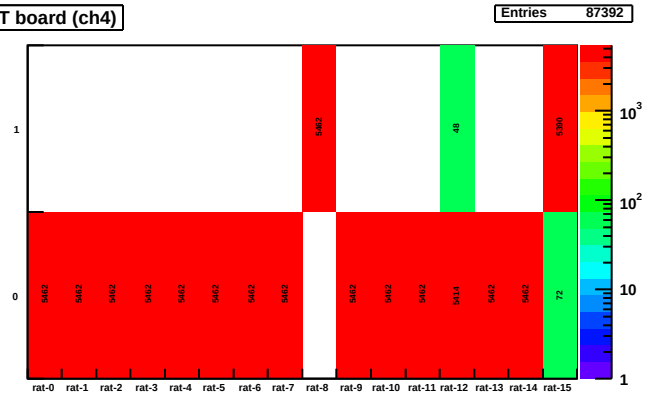

Unused (ch2)
Entries 87392

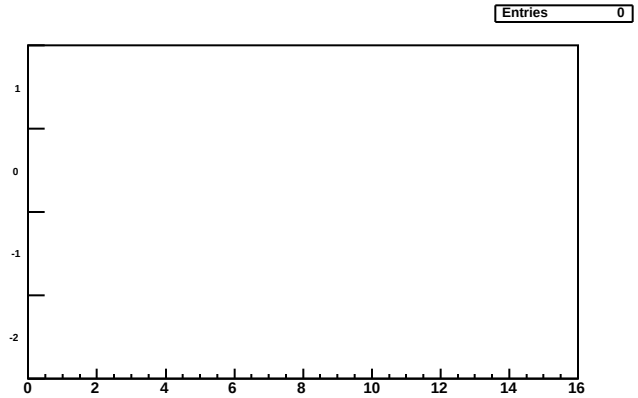

Entries 0


EM201 0-15 (ch3)
Entries 87392
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

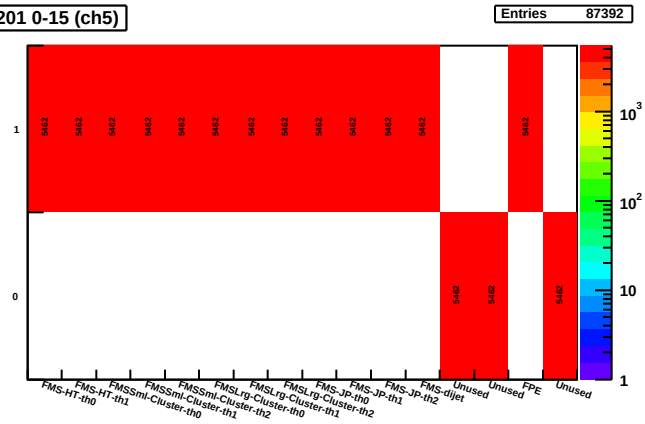

EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

RAT board (ch4)

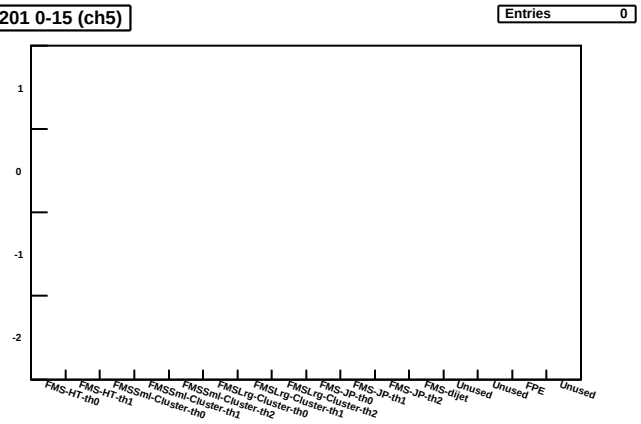




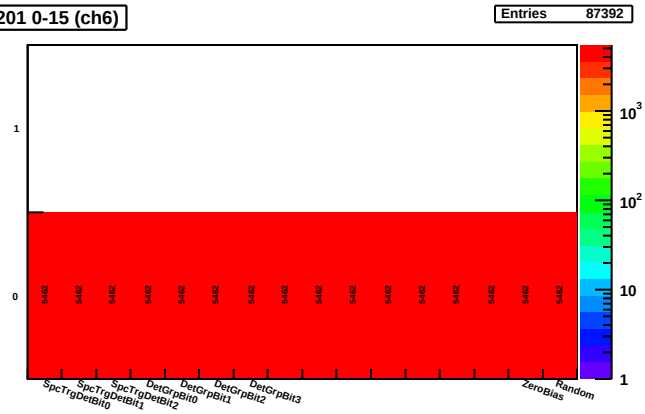
FP201 0-15 (ch5)

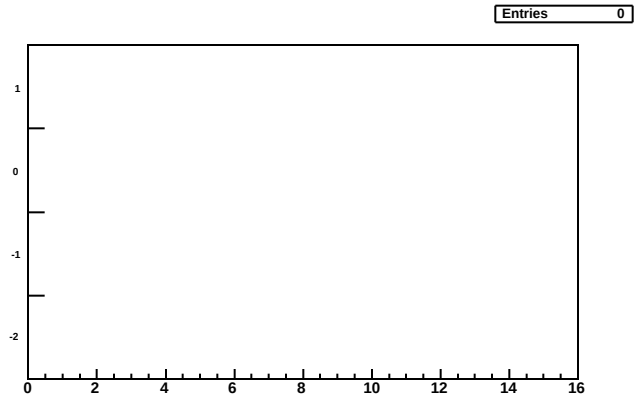
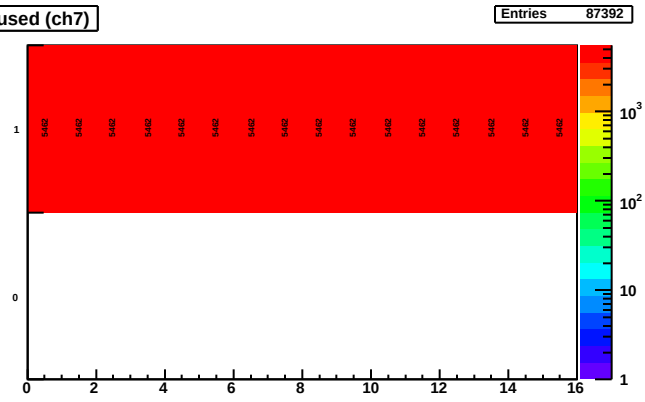
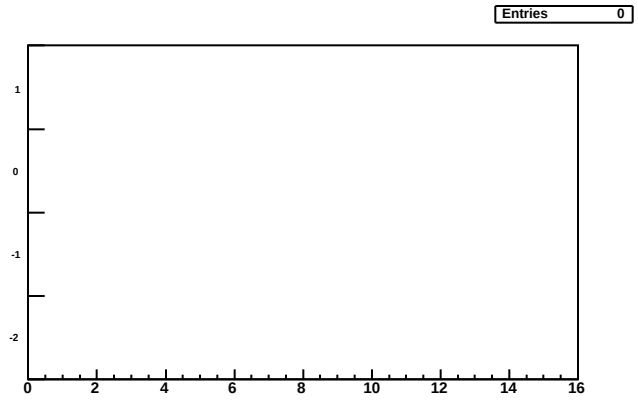


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)