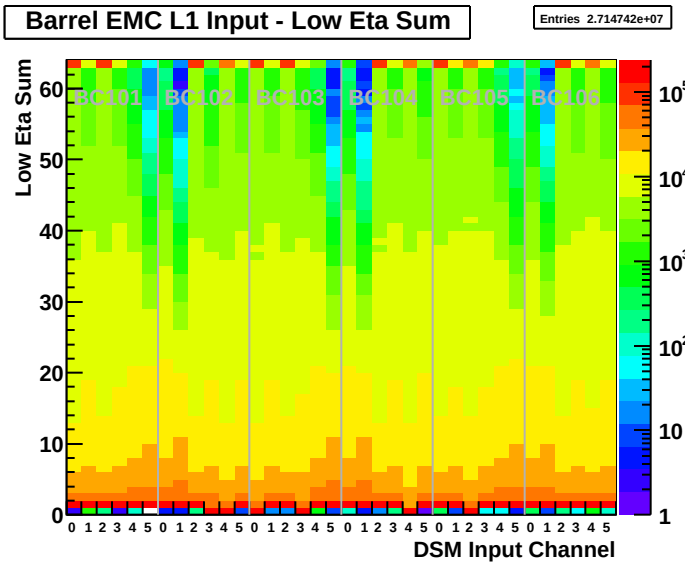
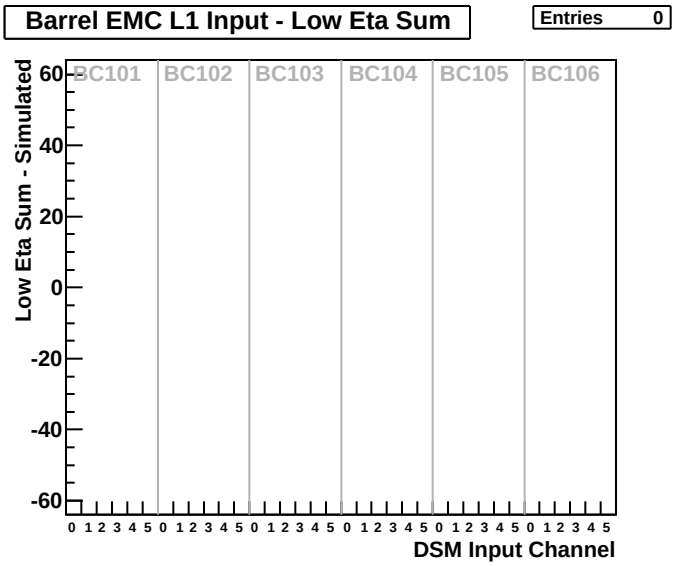
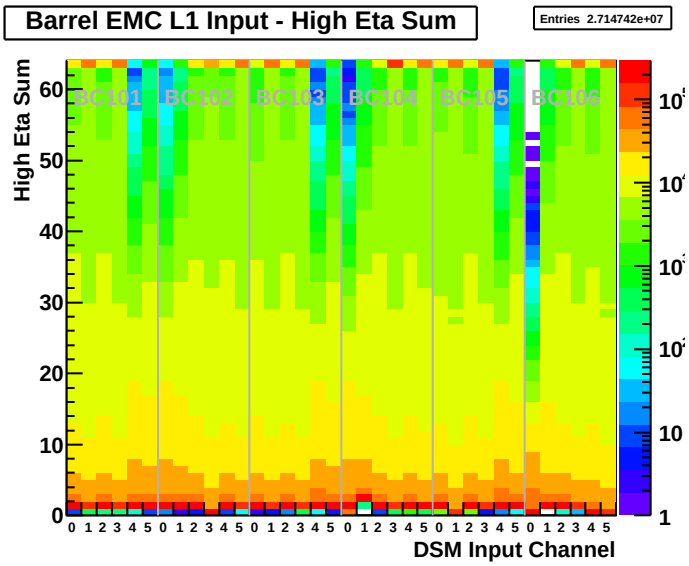
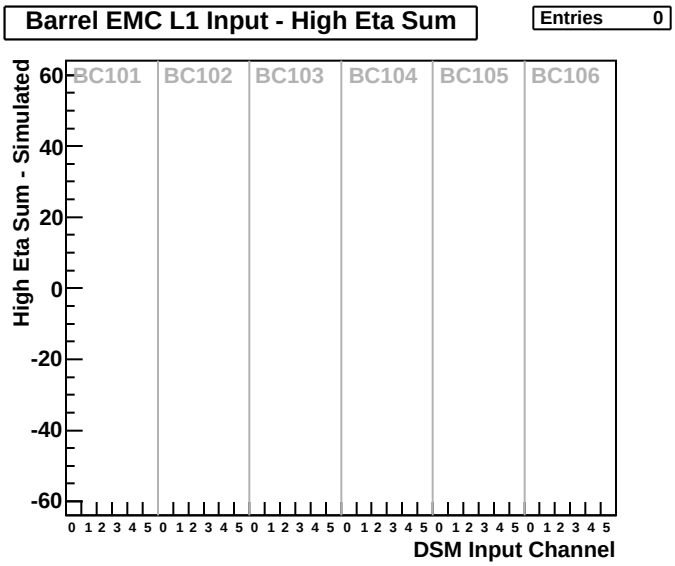
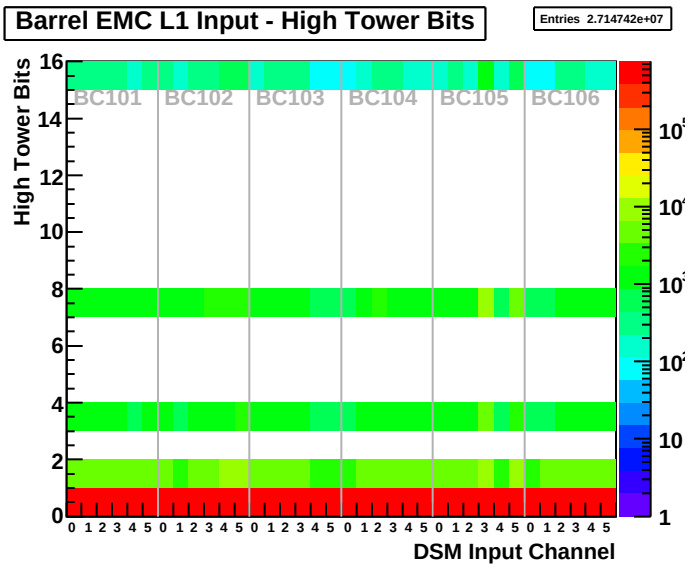
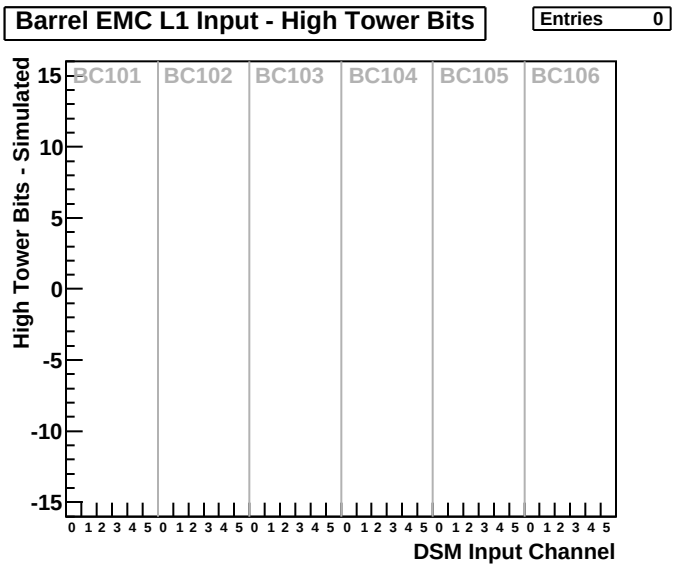

Barrel EMC L1 Input - Low Eta Sum
Entries 2.714742e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

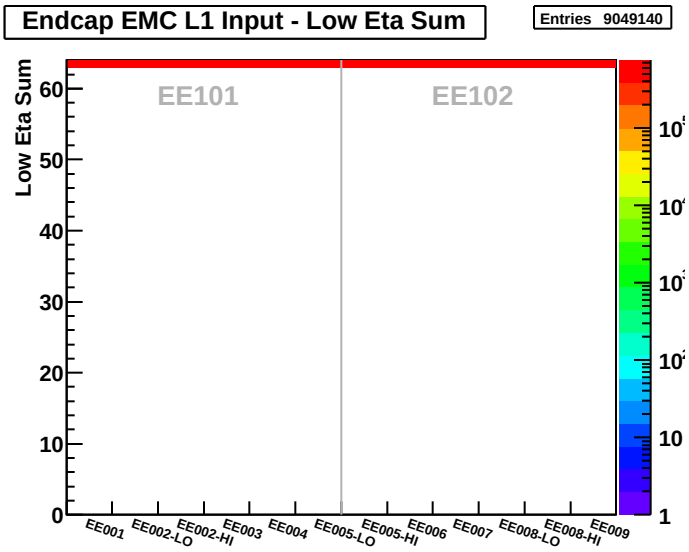

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

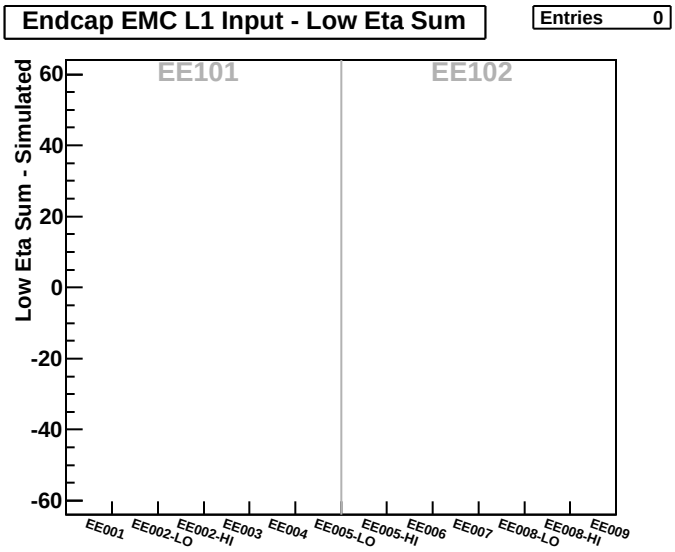

Barrel EMC L1 Input - High Eta Sum
Entries 2.714742e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

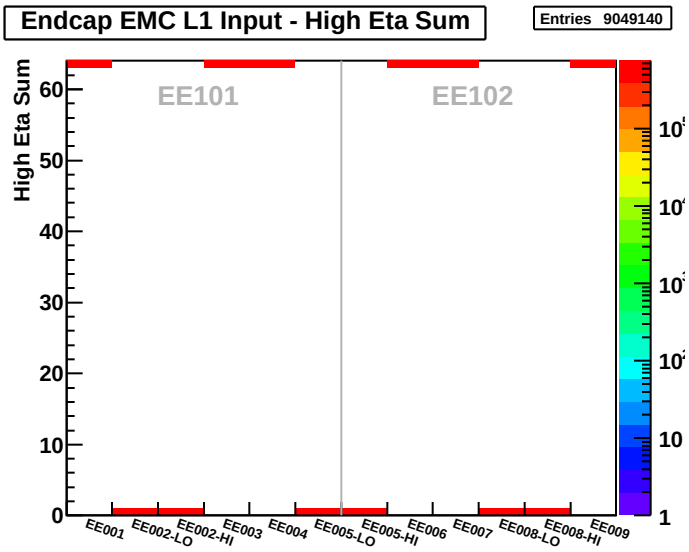

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

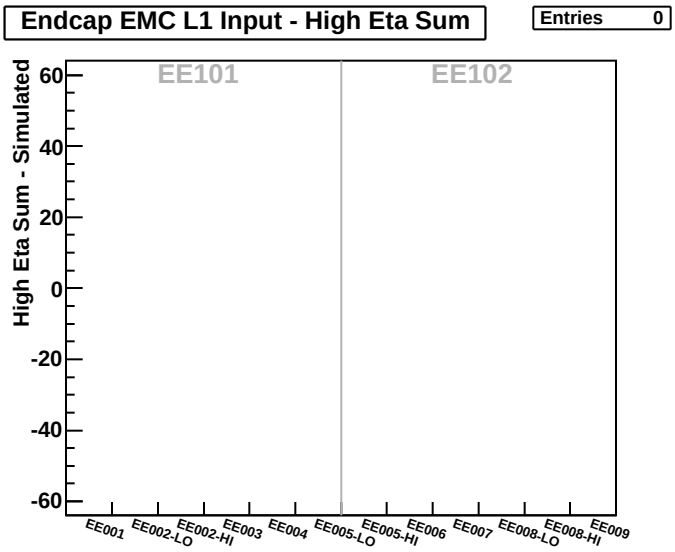

Barrel EMC L1 Input - High Tower Bits
Entries 2.714742e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

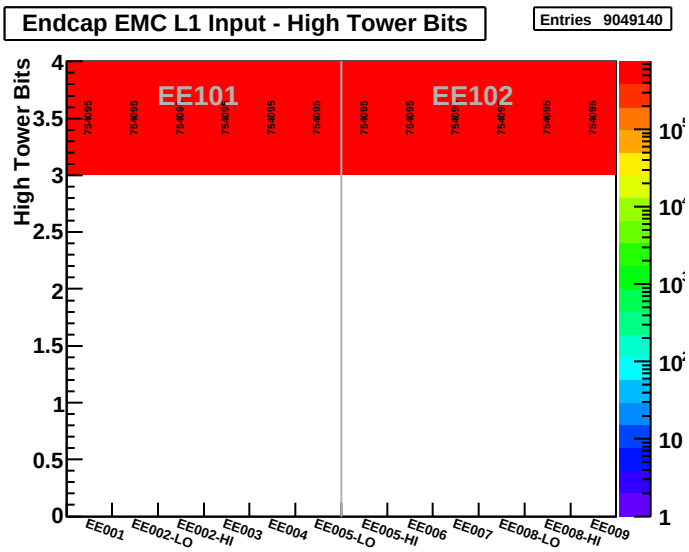

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

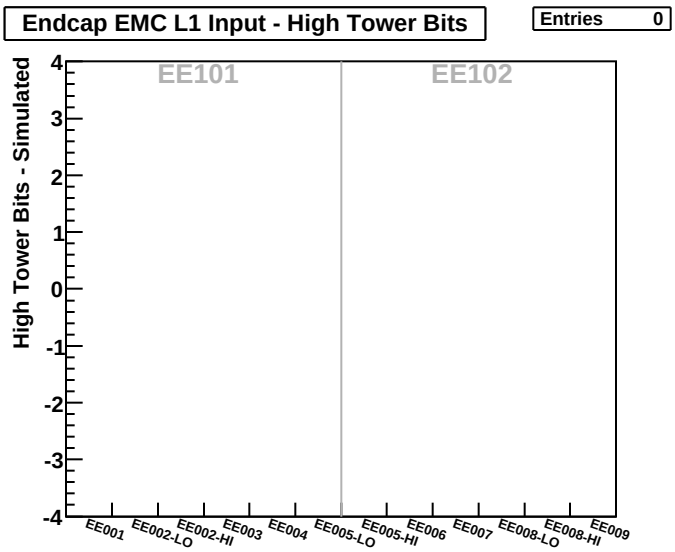

Endcap EMC L1 Input - Low Eta Sum
Entries 9049140
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

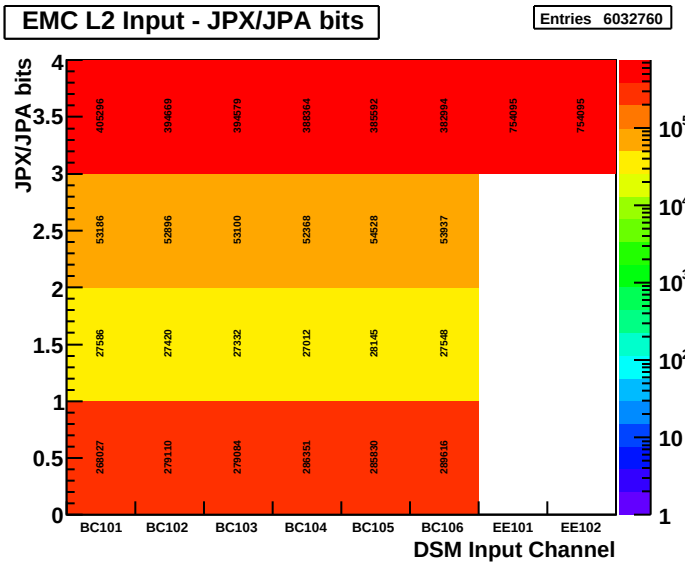

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

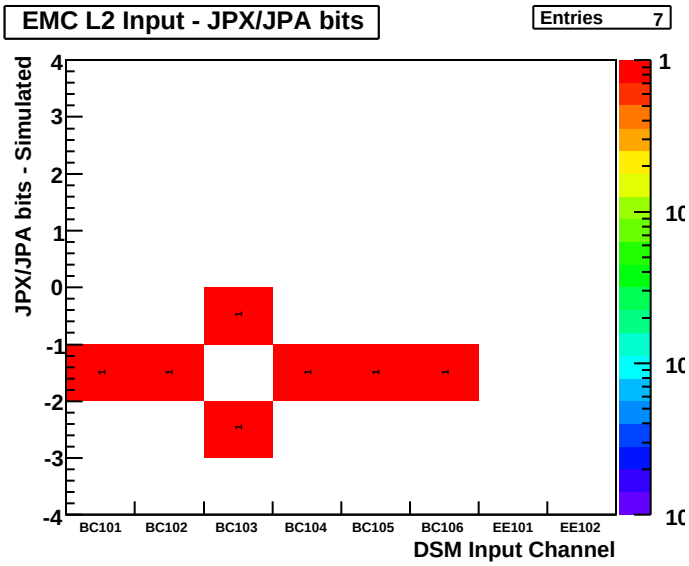

Endcap EMC L1 Input - High Eta Sum
Entries 9049140
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

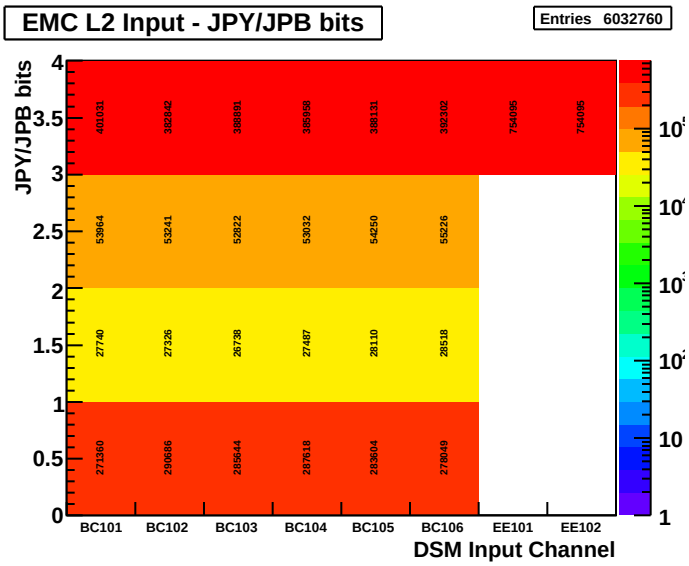

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

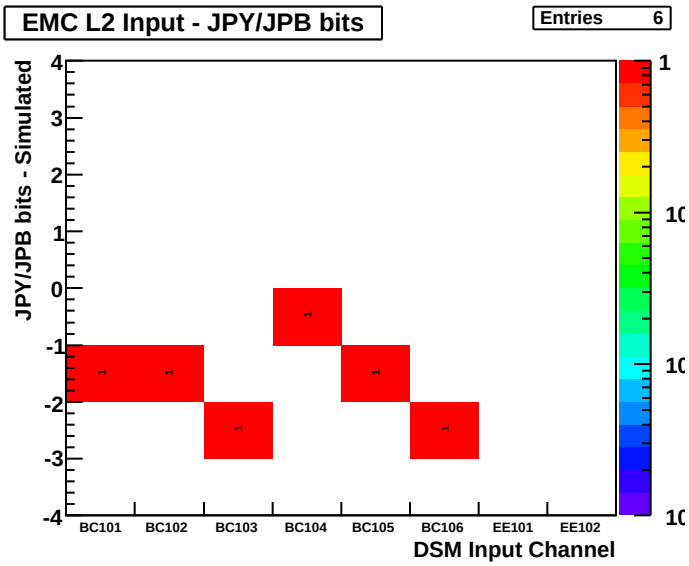

Endcap EMC L1 Input - High Tower Bits
Entries 9049140
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

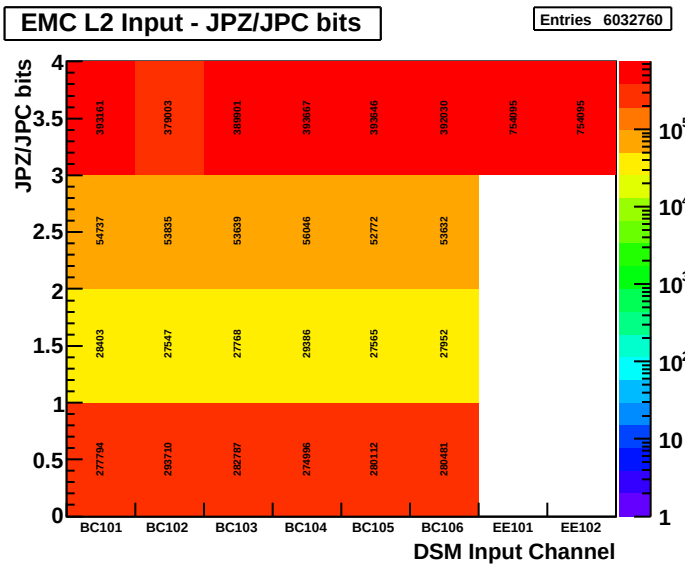

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

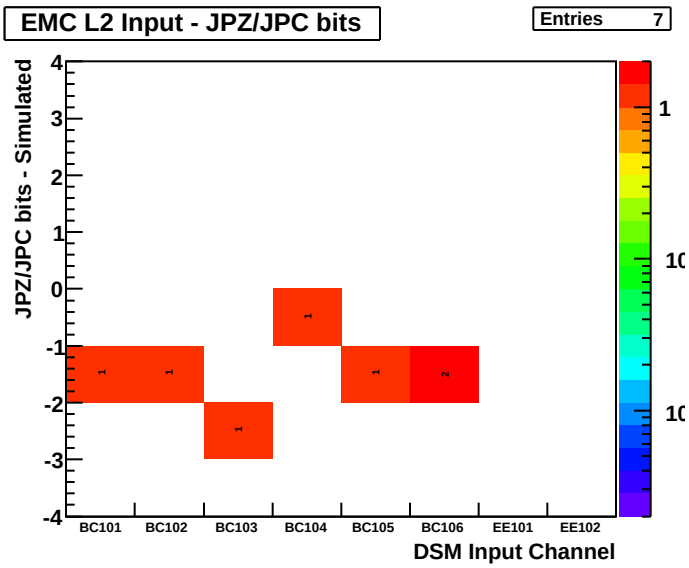

EMC L2 Input - JPX/JPA bits
Entries 6032760
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
405296 394669 394679 388364 385592 382994 754095 754095
53186 52896 53100 52268 54528 53937
27586 27420 27332 27012 28145 27548
268027 279110 279084 286351 285830 289616

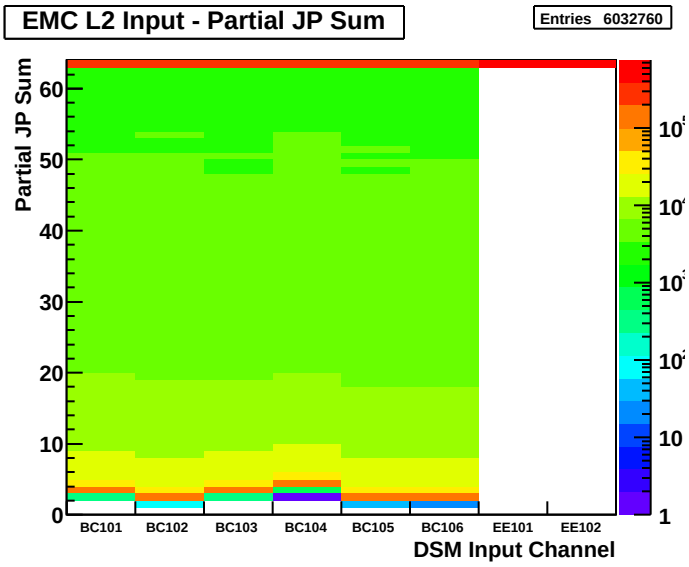

EMC L2 Input - JPX/JPA bits
Entries 7
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

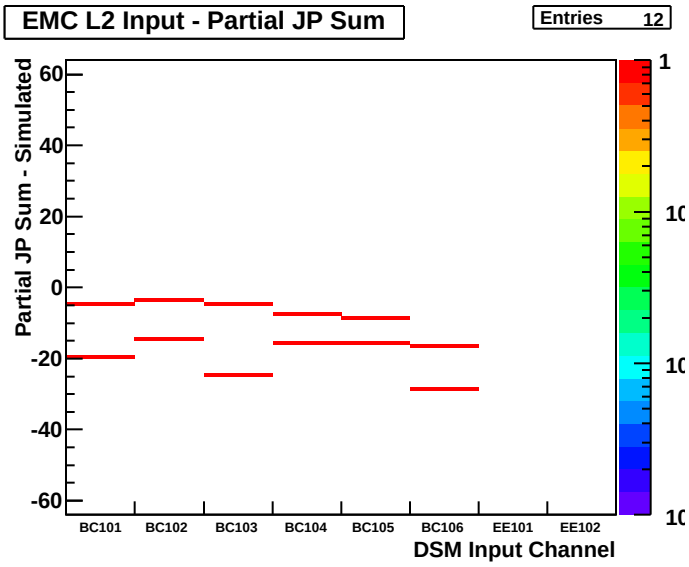

EMC L2 Input - JPY/JPB bits
Entries 6032760
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
401031 382842 388831 385958 388131 392302 754095 754095
53964 53241 52822 53032 54250 53226
27740 27326 26738 27487 28110 28518
271360 290686 285644 287618 283604 278049

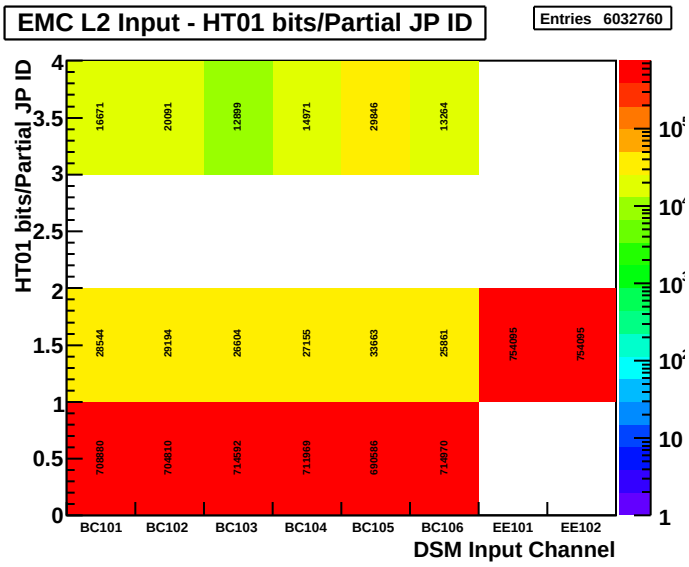

EMC L2 Input - JPY/JPB bits
Entries 6
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

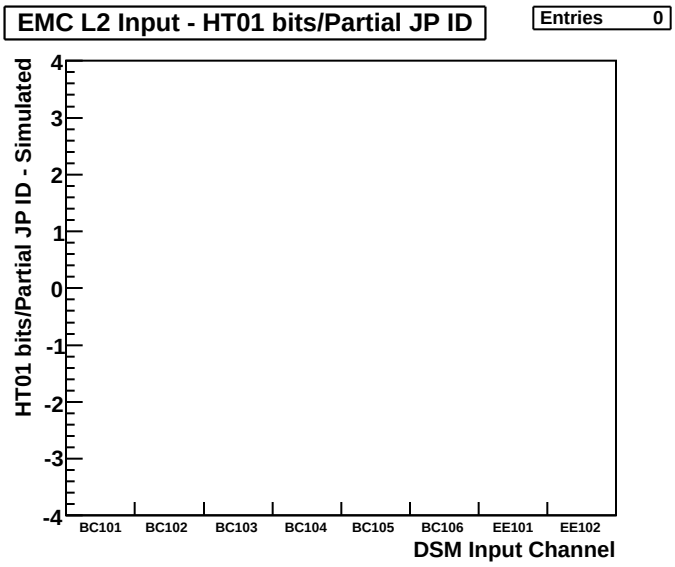

EMC L2 Input - JPZ/JPC bits
Entries 6032760
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
393161 379003 388001 393867 393646 392030 754095 754095
54737 53835 53639 56046 52772 53632
28403 27547 27768 29386 27565 27952
277794 293710 282787 274896 280112 280481

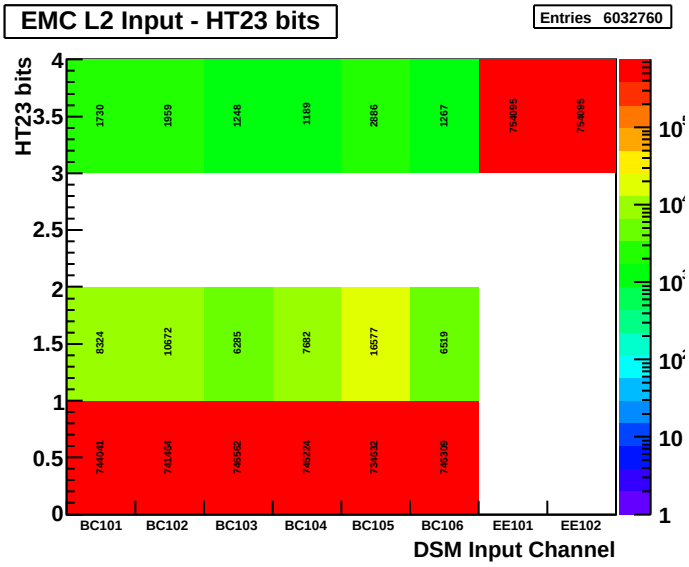

EMC L2 Input - JPZ/JPC bits
Entries 7
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

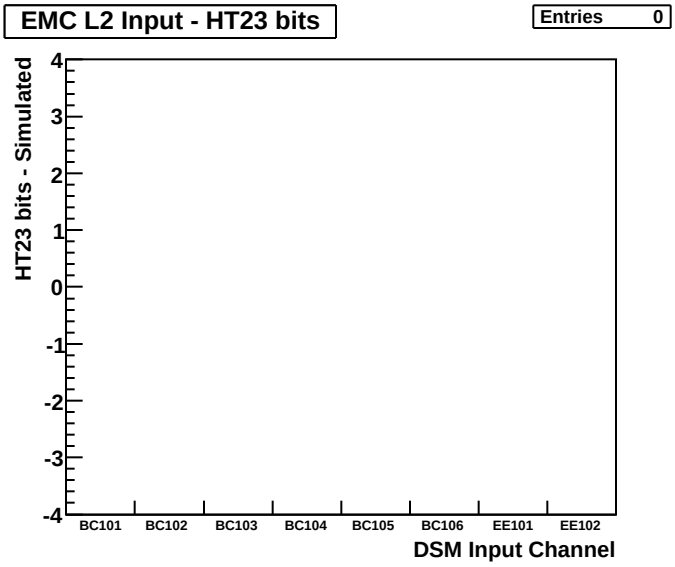

EMC L2 Input - Partial JP Sum
Entries 6032760
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

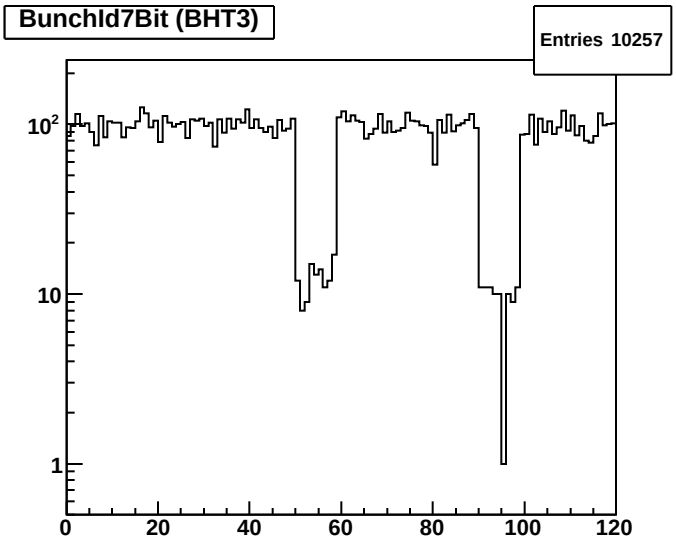

EMC L2 Input - Partial JP Sum
Entries 12
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

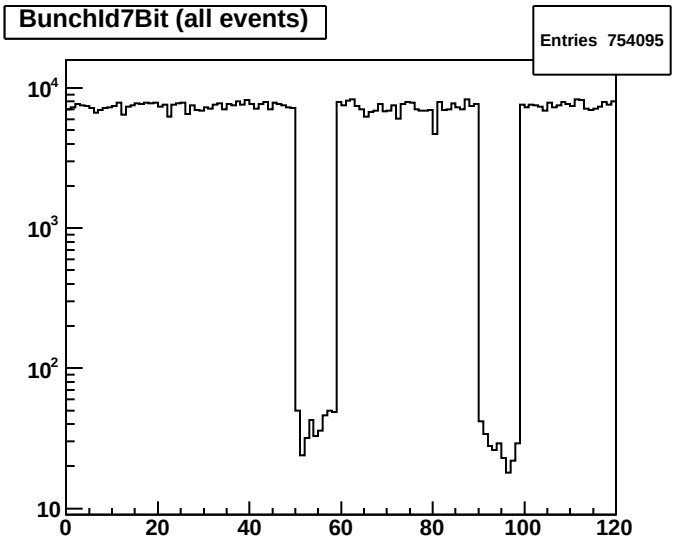

EMC L2 Input - HT01 bits/Partial JP ID
Entries 6032760
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

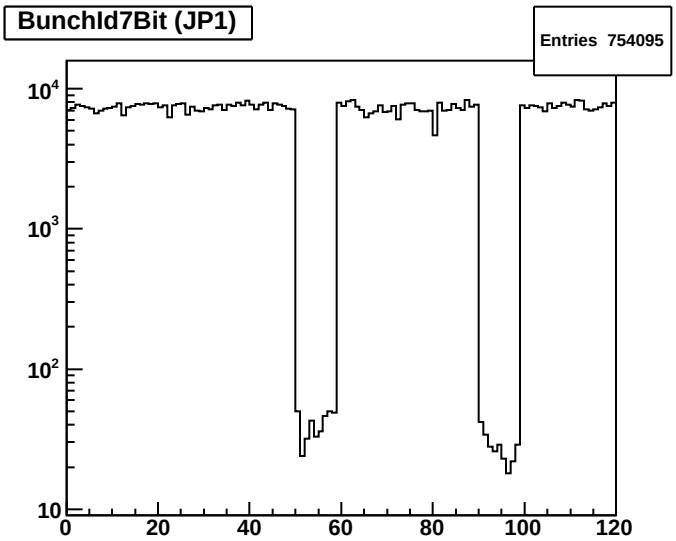

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

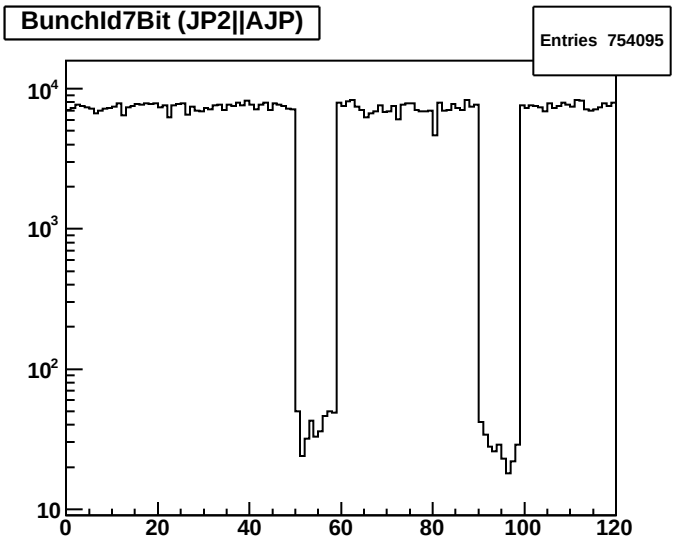

EMC L2 Input - HT23 bits
Entries 6032760
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

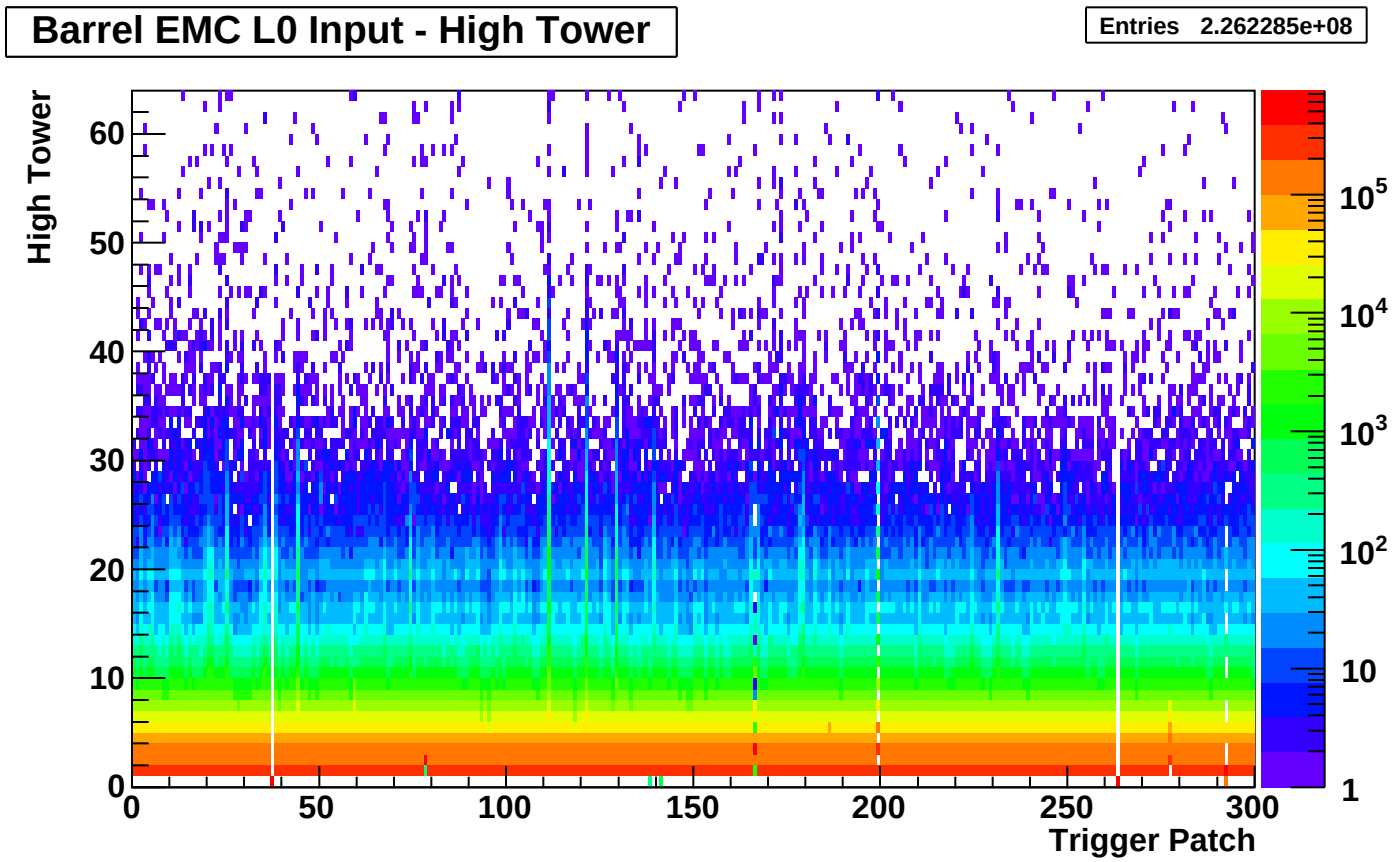

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

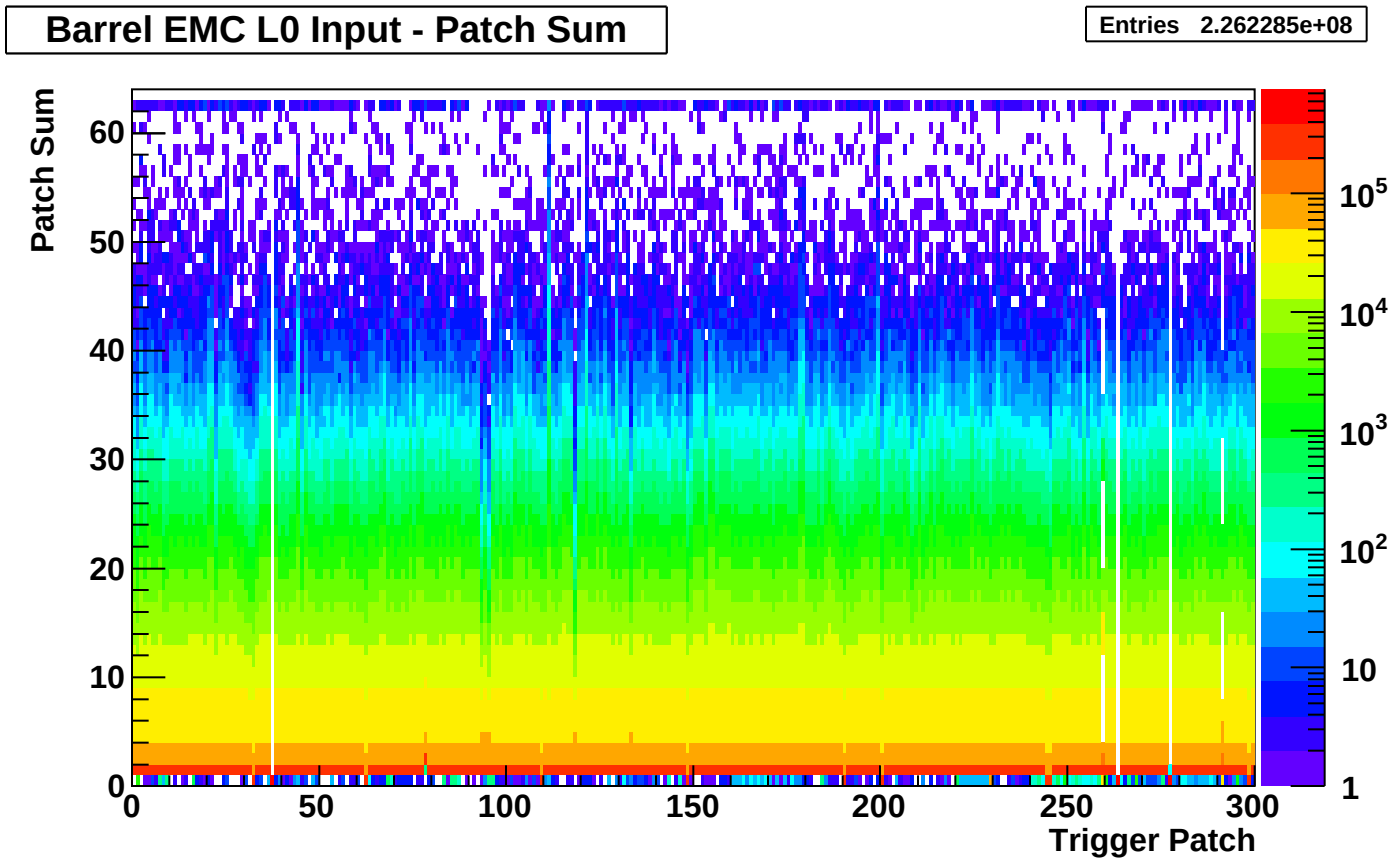

BunchId7Bit (BHT3)
Entries 10257
10^2
10
1
0
20
40
60
80
100
120

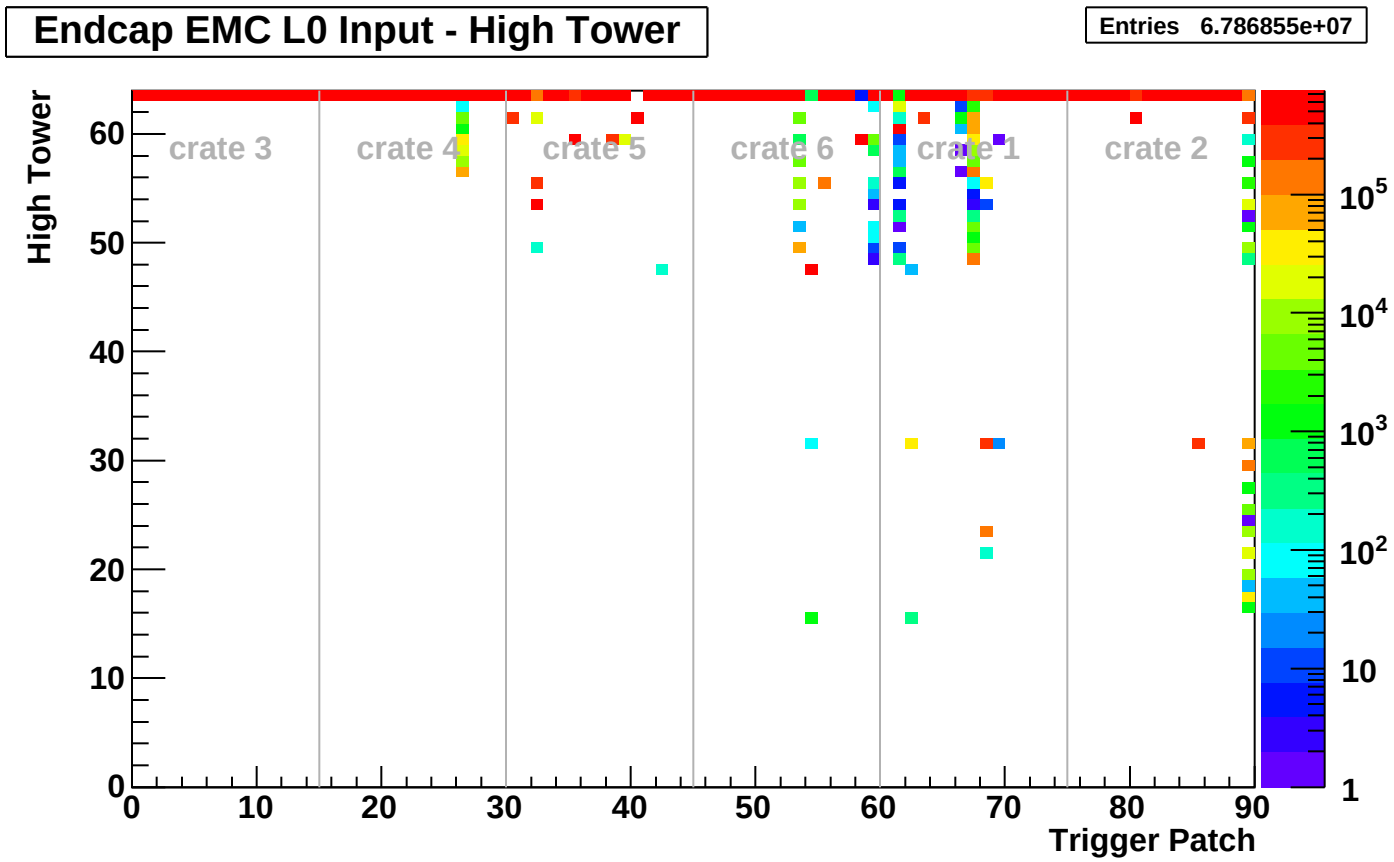

BunchId7Bit (all events)
Entries 754095
10^4
10^3
10^2
10
0
20
40
60
80
100
120

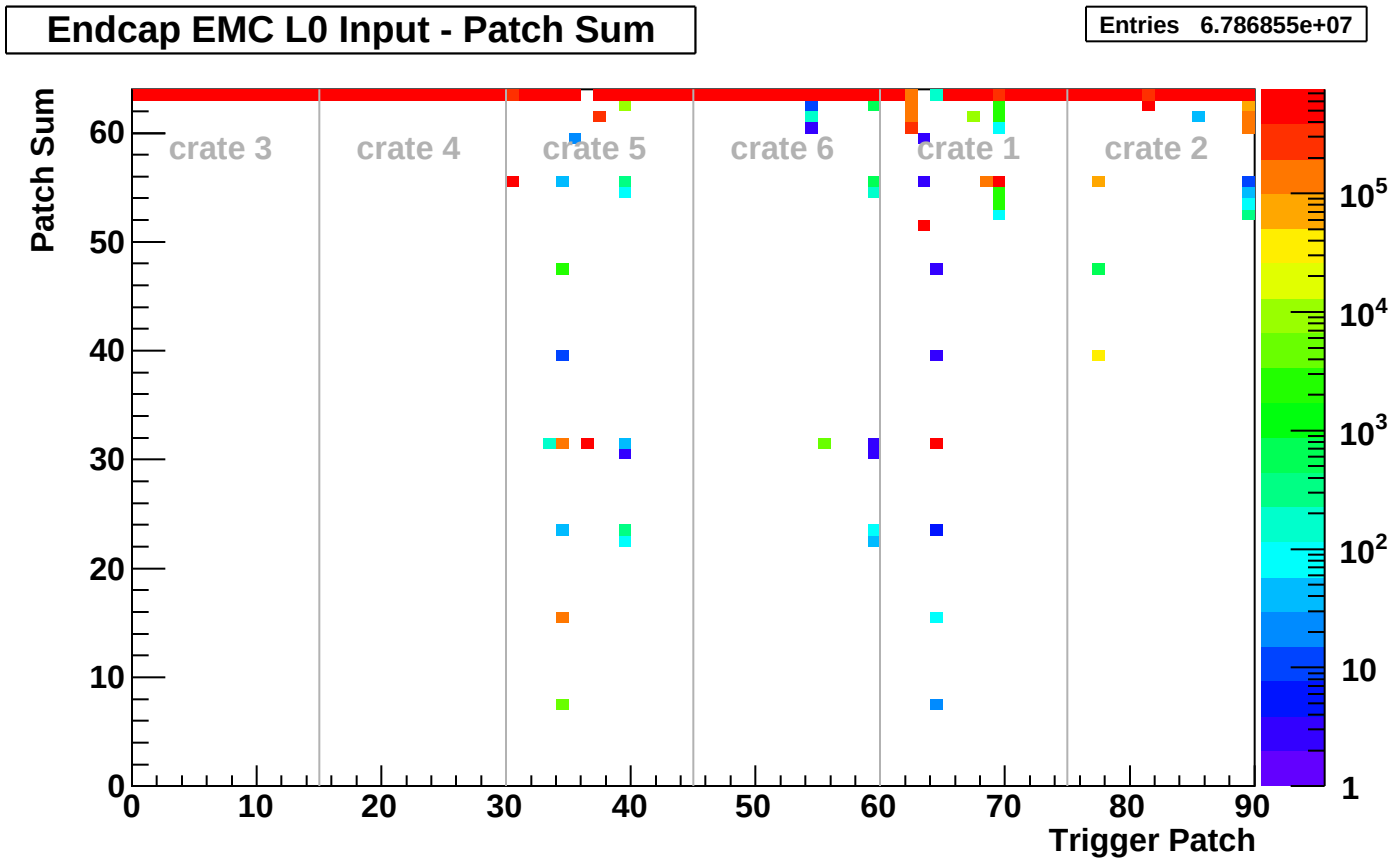

BunchId7Bit (JP1)
Entries 754095
10^4
10^3
10^2
10
0
20
40
60
80
100
120

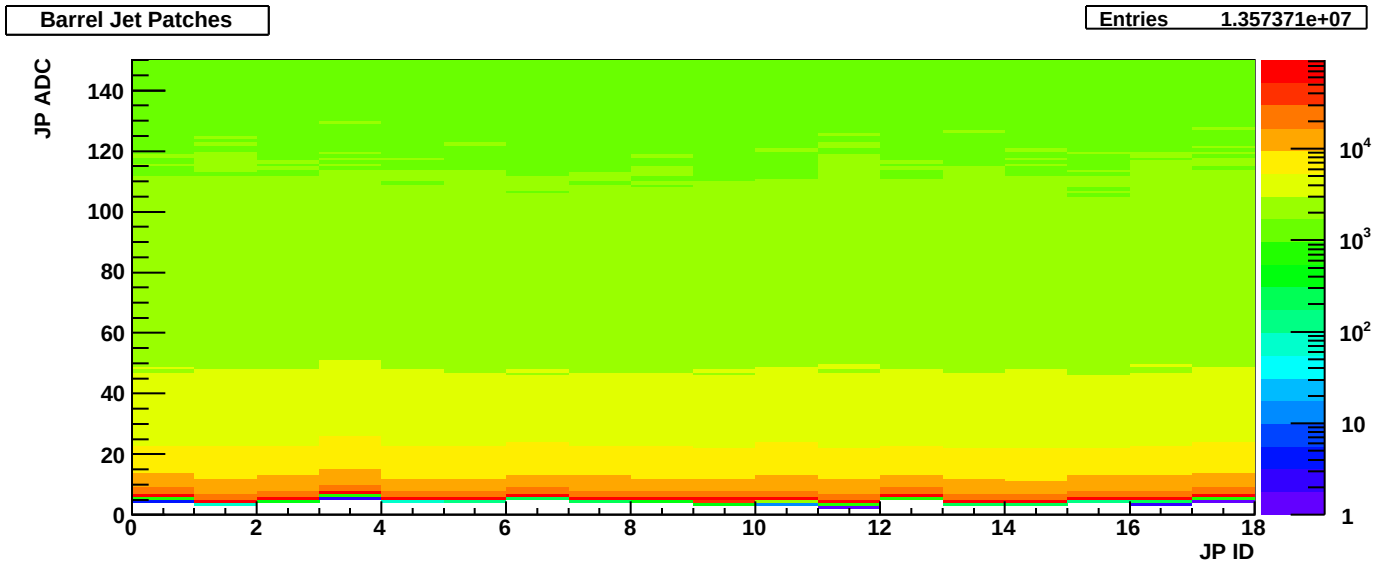

BunchId7Bit (JP2||AJP)
Entries 754095
10^4
10^3
10^2
10
0
20
40
60
80
100
120

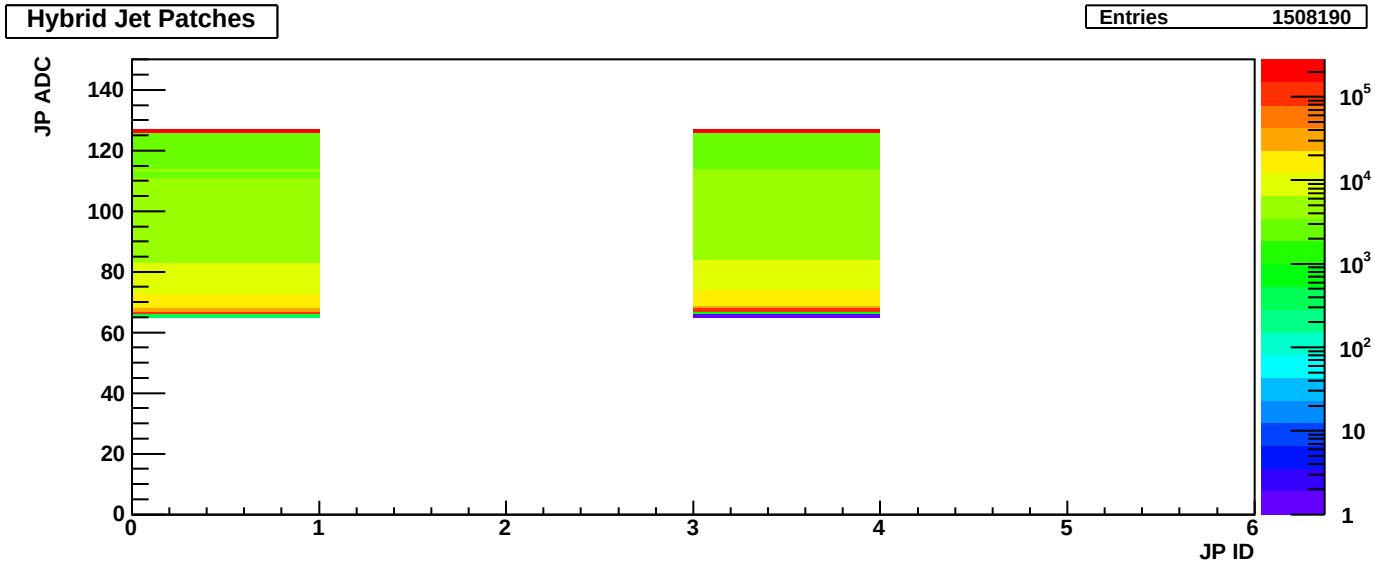

Barrel EMC L0 Input - High Tower
Entries 2.262285e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

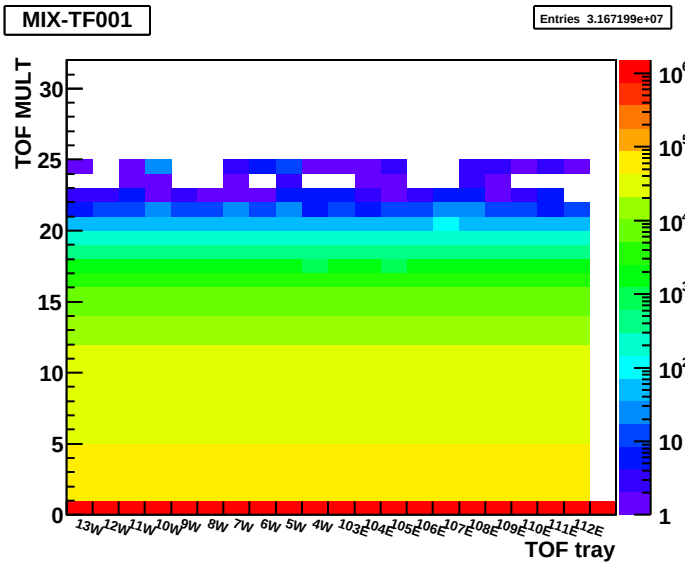

Barrel EMC L0 Input - Patch Sum
Entries 2.262285e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

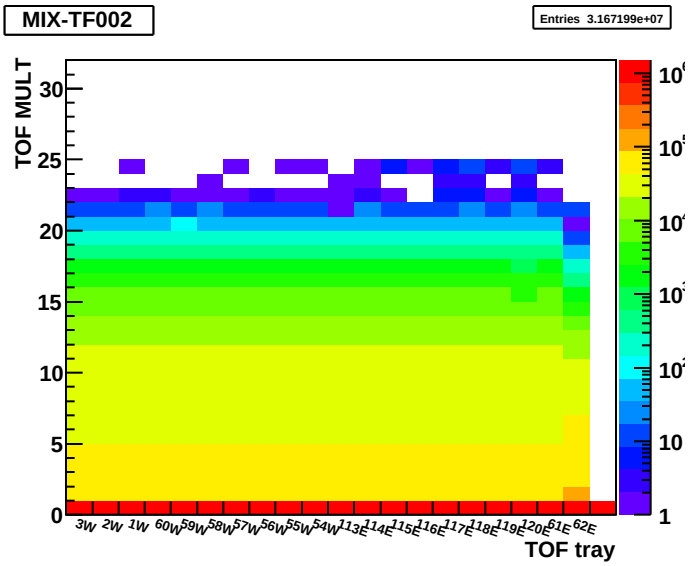

Endcap EMC L0 Input - High Tower
Entries 6.786855e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

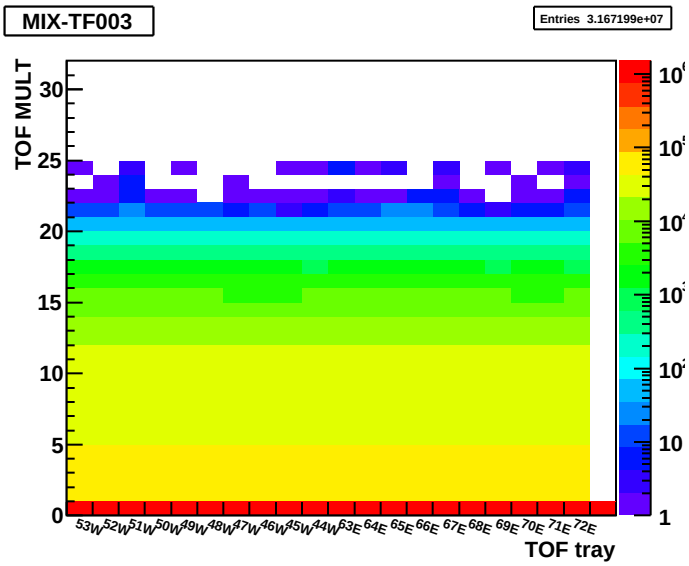

Endcap EMC L0 Input - Patch Sum
Entries 6.786855e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

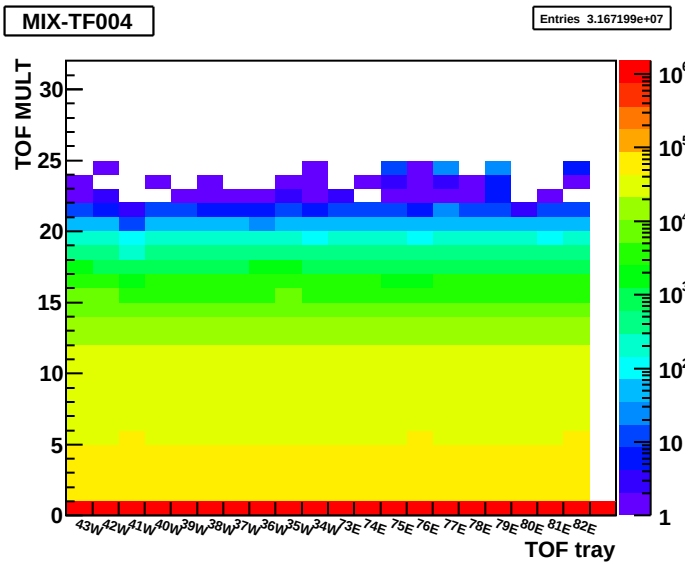

Barrel Jet Patches
Entries 1.357371e+07
JP ADC
JP ID

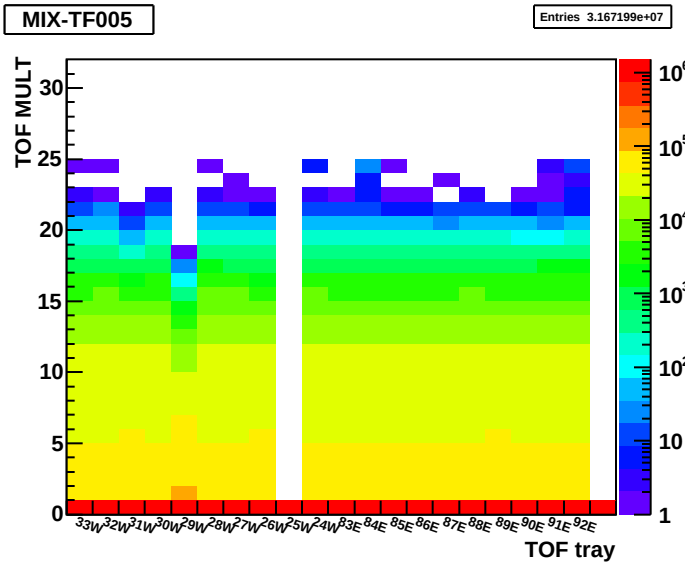

Hybrid Jet Patches
Entries 1508190
JP ADC
JP ID

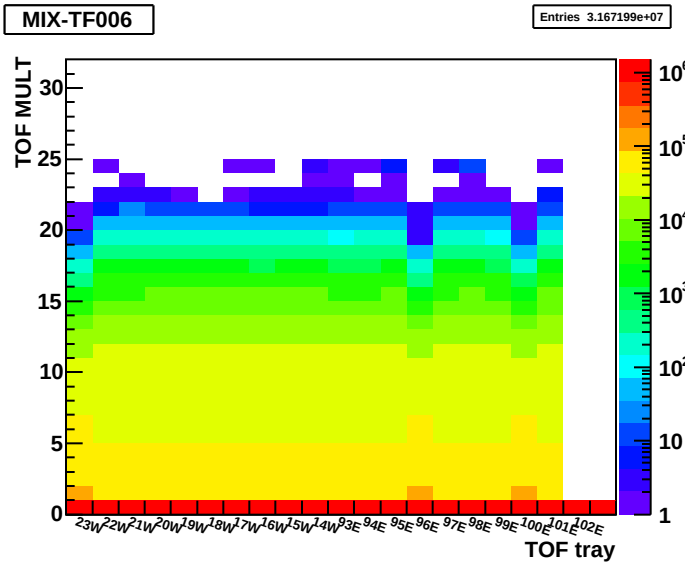

MIX-TF001
Entries 3.167199e+07
TOF MULT
TOF tray

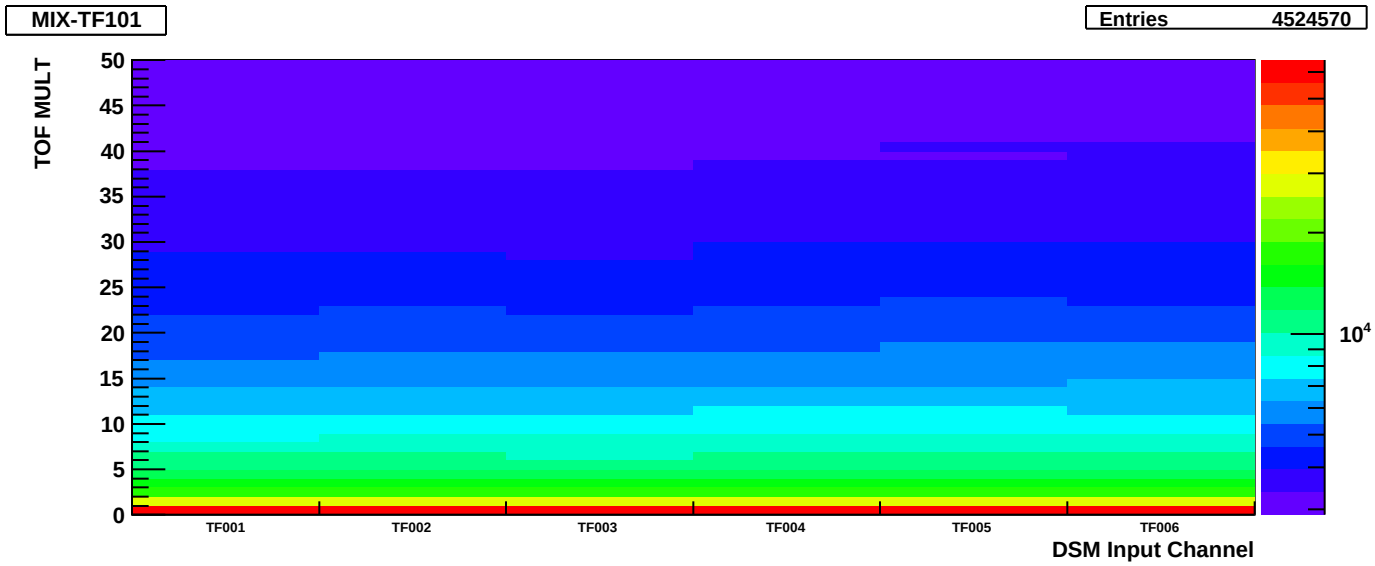

MIX-TF002
Entries 3.167199e+07
TOF MULT
TOF tray

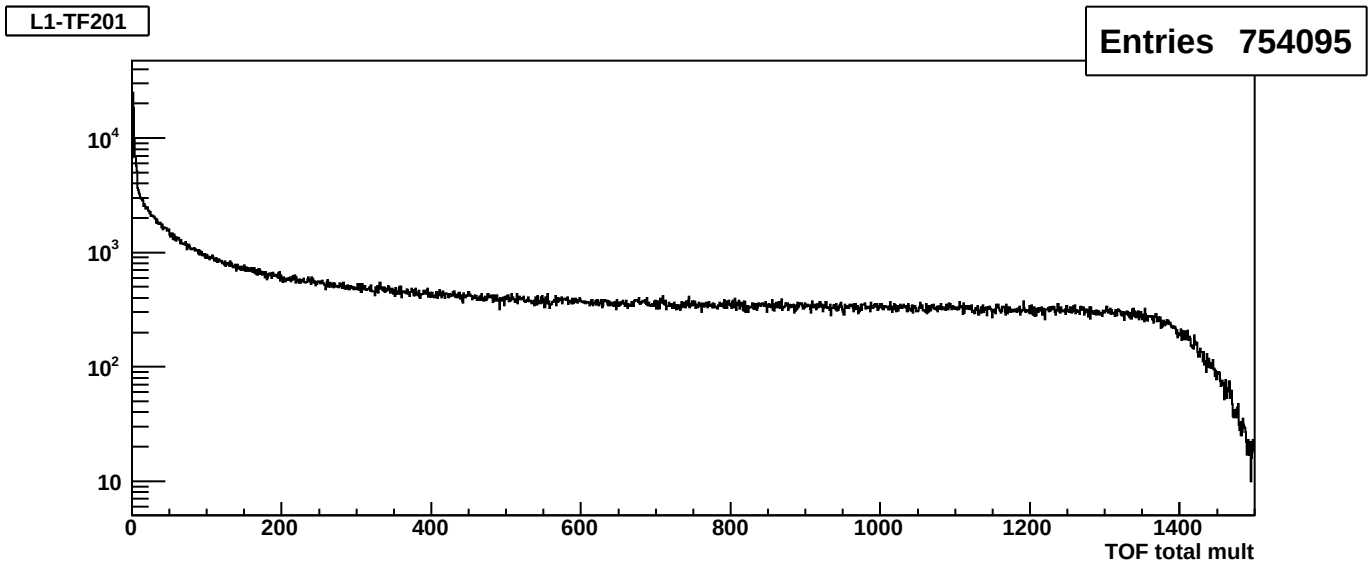

MIX-TF003
Entries 3.167199e+07
TOF MULT
TOF tray

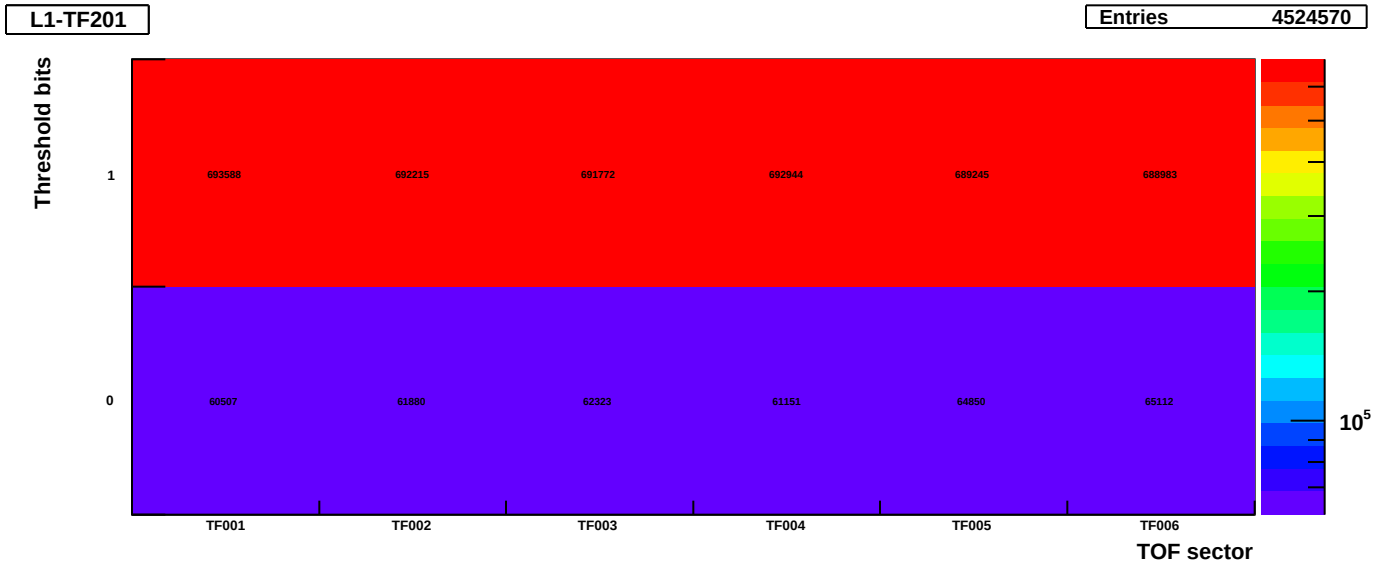

MIX-TF004
Entries 3.167199e+07
TOF MULT
TOF tray

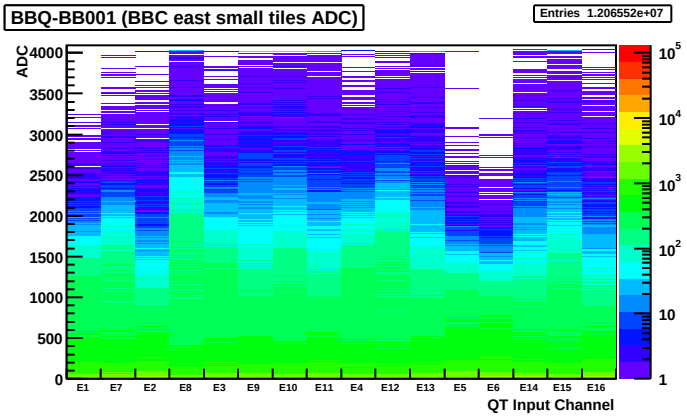

MIX-TF005
Entries 3.167199e+07
TOF MULT
TOF tray

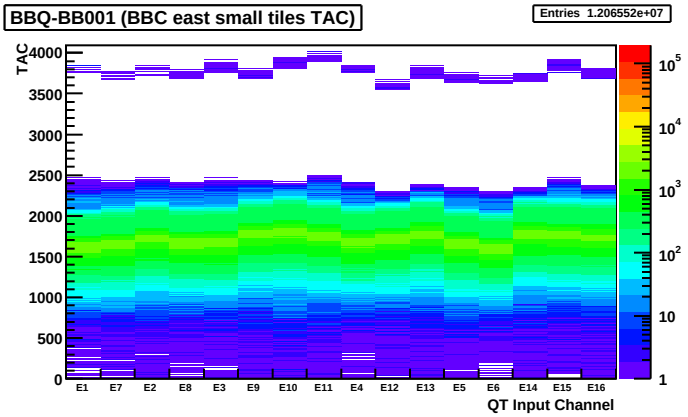

MIX-TF006
Entries 3.167199e+07
TOF MULT
TOF tray

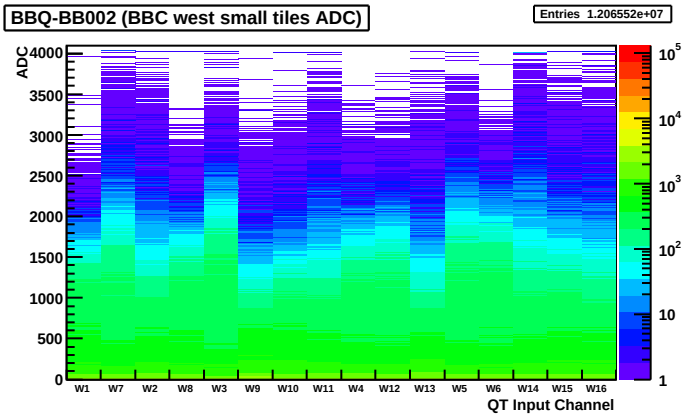

MIX-TF101
Entries 4524570
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

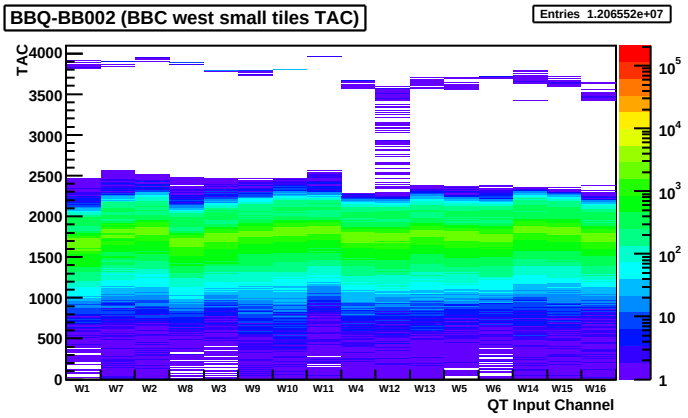

L1-TF201
Entries 754095
10^4
10^3
10^2
10
0
200
400
600
800
1000
1200
1400
TOF total mult

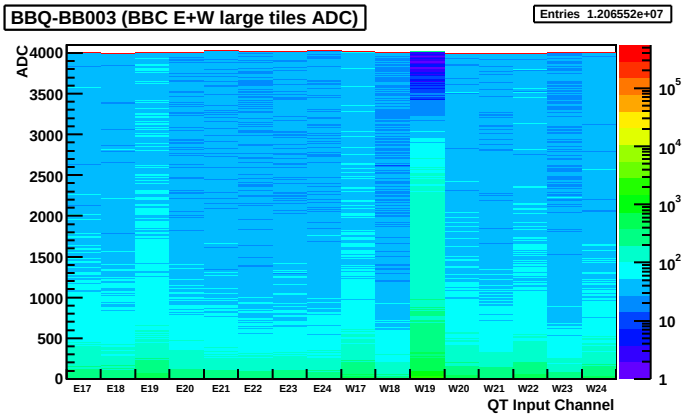

L1-TF201
Entries 4524570
Threshold bits
1
0
693588
692215
691772
692944
689245
688983
60507
61880
62323
61151
64850
65112
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

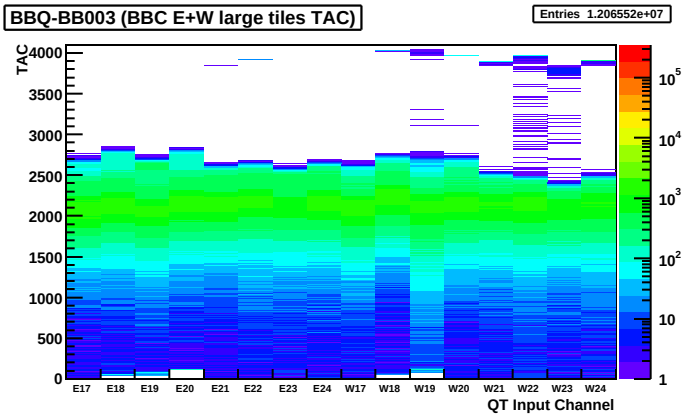

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.206552e+07
ADC
QT Input Channel

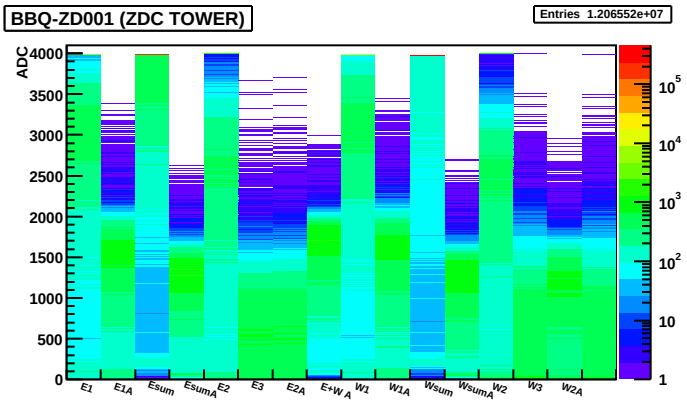

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.206552e+07
TAC
QT Input Channel

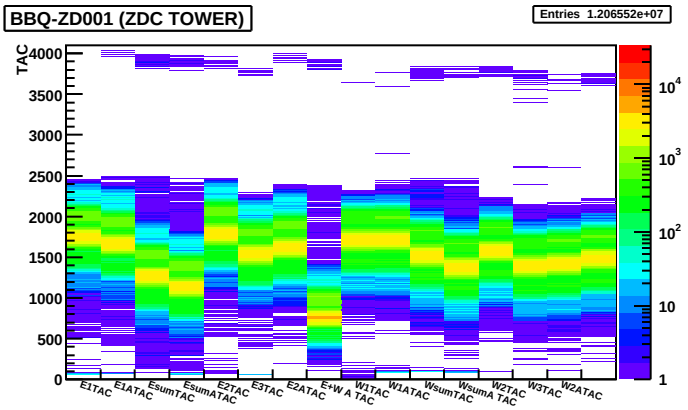

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.206552e+07
ADC
QT Input Channel

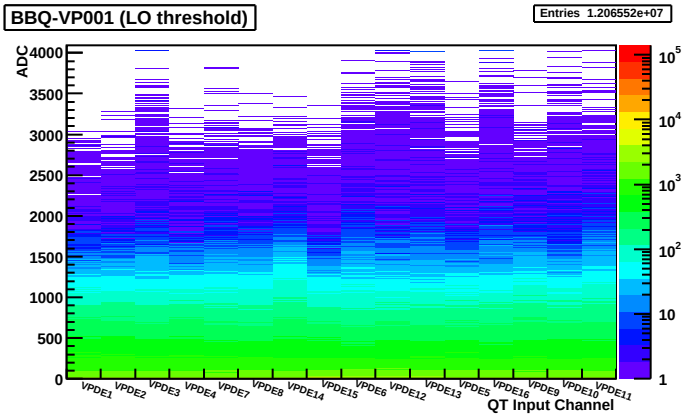

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.206552e+07
TAC
QT Input Channel

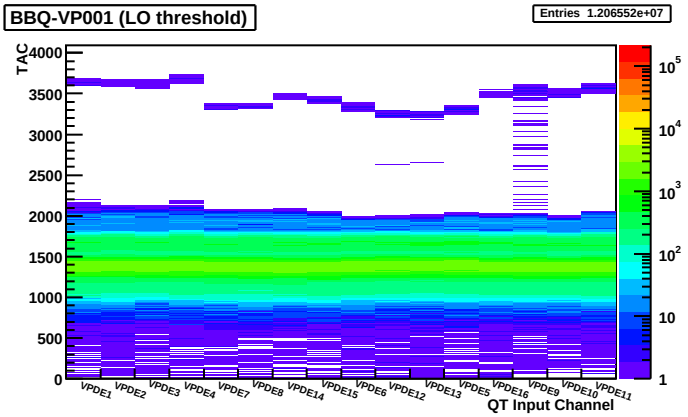

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.206552e+07
ADC
QT Input Channel

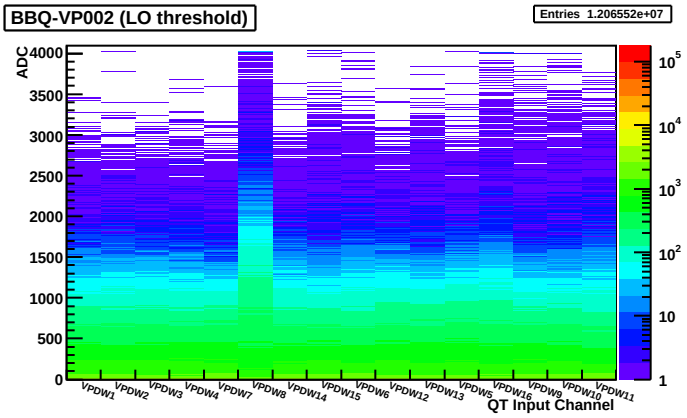

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.206552e+07
TAC
QT Input Channel

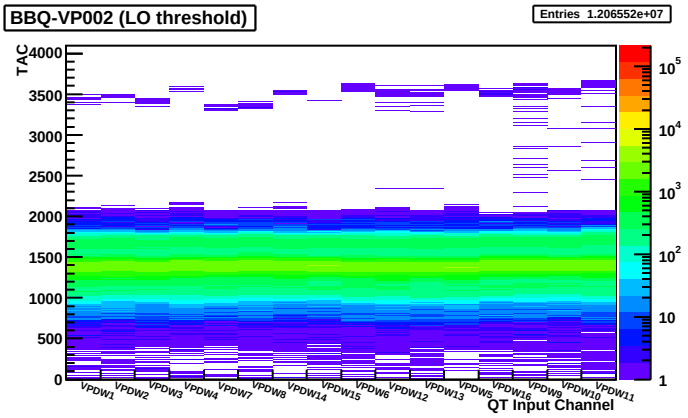

BBQ-ZD001 (ZDC TOWER)
Entries 1.206552e+07
ADC

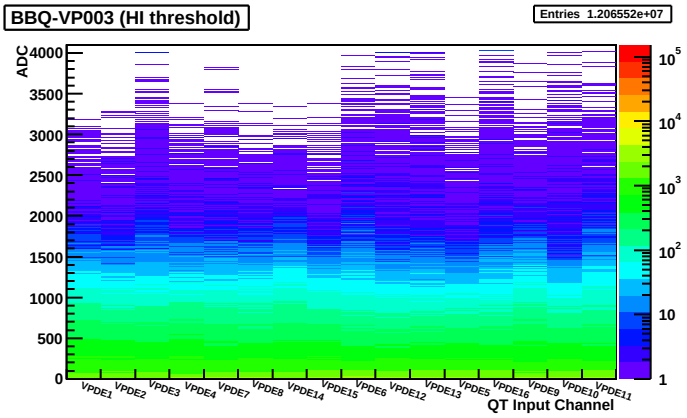

BBQ-ZD001 (ZDC TOWER)
Entries 1.206552e+07
TAC

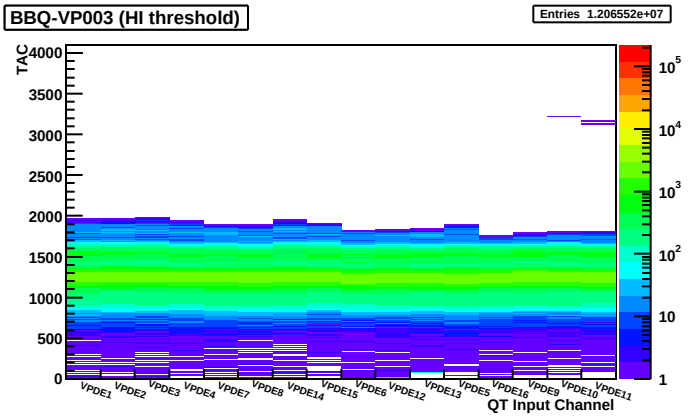

BBQ-VP001 (LO threshold)
Entries 1.206552e+07
ADC
QT Input Channel

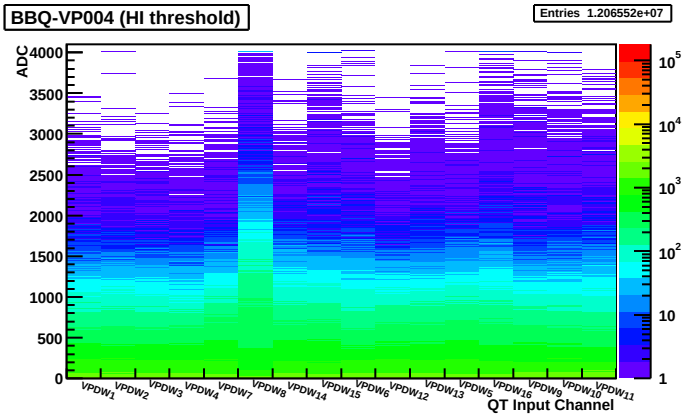

BBQ-VP001 (LO threshold)
Entries 1.206552e+07
TAC
QT Input Channel

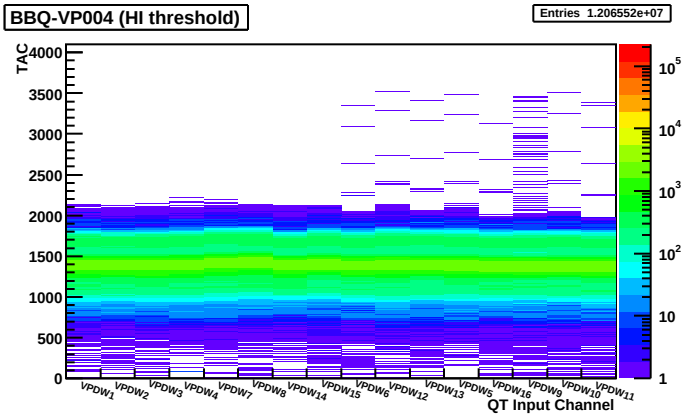

BBQ-VP002 (LO threshold)
Entries 1.206552e+07
ADC
QT Input Channel

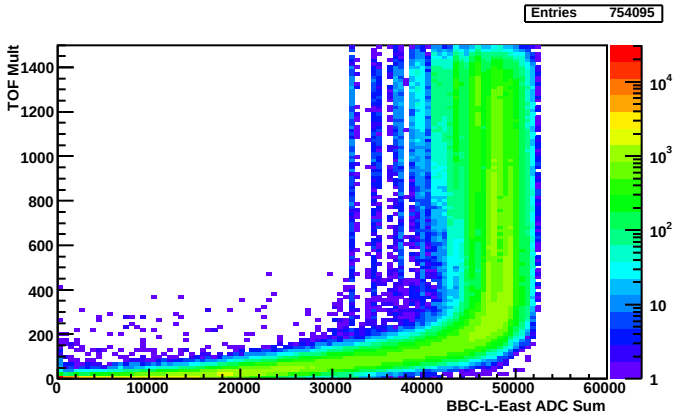

BBQ-VP002 (LO threshold)
Entries 1.206552e+07
TAC
QT Input Channel

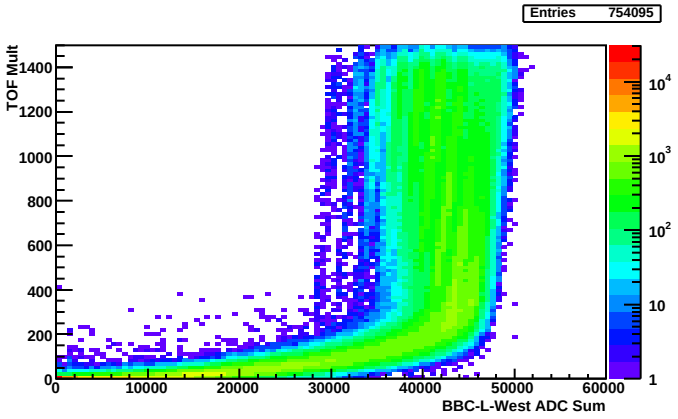

BBQ-VP003 (HI threshold)
Entries 1.206552e+07
ADC
QT Input Channel

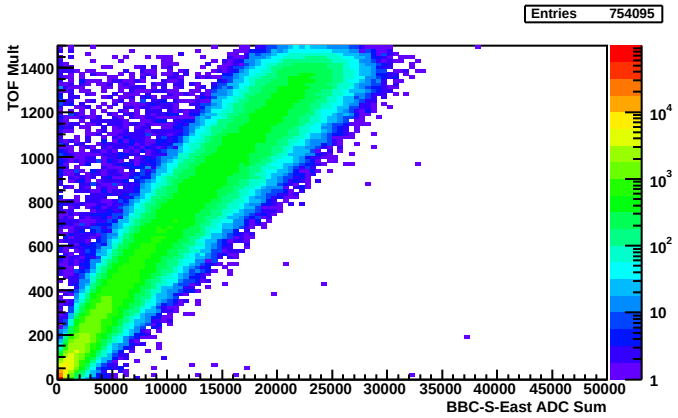

BBQ-VP003 (HI threshold)
Entries 1.206552e+07
TAC
QT Input Channel

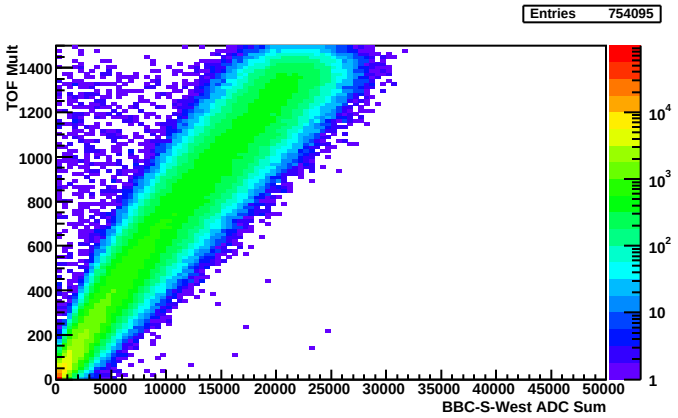

BBQ-VP004 (HI threshold)
Entries 1.206552e+07
ADC
QT Input Channel

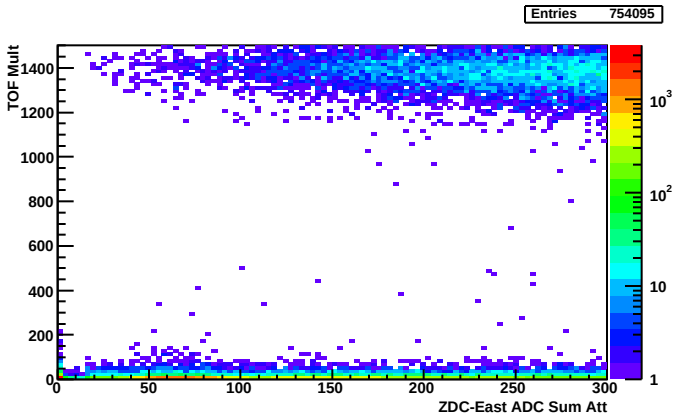

BBQ-VP004 (HI threshold)
Entries 1.206552e+07
TAC
QT Input Channel

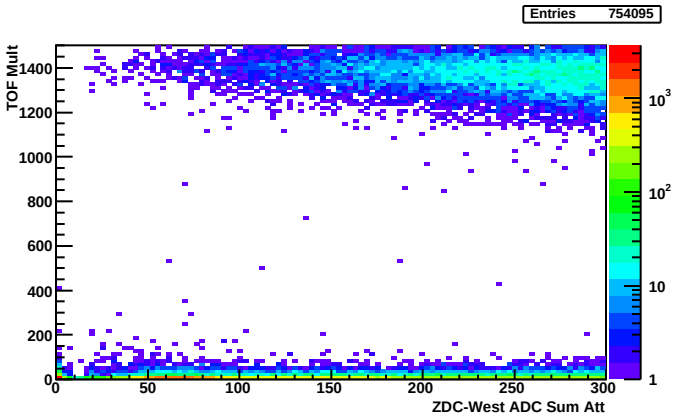

Entries 754095
TOF Mult
BBC-L-East ADC Sum

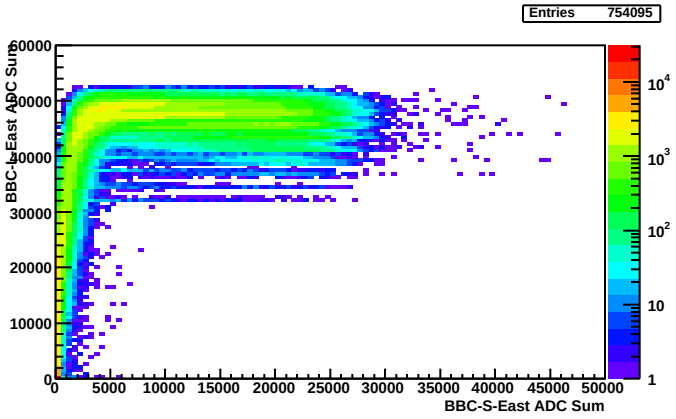

Entries 754095
TOF Mult
BBC-L-West ADC Sum

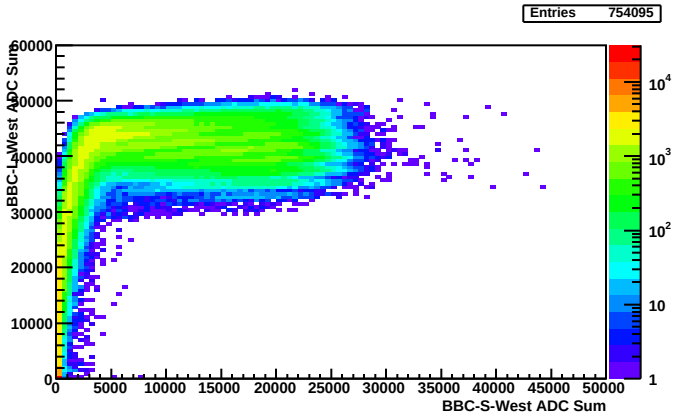

Entries 754095
TOF Mult
BBC-S-East ADC Sum

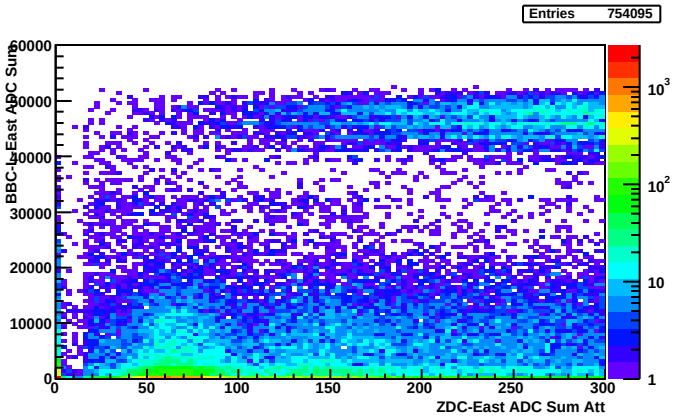

Entries 754095
TOF Mult
BBC-S-West ADC Sum

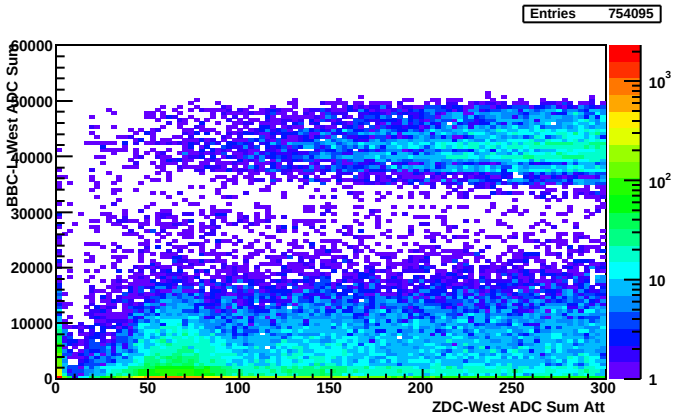

Entries 754095
TOF Mult
ZDC-East ADC Sum Att

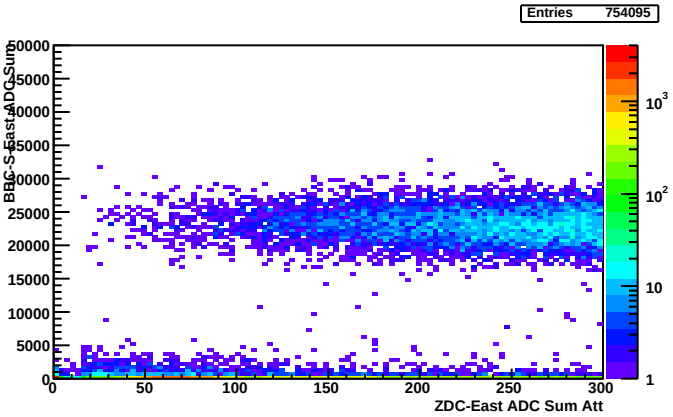

Entries 754095
TOF Mult
ZDC-West ADC Sum Att

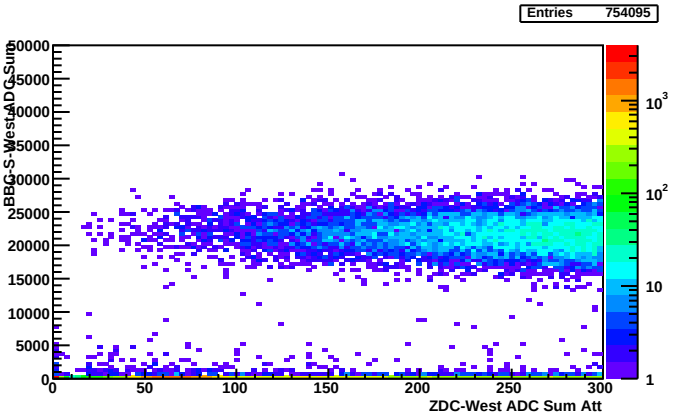

Entries 754095
BBC-L-East ADC Sum
BBC-S-East ADC Sum

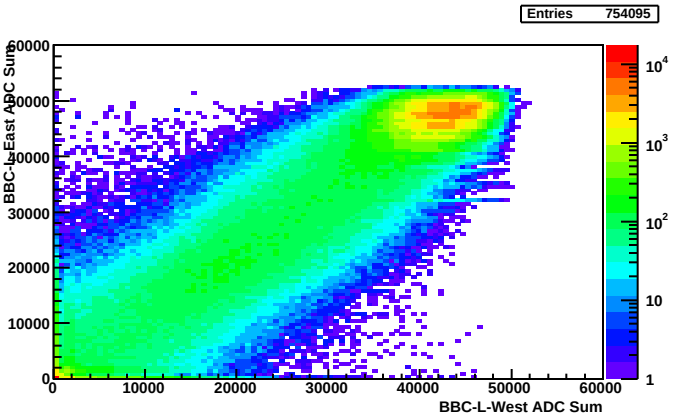

Entries 754095
BBC-L-West ADC Sum
BBC-S-West ADC Sum

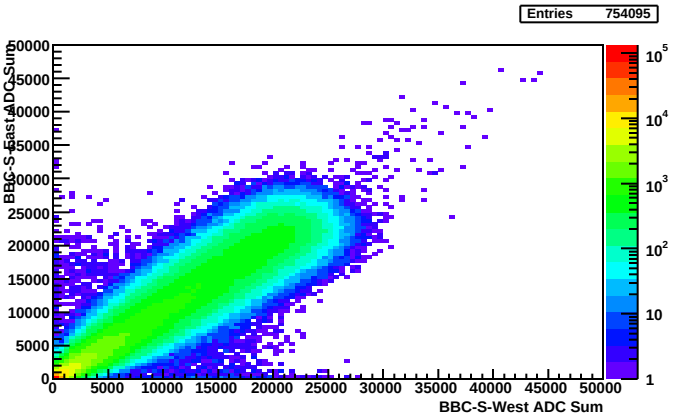

Entries 754095
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

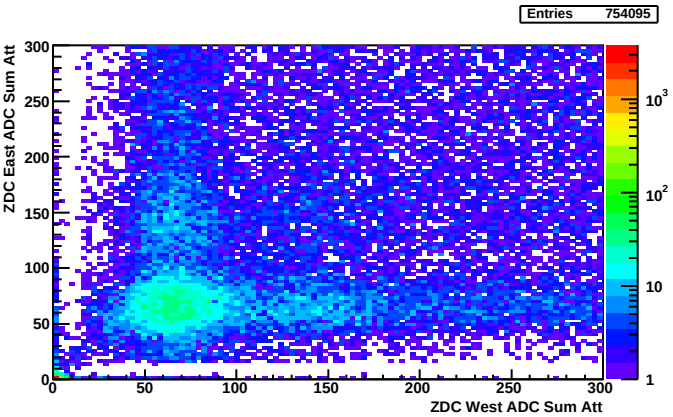

Entries 754095
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

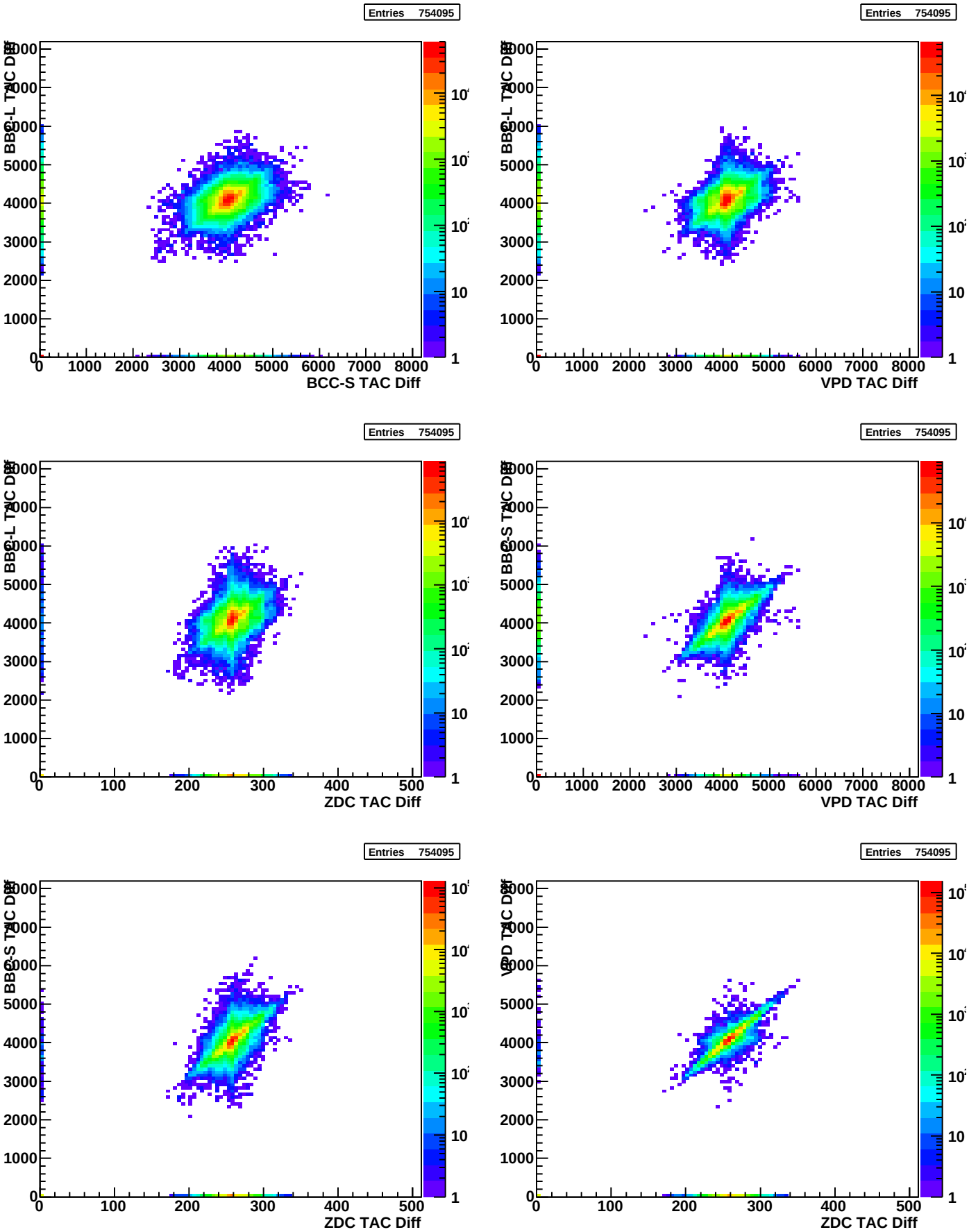

Entries 754095
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

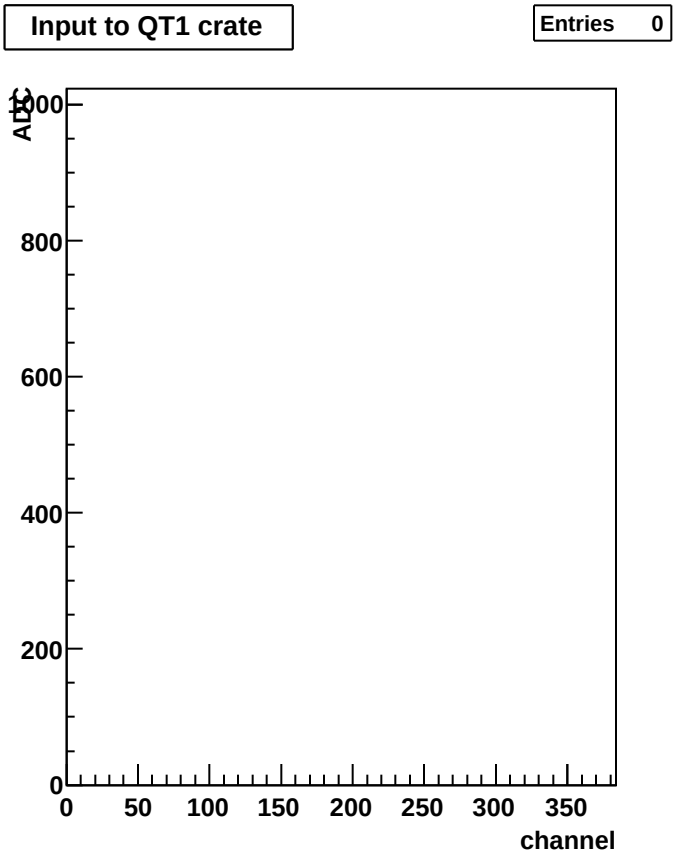

Entries 754095
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

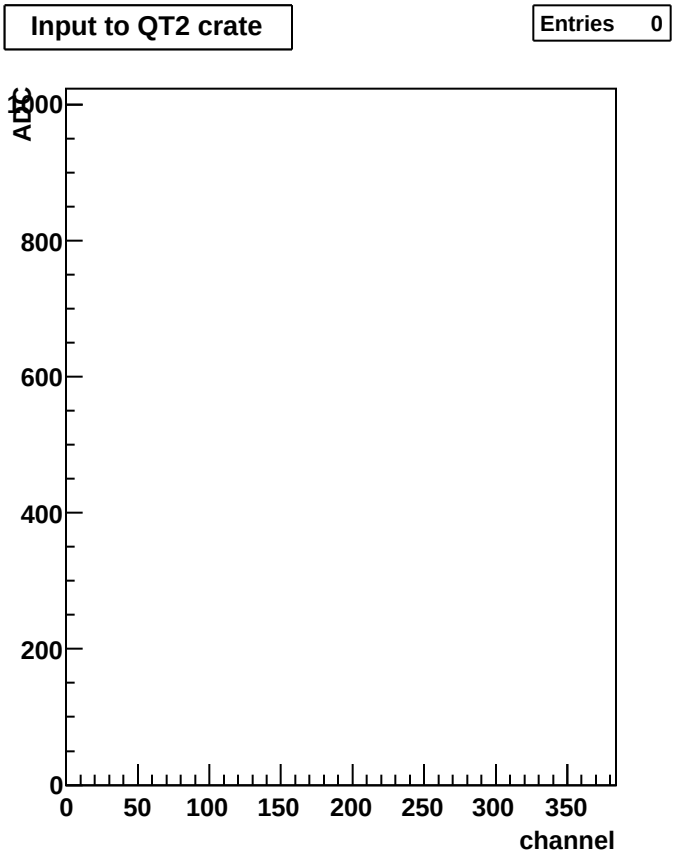

Entries 754095
BBC-L-East ADC Sum
BBC-L-West ADC Sum

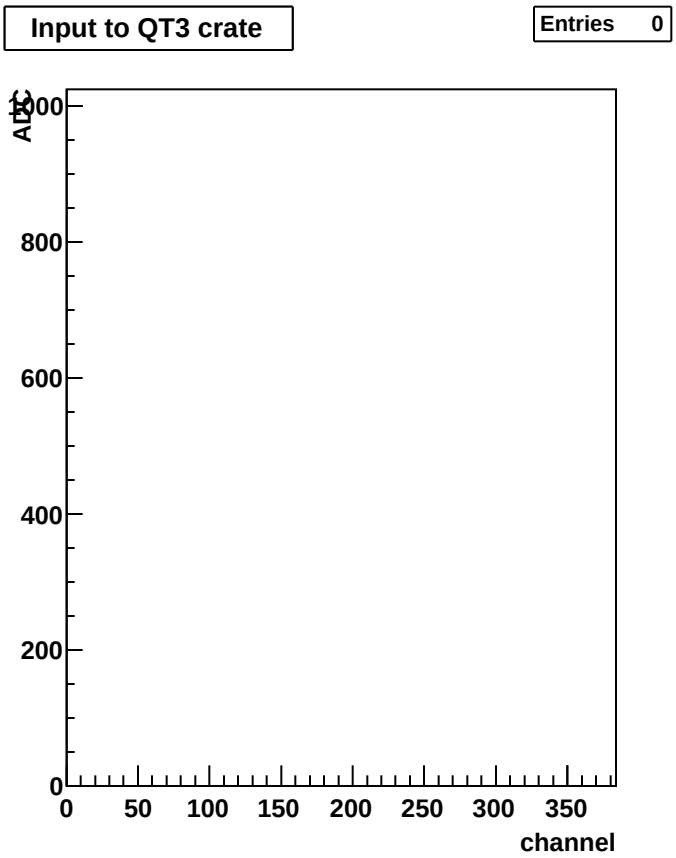

Entries 754095
BBC-S-East ADC Sum
BBC-S-West ADC Sum

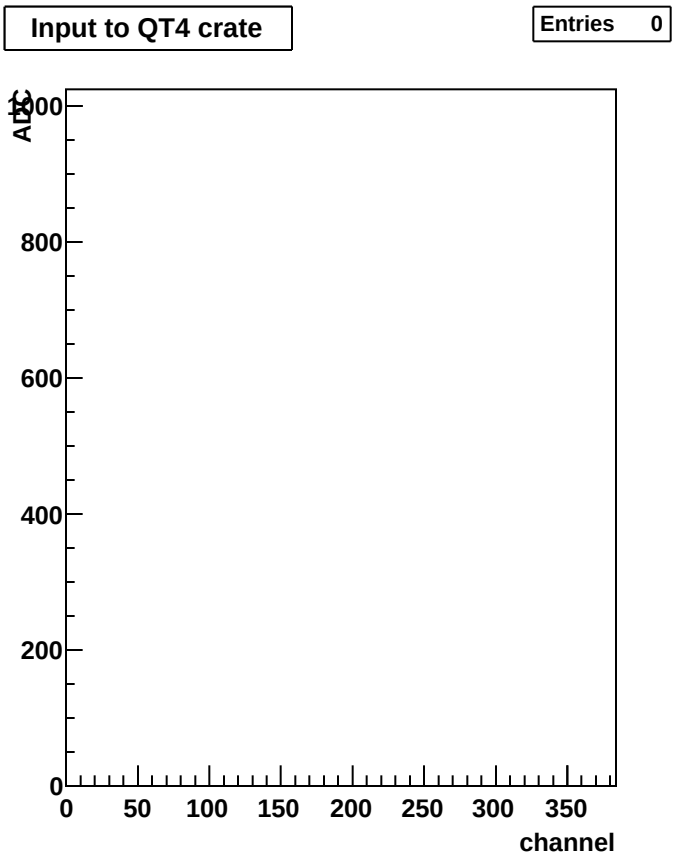

Entries 754095
ZDC East ADC Sum Att
ZDC West ADC Sum Att

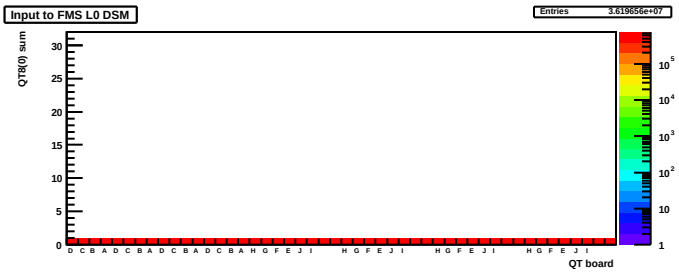

Entries 754095
BBC-L TAC Diff
BCC-S TAC Diff
Entries 754095
BBC-L TAC Diff
VPD TAC Diff
Entries 754095
BBC-L TAC Diff
ZDC TAC Diff
Entries 754095
BBC-S TAC Diff
VPD TAC Diff
Entries 754095
BBC-S TAC Diff
ZDC TAC Diff
Entries 754095
VPD TAC Diff
ZDC TAC Diff

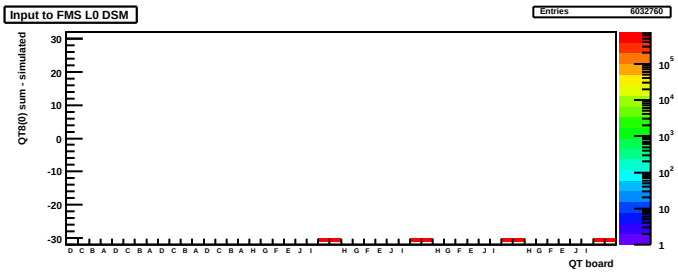

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

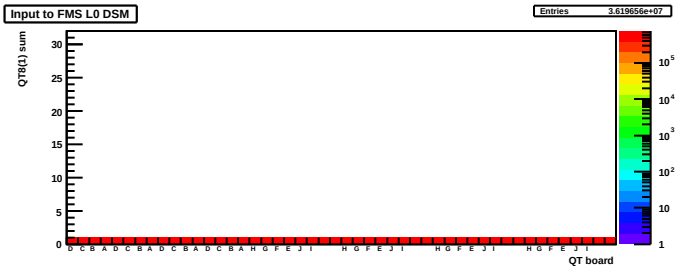

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

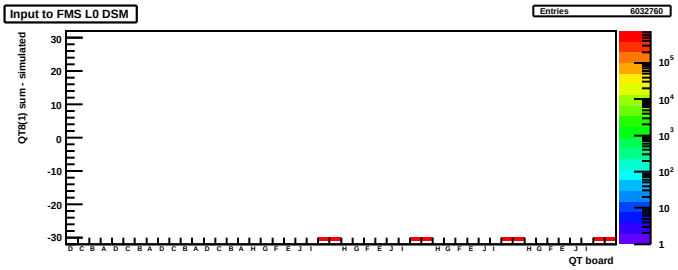

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

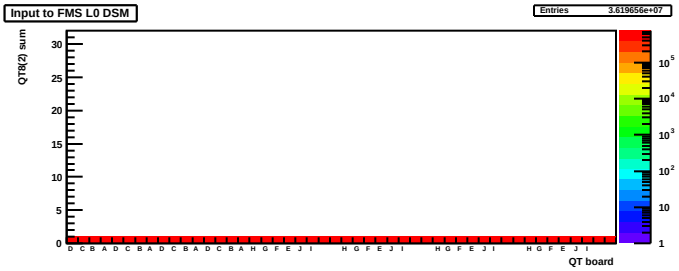

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

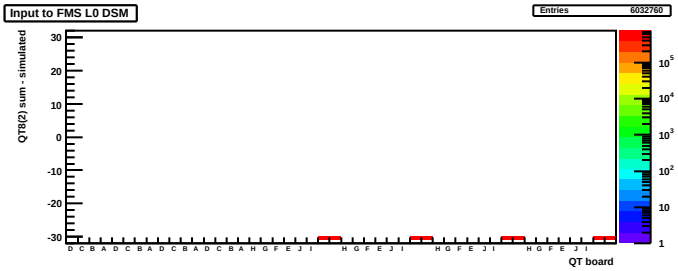

Input to FMS L0 DSM
Entries 3.619656e+07
QT8(0) sum
QT board

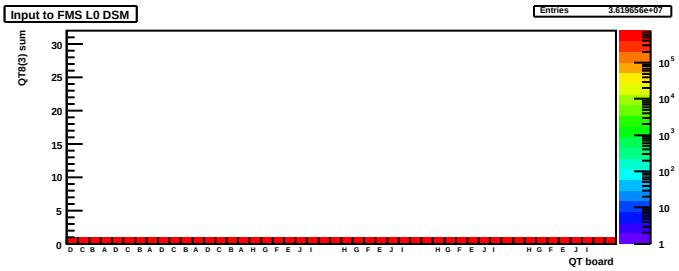

Input to FMS L0 DSM
Entries 6032760
QT8(0) sum - simulated
QT board

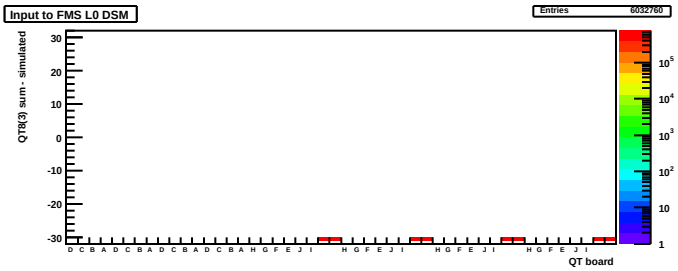

Input to FMS L0 DSM
Entries 3.619656e+07
QT8(1) sum
QT board

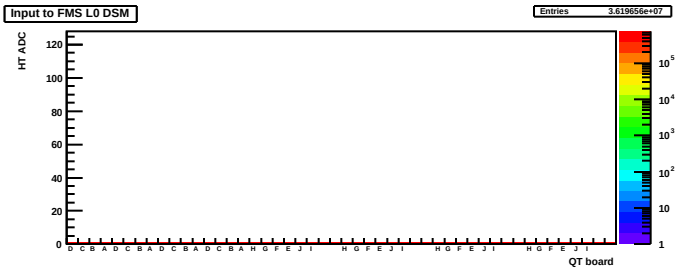

Input to FMS L0 DSM
Entries 6032760
QT8(1) sum - simulated
QT board

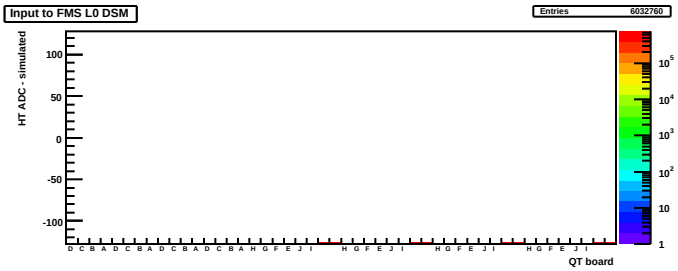

Input to FMS L0 DSM
Entries 3.619656e+07
QT8(2) sum
QT board

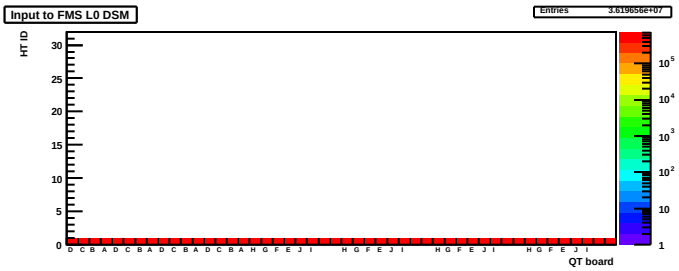

Input to FMS L0 DSM
Entries 6032760
QT8(2) sum - simulated
QT board

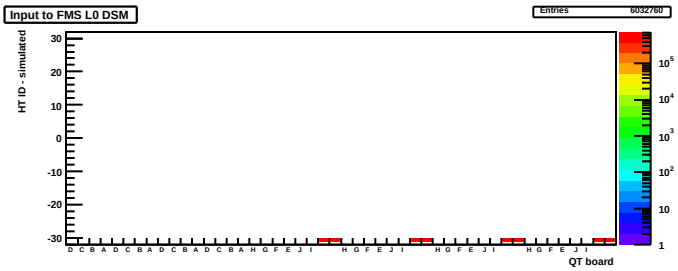

Input to FMS L0 DSM
Entries 3.619656e+07
QT8(3) sum
QT board

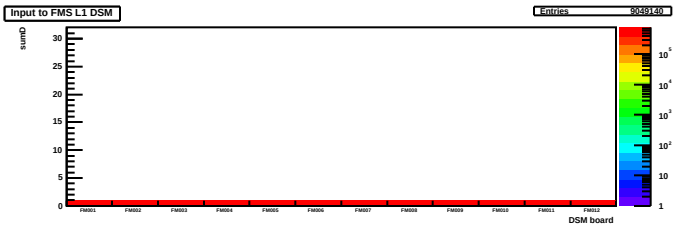

Input to FMS L0 DSM
Entries 6032760
QT8(3) sum - simulated
QT board

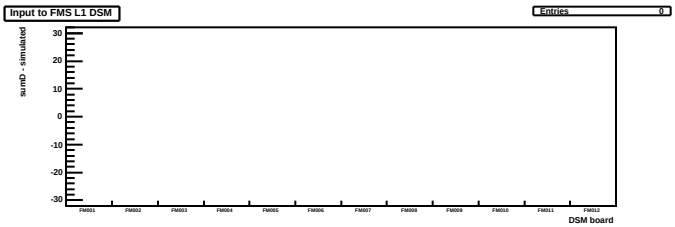

Input to FMS L0 DSM
Entries 3.619656e+07
HT ADC
QT board

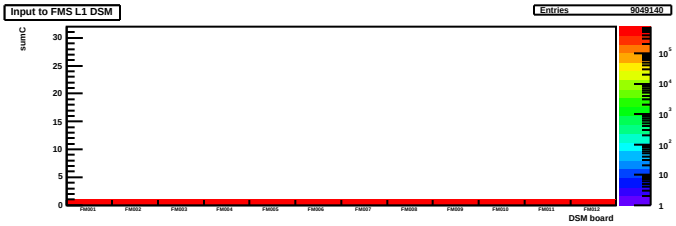

Input to FMS L0 DSM
Entries 6032760
HT ADC - simulated
QT board

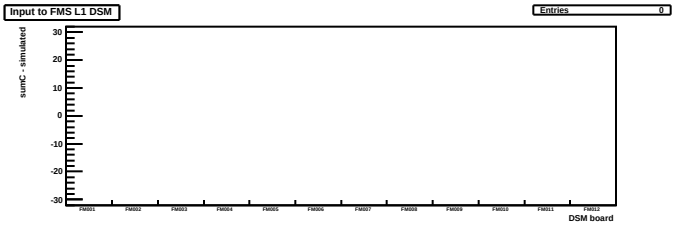

Input to FMS L0 DSM
Entries 3.619656e+07
HT ID
QT board

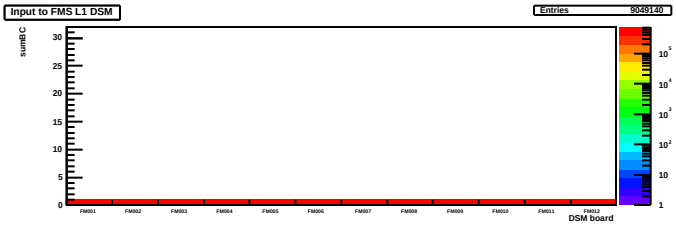

Input to FMS L0 DSM
Entries 6032760
HT ID - simulated
QT board

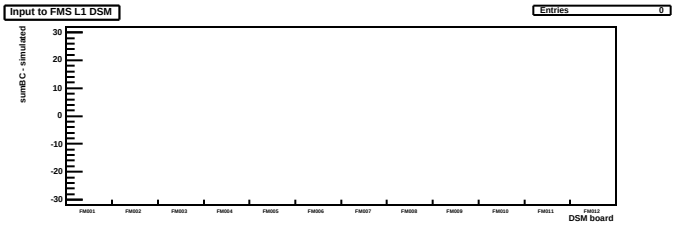

Input to FMS L1 DSM
Entries 9049140
sumD
DSM board

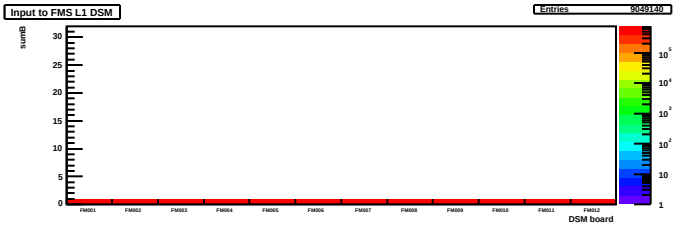

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

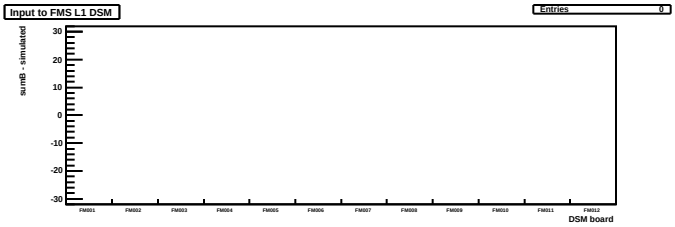

Input to FMS L1 DSM
Entries 9049140
sumC
DSM board

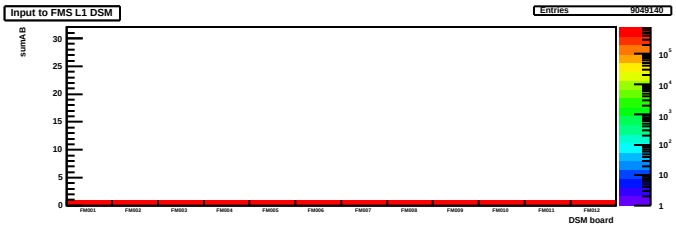

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

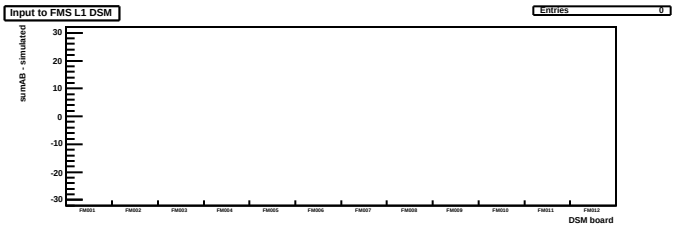

Input to FMS L1 DSM
Entries 9049140
sumBC
DSM board

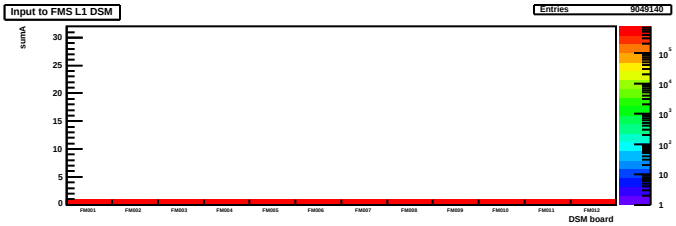

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

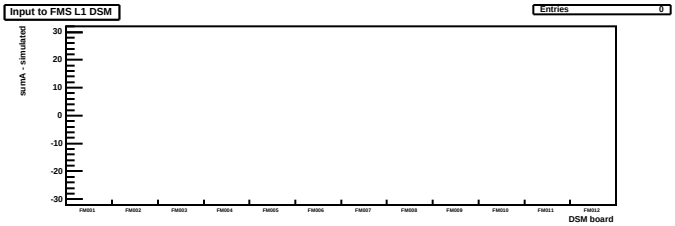

Input to FMS L1 DSM
Entries 9049140
sumB
DSM board

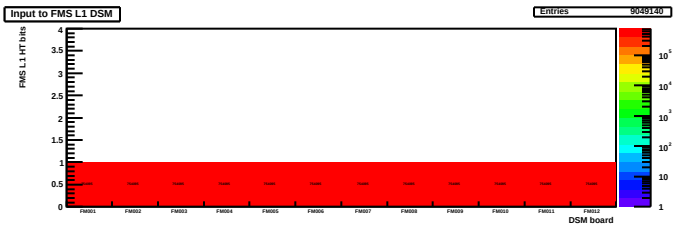

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

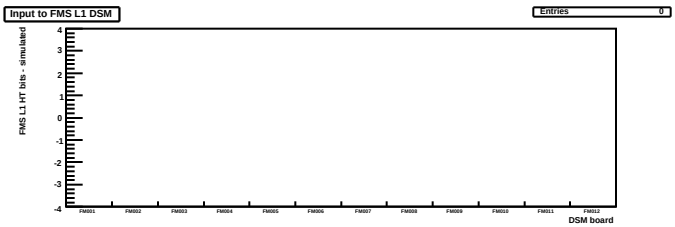

Input to FMS L1 DSM
Entries 9049140
sumAB
DSM board

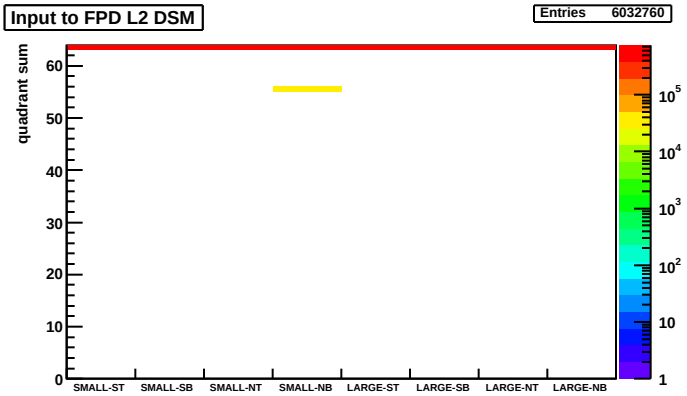

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

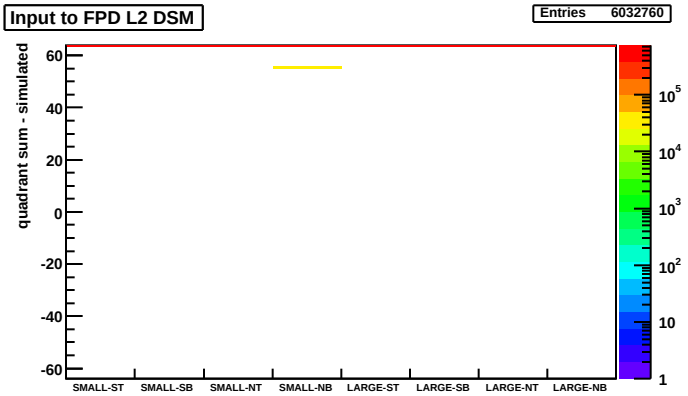

Input to FMS L1 DSM
Entries 9049140
sumA
DSM board

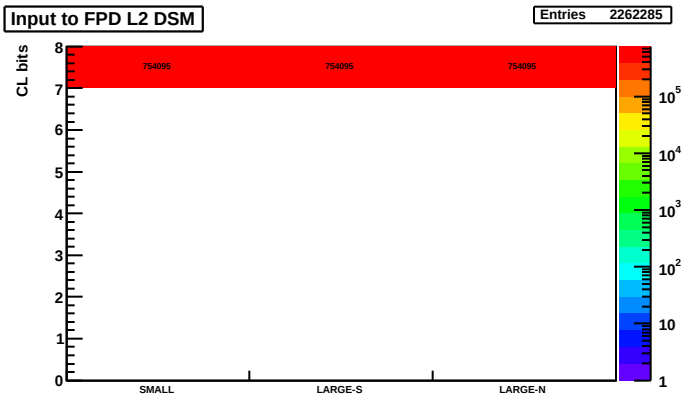

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

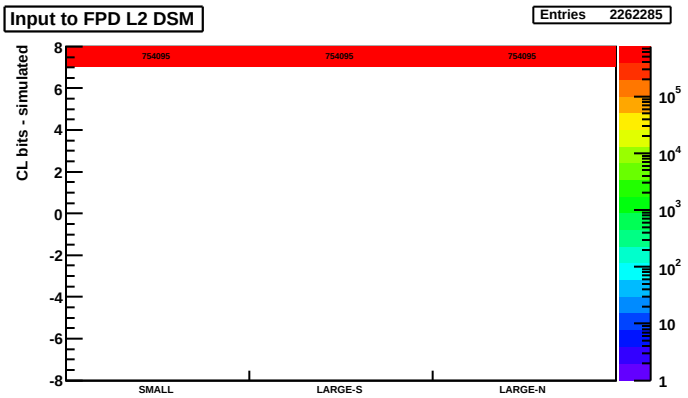

Input to FMS L1 DSM
Entries 9049140
FMS L1 HT bits
DSM board

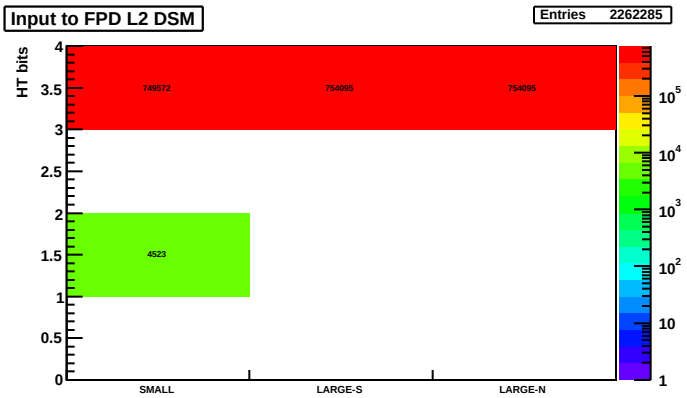

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

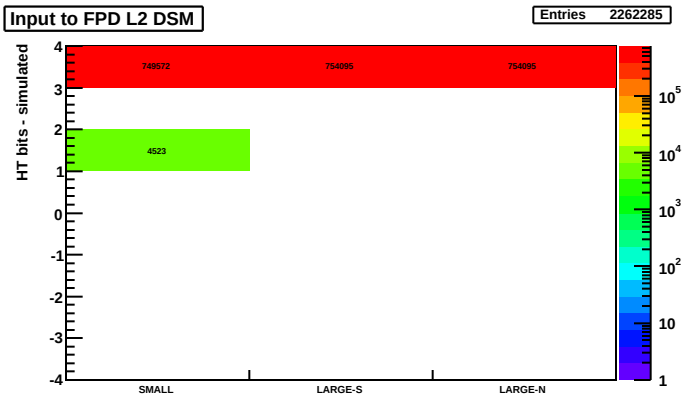

Input to FPD L2 DSM
Entries 6032760
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

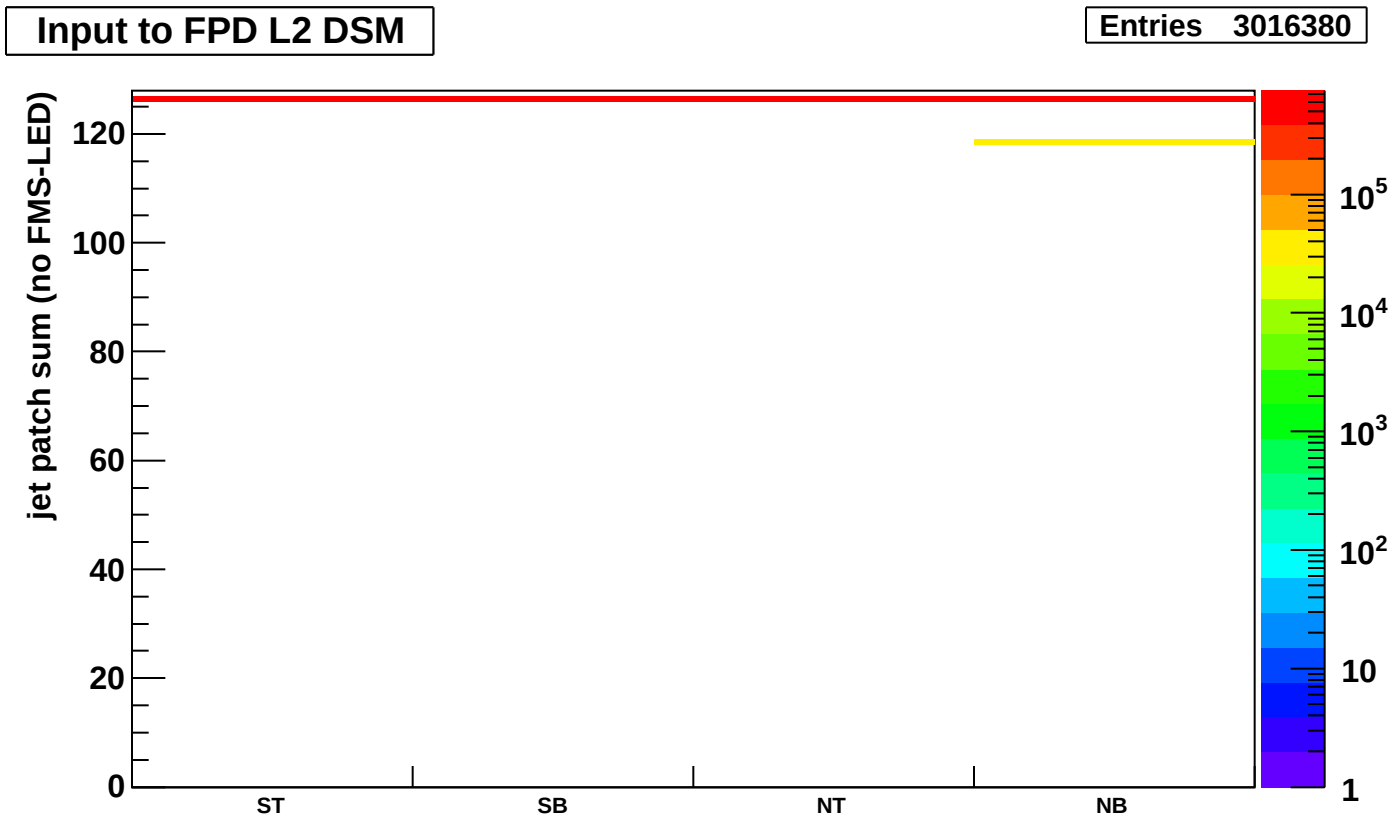

Input to FPD L2 DSM
Entries 6032760
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

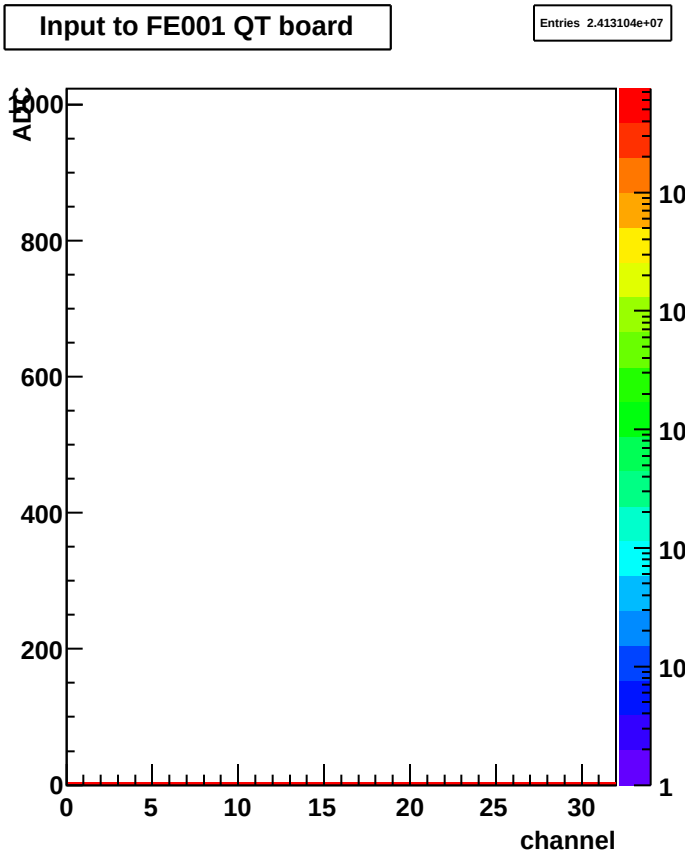

Input to FPD L2 DSM
Entries 2262285
CL bits
754095
754095
754095
SMALL
LARGE-S
LARGE-N

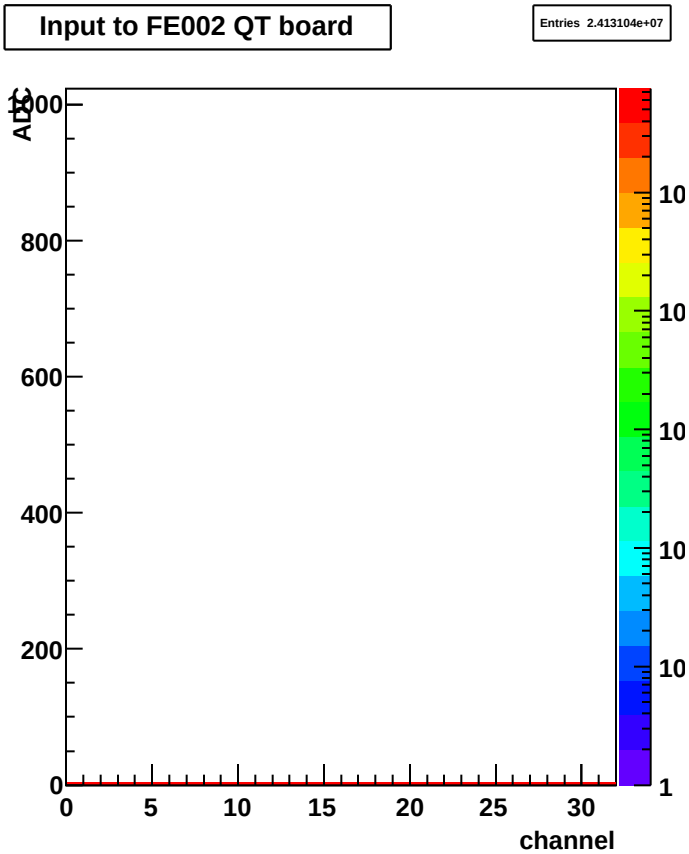

Input to FPD L2 DSM
Entries 2262285
CL bits - simulated
754095
754095
754095
SMALL
LARGE-S
LARGE-N

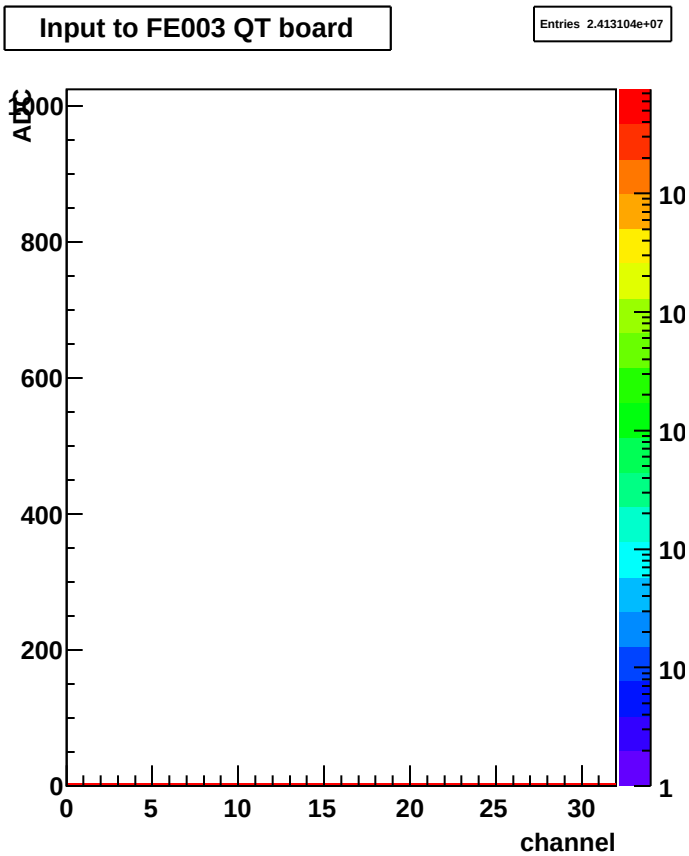

Input to FPD L2 DSM
Entries 2262285
HT bits
749572
754095
754095
4523
SMALL
LARGE-S
LARGE-N

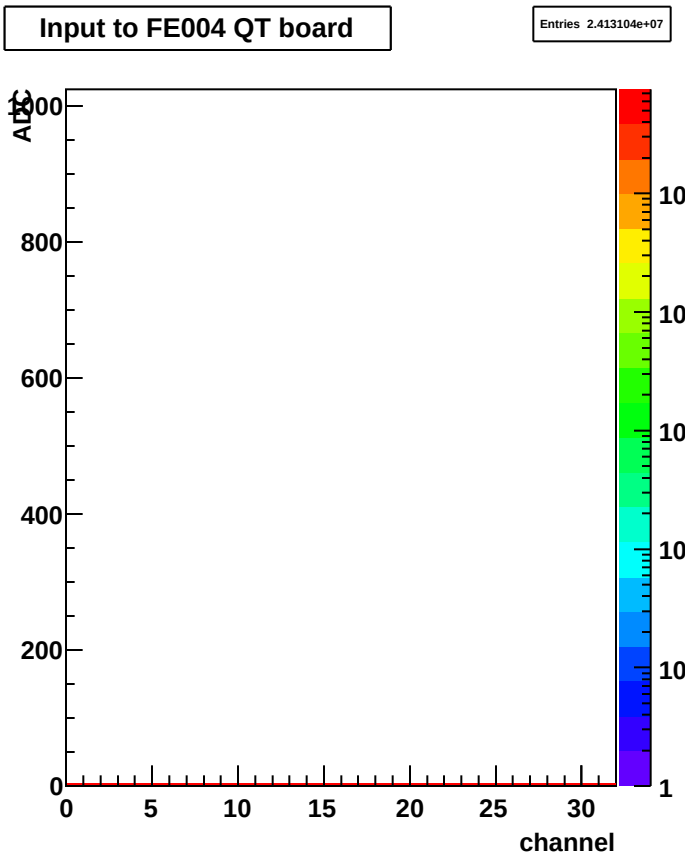

Input to FPD L2 DSM
Entries 2262285
HT bits - simulated
749572
754095
754095
4523
SMALL
LARGE-S
LARGE-N


Input to FPD L2 DSM
Entries 3016380
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

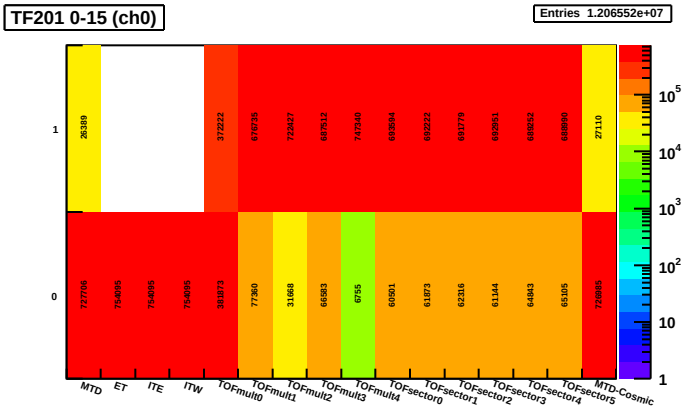

Input to FE001 QT board
Entries 2.413104e+07
ADC
channel


Input to FE002 QT board
Entries 2.413104e+07
ADC
channel

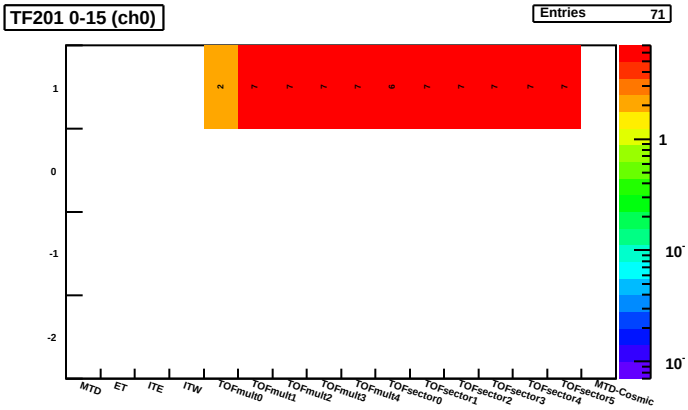

Input to FE003 QT board
Entries 2.413104e+07
ADC
channel


Input to FE004 QT board
Entries 2.413104e+07
ADC
channel

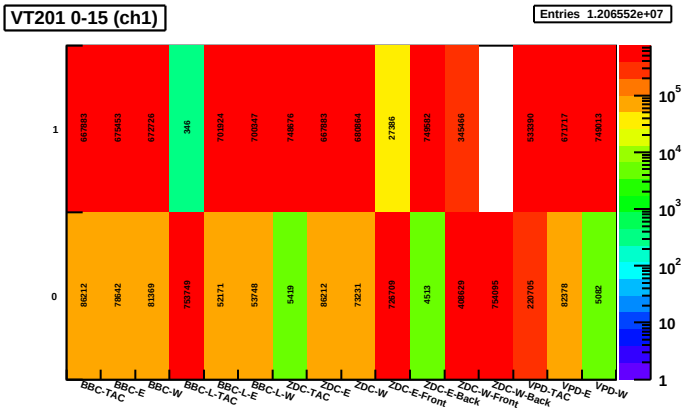
TF201 0-15 (ch0)



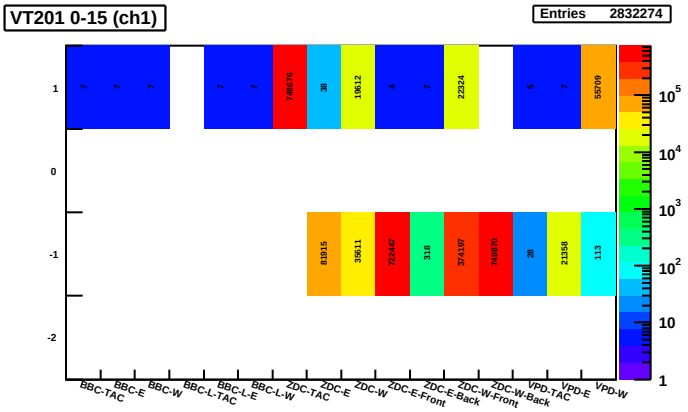
TF201 0-15 (ch0)



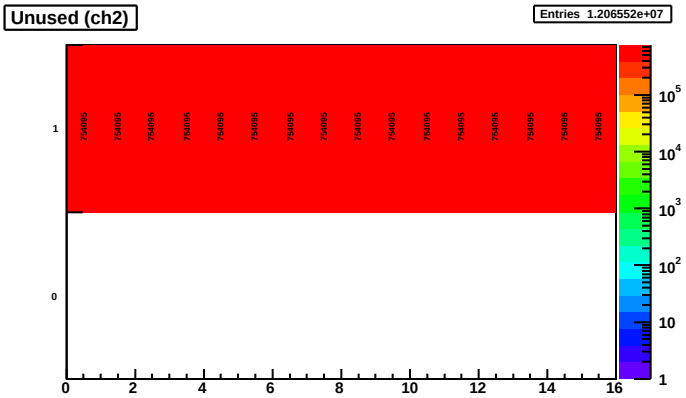
VT201 0-15 (ch1)

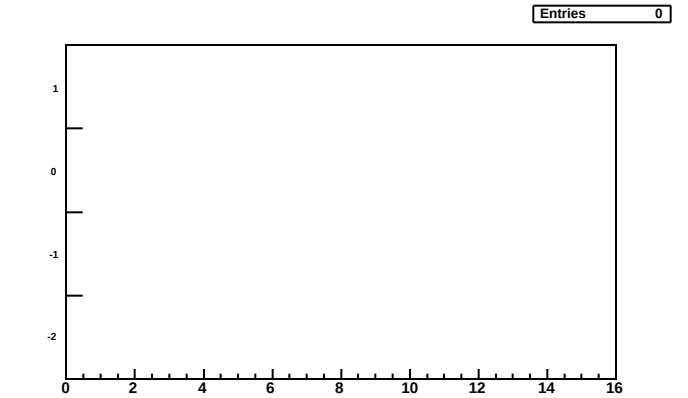


VT201 0-15 (ch1)

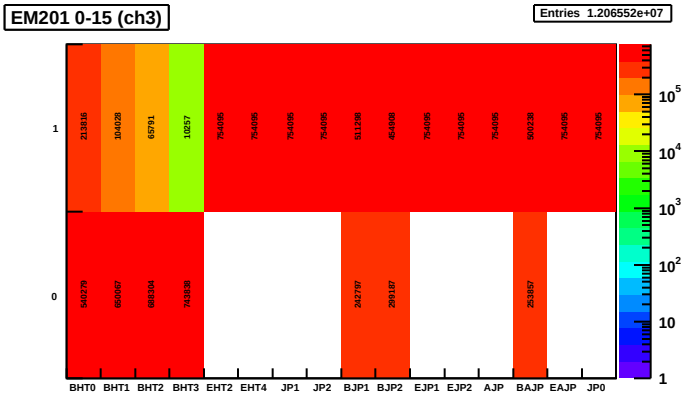


Unused (ch2)

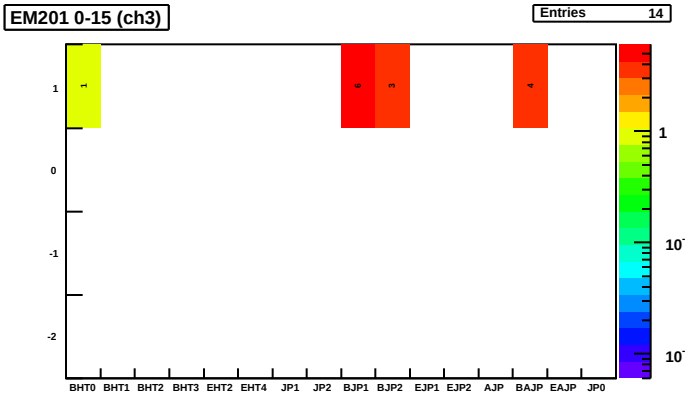


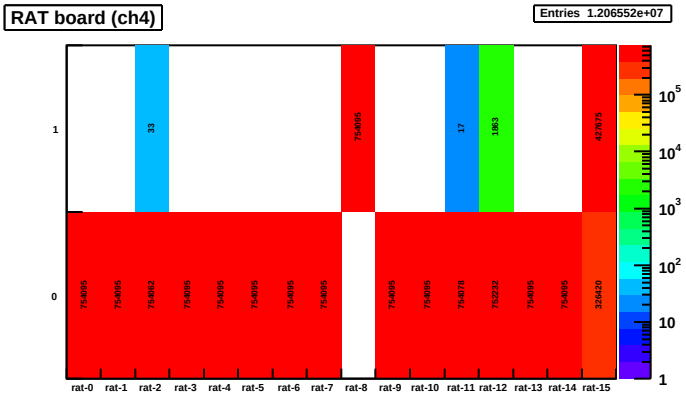


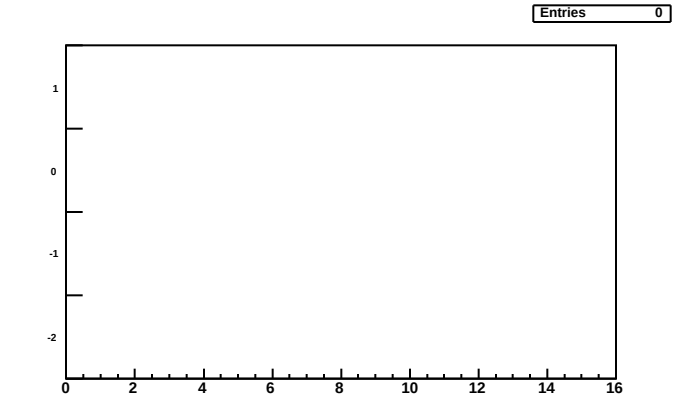
EM201 0-15 (ch3)

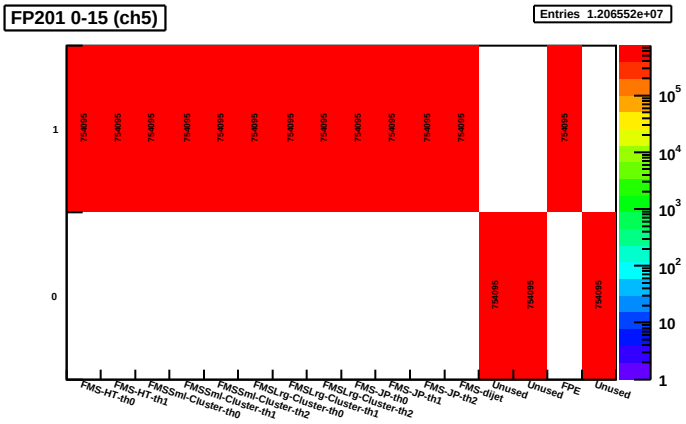


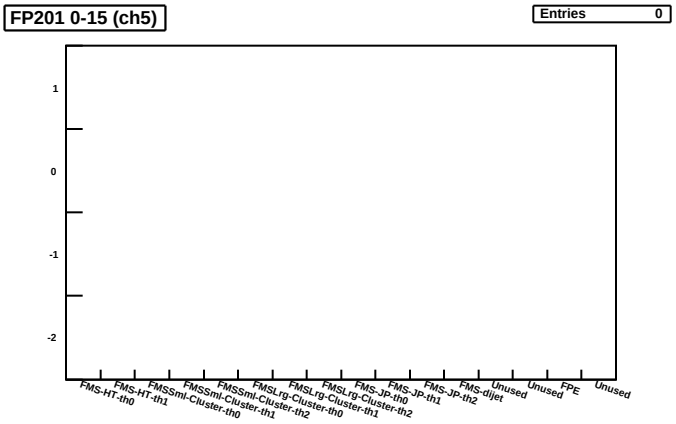
EM201 0-15 (ch3)

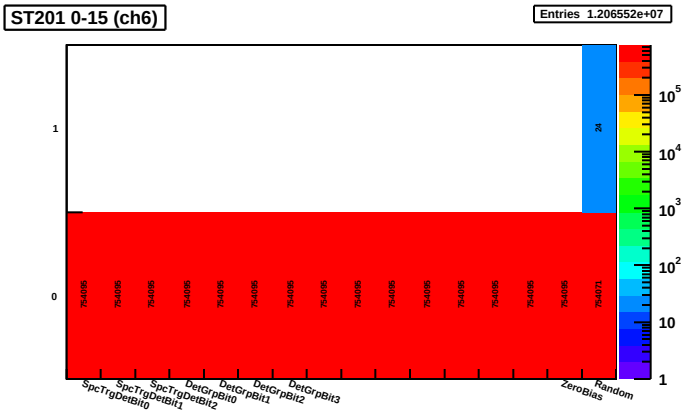


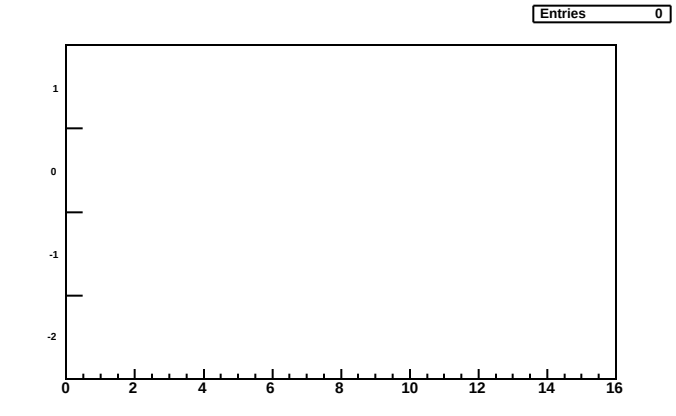

RAT board (ch4)
Entries 1.206552e+07
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

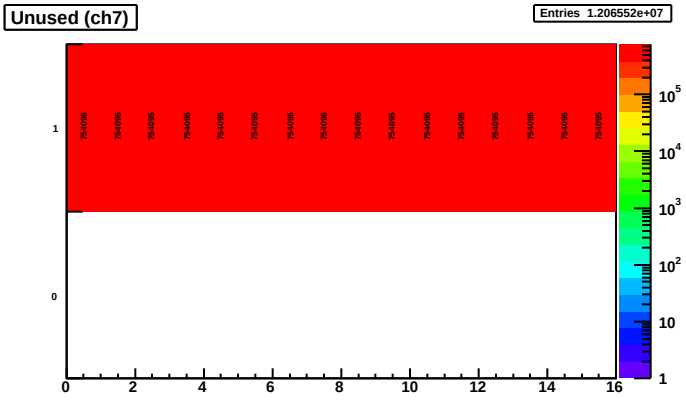

Entries 0

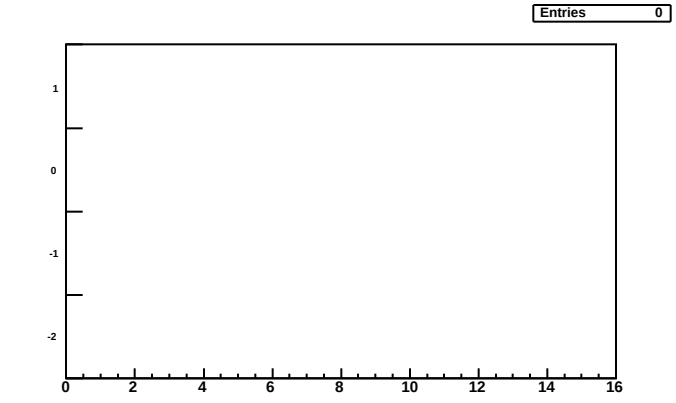

FP201 0-15 (ch5)
Entries 1.206552e+07
FMS-HT-th0
FMS-HT-th1
FMSSml-Cluster-th0
FMSSml-Cluster-th1
FMSSml-Cluster-th2
FMSLrg-Cluster-th0
FMSLrg-Cluster-th1
FMSLrg-Cluster-th2
FMS-JP-th0
FMS-JP-th1
FMS-JP-th2
FMS-diJet
Unused
Unused
FPE
Unused


FP201 0-15 (ch5)
Entries 0
FMS-HT-th0
FMS-HT-th1
FMSSml-Cluster-th0
FMSSml-Cluster-th1
FMSSml-Cluster-th2
FMSLrg-Cluster-th0
FMSLrg-Cluster-th1
FMSLrg-Cluster-th2
FMS-JP-th0
FMS-JP-th1
FMS-JP-th2
FMS-diJet
Unused
Unused
FPE
Unused


ST201 0-15 (ch6)
Entries 1.206552e+07
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetGrpBit0
DetGrpBit1
DetGrpBit2
DetGrpBit3
ZeroBias
Random


Entries 0


Unused (ch7)
Entries 1.206552e+07


Entries 0