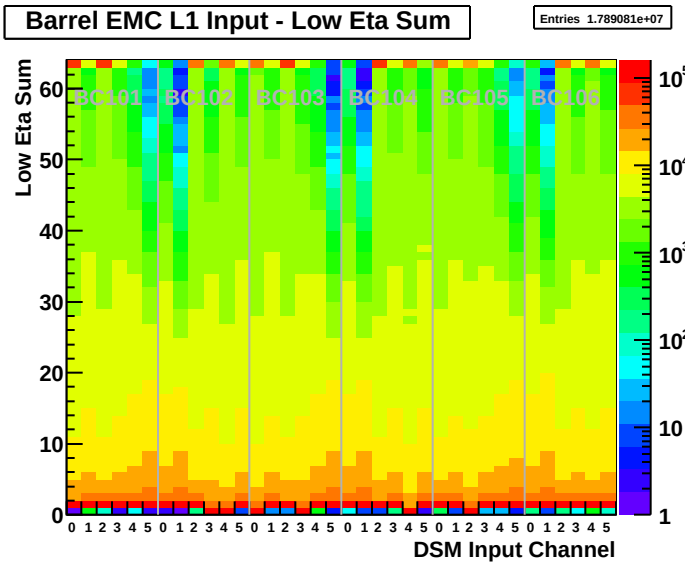
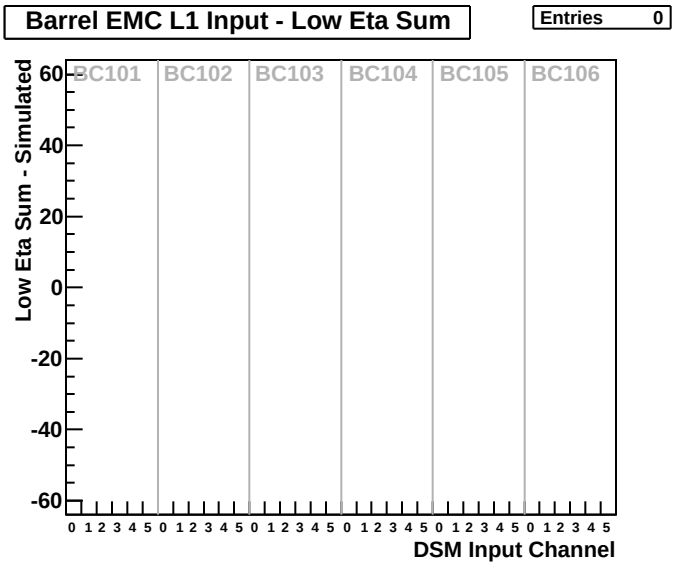
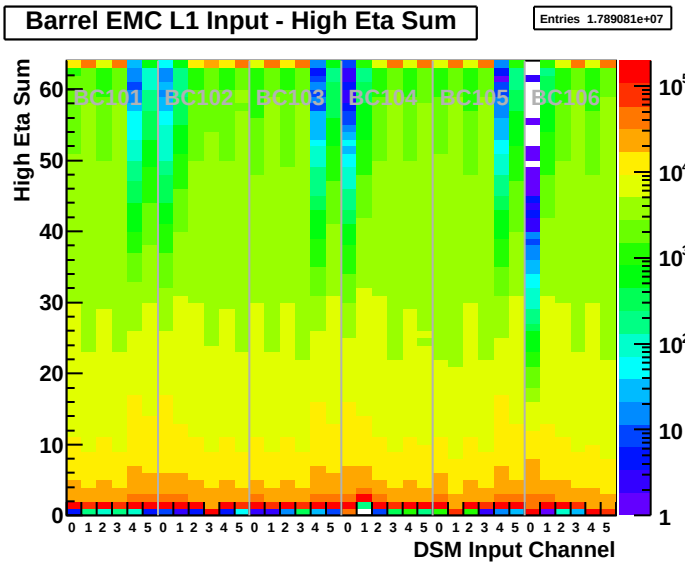
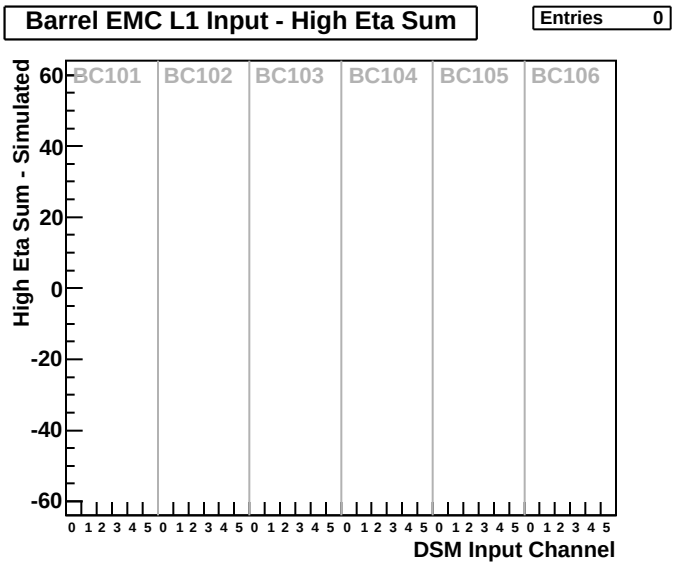
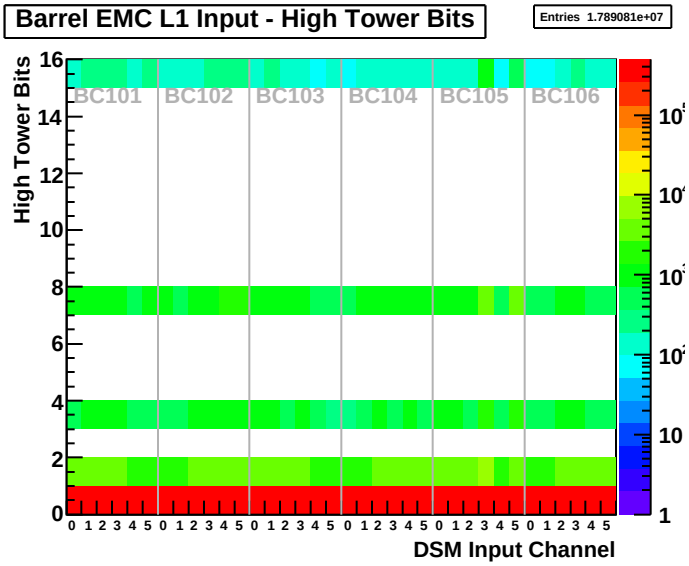
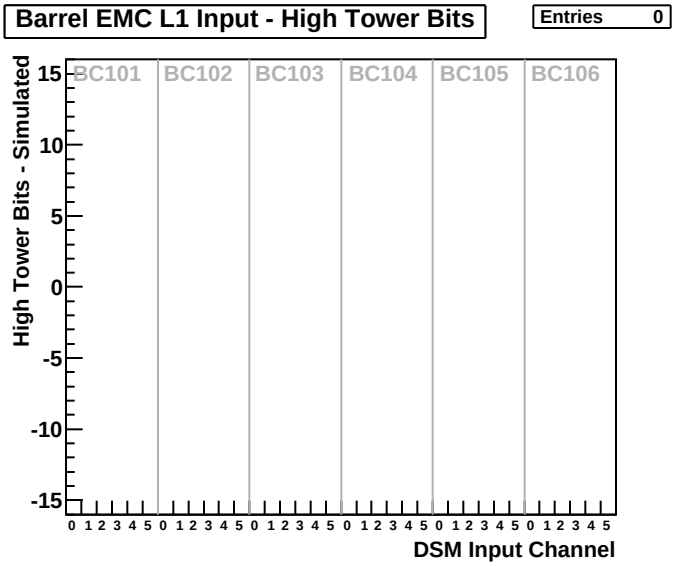

Barrel EMC L1 Input - Low Eta Sum
Entries 1.789081e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

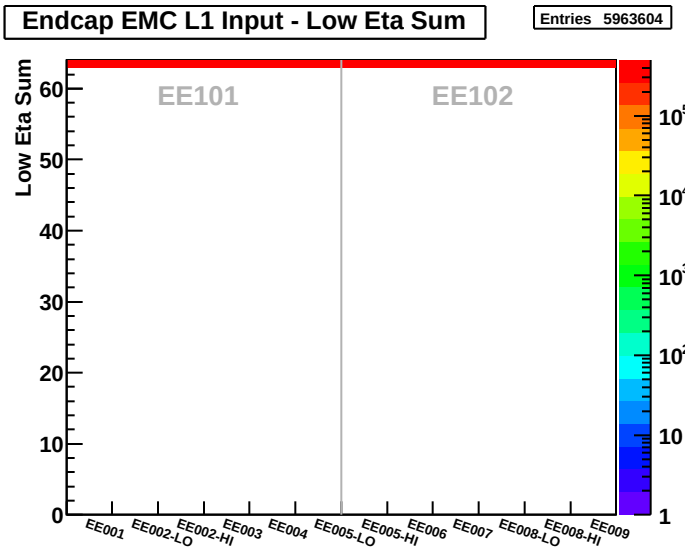

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

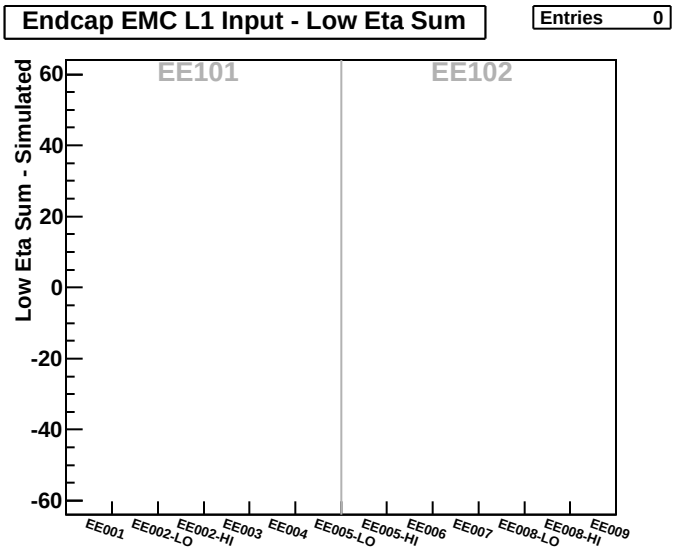

Barrel EMC L1 Input - High Eta Sum
Entries 1.789081e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

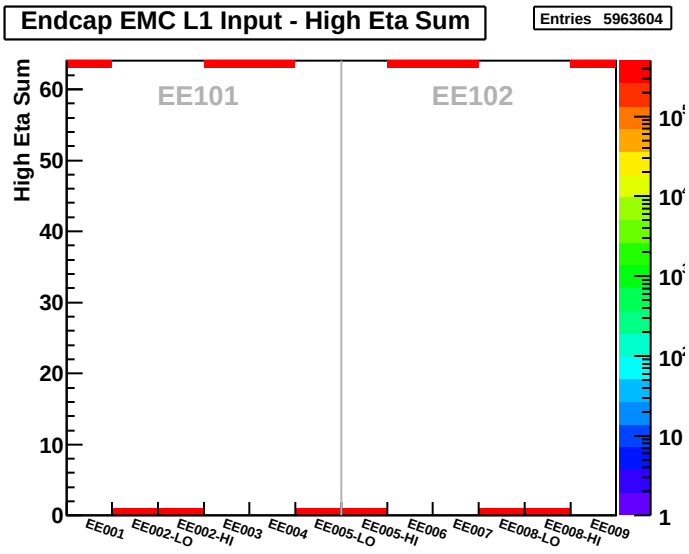

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

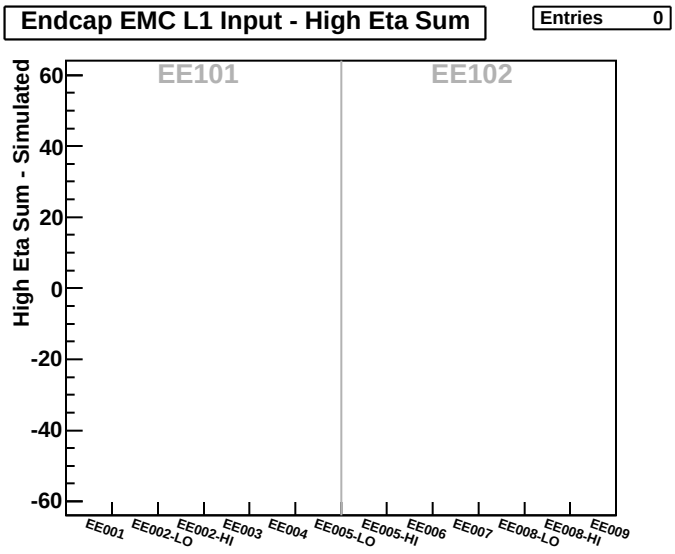

Barrel EMC L1 Input - High Tower Bits
Entries 1.789081e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

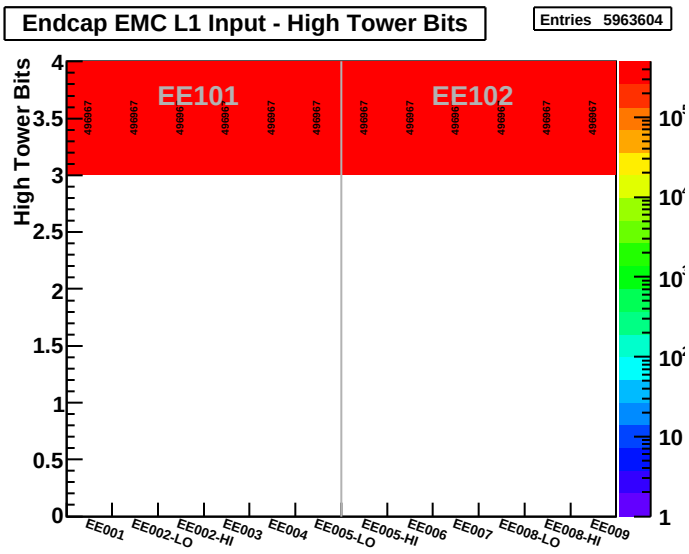

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

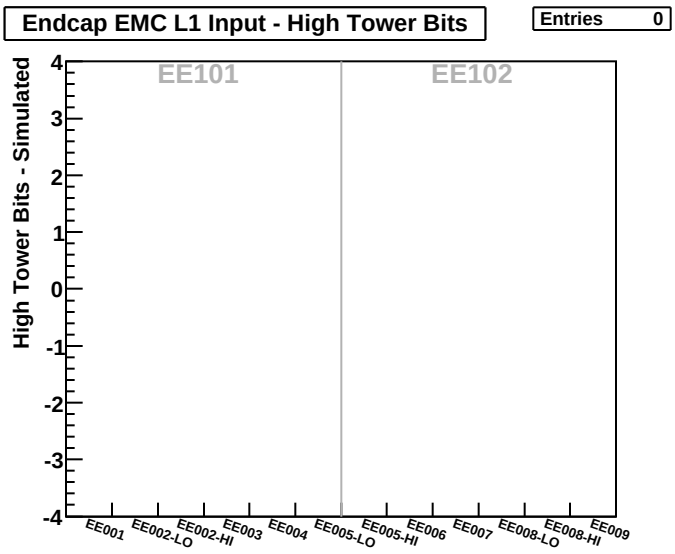

Endcap EMC L1 Input - Low Eta Sum
Entries 5963604
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

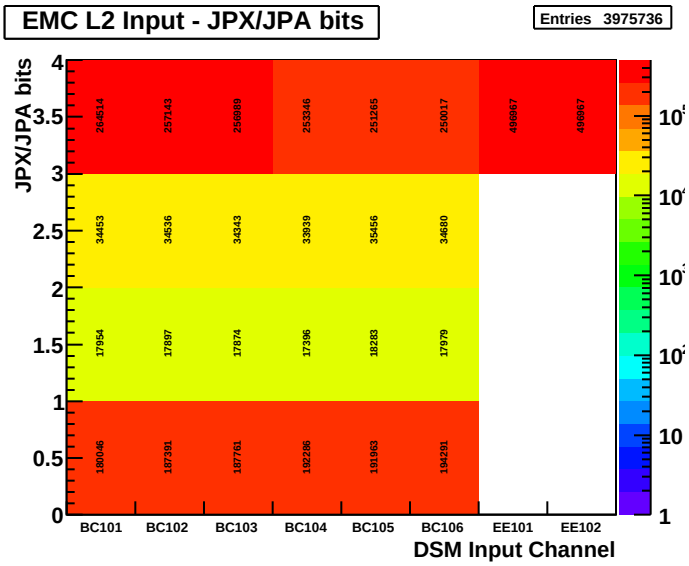

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

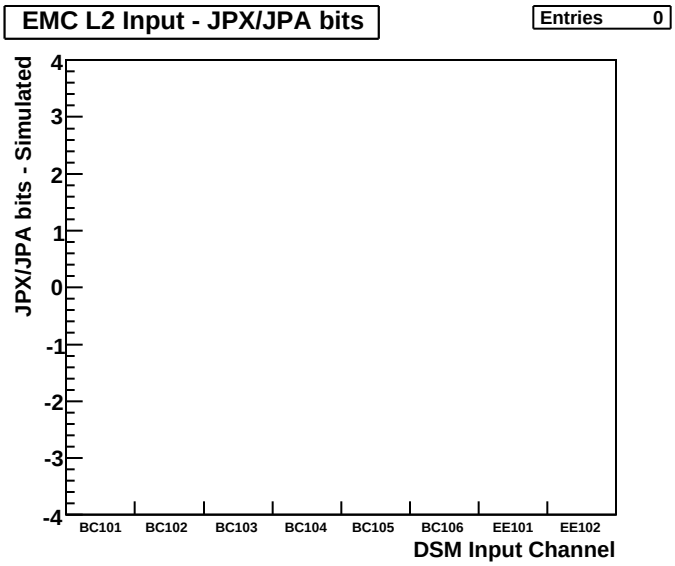

Endcap EMC L1 Input - High Eta Sum
Entries 5963604
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

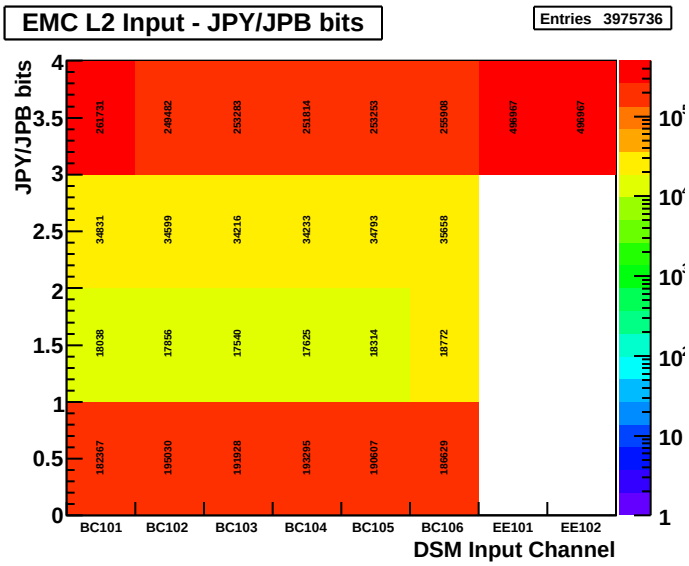

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

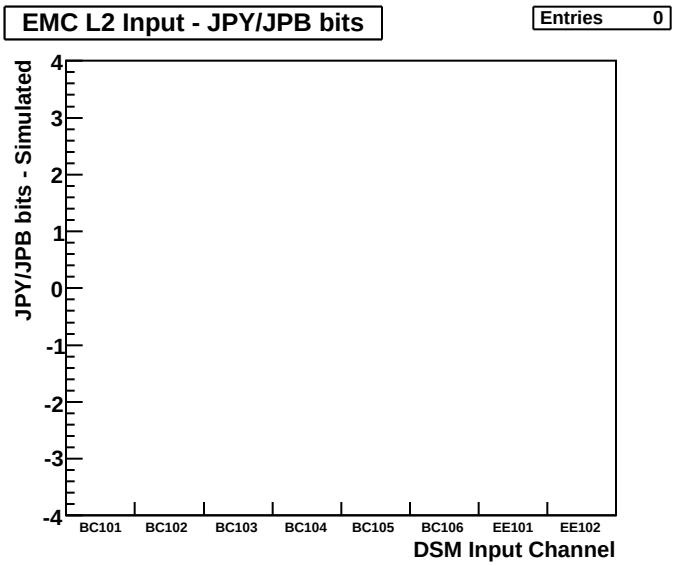

Endcap EMC L1 Input - High Tower Bits
Entries 5963604
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

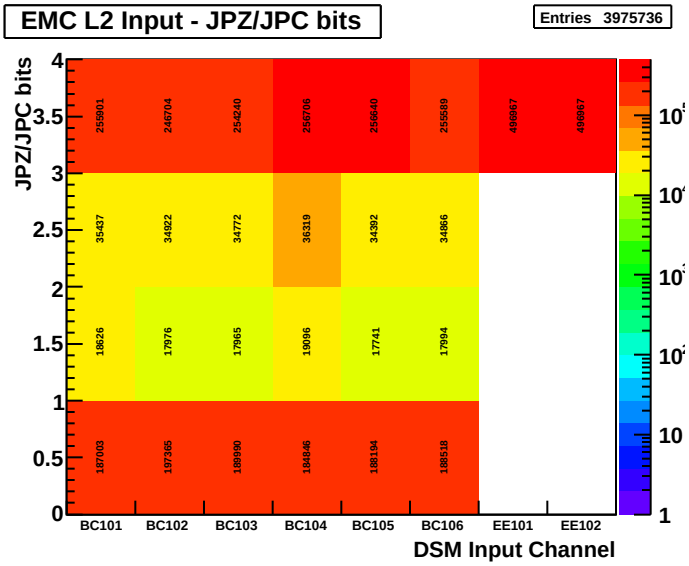

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

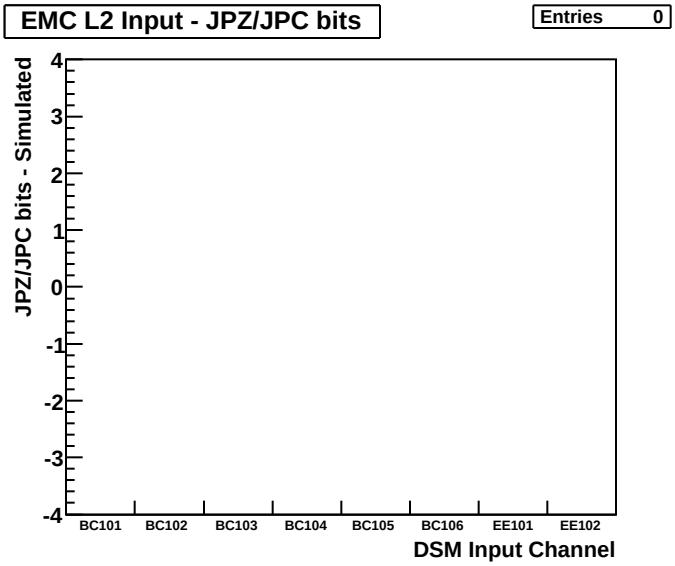

EMC L2 Input - JPX/JPA bits
Entries 3975736
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
254514 257143 256889 253346 251265 250017 496967 496967
34453 34536 34343 33939 35456 34680
17954 17897 17874 17396 18283 17979
180046 187391 187761 192286 191963 194291

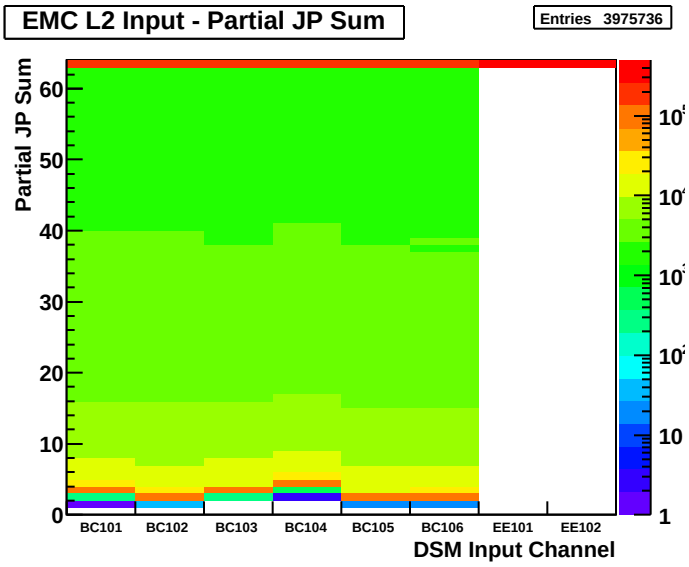

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

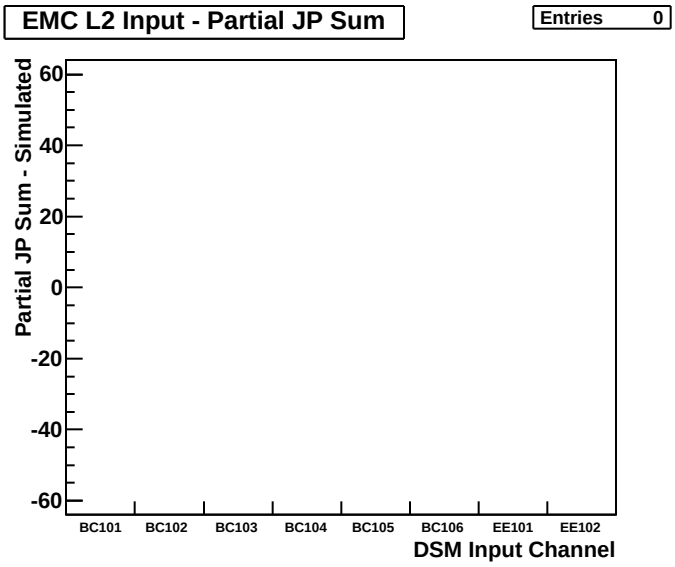

EMC L2 Input - JPY/JPB bits
Entries 3975736
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
261731 240482 253283 251814 253253 255908 496967 496967
34831 34599 34216 34233 34793 35658
18038 17856 17540 17625 18314 18772
182367 190030 191928 193295 190607 186629

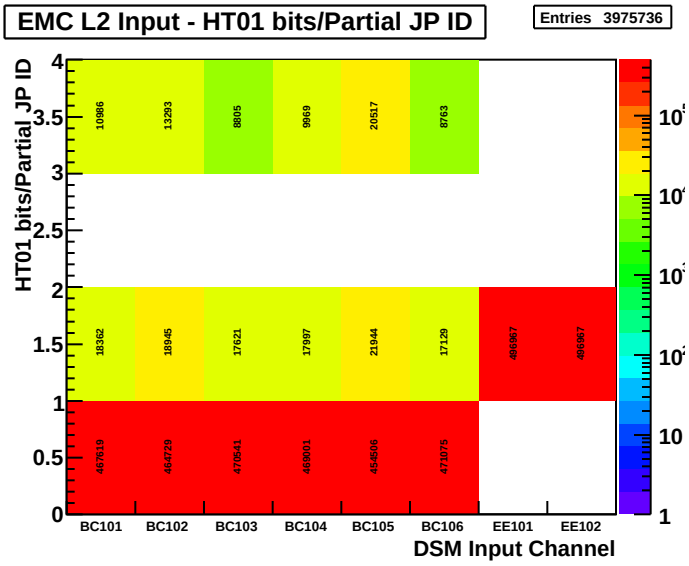

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

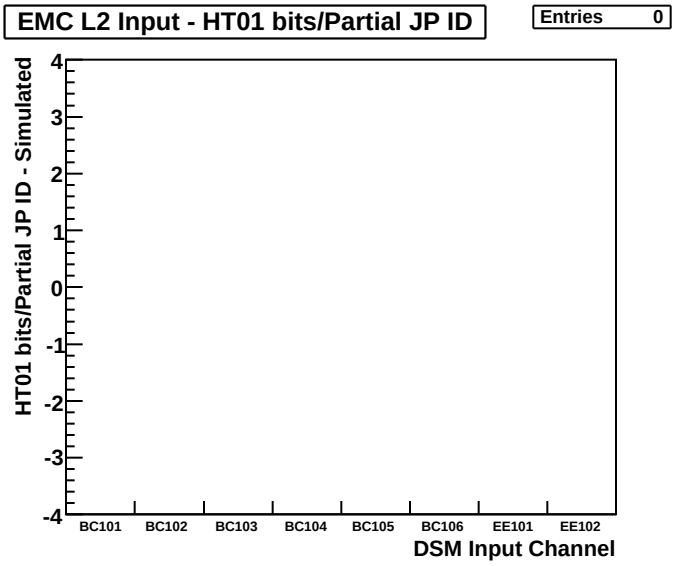

EMC L2 Input - JPZ/JPC bits
Entries 3975736
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
255901 246704 254240 256706 256640 255589 496967 496967
35437 34822 34772 36319 34392 34866
18626 17976 17965 19096 17741 17994
187003 197365 189990 184846 188194 188518

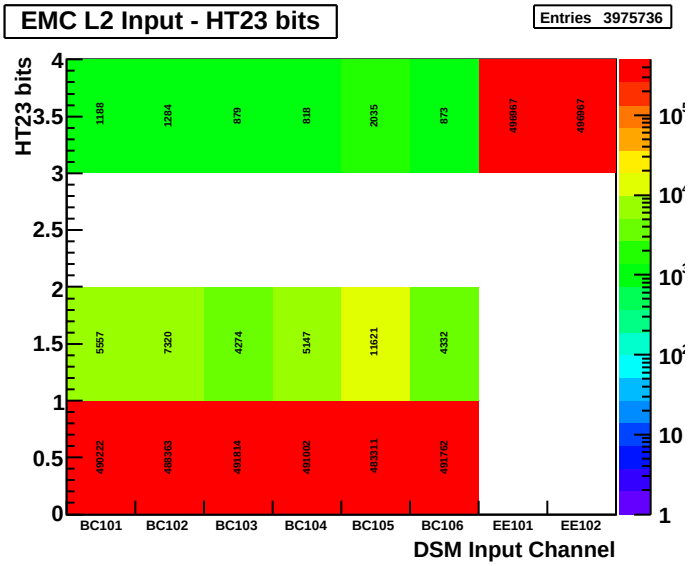

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

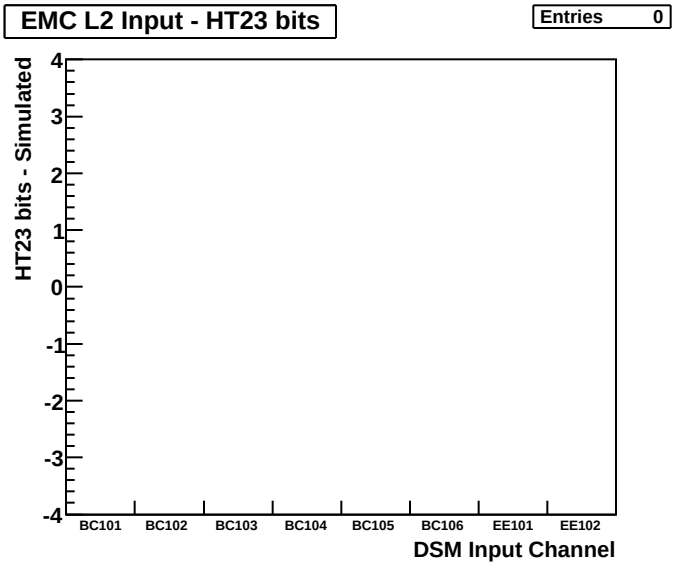

EMC L2 Input - Partial JP Sum
Entries 3975736
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

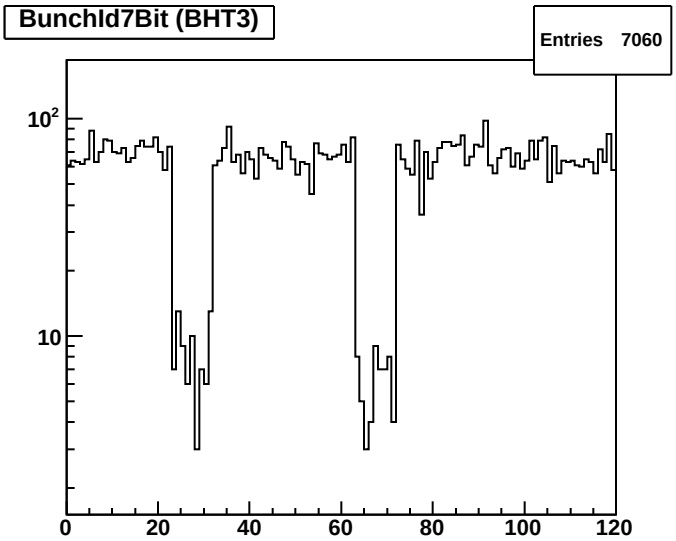

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

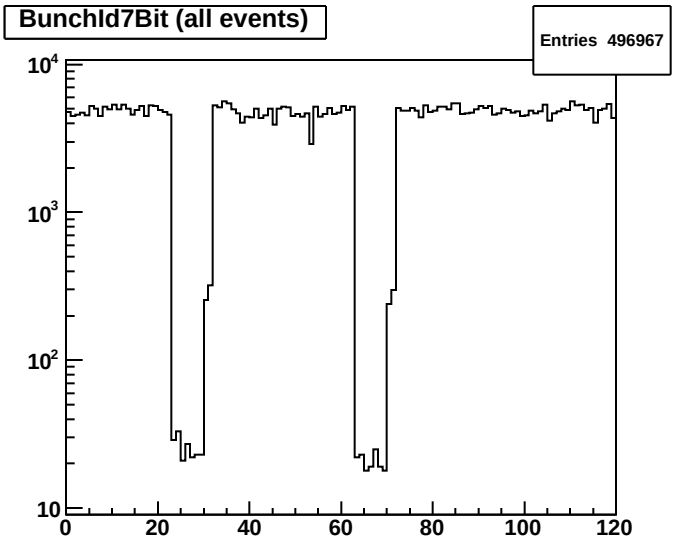

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3975736
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

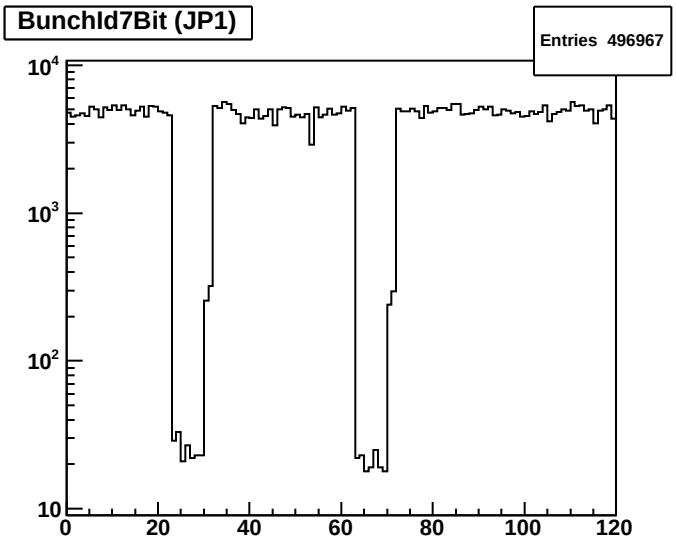

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

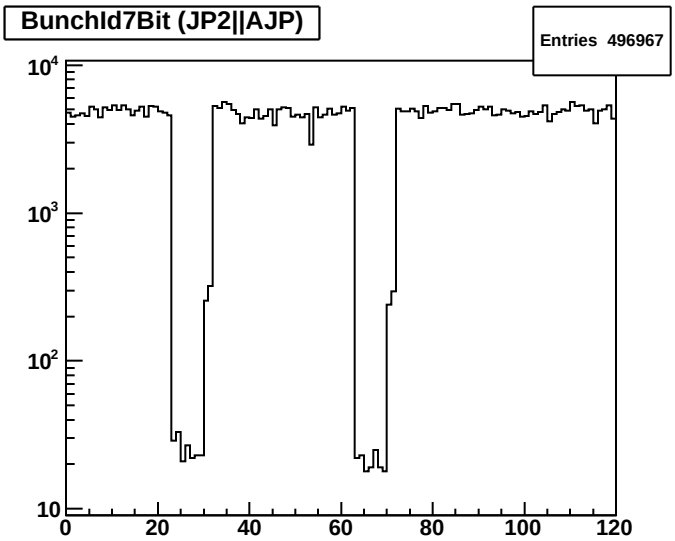

EMC L2 Input - HT23 bits
Entries 3975736
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

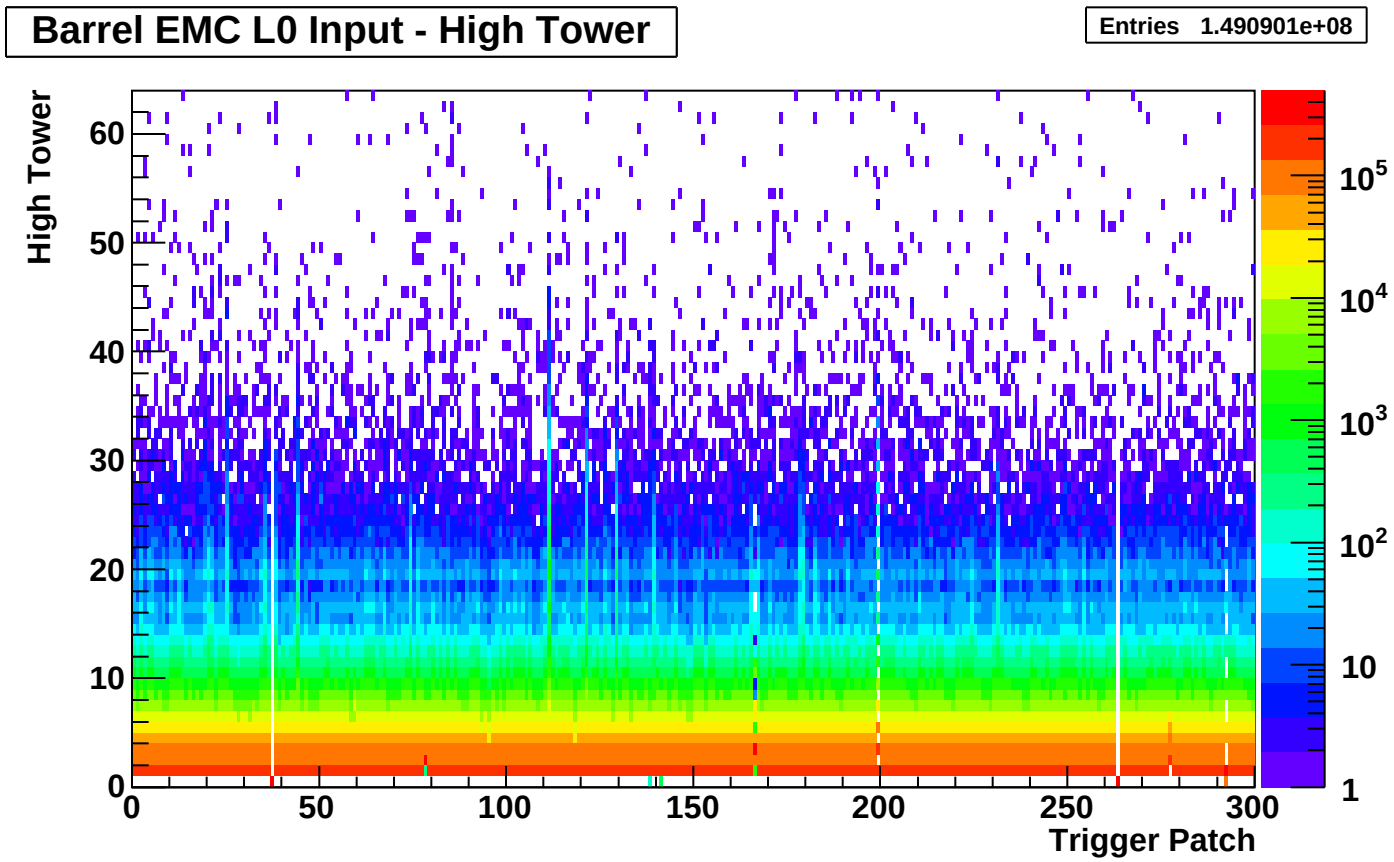

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

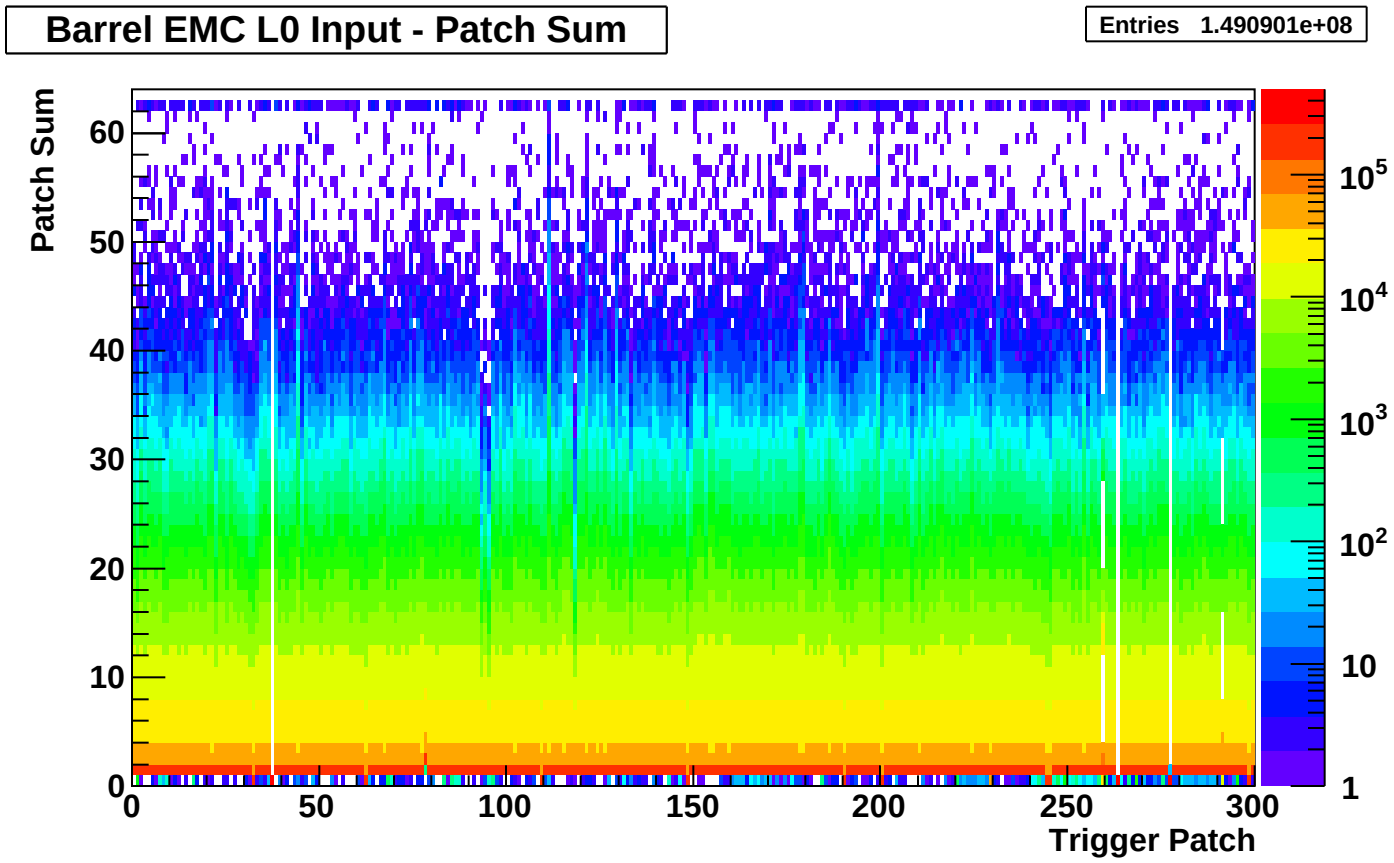

BunchId7Bit (BHT3)
Entries 7060
10^2
10
0
20
40
60
80
100
120

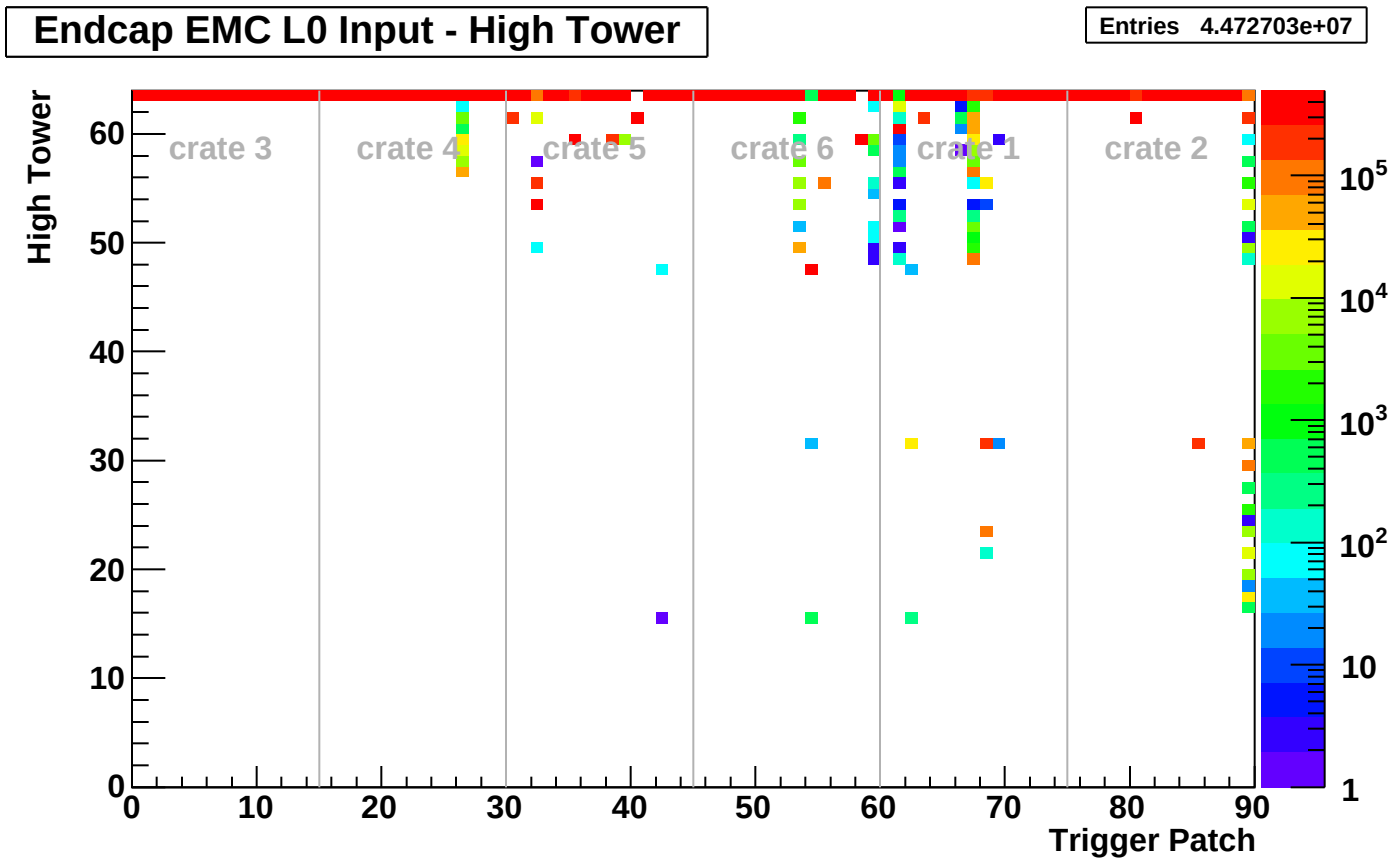

BunchId7Bit (all events)
Entries 496967
10^4
10^3
10^2
10
0
20
40
60
80
100
120

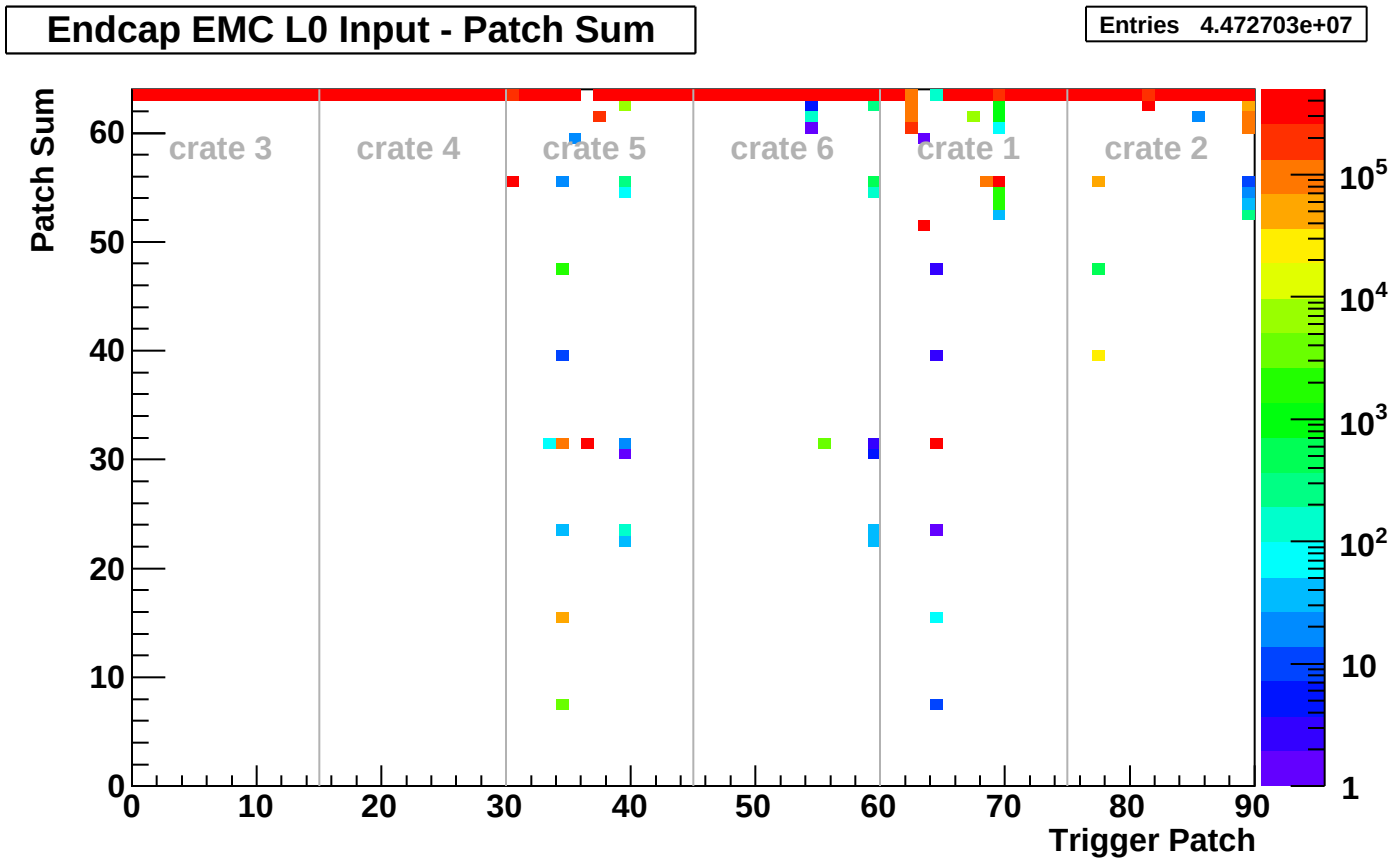

BunchId7Bit (JP1)
Entries 496967
10^4
10^3
10^2
10
0
20
40
60
80
100
120

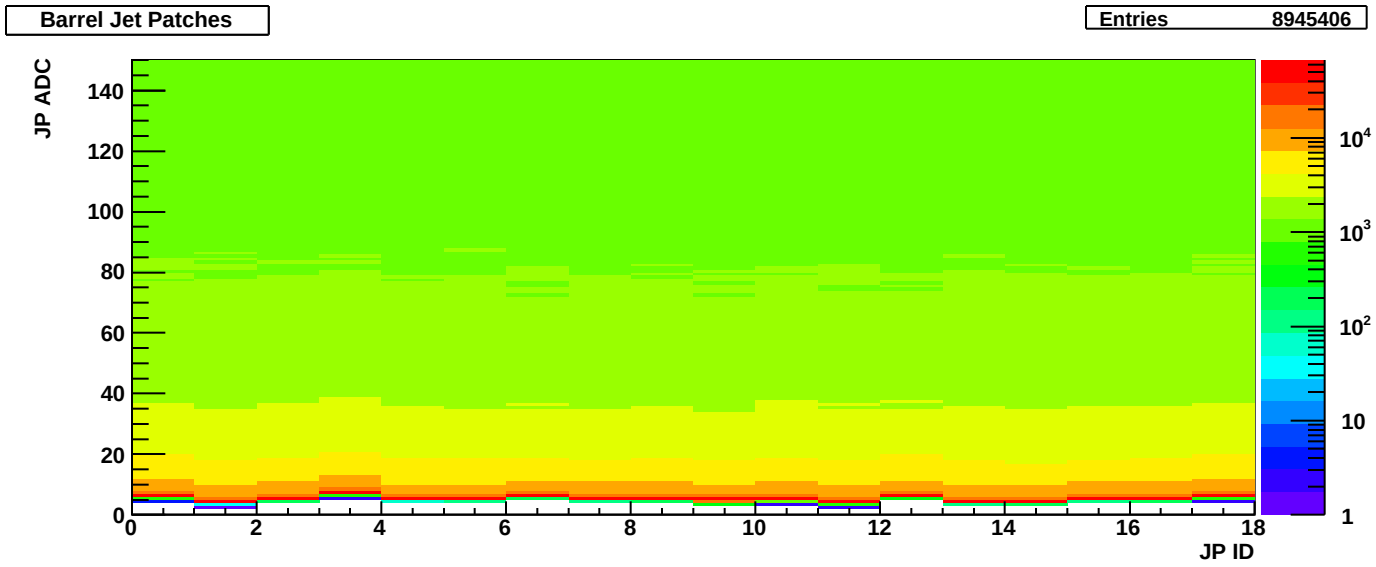

BunchId7Bit (JP2||AJP)
Entries 496967
10^4
10^3
10^2
10
0
20
40
60
80
100
120

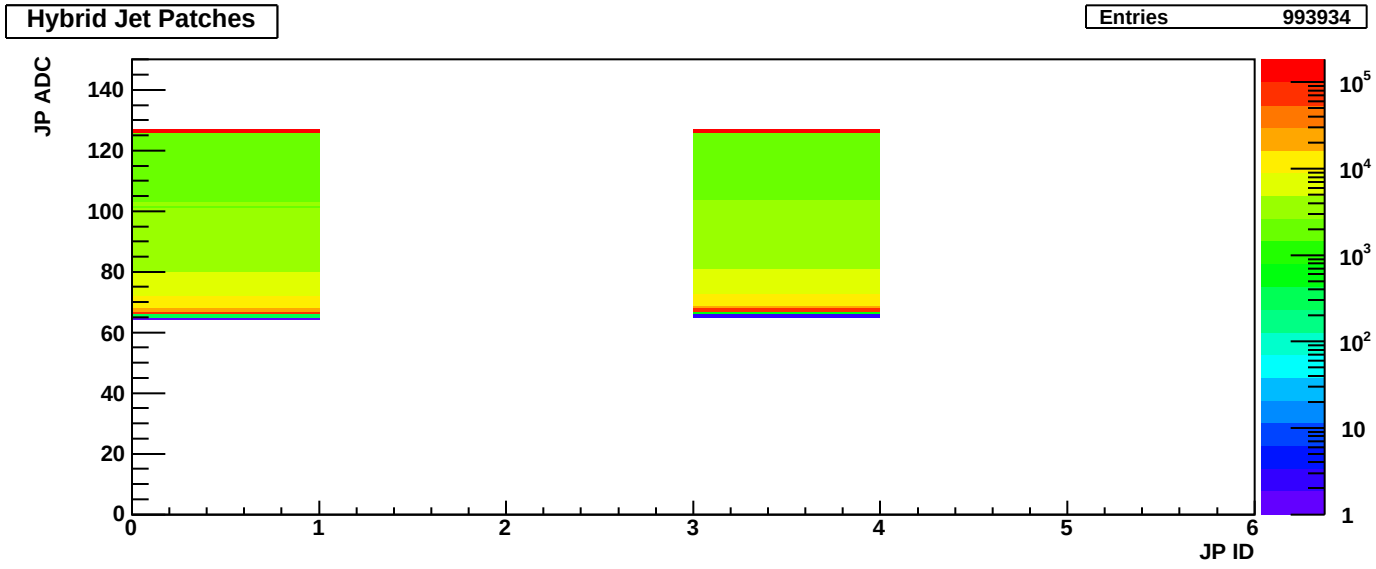

Barrel EMC L0 Input - High Tower
Entries 1.490901e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

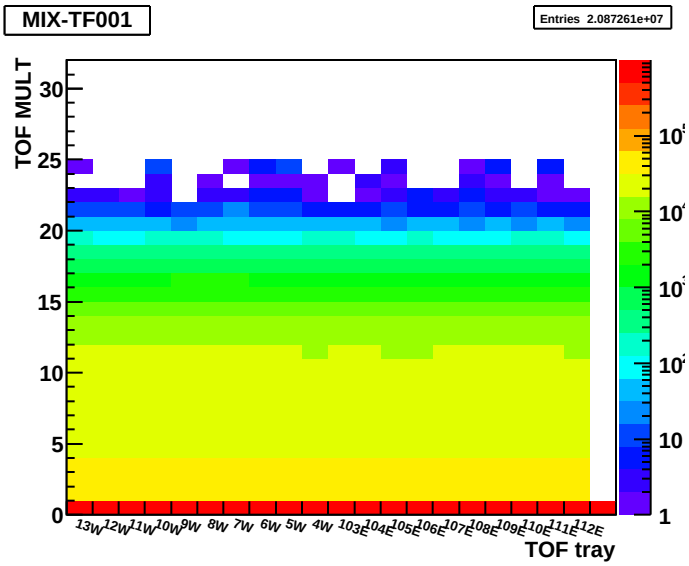

Barrel EMC L0 Input - Patch Sum
Entries 1.490901e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

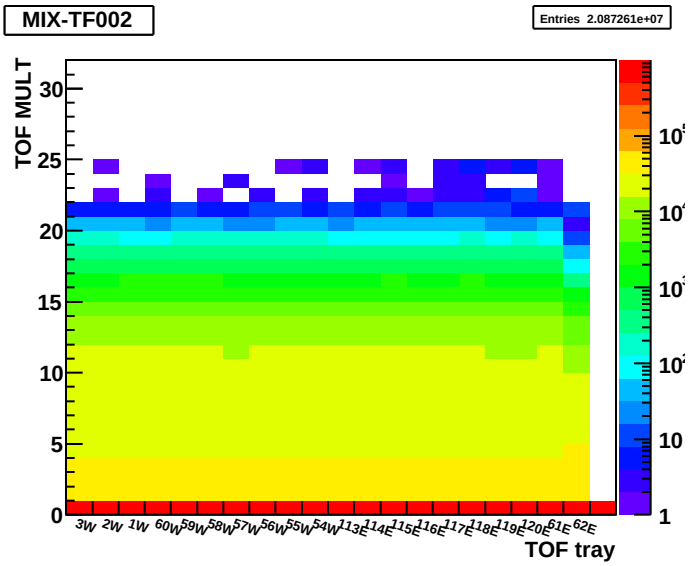

Endcap EMC L0 Input - High Tower
Entries 4.472703e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

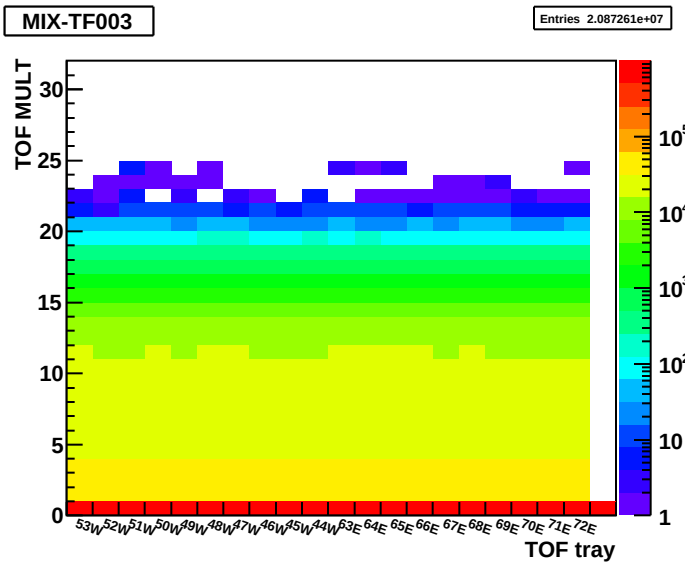

Endcap EMC L0 Input - Patch Sum
Entries 4.472703e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

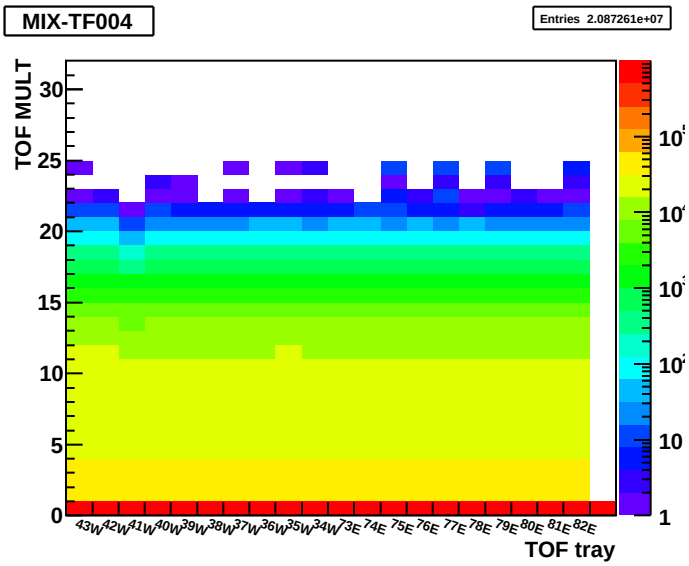

Barrel Jet Patches
Entries 8945406
JP ADC
JP ID

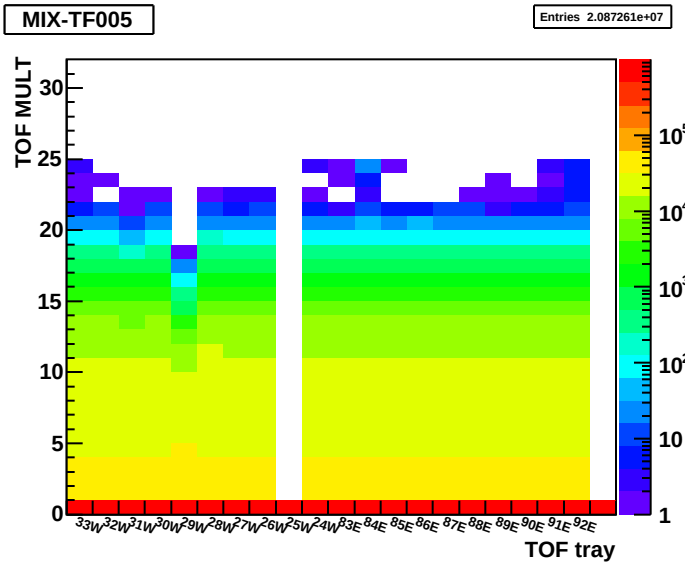

Hybrid Jet Patches
Entries 993934
JP ADC
JP ID

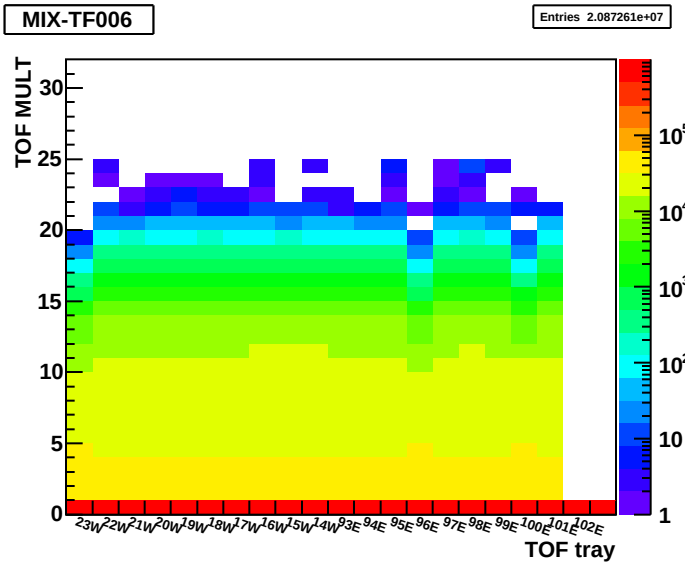

MIX-TF001
Entries 2.087261e+07
TOF MULT
TOF tray

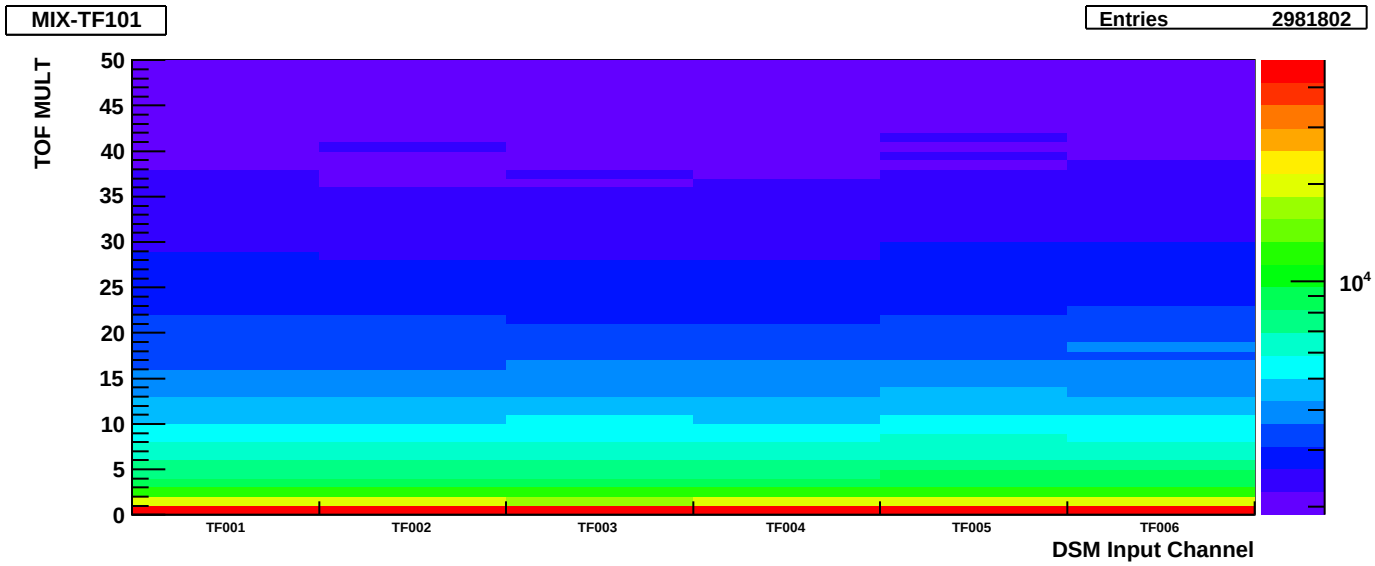

MIX-TF002
Entries 2.087261e+07
TOF MULT
TOF tray

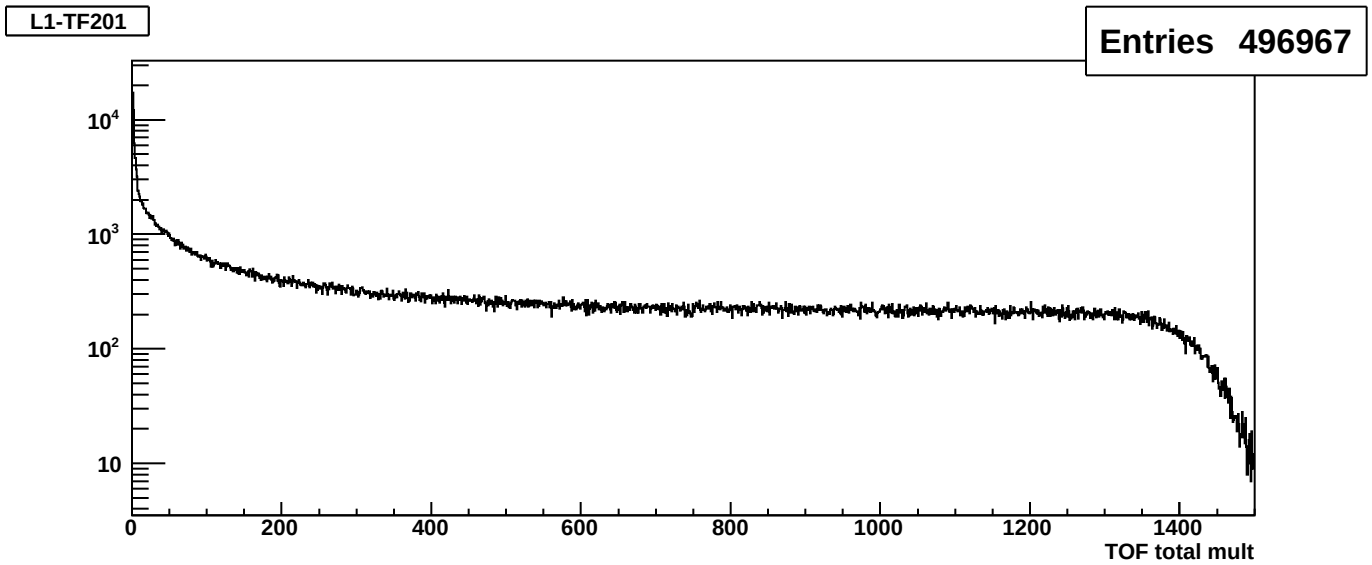

MIX-TF003
Entries 2.087261e+07
TOF MULT
TOF tray

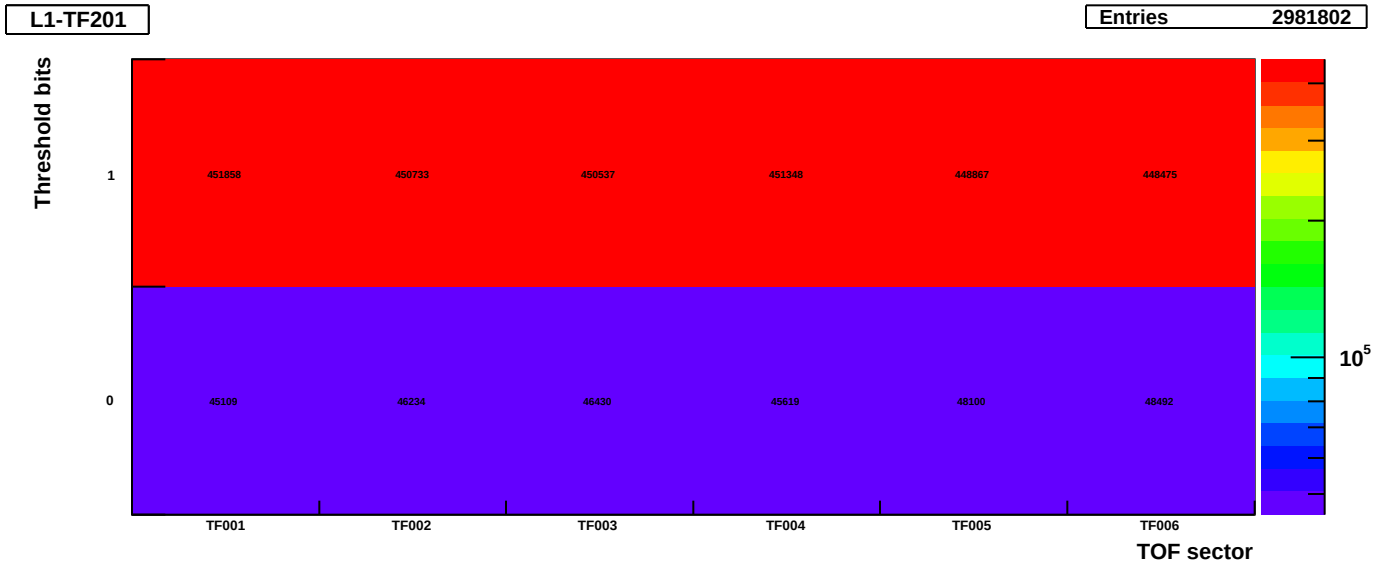

MIX-TF004
Entries 2.087261e+07
TOF MULT
TOF tray

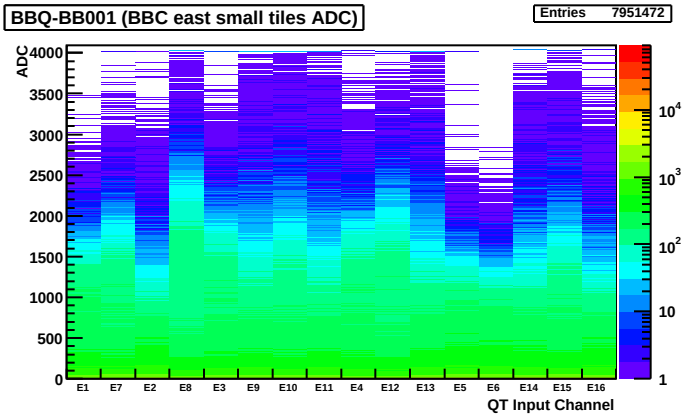

MIX-TF005
Entries 2.087261e+07
TOF MULT
TOF tray

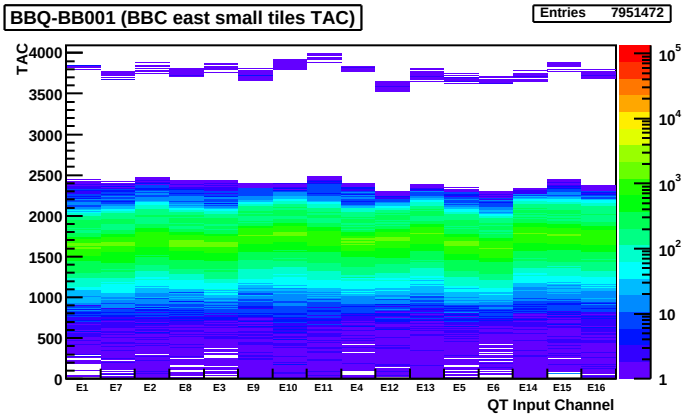

MIX-TF006
Entries 2.087261e+07
TOF MULT
TOF tray

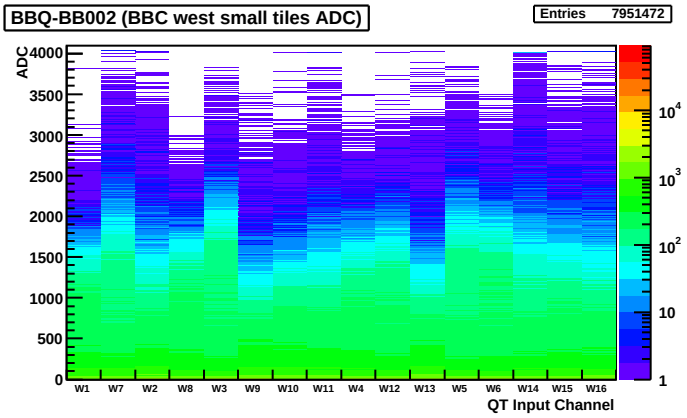

MIX-TF101
Entries 2981802
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

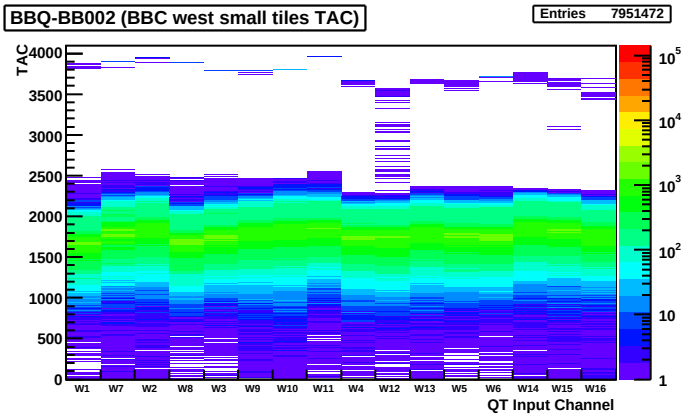

L1-TF201
Entries 496967
10^4
10^3
10^2
10
0
200
400
600
800
1000
1200
1400
TOF total mult

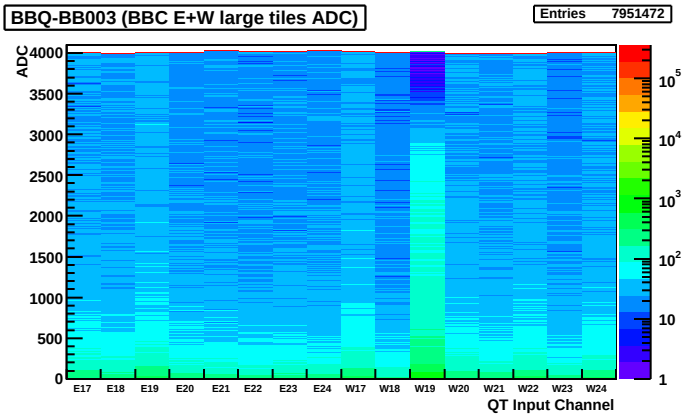

L1-TF201
Entries 2981802
Threshold bits
1
0
451858
450733
450537
451348
448867
448475
45109
46234
46430
45619
48100
48492
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

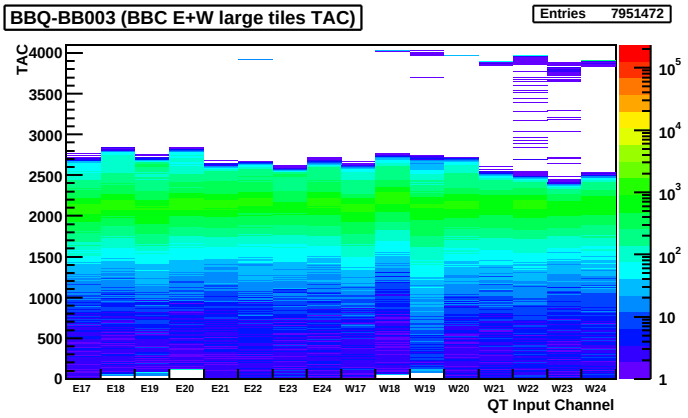

BBQ-BB001 (BBC east small tiles ADC)
Entries 7951472
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

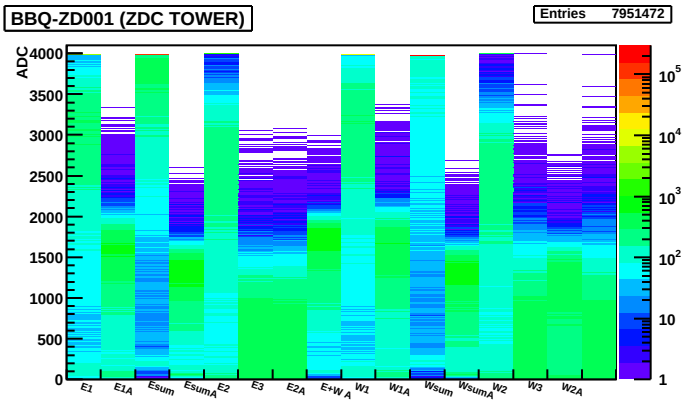

BBQ-BB001 (BBC east small tiles TAC)
Entries 7951472
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

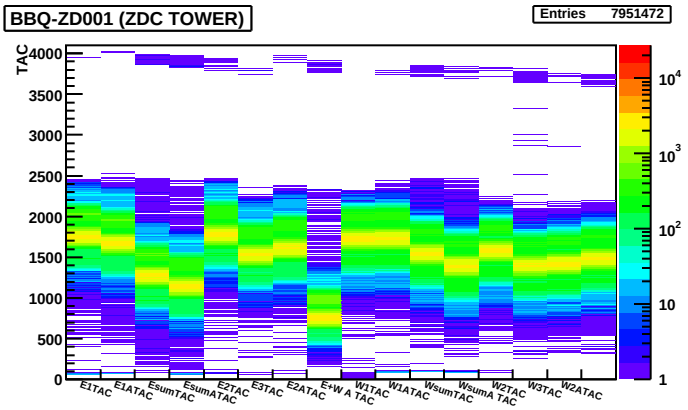

BBQ-BB002 (BBC west small tiles ADC)
Entries 7951472
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

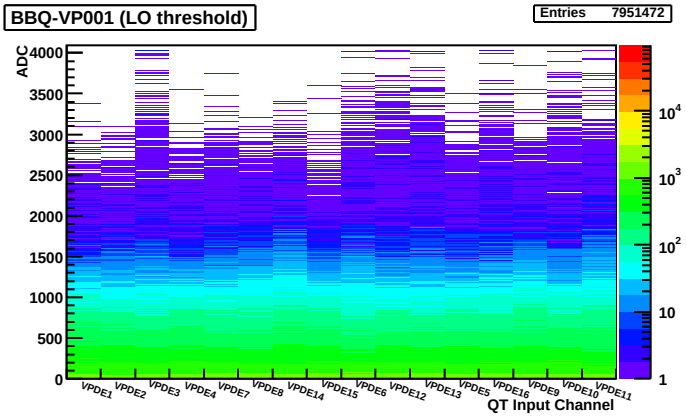

BBQ-BB002 (BBC west small tiles TAC)
Entries 7951472
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

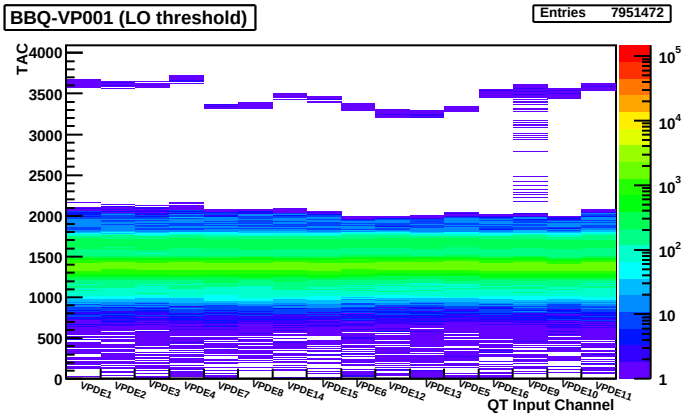

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 7951472
ADC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

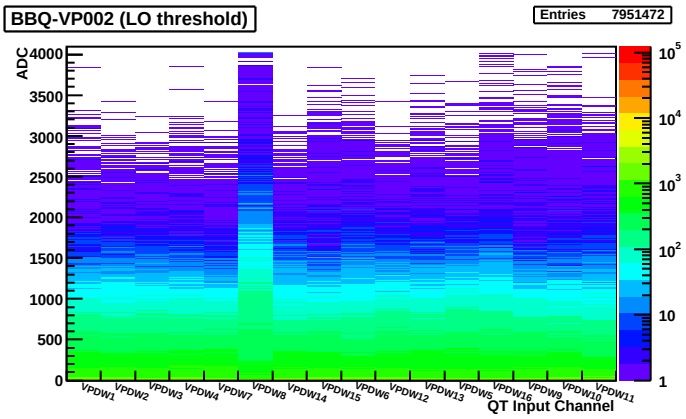

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 7951472
TAC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

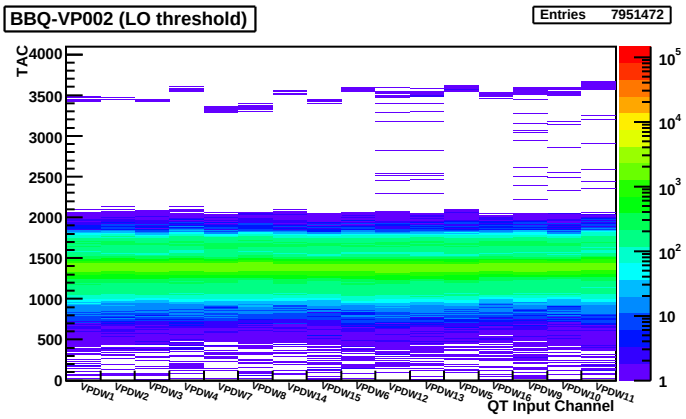

BBQ-ZD001 (ZDC TOWER)
Entries 7951472
ADC
E1 E1A Esum EsumA E2 E3 E2A E+W A W1 W1A Wsum WsumA W2 W3 W2A

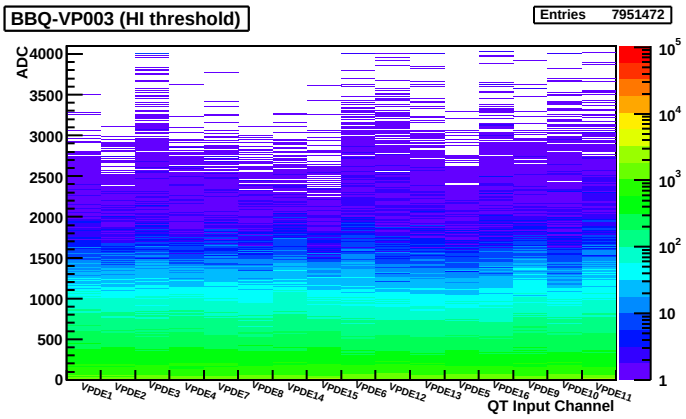

BBQ-ZD001 (ZDC TOWER)
Entries 7951472
TAC
E1TAC E1ATAC EsumTAC EsumATAC E2TAC E3TAC E2ATAC E+W A TAC W1TAC W1ATAC WsumTAC WsumA TAC W2TAC W3TAC W2ATAC

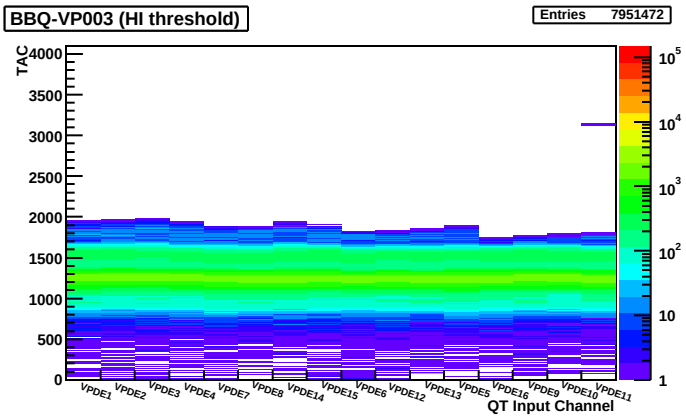

BBQ-VP001 (LO threshold)
Entries 7951472
ADC
QT Input Channel

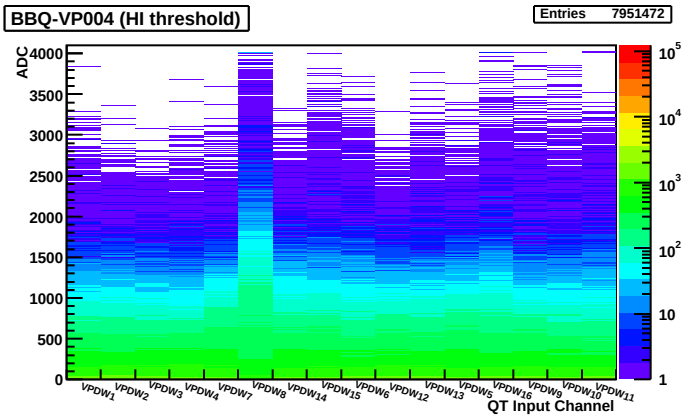

BBQ-VP001 (LO threshold)
Entries 7951472
TAC
QT Input Channel

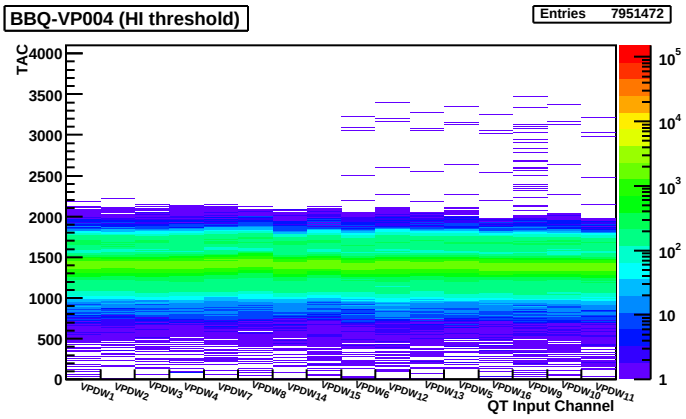

BBQ-VP002 (LO threshold)
Entries 7951472
ADC
QT Input Channel

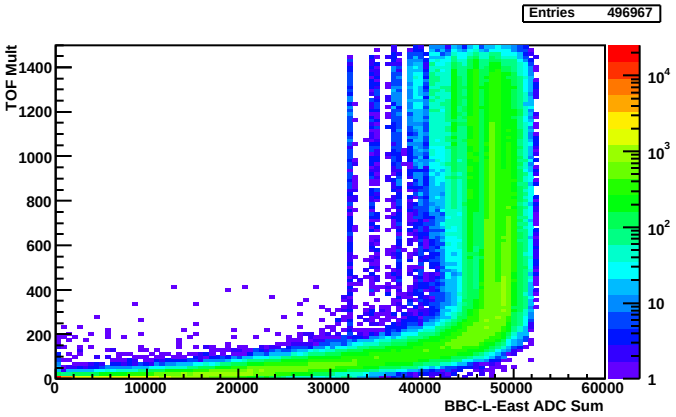

BBQ-VP002 (LO threshold)
Entries 7951472
TAC
QT Input Channel

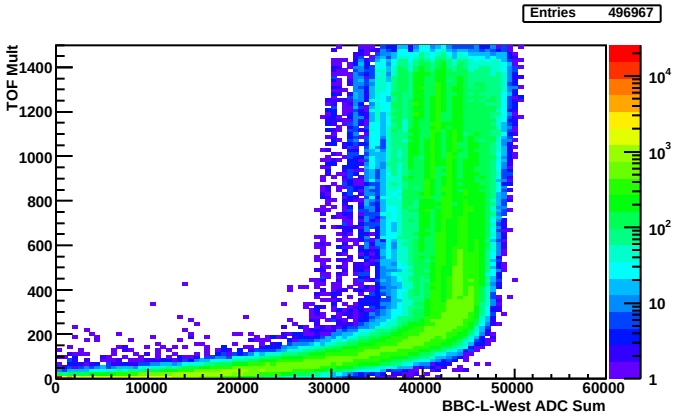

BBQ-VP003 (HI threshold)
Entries 7951472
ADC
QT Input Channel

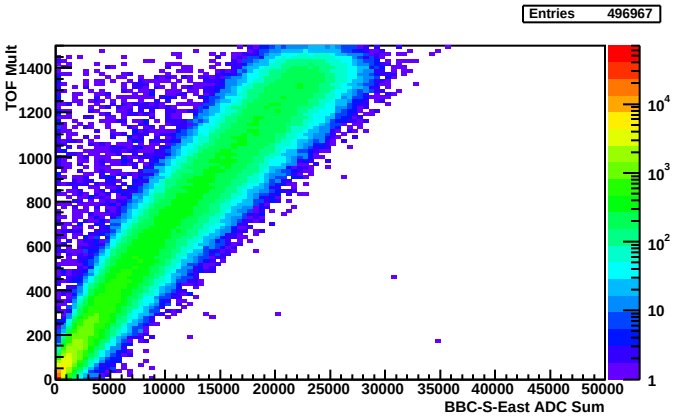

BBQ-VP003 (HI threshold)
Entries 7951472
TAC
QT Input Channel

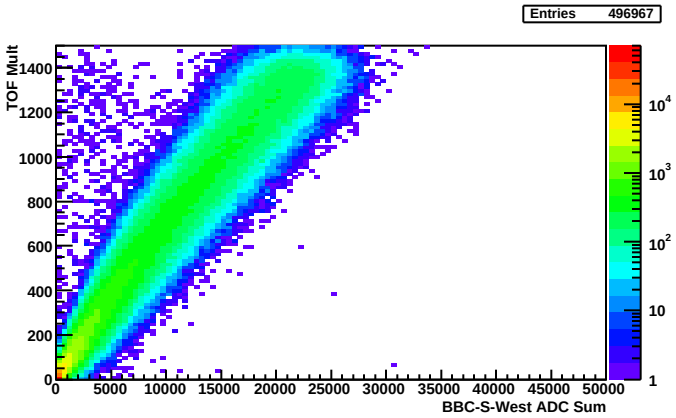

BBQ-VP004 (HI threshold)
Entries 7951472
ADC
QT Input Channel

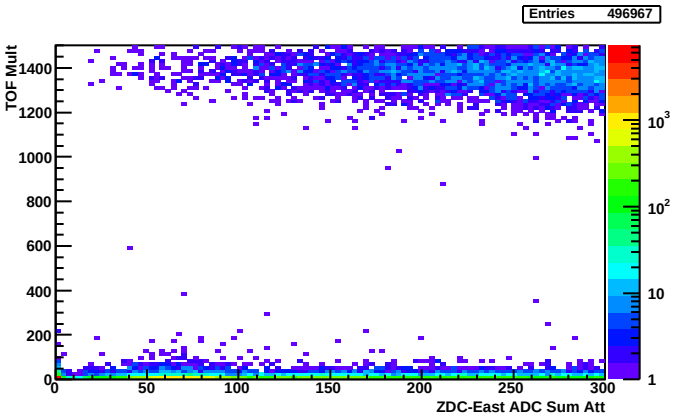

BBQ-VP004 (HI threshold)
Entries 7951472
TAC
QT Input Channel

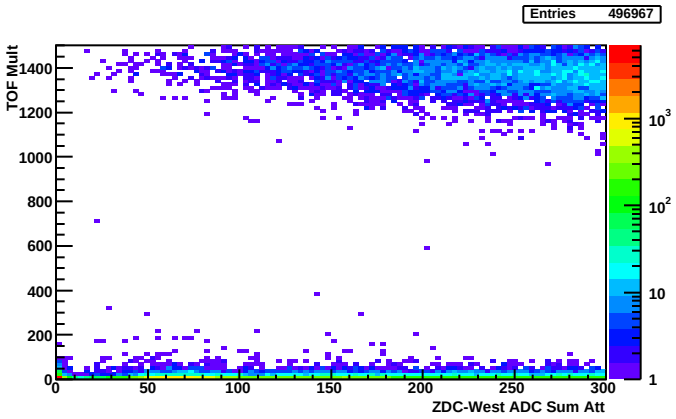

Entries 496967
TOF Mult
BBC-L-East ADC Sum

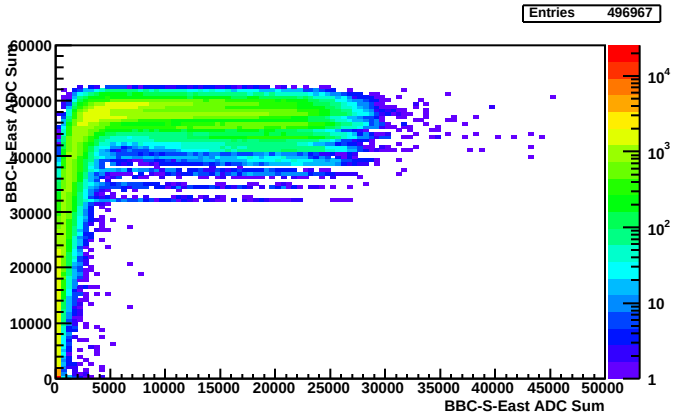

Entries 496967
TOF Mult
BBC-L-West ADC Sum

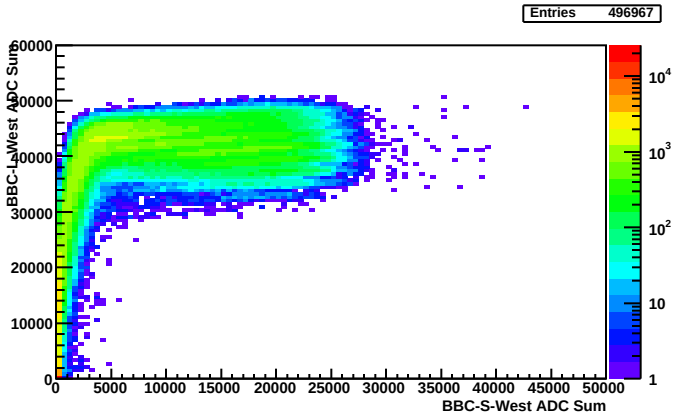

Entries 496967
TOF Mult
BBC-S-East ADC Sum

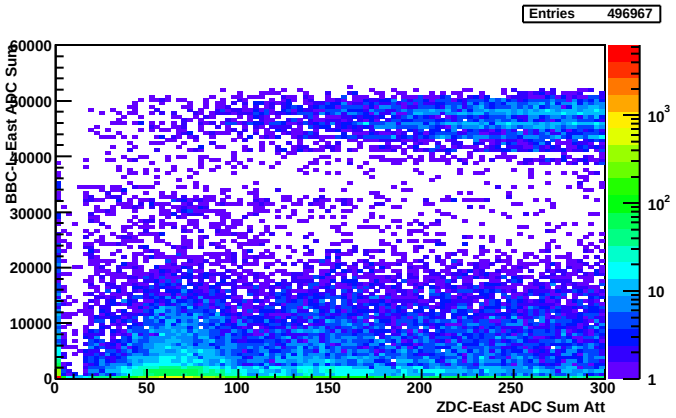

Entries 496967
TOF Mult
BBC-S-West ADC Sum

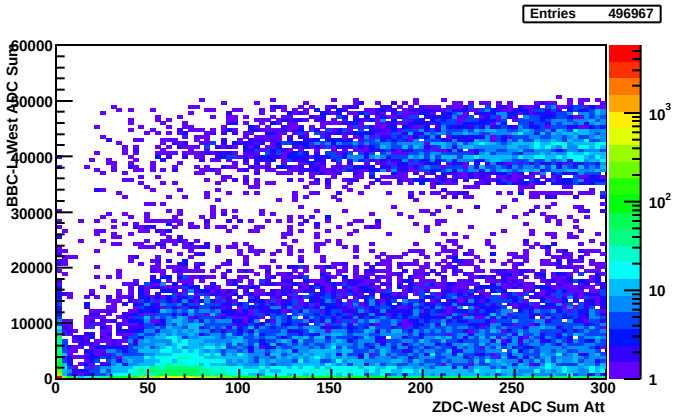

Entries 496967
TOF Mult
ZDC-East ADC Sum Att

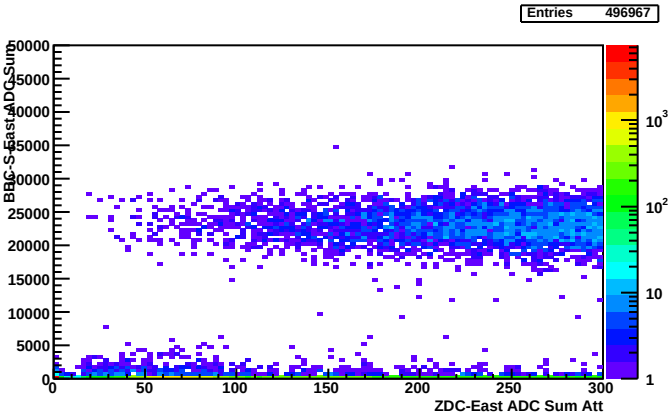

Entries 496967
TOF Mult
ZDC-West ADC Sum Att

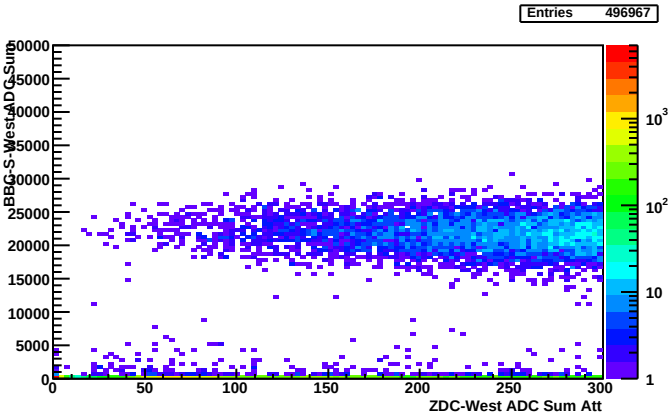

Entries 496967
BBC-L-East ADC Sum
BBC-S-East ADC Sum

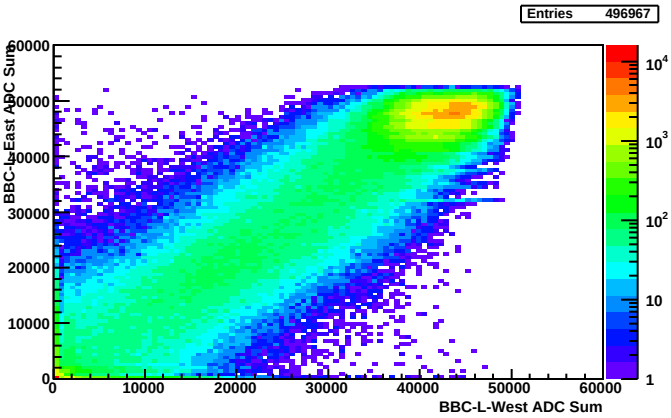

Entries 496967
BBC-L-West ADC Sum
BBC-S-West ADC Sum

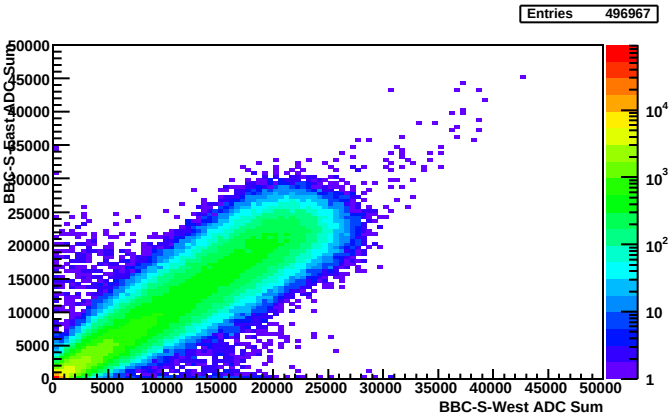

Entries 496967
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

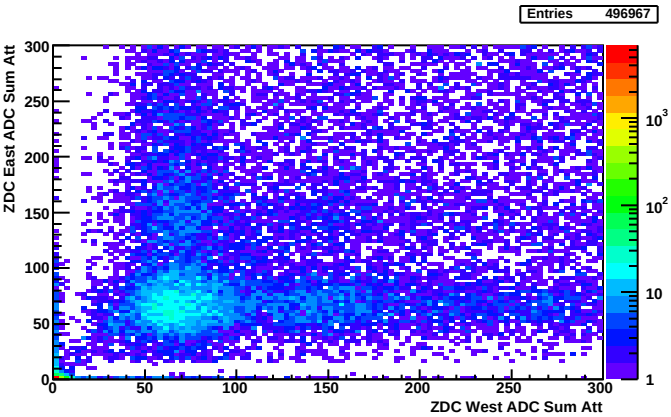

Entries 496967
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

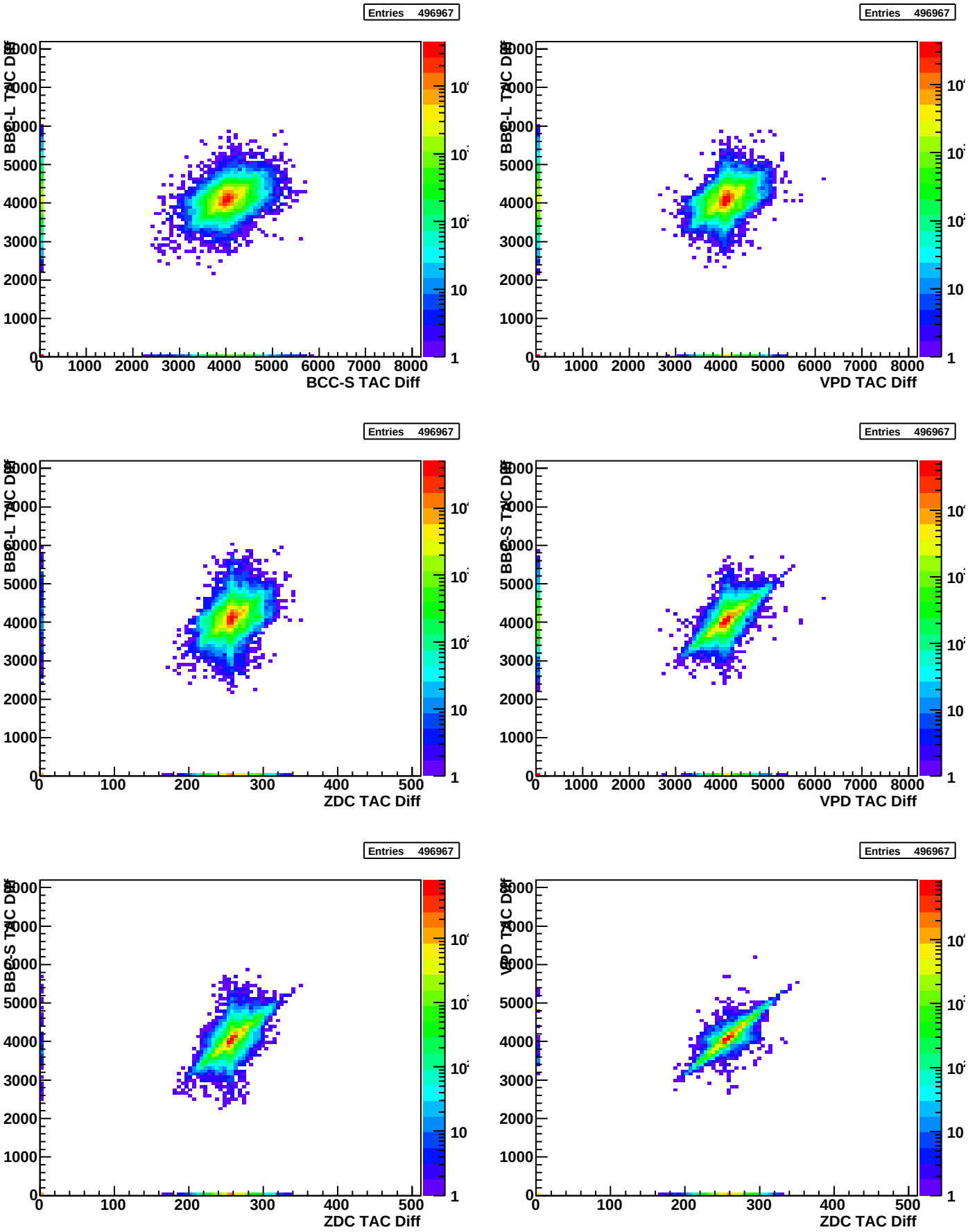

Entries 496967
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

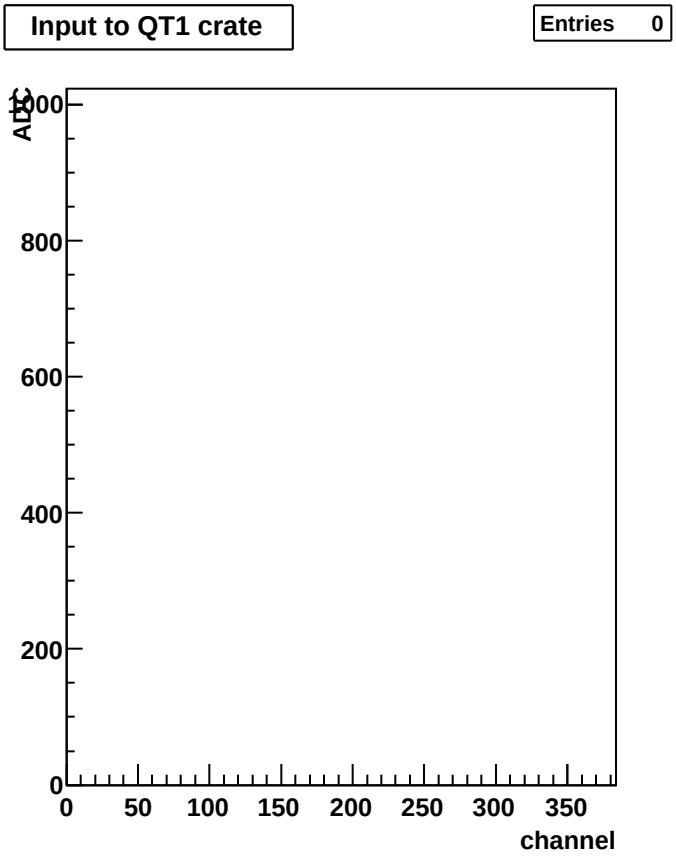

Entries 496967
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

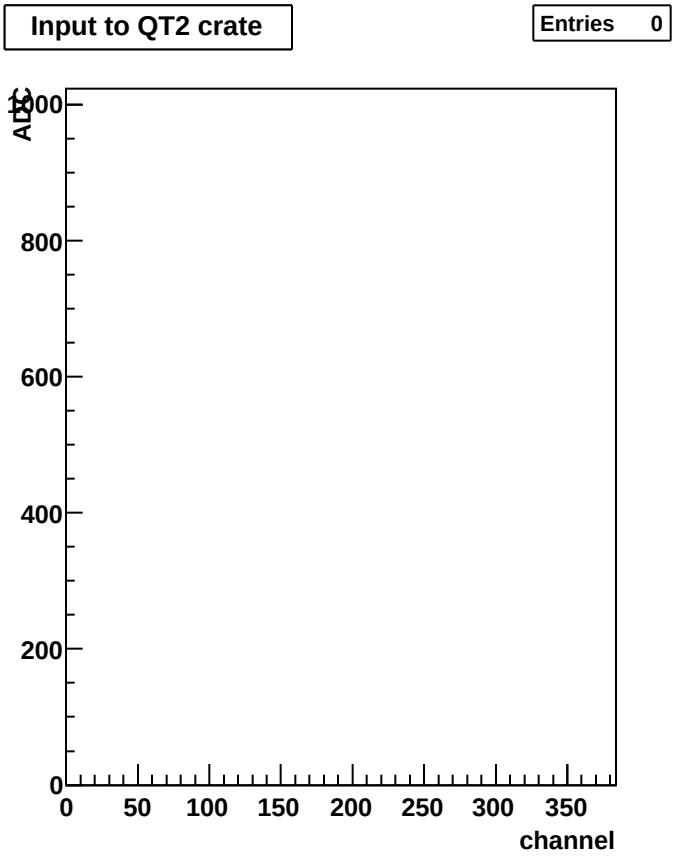

Entries 496967
BBC-L-East ADC Sum
BBC-L-West ADC Sum

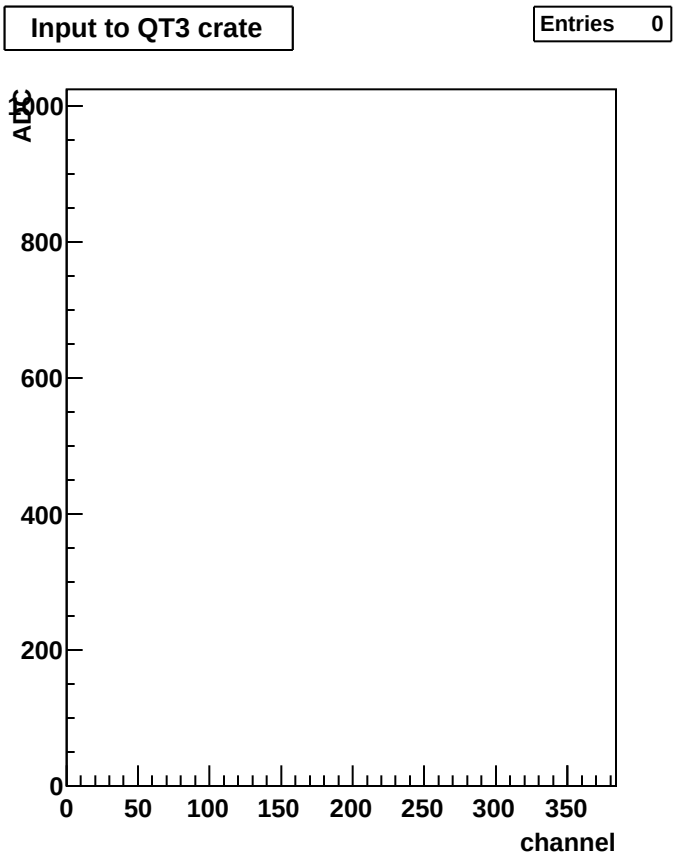

Entries 496967
BBC-S-East ADC Sum
BBC-S-West ADC Sum

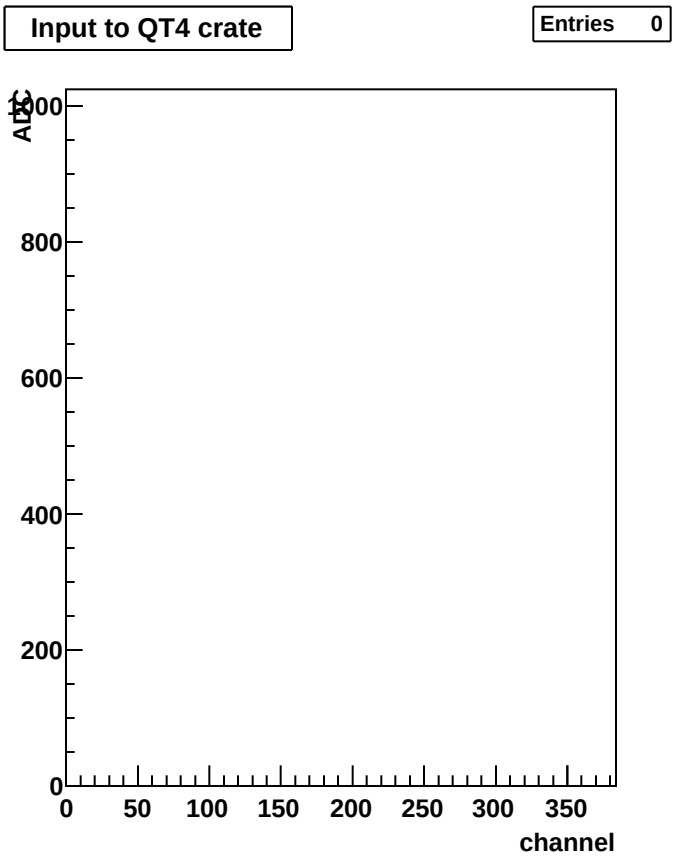

Entries 496967
ZDC East ADC Sum Att
ZDC West ADC Sum Att

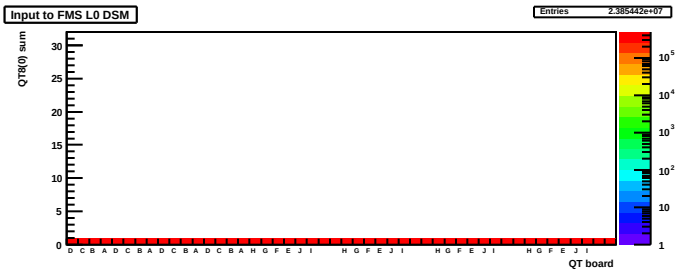

Entries 496967
BBC-L TAC Diff
BCC-S TAC Diff
Entries 496967
BBC-L TAC Diff
VPD TAC Diff
Entries 496967
BBC-L TAC Diff
ZDC TAC Diff
Entries 496967
BBC-S TAC Diff
VPD TAC Diff
Entries 496967
BBC-S TAC Diff
ZDC TAC Diff
Entries 496967
VPD TAC Diff
ZDC TAC Diff

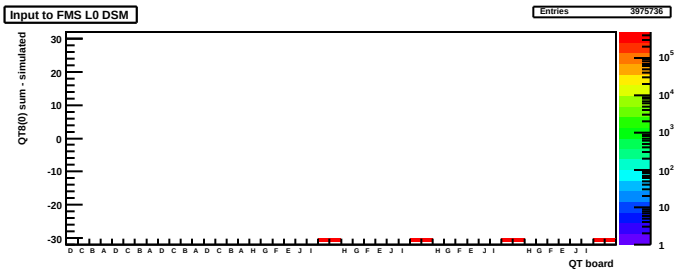

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

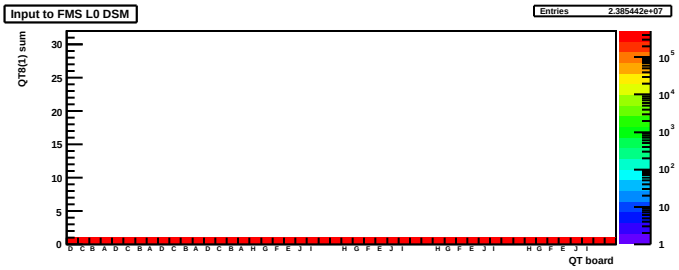

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

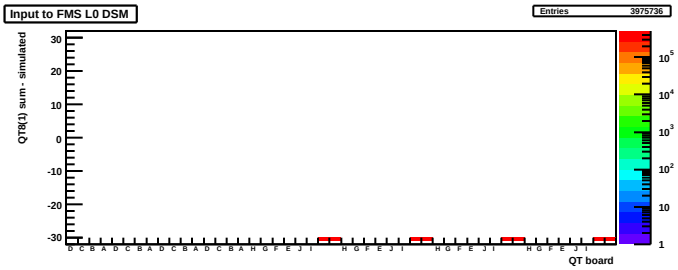

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

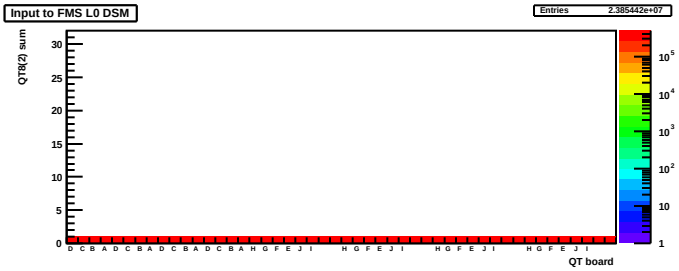

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

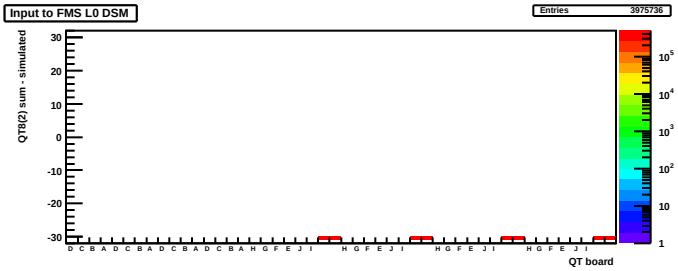

Input to FMS L0 DSM
Entries 2.385442e+07
QT8(0) sum
QT board

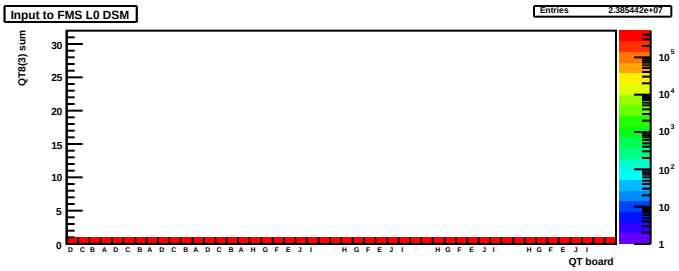

Input to FMS L0 DSM
Entries 3975736
QT8(0) sum - simulated
QT board

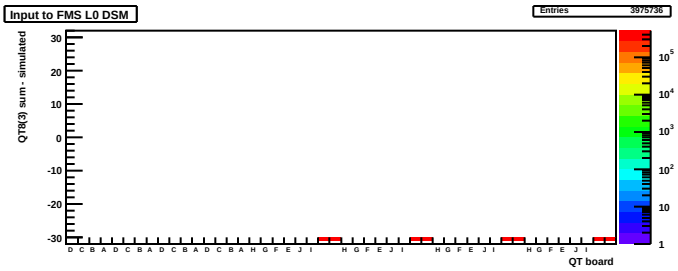

Input to FMS L0 DSM
Entries 2.385442e+07
QT8(1) sum
QT board

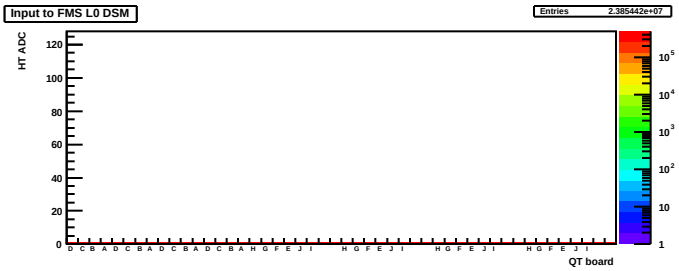

Input to FMS L0 DSM
Entries 3975736
QT8(1) sum - simulated
QT board

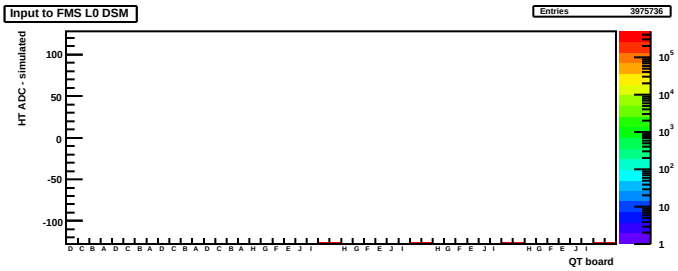

Input to FMS L0 DSM
Entries 2.385442e+07
QT8(2) sum
QT board

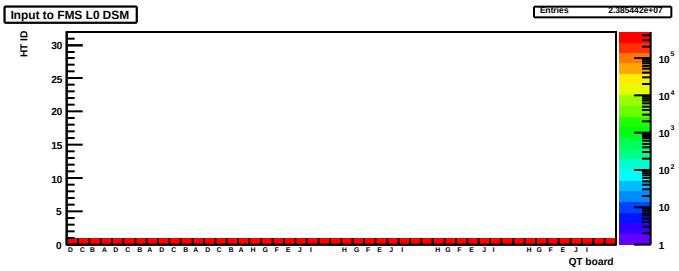

Input to FMS L0 DSM
Entries 3975736
QT8(2) sum - simulated
QT board

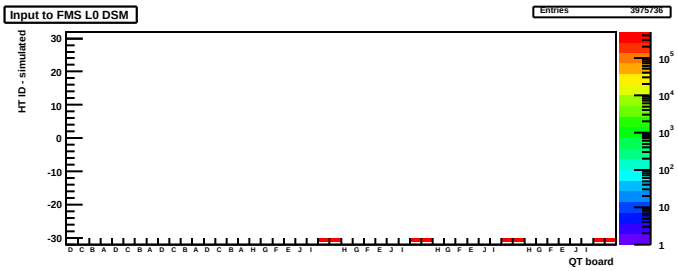

Input to FMS L0 DSM
Entries 2.385442e+07
QT8(3) sum
QT board

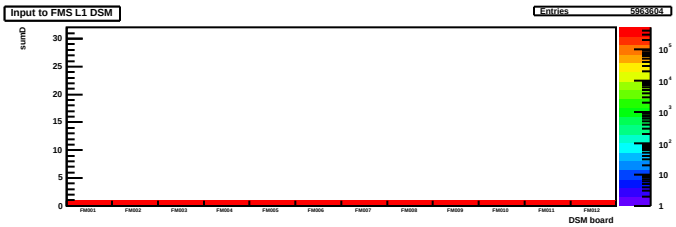

Input to FMS L0 DSM
Entries 3975736
QT8(3) sum - simulated
QT board

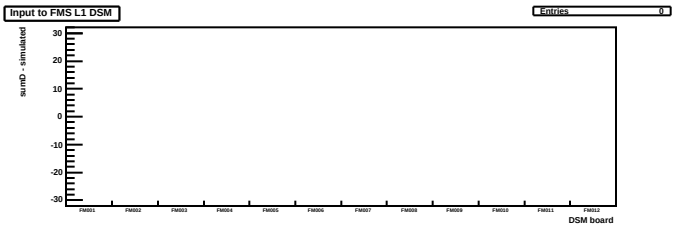

Input to FMS L0 DSM
Entries 2.385442e+07
HT ADC
QT board

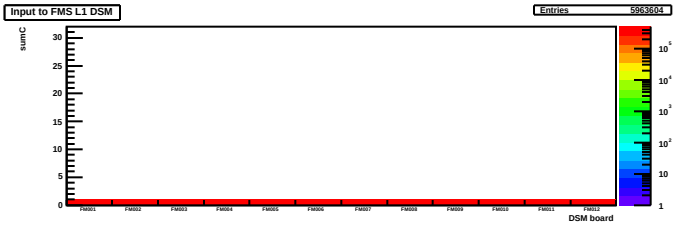

Input to FMS L0 DSM
Entries 3975736
HT ADC - simulated
QT board

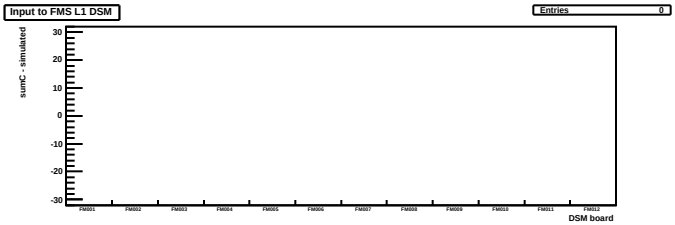

Input to FMS L0 DSM
Entries 2.385442e+07
HT ID
QT board

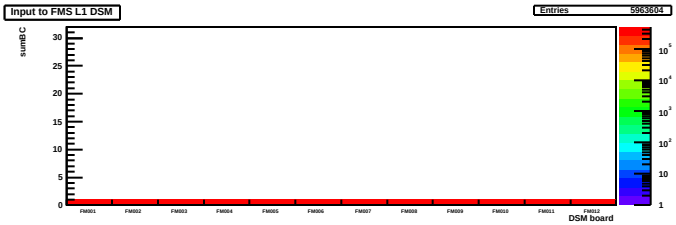

Input to FMS L0 DSM
Entries 3975736
HT ID - simulated
QT board

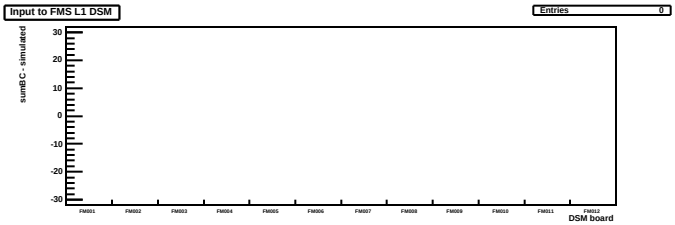

Input to FMS L1 DSM
Entries 5963604
sumD
DSM board

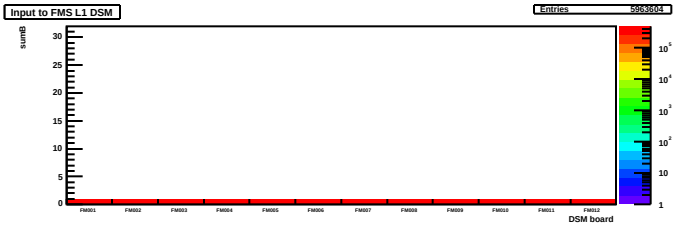

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

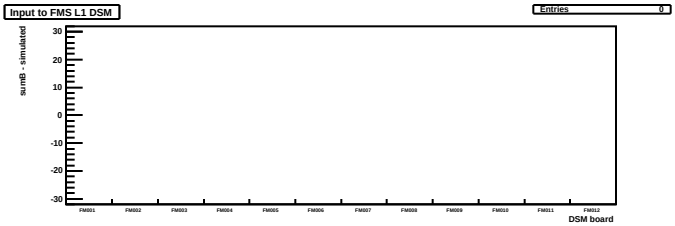

Input to FMS L1 DSM
Entries 5963604
sumC
DSM board

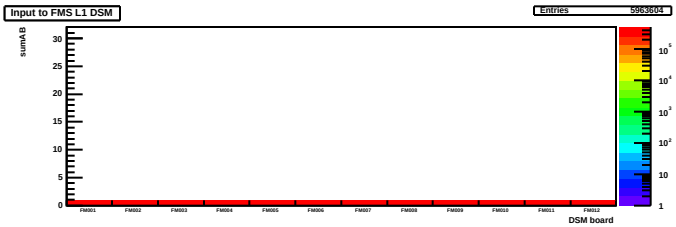

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

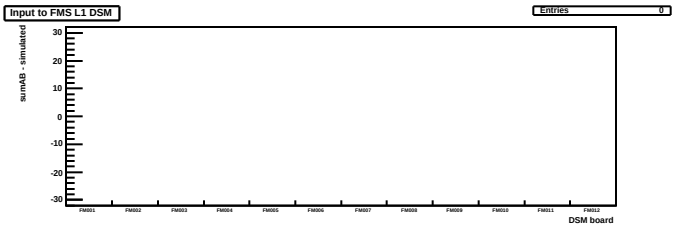

Input to FMS L1 DSM
Entries 5963604
sumBC
DSM board

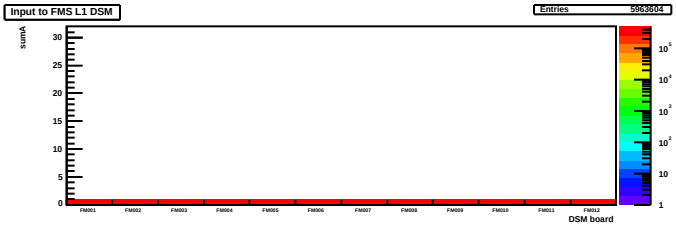

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

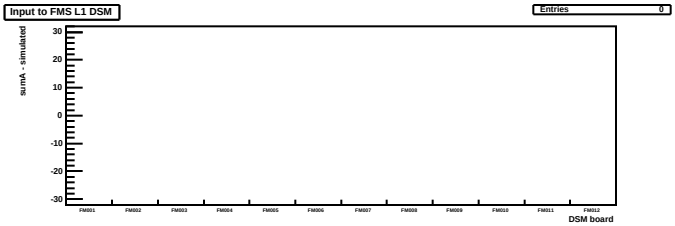

Input to FMS L1 DSM
Entries 5963604
sumB
DSM board

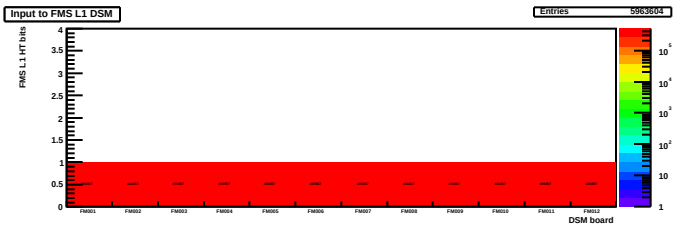

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

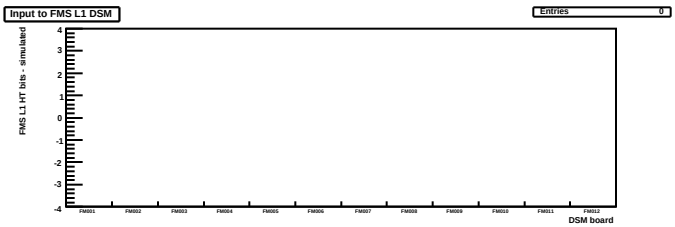

Input to FMS L1 DSM
Entries 5963604
sumAB
DSM board

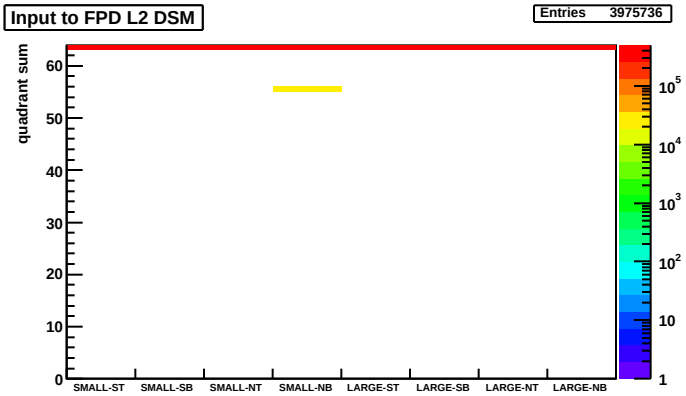

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

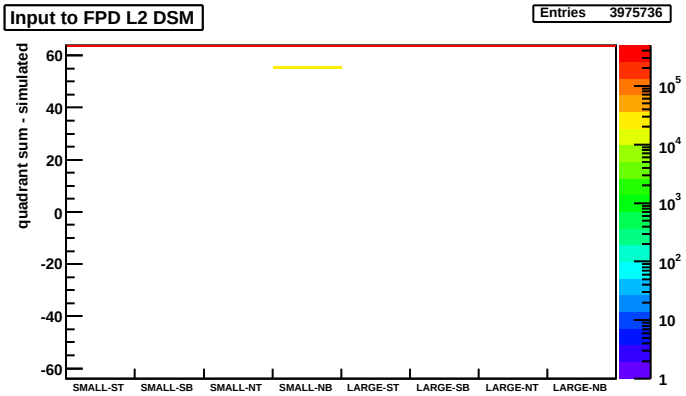

Input to FMS L1 DSM
Entries 5963604
sumA
DSM board

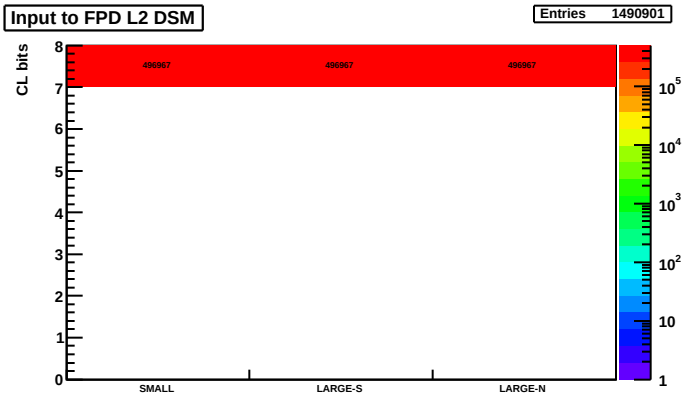

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

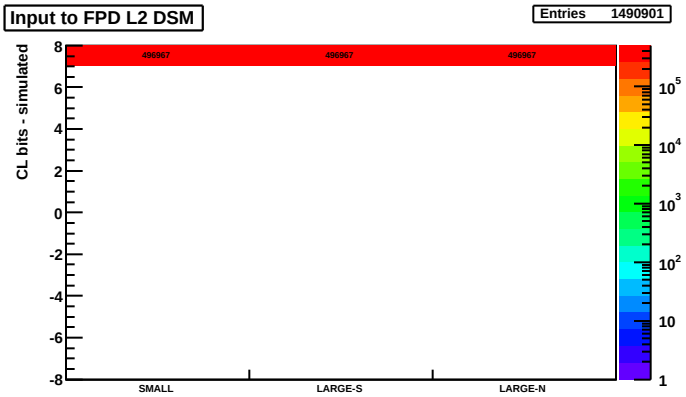

Input to FMS L1 DSM
Entries 5963604
FMS L1 HT bits
DSM board

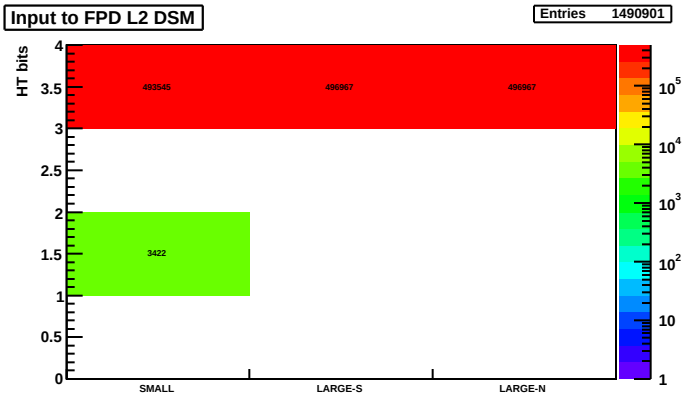

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

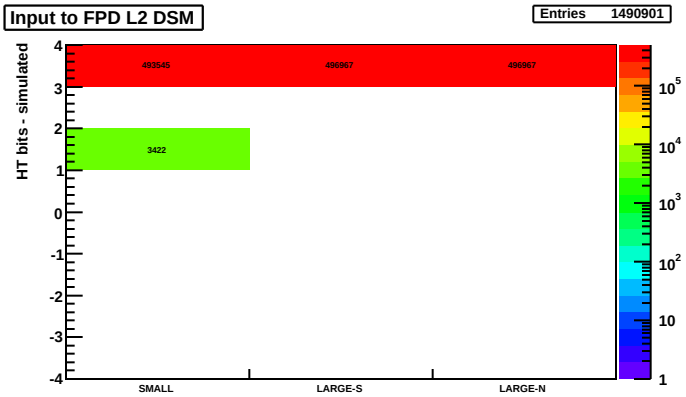

Input to FPD L2 DSM
Entries 3975736
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

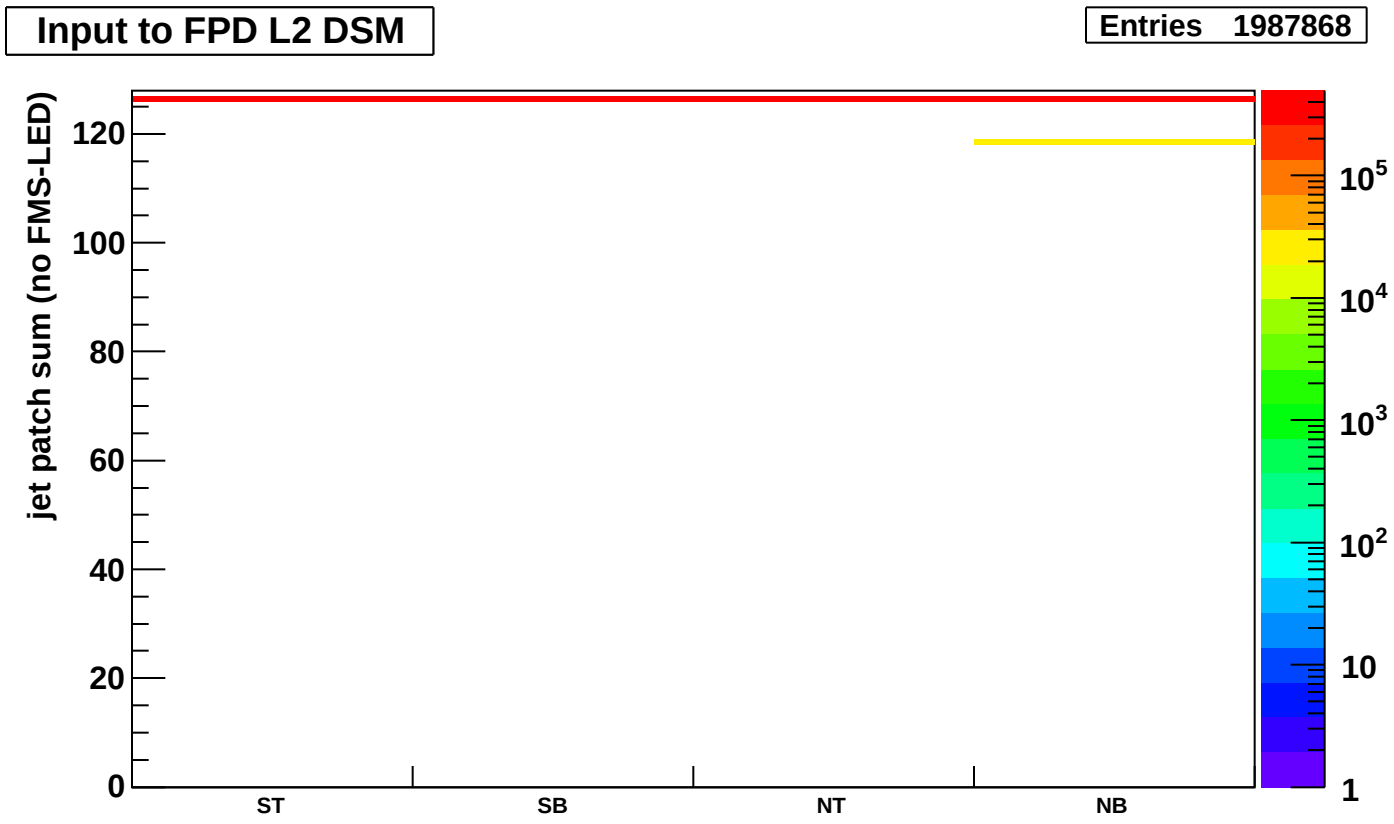

Input to FPD L2 DSM
Entries 3975736
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

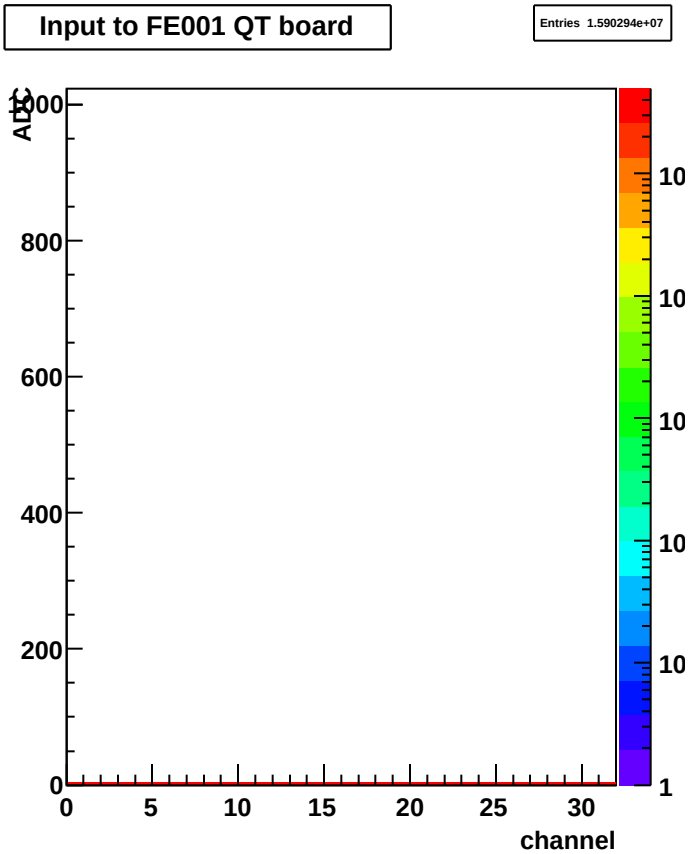

Input to FPD L2 DSM
Entries 1490901
CL bits
496967
496967
496967
SMALL
LARGE-S
LARGE-N

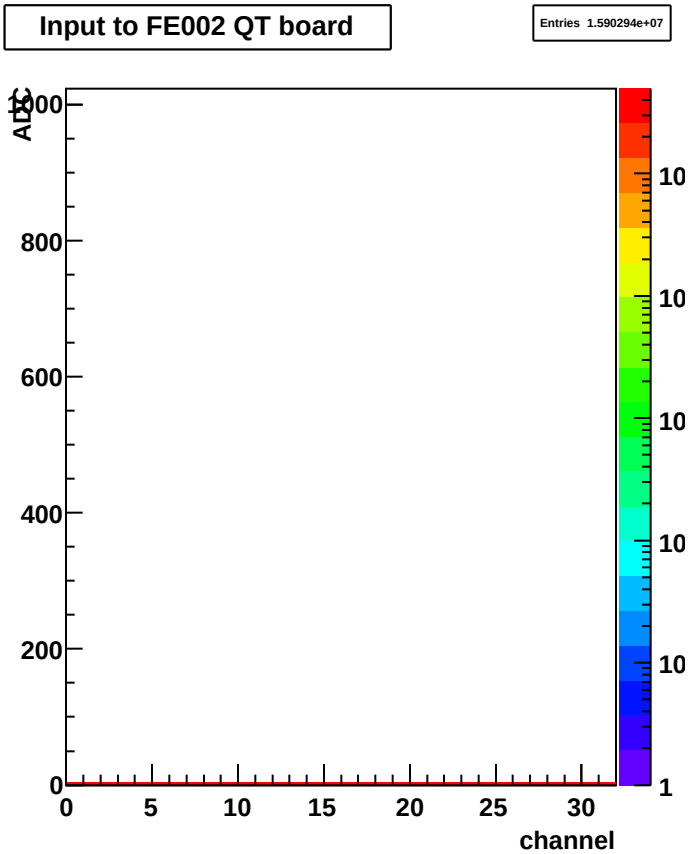

Input to FPD L2 DSM
Entries 1490901
CL bits - simulated
496967
496967
496967
SMALL
LARGE-S
LARGE-N

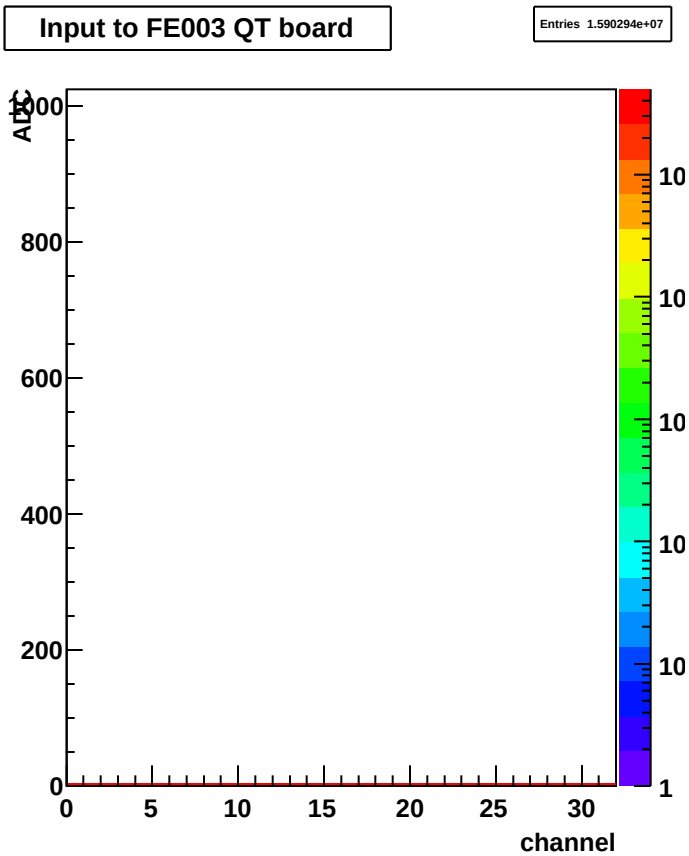

Input to FPD L2 DSM
Entries 1490901
HT bits
493545
496967
496967
3422
SMALL
LARGE-S
LARGE-N

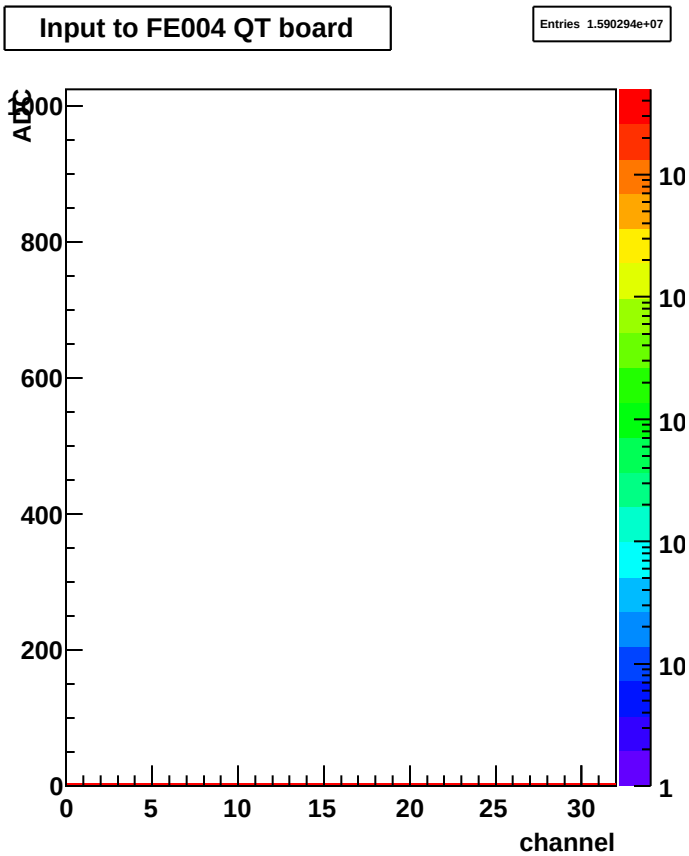

Input to FPD L2 DSM
Entries 1490901
HT bits - simulated
493545
496967
496967
3422
SMALL
LARGE-S
LARGE-N


Input to FPD L2 DSM
Entries 1987868
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

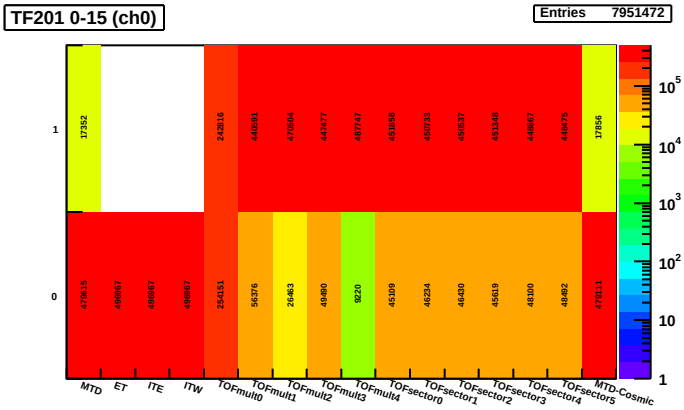

Input to FE001 QT board
Entries 1.590294e+07
ADC
channel


Input to FE002 QT board
Entries 1.590294e+07
ADC
channel

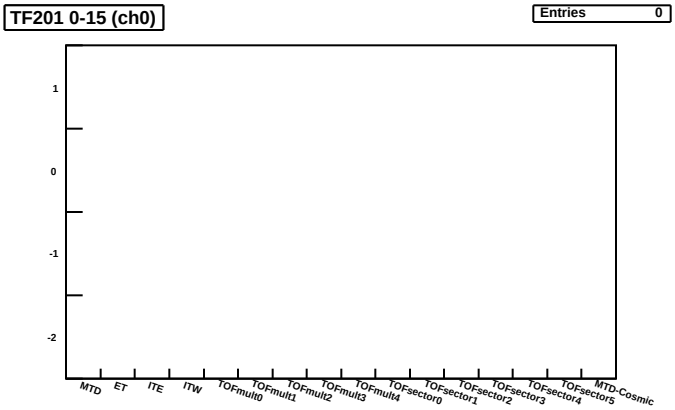

Input to FE003 QT board
Entries 1.590294e+07
ADC
channel


Input to FE004 QT board
Entries 1.590294e+07
ADC
channel

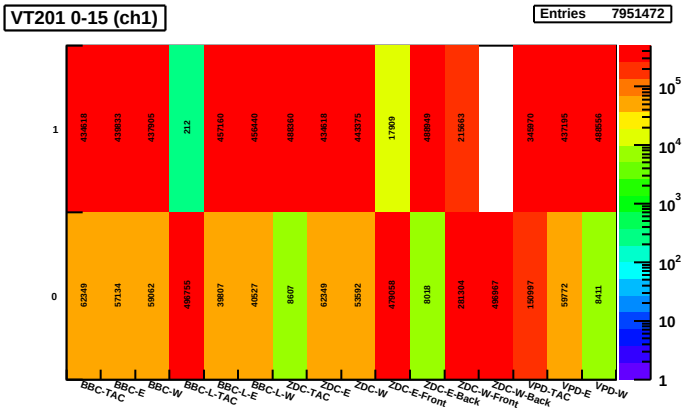
TF201 0-15 (ch0)



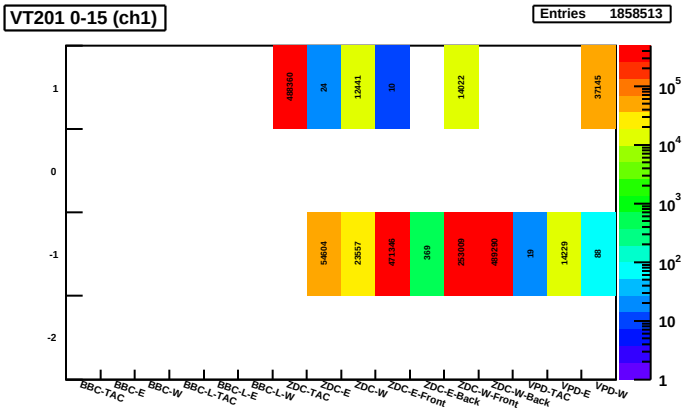
TF201 0-15 (ch0)



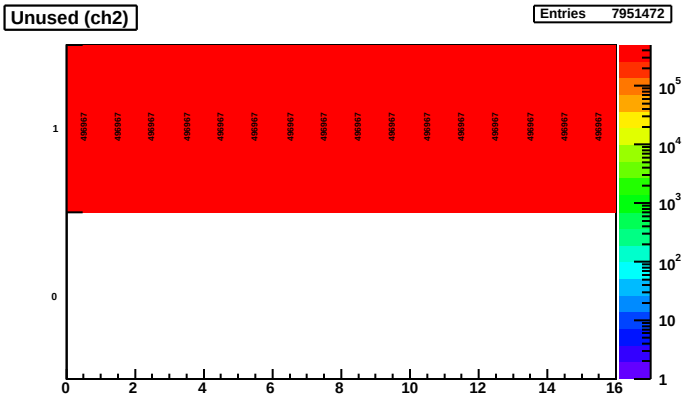
VT201 0-15 (ch1)

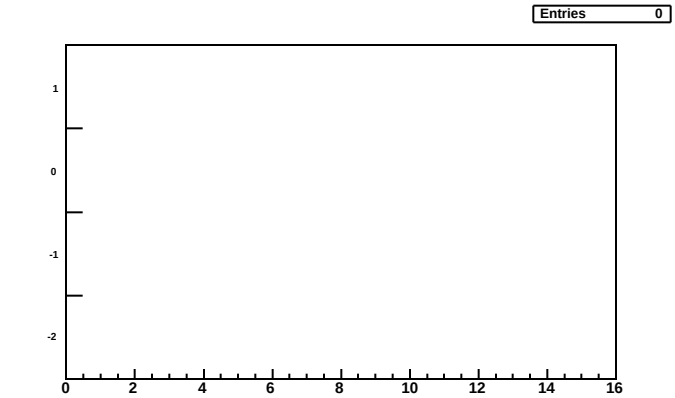


VT201 0-15 (ch1)

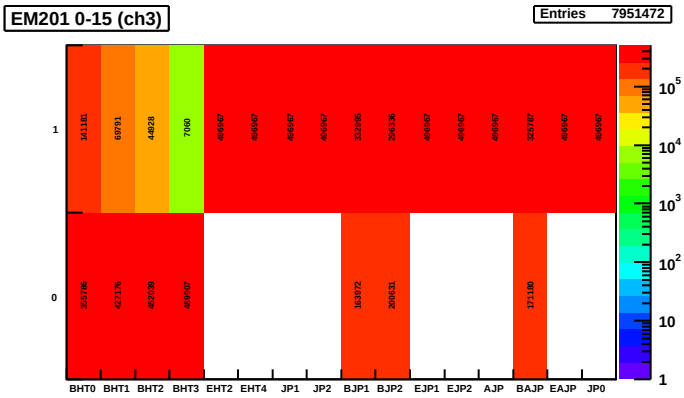


Unused (ch2)

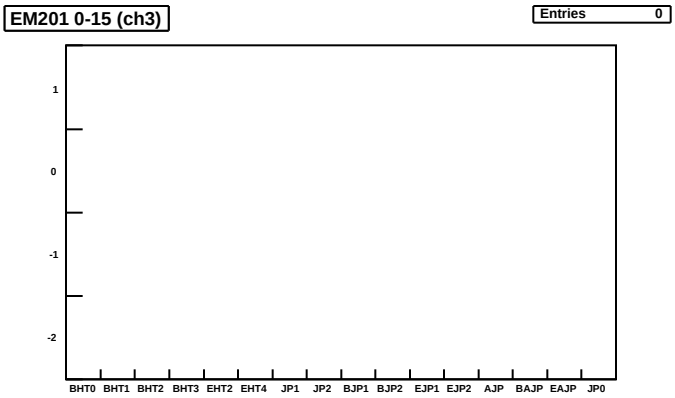




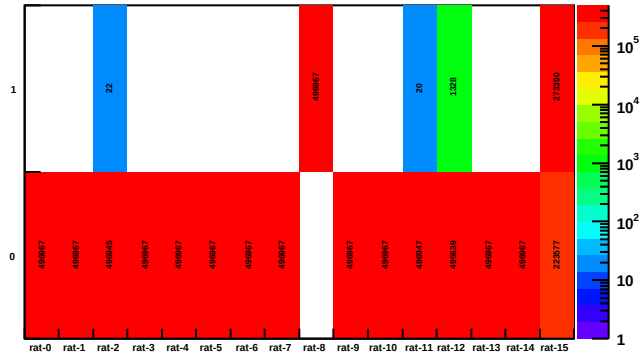
EM201 0-15 (ch3)

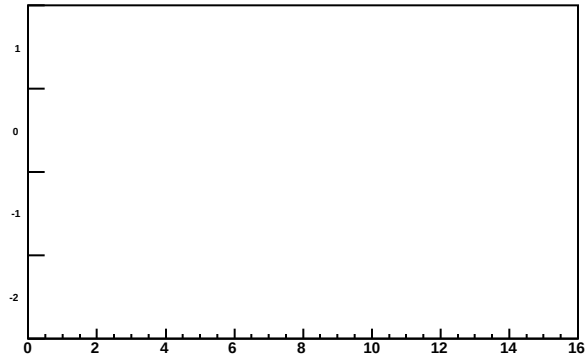


EM201 0-15 (ch3)

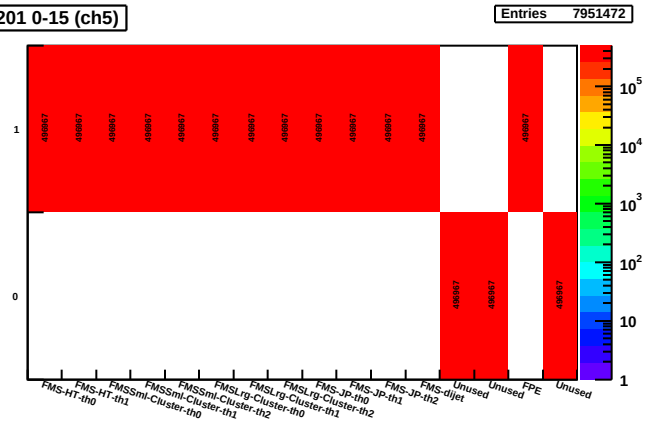


RAT board (ch4)

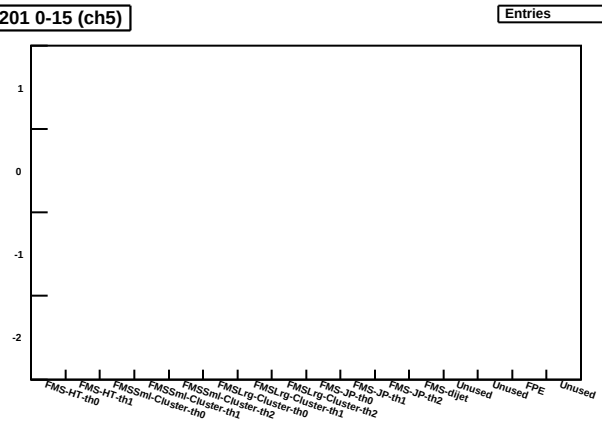




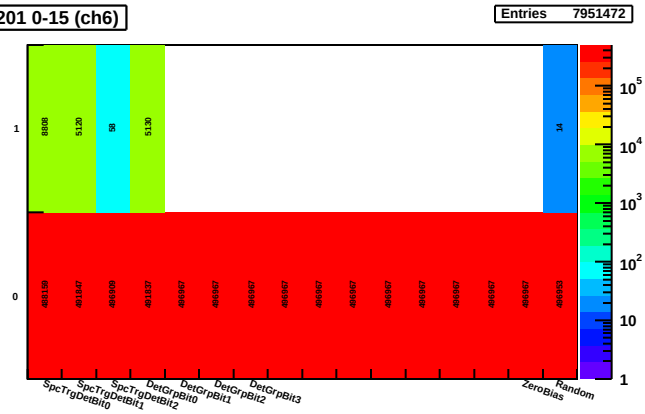
FP201 0-15 (ch5)

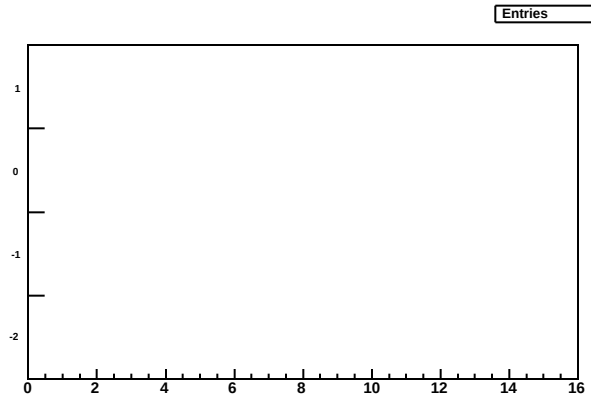
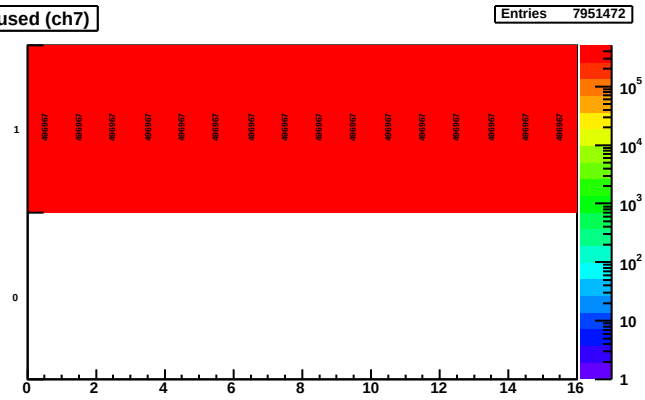
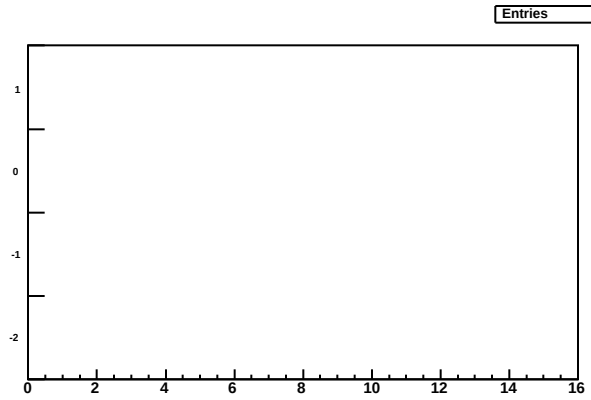


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)