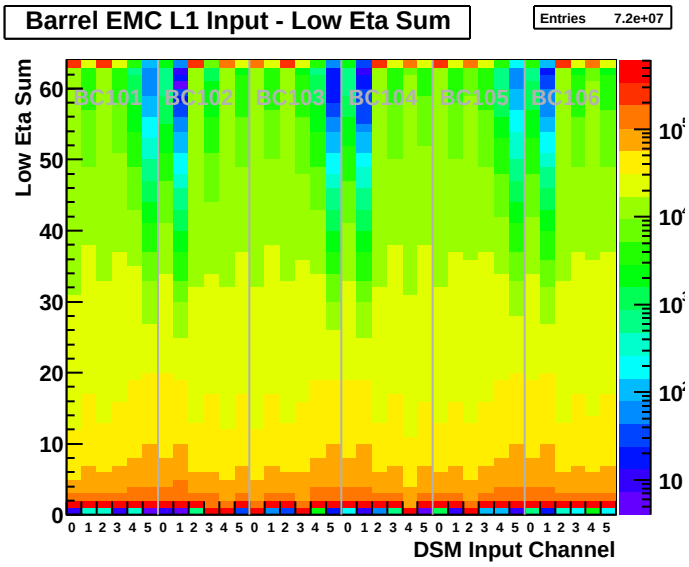
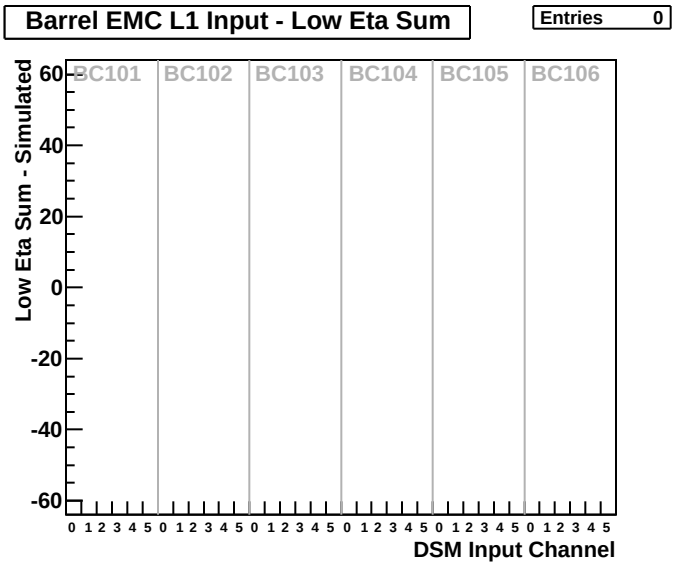
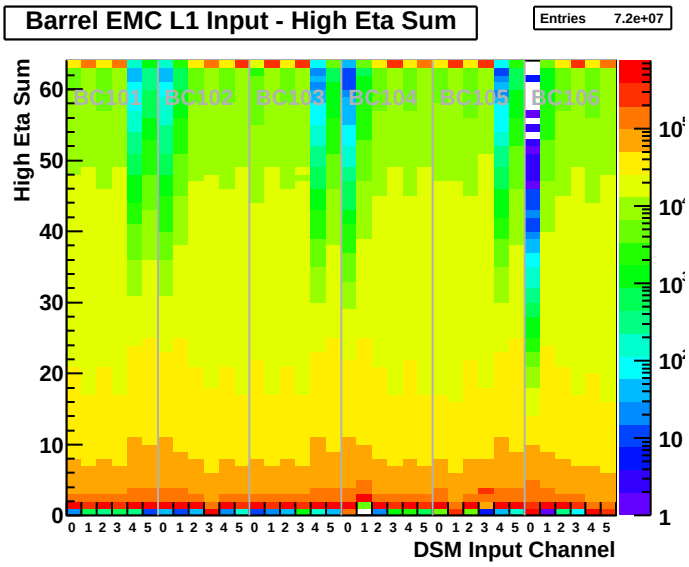
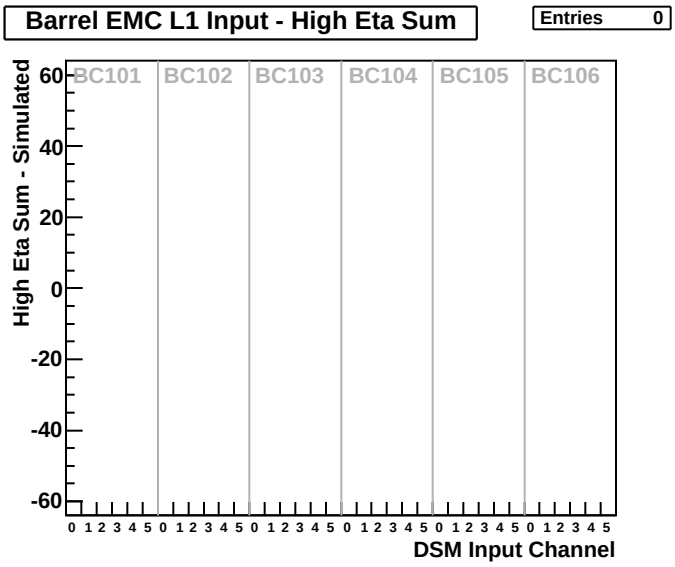
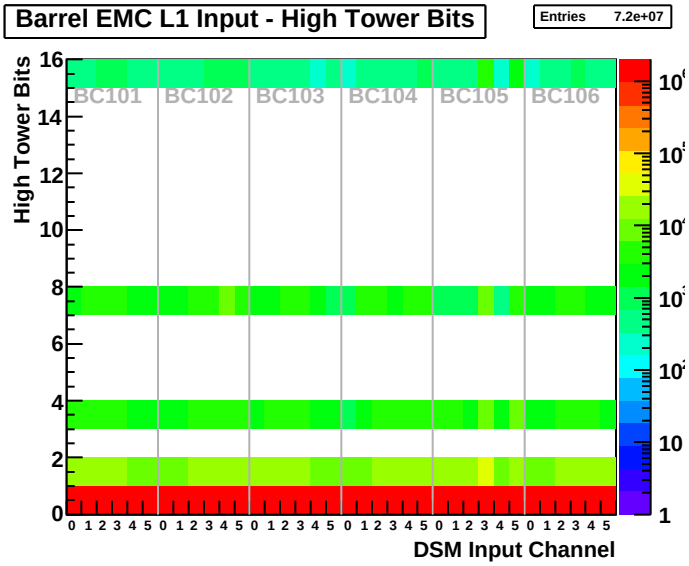
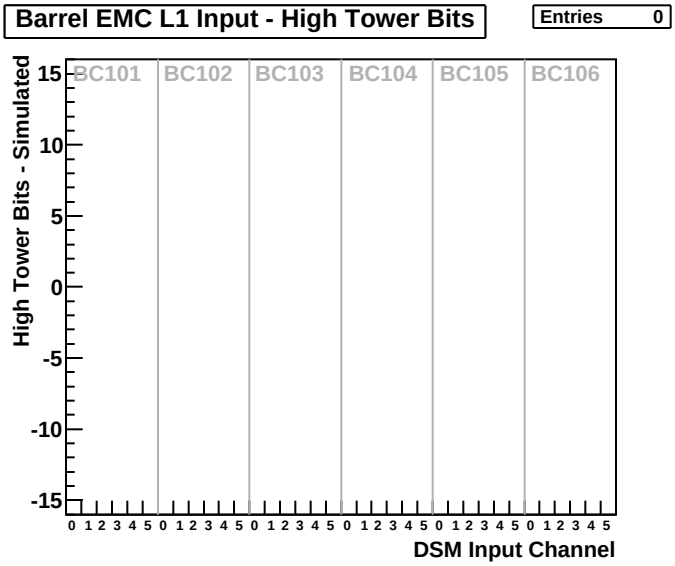

Barrel EMC L1 Input - Low Eta Sum
Entries 7.2e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

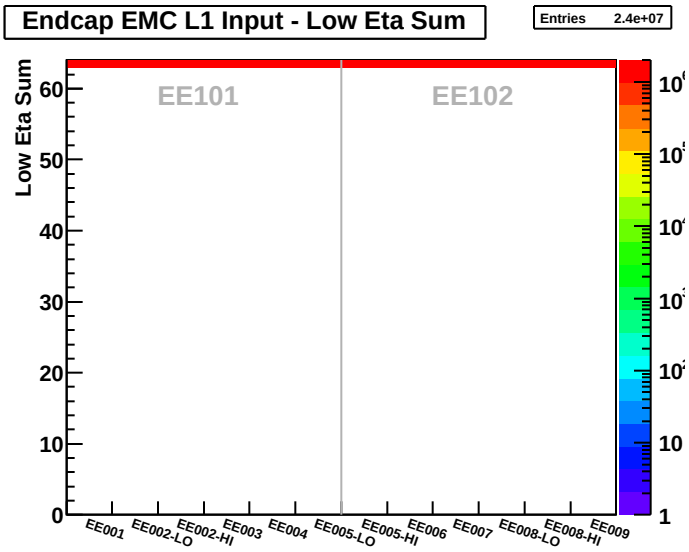

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

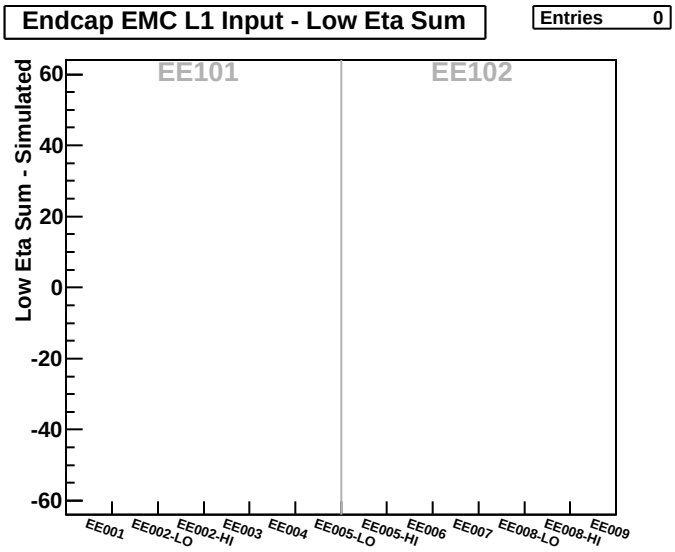

Barrel EMC L1 Input - High Eta Sum
Entries 7.2e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

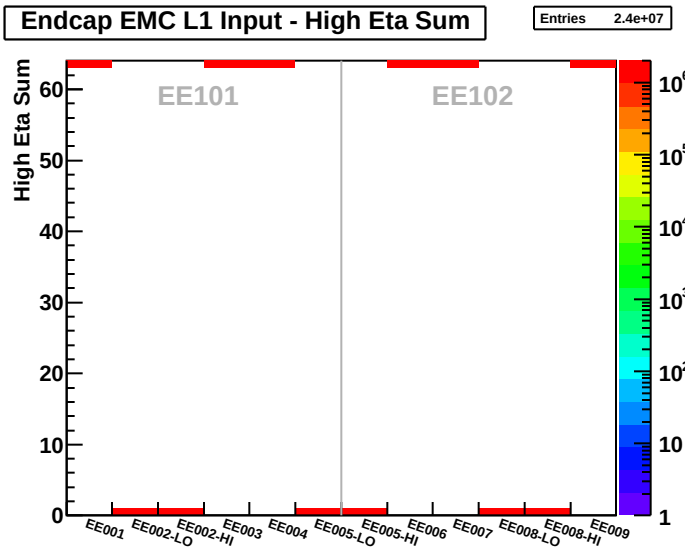

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

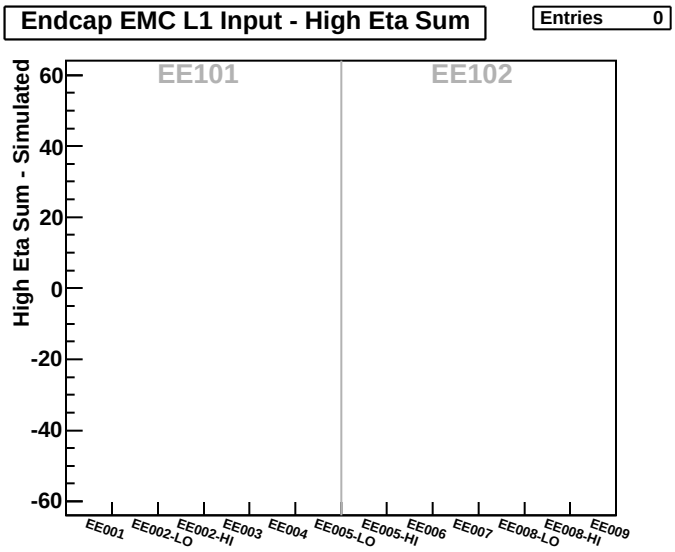

Barrel EMC L1 Input - High Tower Bits
Entries 7.2e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

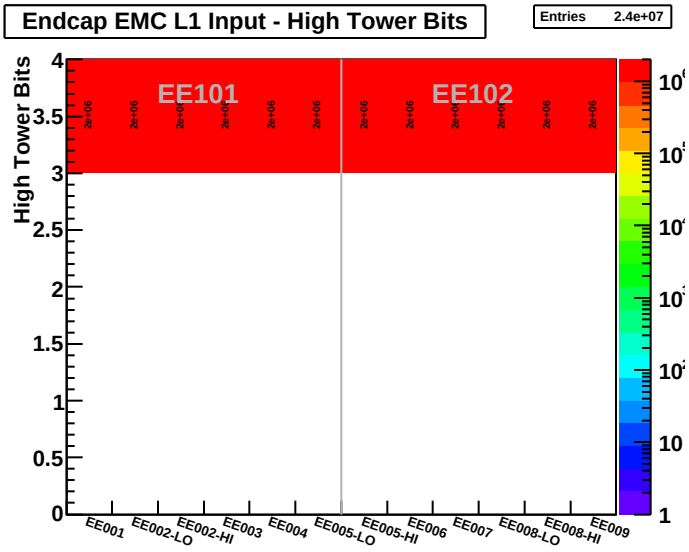

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

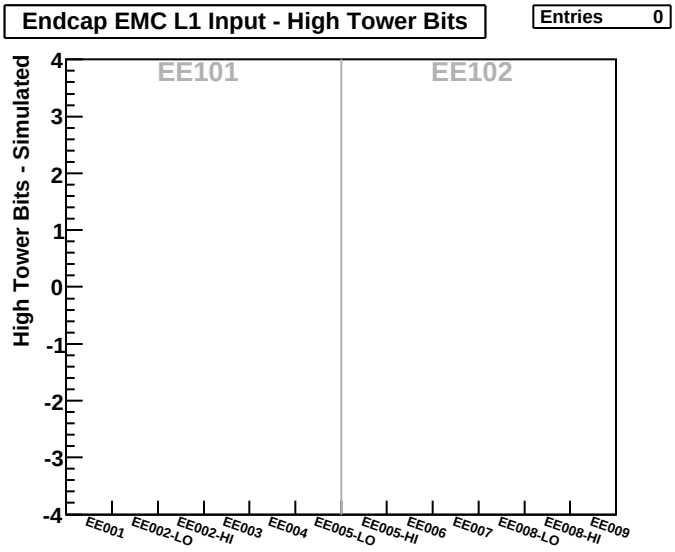

Endcap EMC L1 Input - Low Eta Sum
Entries 2.4e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

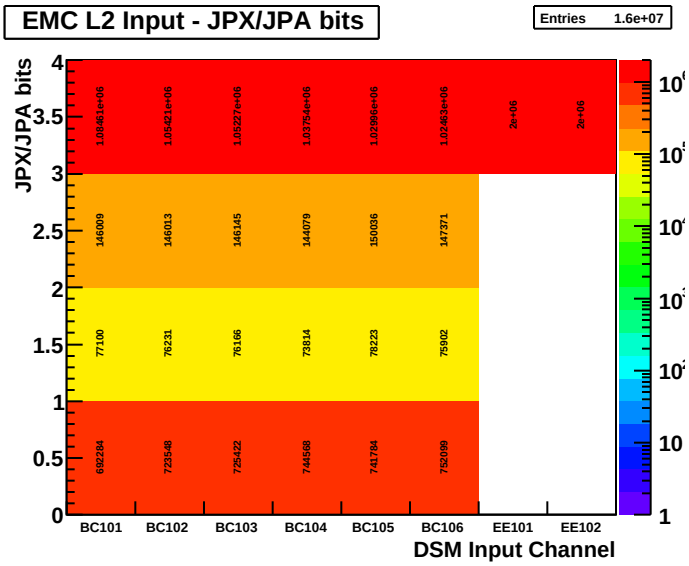

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

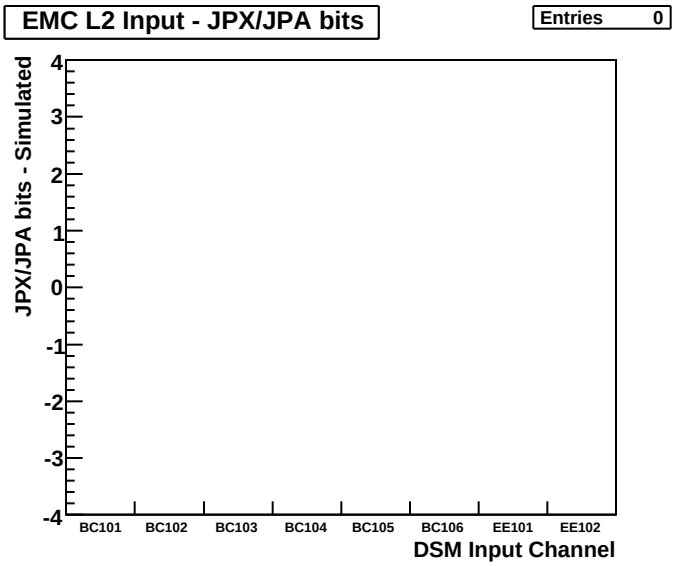

Endcap EMC L1 Input - High Eta Sum
Entries 2.4e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

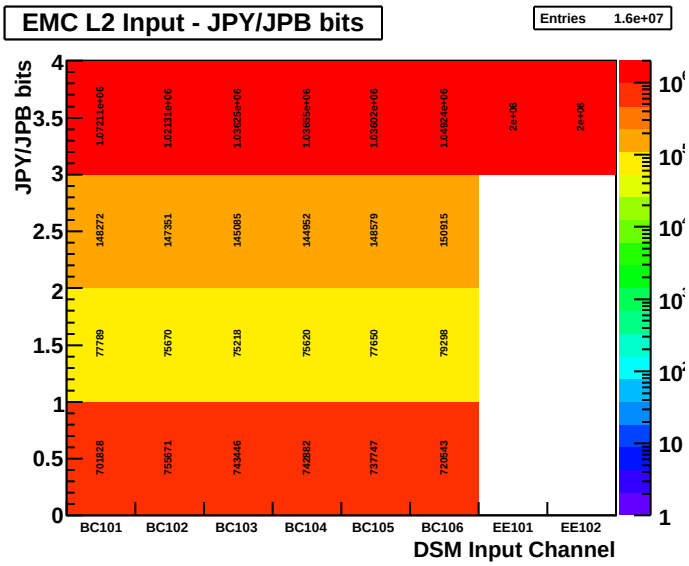

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

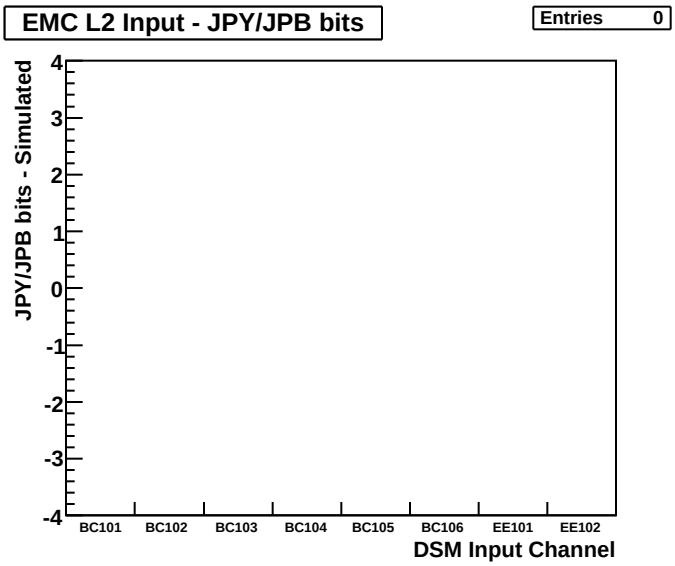

Endcap EMC L1 Input - High Tower Bits
Entries 2.4e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

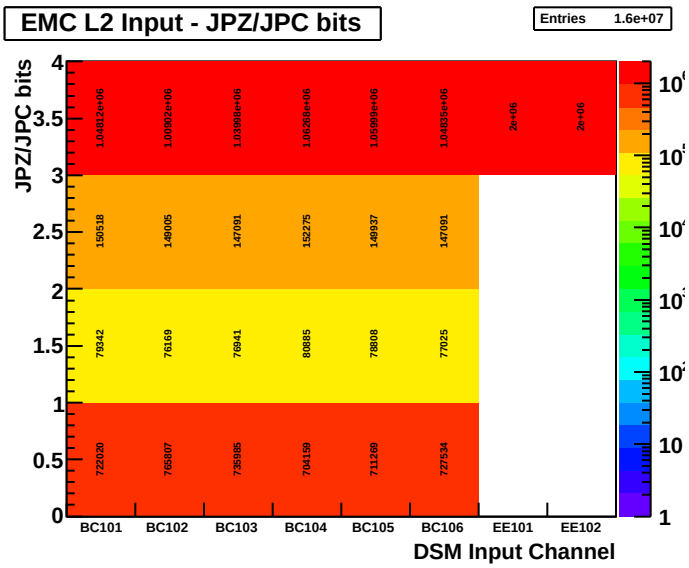

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

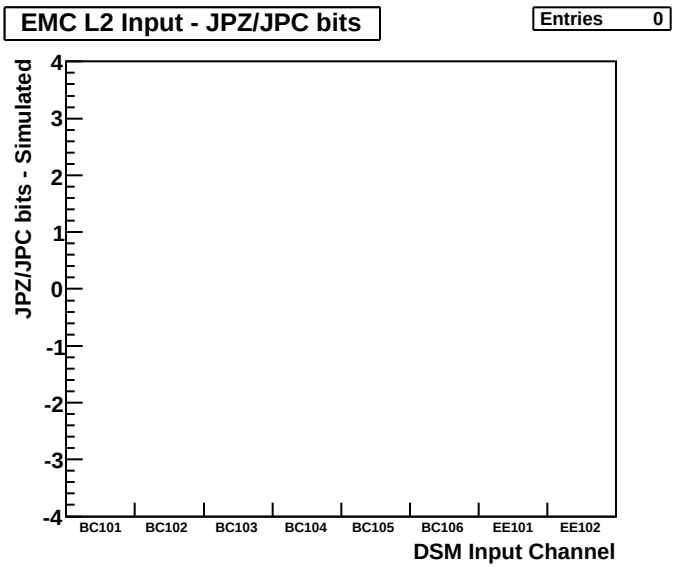

EMC L2 Input - JPX/JPA bits
Entries 1.6e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1.08461e+06 1.05421e+06 1.05227e+06 1.03754e+06 1.02896e+06 1.02463e+06 2e+06 2e+06
146009 146013 146145 144079 150036 147371
77100 76231 76166 73814 78223 75902
692284 723548 725422 744568 741784 752099

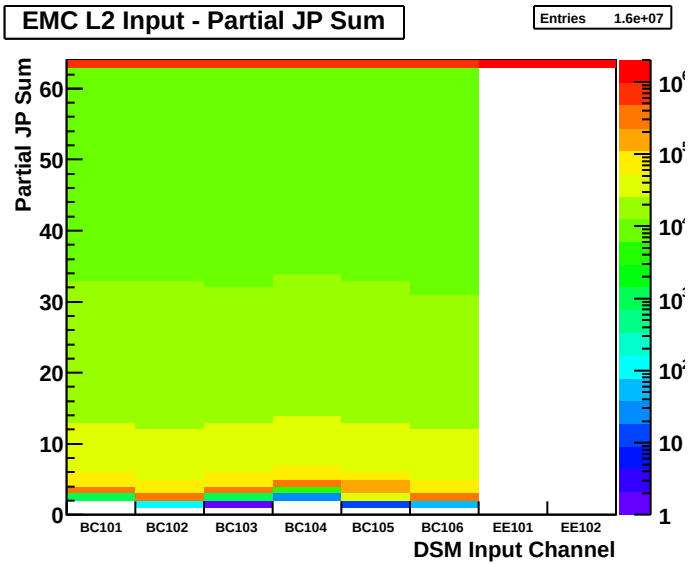

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

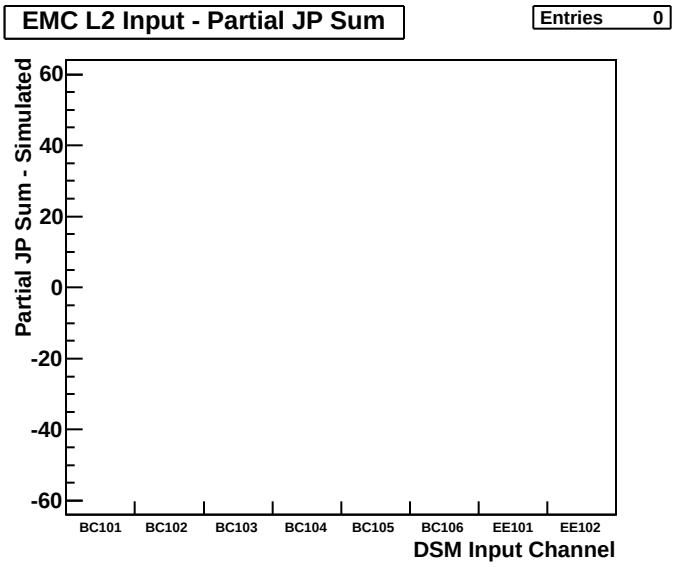

EMC L2 Input - JPY/JPB bits
Entries 1.6e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1.07211e+06 1.02131e+06 1.03625e+06 1.03655e+06 1.03902e+06 1.04924e+06 2e+06 2e+06
148272 147351 145085 144952 148579 150915
77789 75670 75218 75620 77650 79298
701828 755671 743446 742882 737747 720543

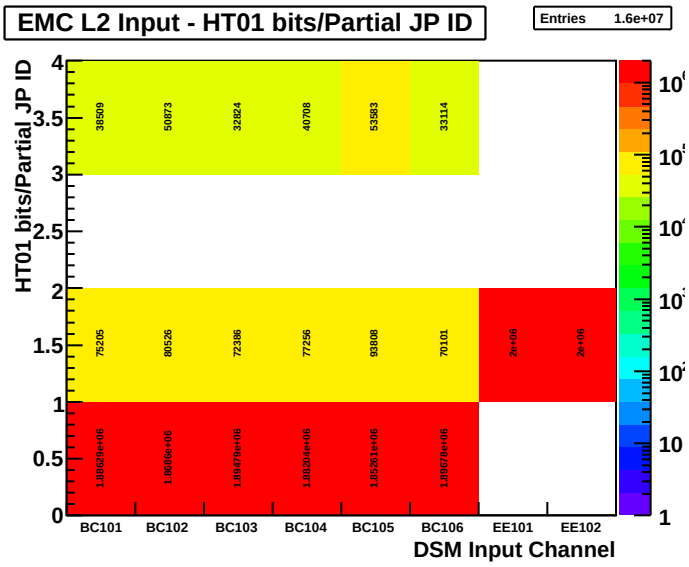

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

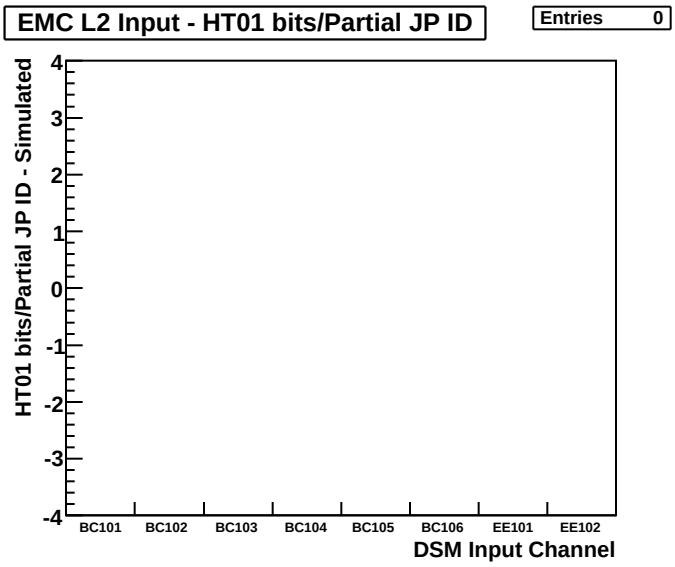

EMC L2 Input - JPZ/JPC bits
Entries 1.6e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1.04812e+06 1.00902e+06 1.03908e+06 1.06268e+06 1.05999e+06 1.04835e+06 2e+06 2e+06
150518 149005 147091 152275 149937 147091
79342 76169 76941 80885 78808 77025
722020 765807 735985 704159 711269 727534

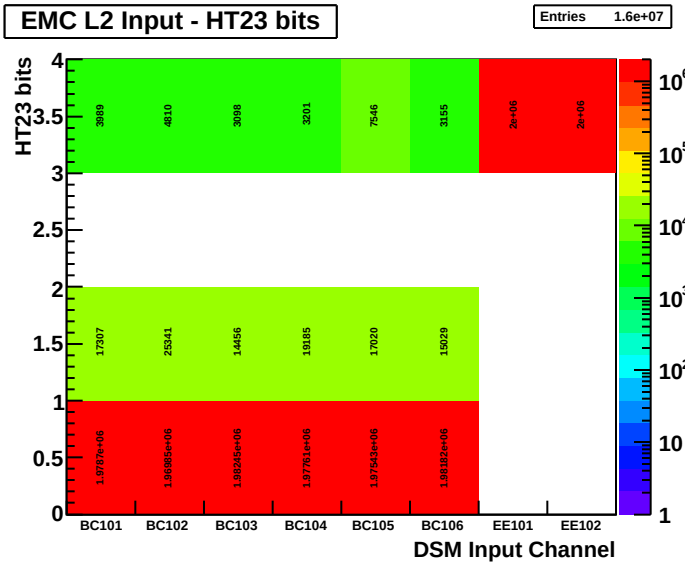

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

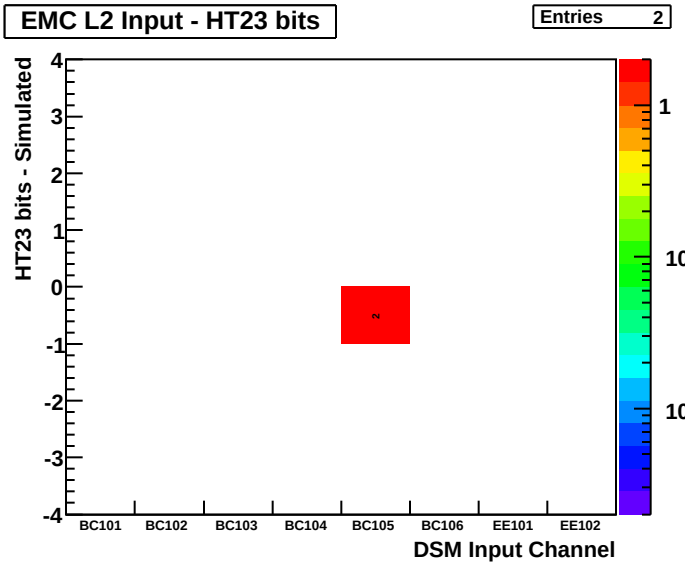

EMC L2 Input - Partial JP Sum
Entries 1.6e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

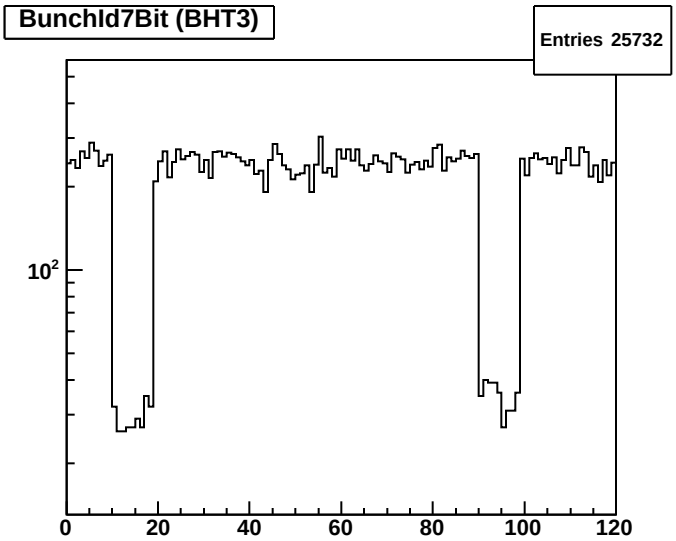

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

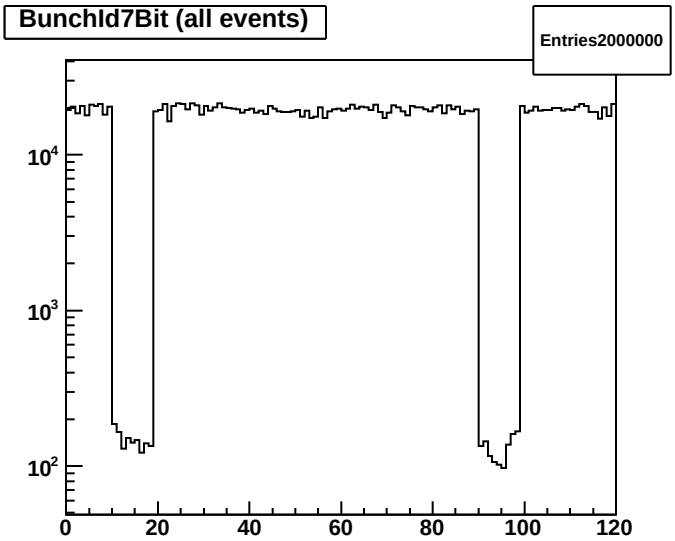

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.6e+07
HT01 bits/Partial JP ID
38509 50873 32824 40708 53583 33114
75205 80526 72386 77256 93808 70101 2e+06 2e+06
1.88629e+06 1.8686e+06 1.89479e+06 1.88204e+06 1.85281e+06 1.89678e+06
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

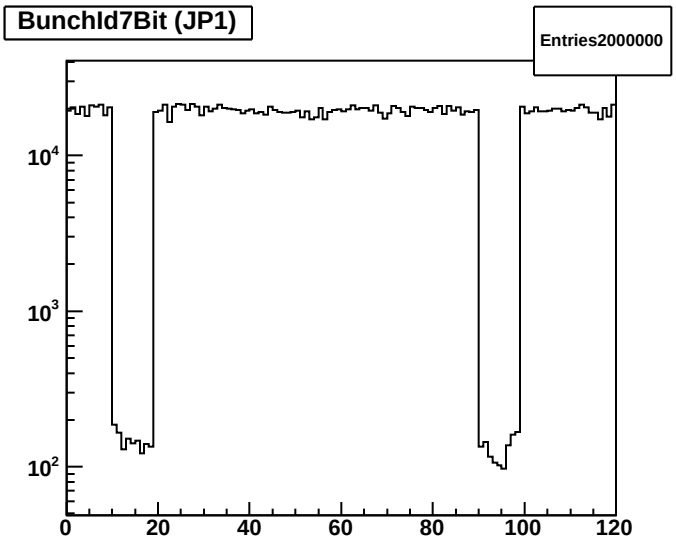

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

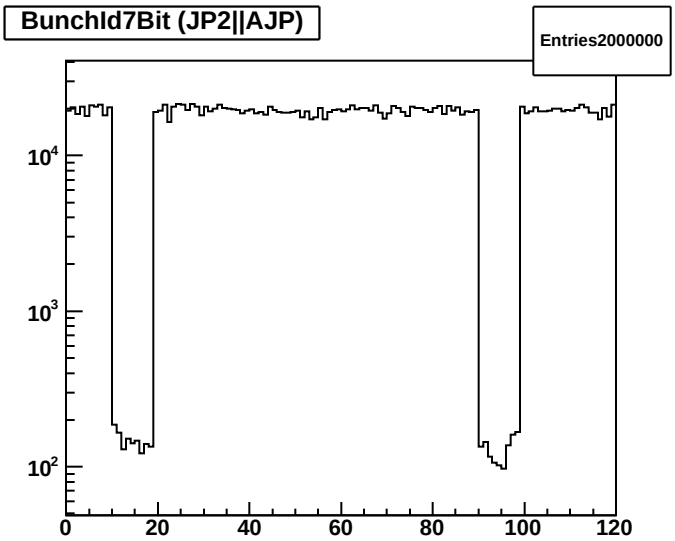

EMC L2 Input - HT23 bits
Entries 1.6e+07
HT23 bits
3989 4810 3098 3201 7546 3155 2e+06 2e+06
17307 25341 14456 19185 17020 15029
1.97876e+06 1.96985e+06 1.98245e+06 1.97761e+06 1.97543e+06 1.98182e+06
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

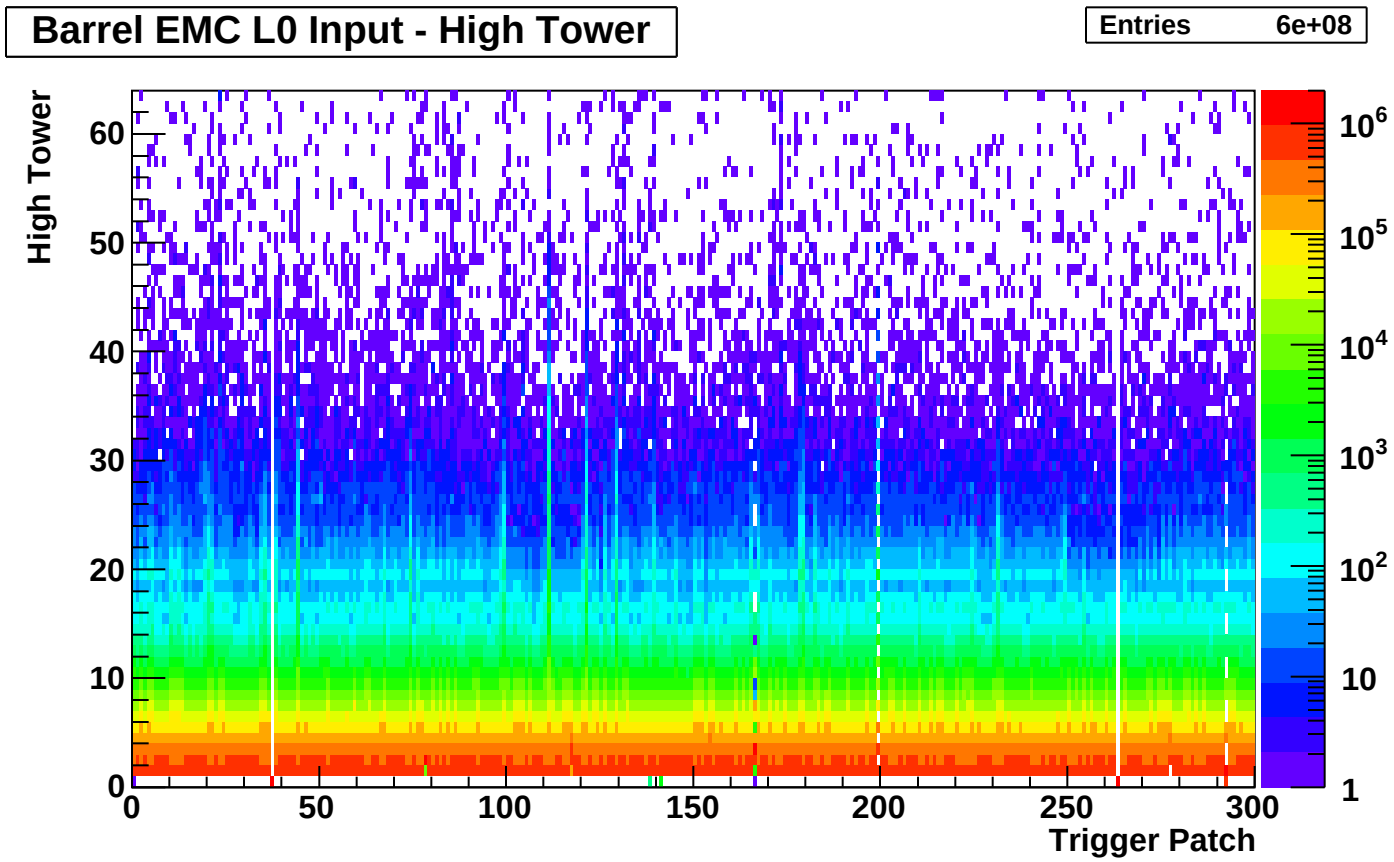

EMC L2 Input - HT23 bits
Entries 2
HT23 bits - Simulated
2
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

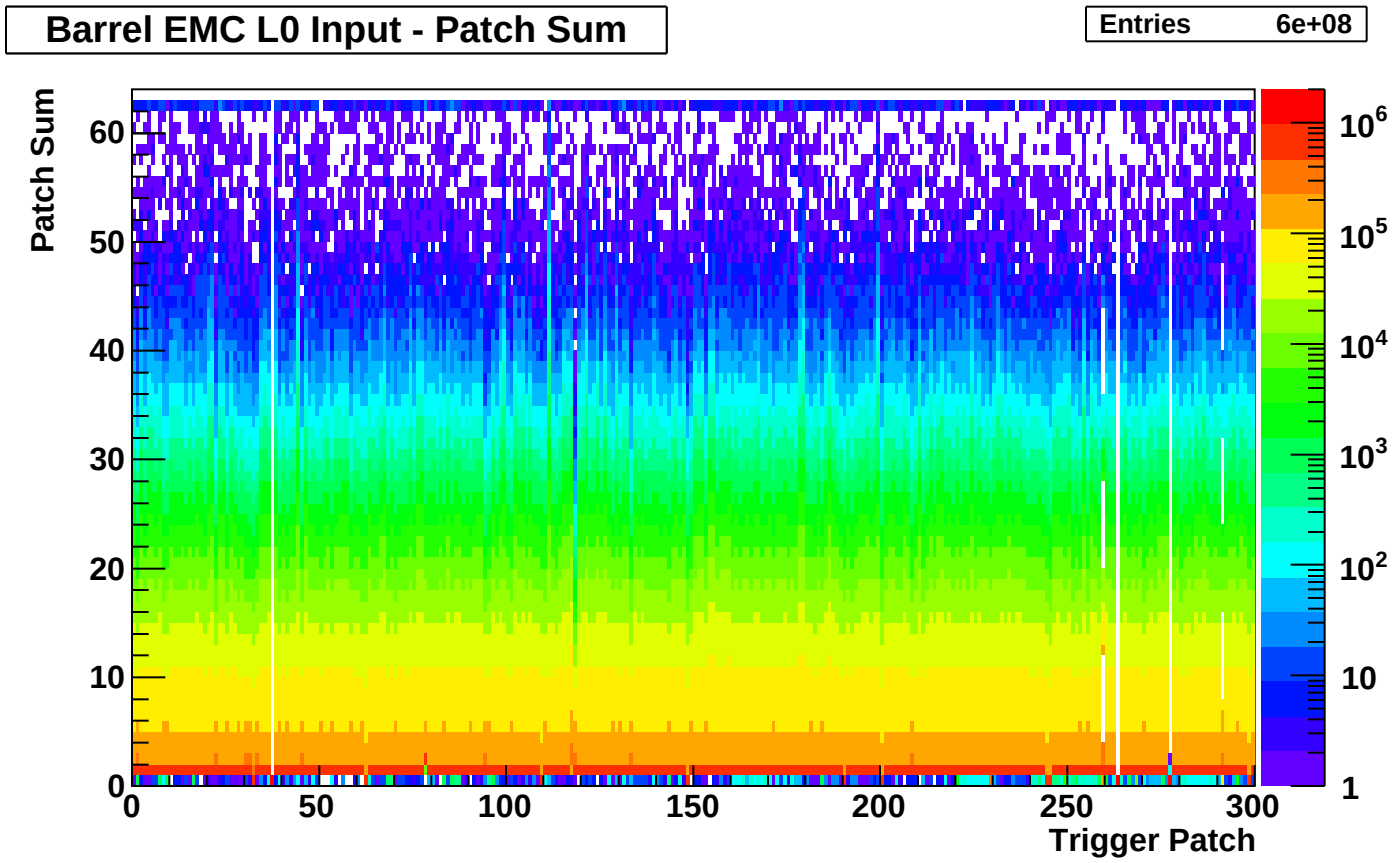

BunchId7Bit (BHT3)
Entries 25732
10^2
0
20
40
60
80
100
120

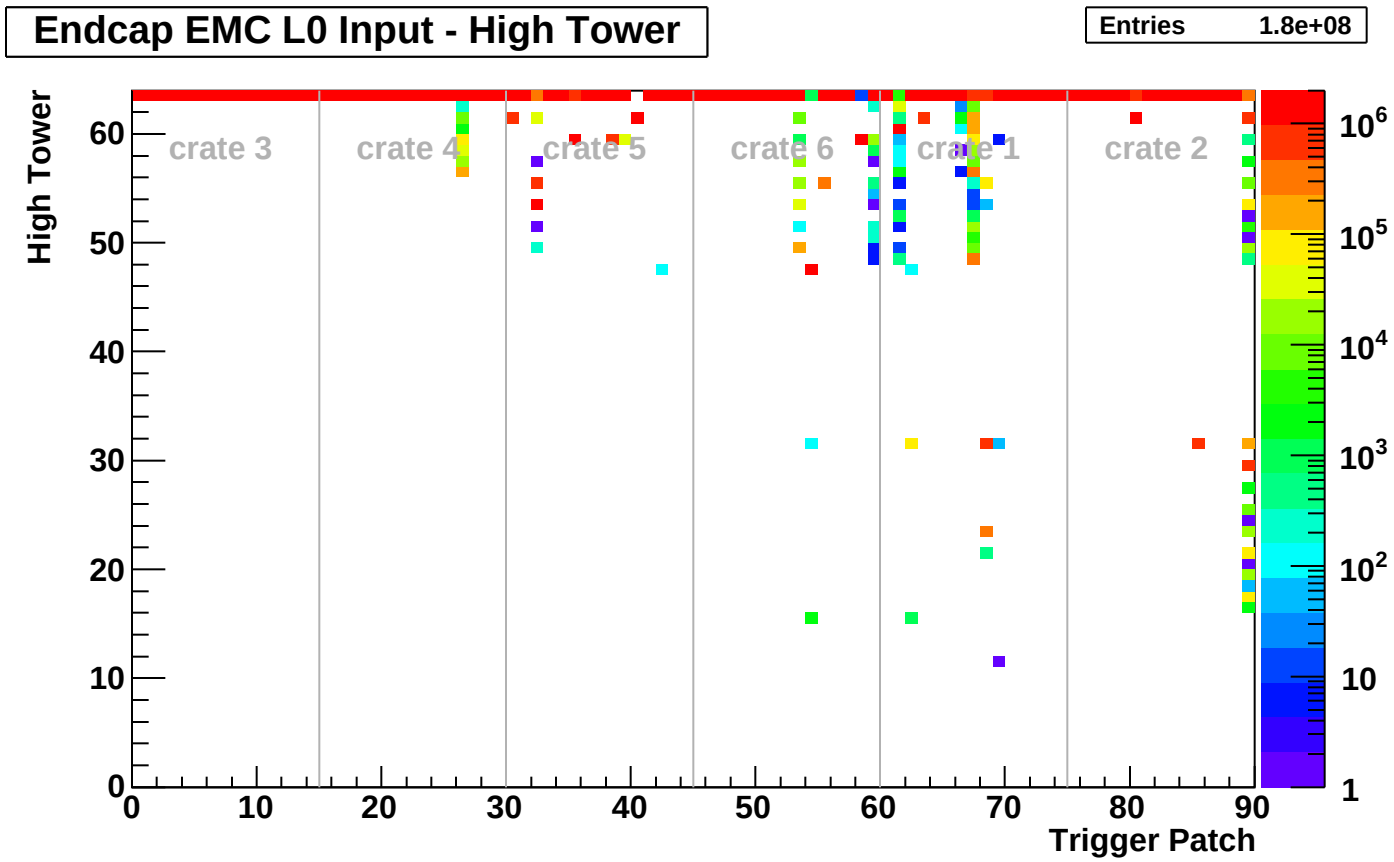

BunchId7Bit (all events)
Entries2000000
10^4
10^3
10^2
0
20
40
60
80
100
120

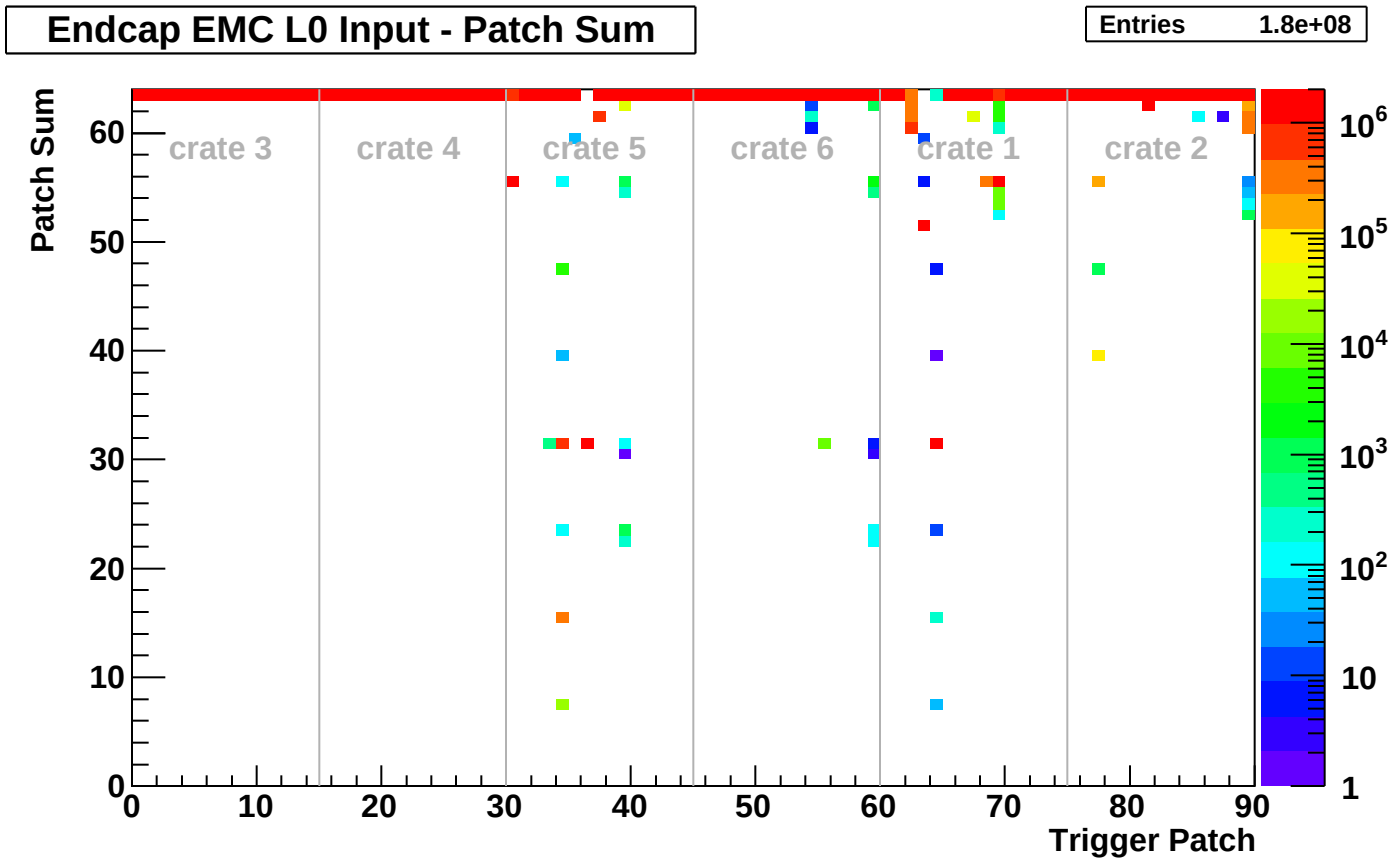

BunchId7Bit (JP1)
Entries2000000
10^4
10^3
10^2
0
20
40
60
80
100
120

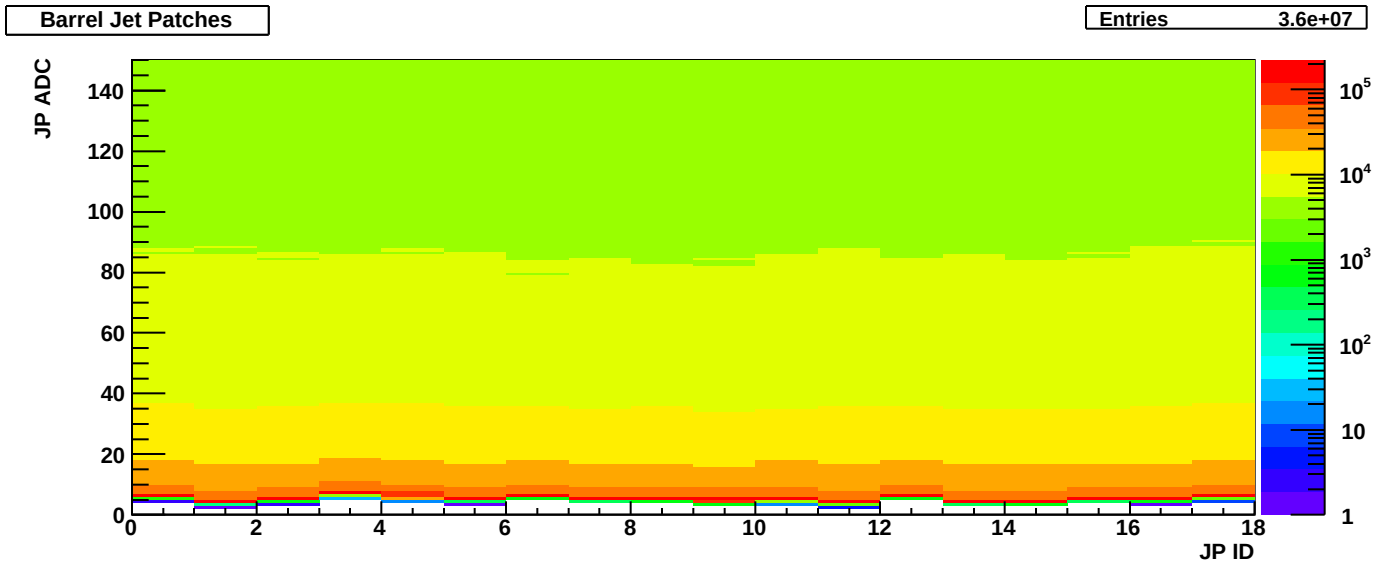

BunchId7Bit (JP2||AJP)
Entries2000000
10^4
10^3
10^2
0
20
40
60
80
100
120

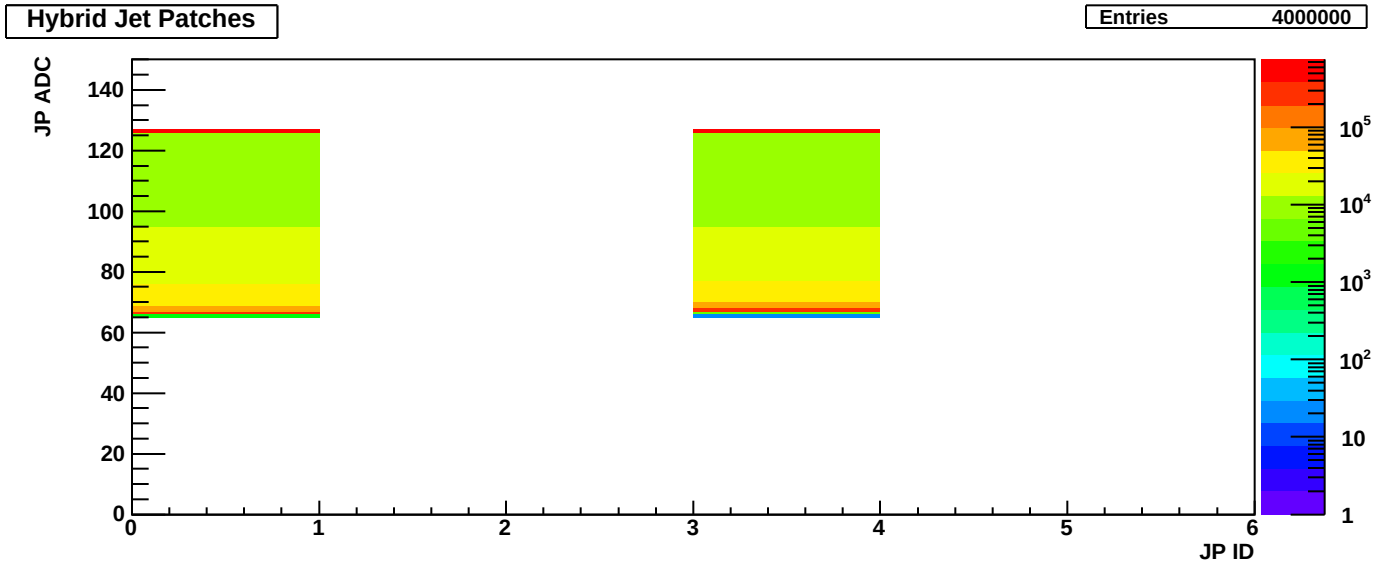

Barrel EMC L0 Input - High Tower
Entries 6e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

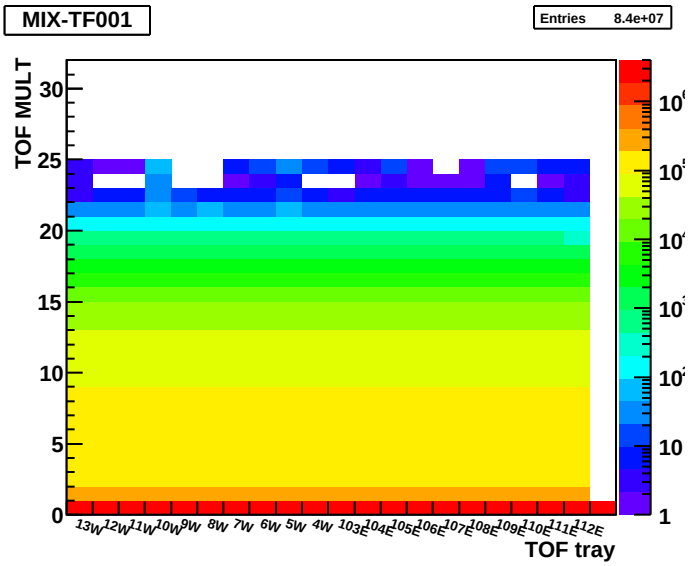

Barrel EMC L0 Input - Patch Sum
Entries 6e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

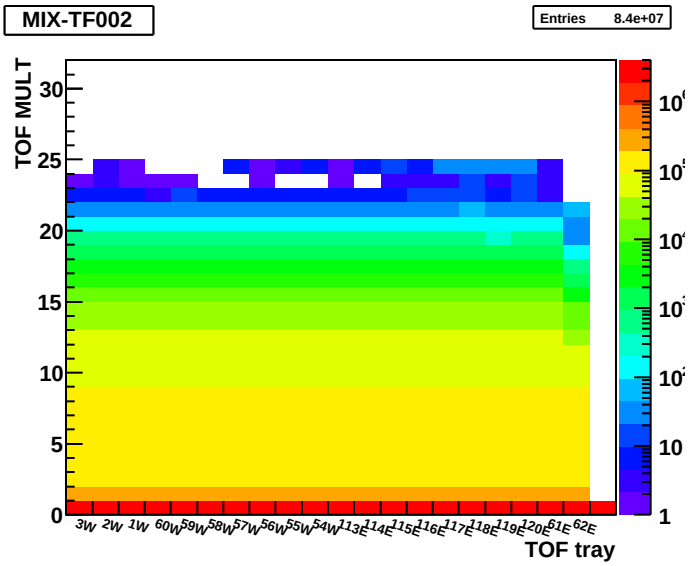

Endcap EMC L0 Input - High Tower
Entries 1.8e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

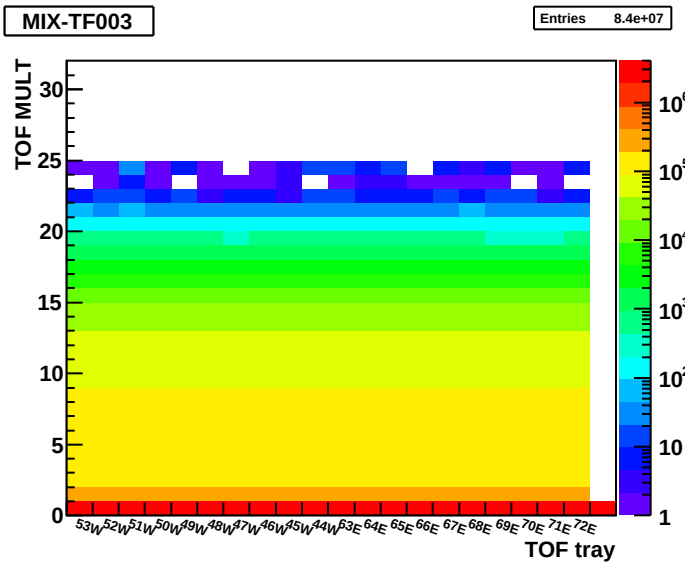

Endcap EMC L0 Input - Patch Sum
Entries 1.8e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

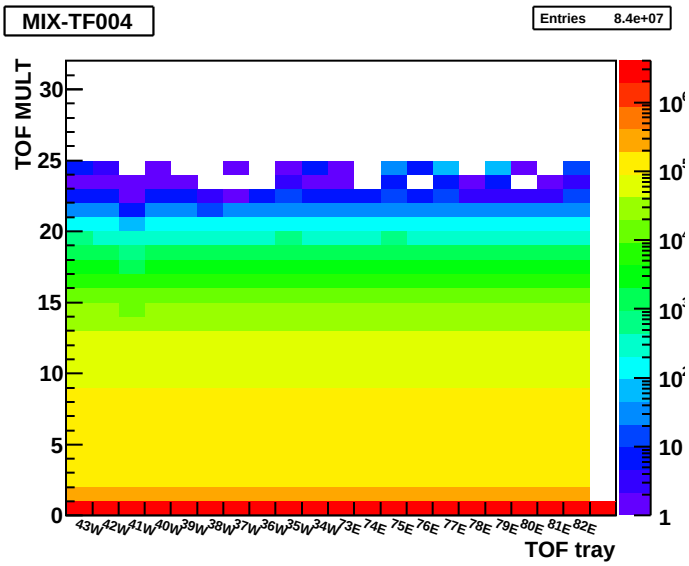

Barrel Jet Patches
Entries 3.6e+07
JP ADC
JP ID

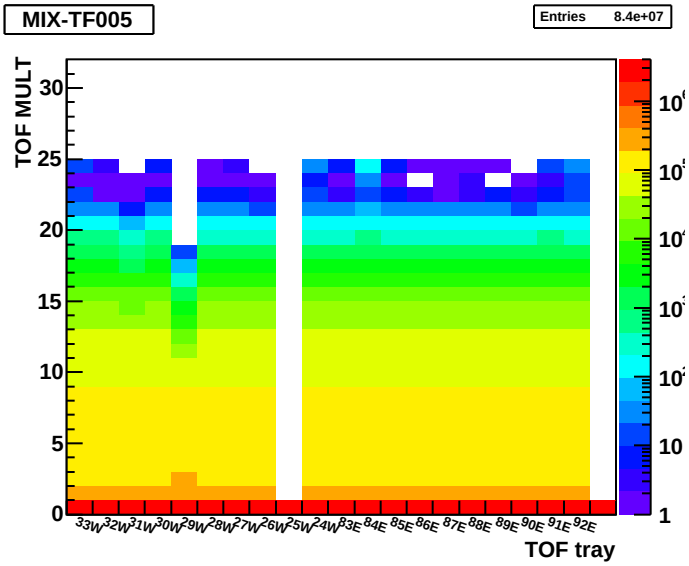

Hybrid Jet Patches
Entries 4000000
JP ADC
JP ID

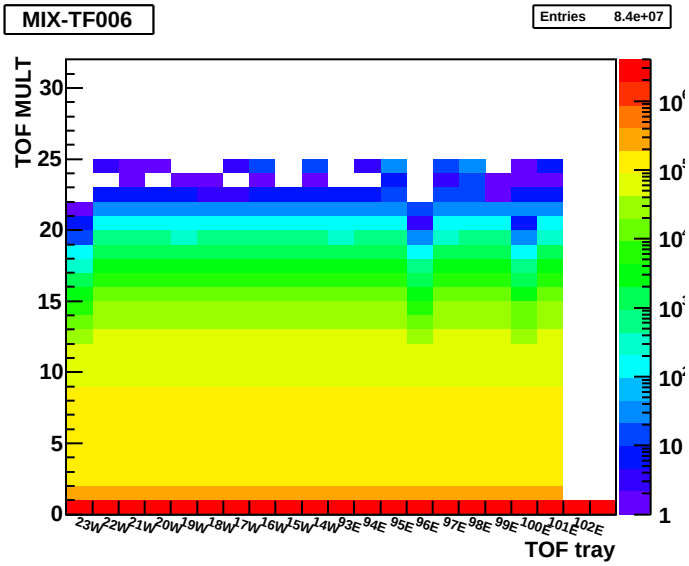

MIX-TF001
Entries 8.4e+07
TOF MULT
TOF tray

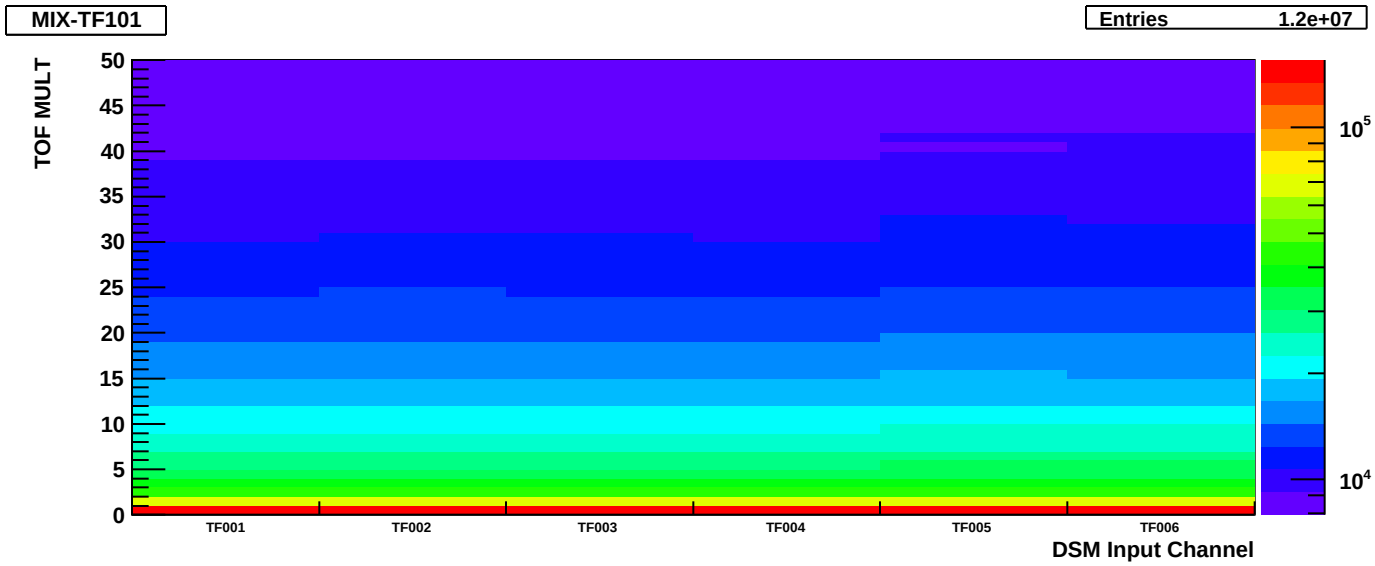

MIX-TF002
Entries 8.4e+07
TOF MULT
TOF tray

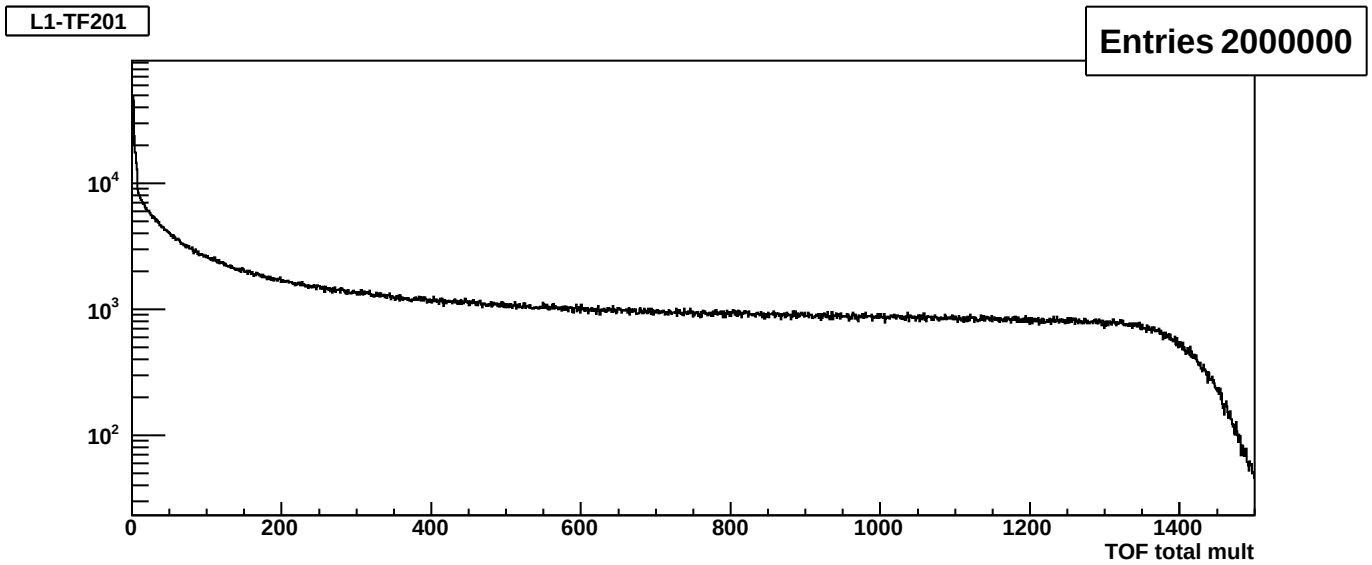

MIX-TF003
Entries 8.4e+07
TOF MULT
TOF tray

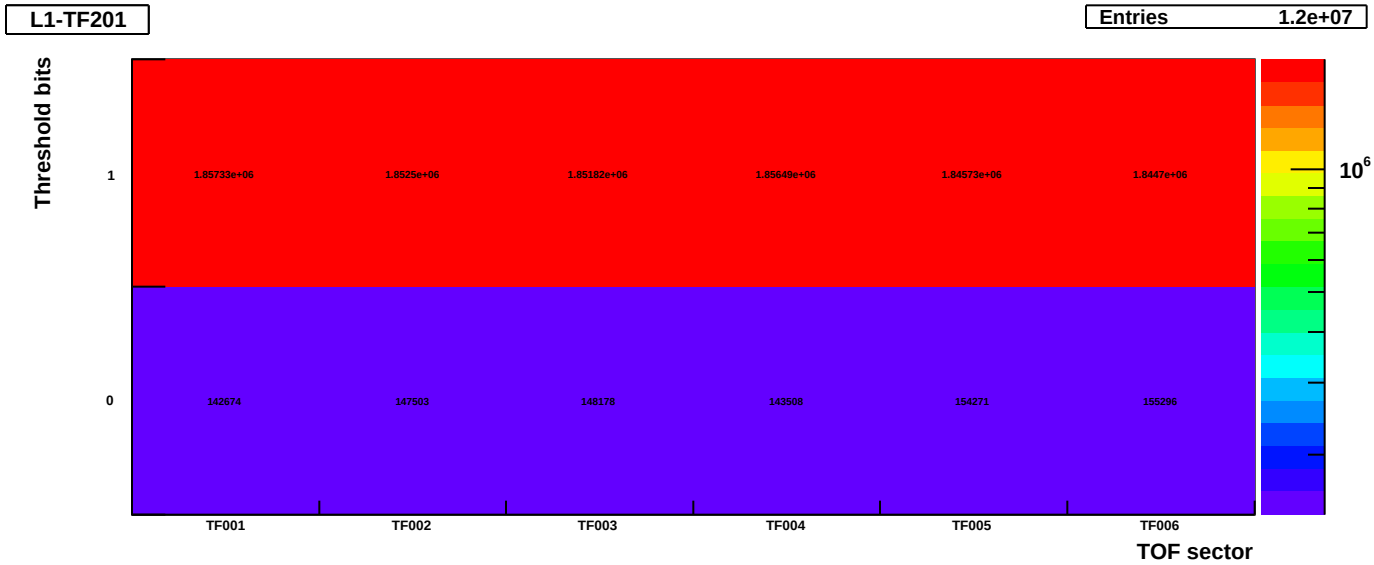

MIX-TF004
Entries 8.4e+07
TOF MULT
TOF tray

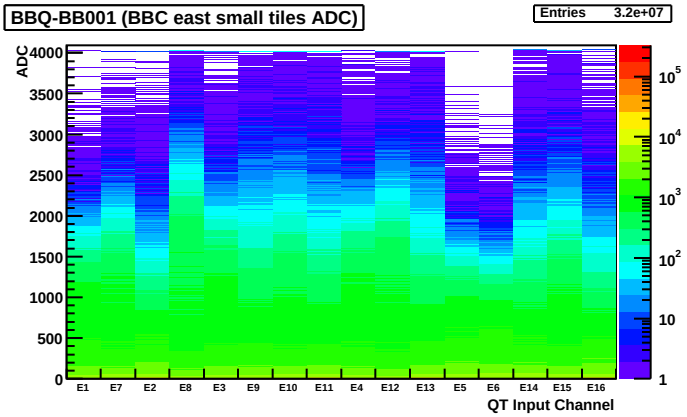

MIX-TF005
Entries 8.4e+07
TOF MULT
TOF tray

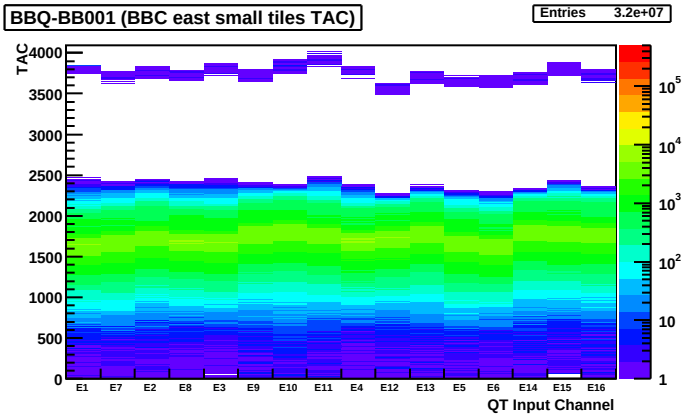

MIX-TF006
Entries 8.4e+07
TOF MULT
TOF tray

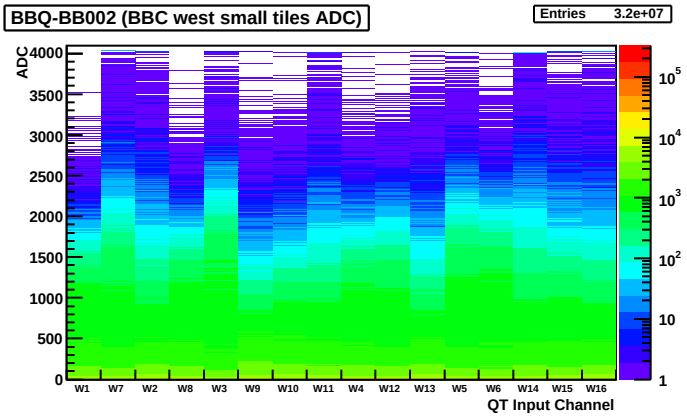

MIX-TF101
Entries 1.2e+07
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4

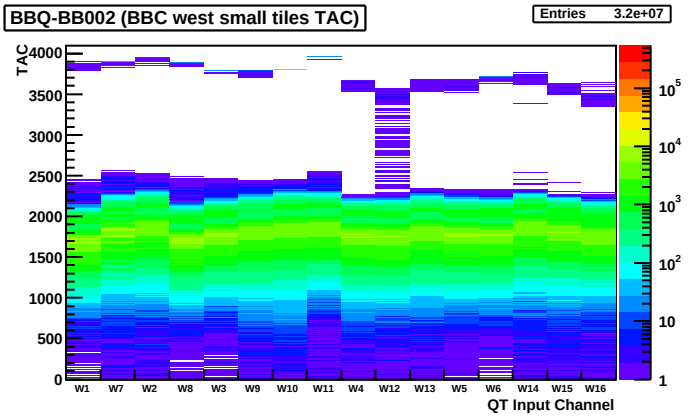

L1-TF201
Entries 2000000
10^4
10^3
10^2
0
200
400
600
800
1000
1200
1400
TOF total mult

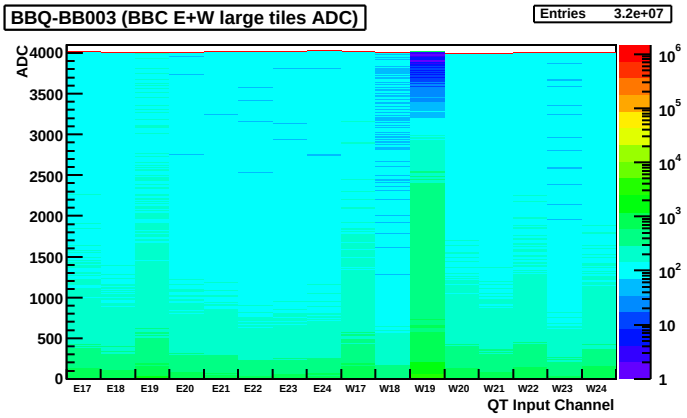

L1-TF201
Entries 1.2e+07
Threshold bits
1
0
1.85733e+06
1.8525e+06
1.85182e+06
1.85649e+06
1.84573e+06
1.8447e+06
142674
147503
148178
143508
154271
155296
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^6

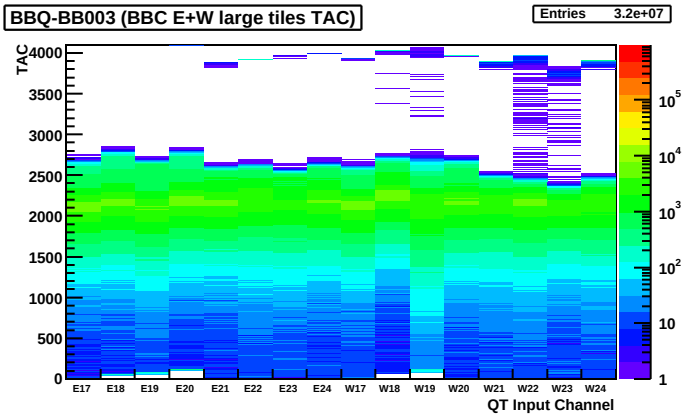

BBQ-BB001 (BBC east small tiles ADC)
Entries 3.2e+07
ADC
QT Input Channel

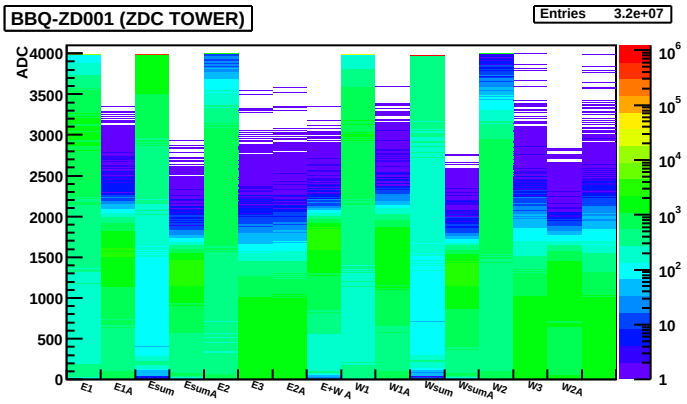

BBQ-BB001 (BBC east small tiles TAC)
Entries 3.2e+07
TAC
QT Input Channel

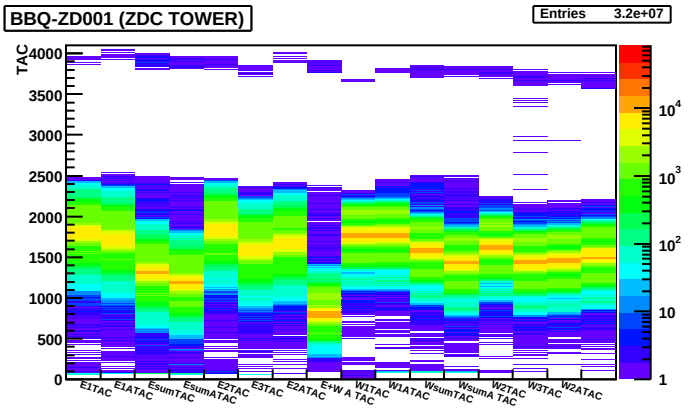

BBQ-BB002 (BBC west small tiles ADC)
Entries 3.2e+07
ADC
QT Input Channel

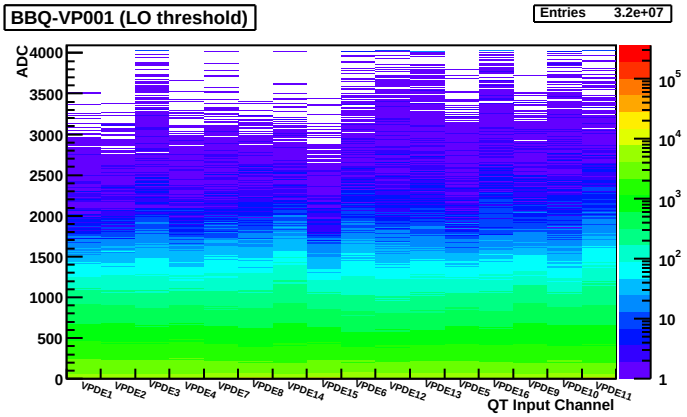

BBQ-BB002 (BBC west small tiles TAC)
Entries 3.2e+07
TAC
QT Input Channel

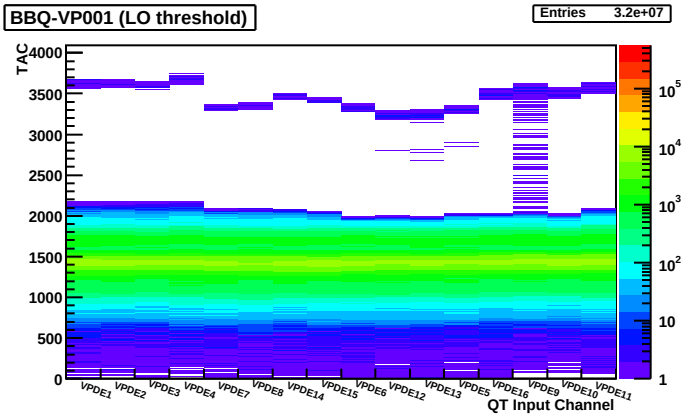

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3.2e+07
ADC
QT Input Channel

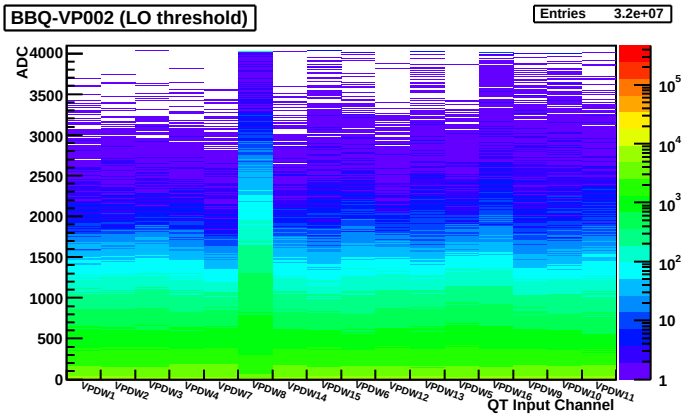

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3.2e+07
TAC
QT Input Channel

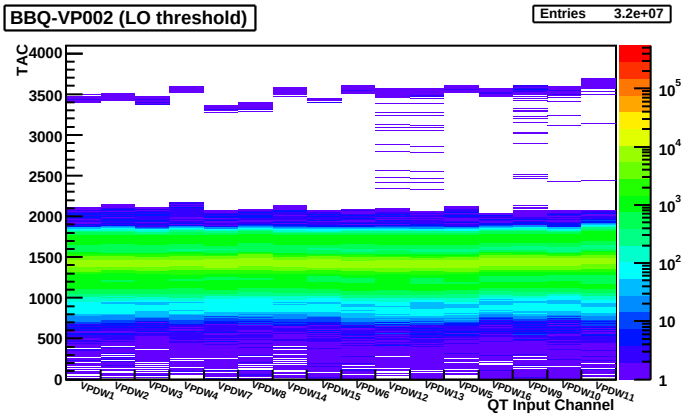

BBQ-ZD001 (ZDC TOWER)
Entries 3.2e+07
ADC

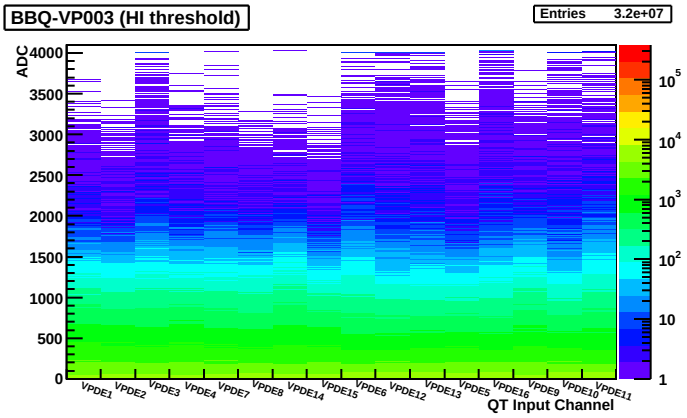

BBQ-ZD001 (ZDC TOWER)
Entries 3.2e+07
TAC

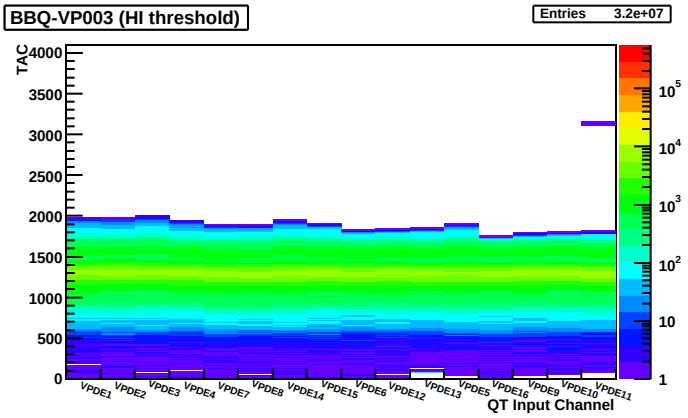

BBQ-VP001 (LO threshold)
Entries 3.2e+07
ADC
QT Input Channel

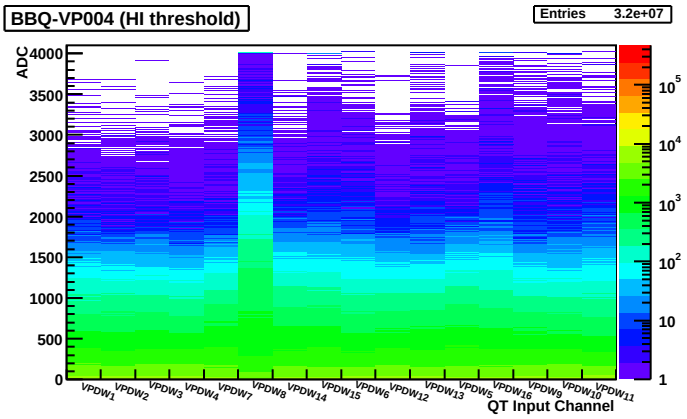

BBQ-VP001 (LO threshold)
Entries 3.2e+07
TAC
QT Input Channel

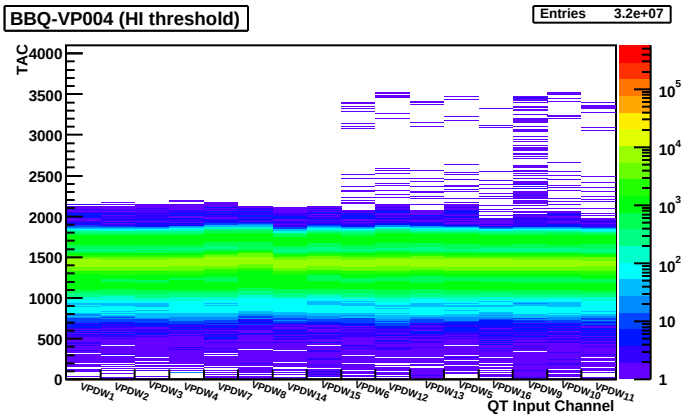

BBQ-VP002 (LO threshold)
Entries 3.2e+07
ADC
QT Input Channel

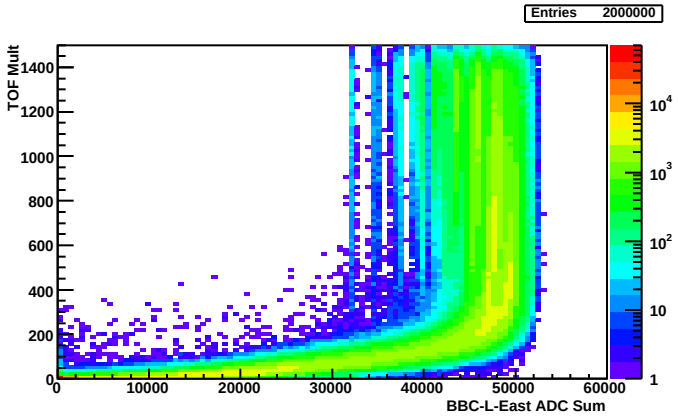

BBQ-VP002 (LO threshold)
Entries 3.2e+07
TAC
QT Input Channel

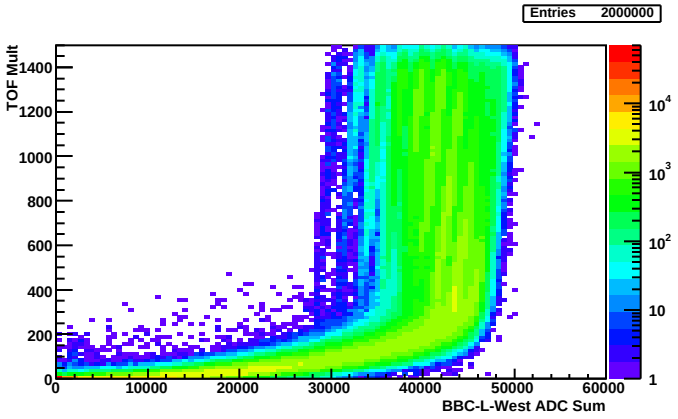

BBQ-VP003 (HI threshold)
Entries 3.2e+07
ADC
QT Input Channel

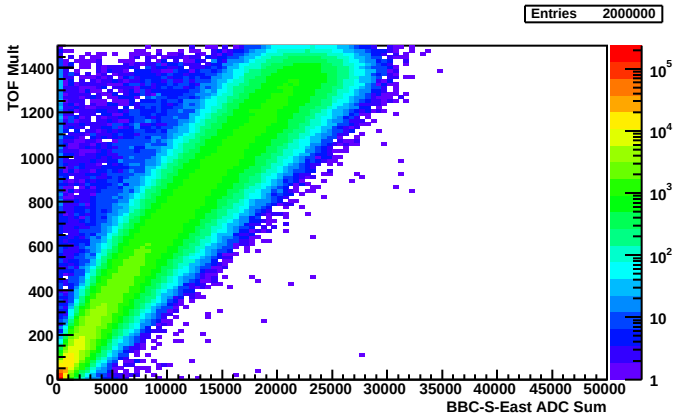

BBQ-VP003 (HI threshold)
Entries 3.2e+07
TAC
QT Input Channel

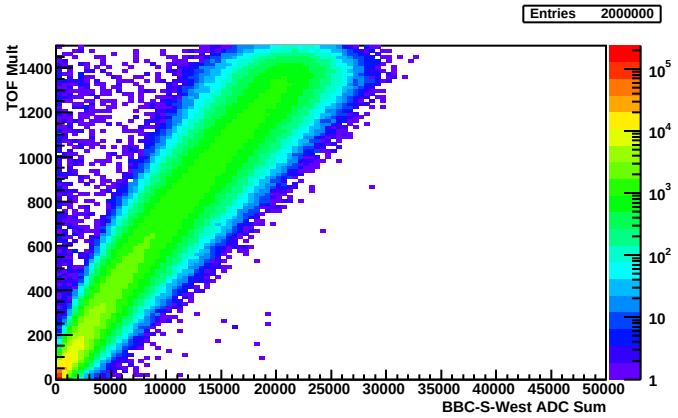

BBQ-VP004 (HI threshold)
Entries 3.2e+07
ADC
QT Input Channel

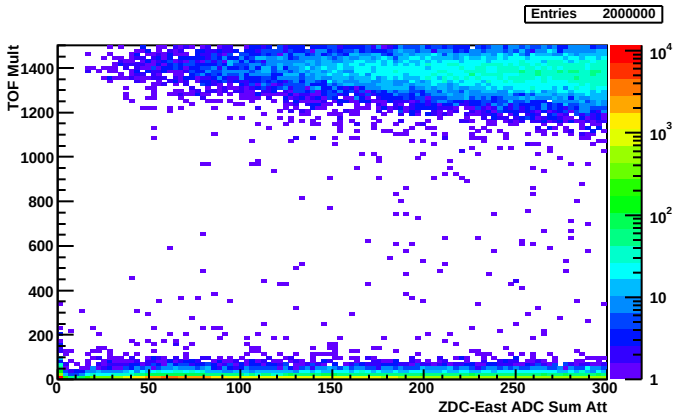

BBQ-VP004 (HI threshold)
Entries 3.2e+07
TAC
QT Input Channel

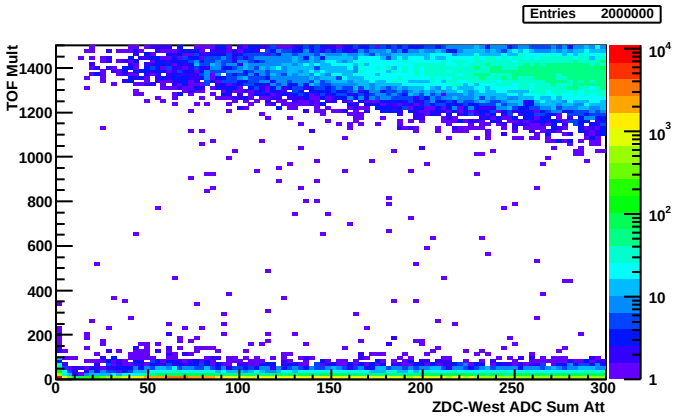

Entries 2000000
TOF Mult
BBC-L-East ADC Sum

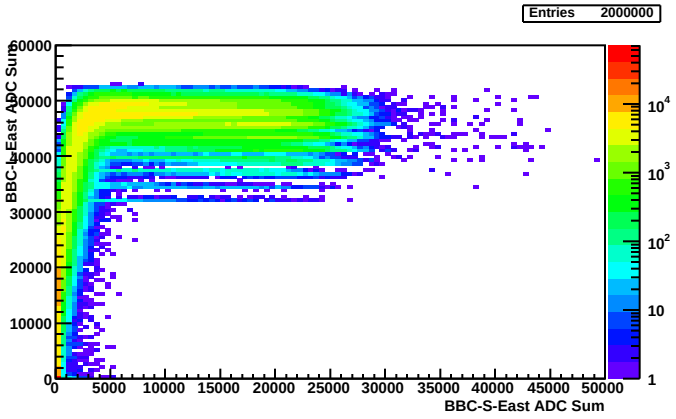

Entries 2000000
TOF Mult
BBC-L-West ADC Sum

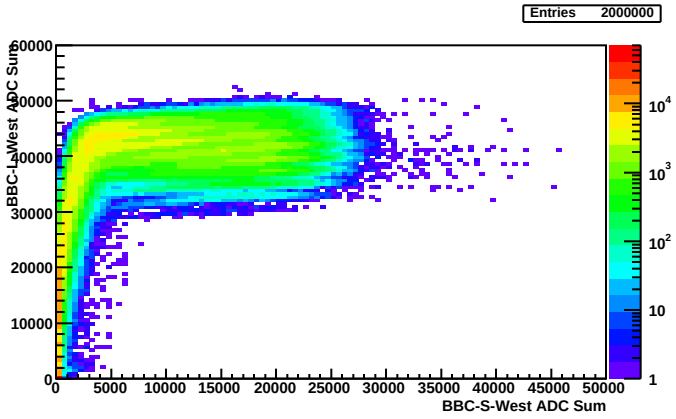

Entries 2000000
TOF Mult
BBC-S-East ADC Sum

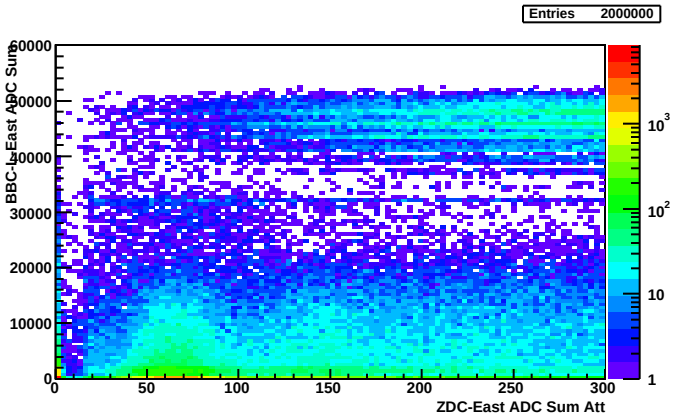

Entries 2000000
TOF Mult
BBC-S-West ADC Sum

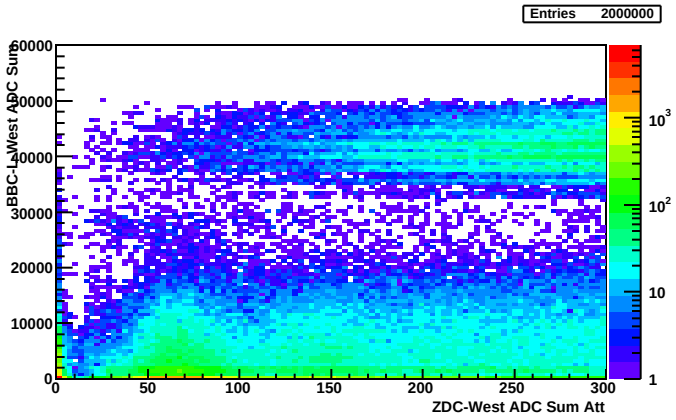

Entries 2000000
TOF Mult
ZDC-East ADC Sum Att

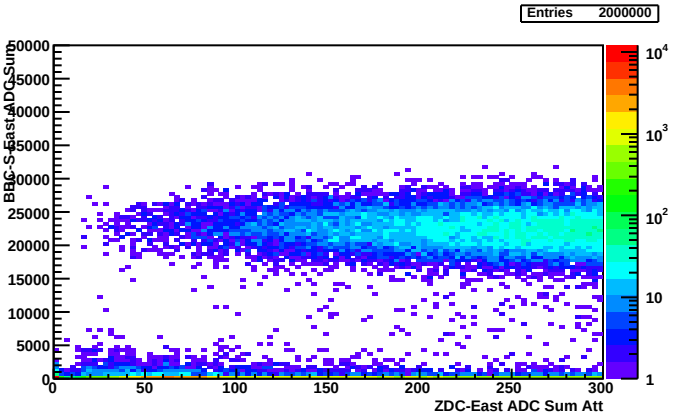

Entries 2000000
TOF Mult
ZDC-West ADC Sum Att

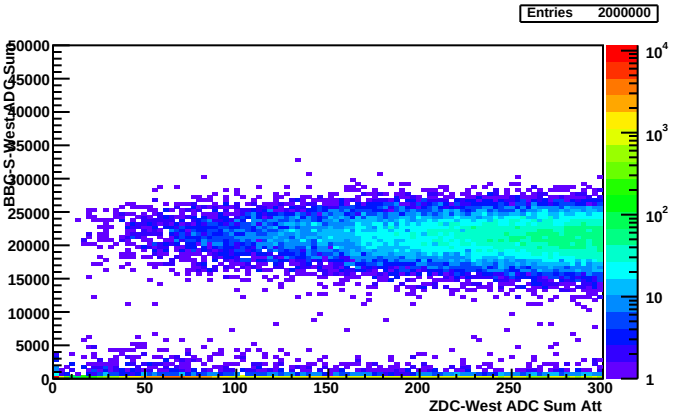

Entries 2000000
BBC-L-East ADC Sum
BBC-S-East ADC Sum

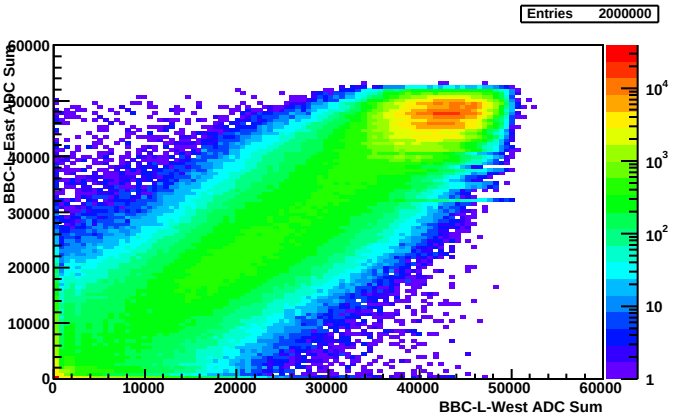

Entries 2000000
BBC-L-West ADC Sum
BBC-S-West ADC Sum

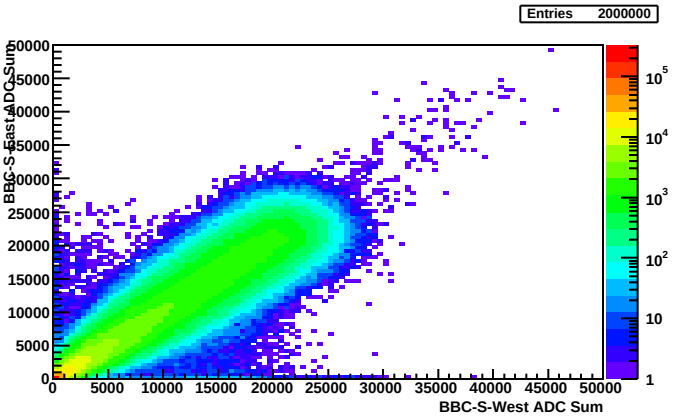

Entries 2000000
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

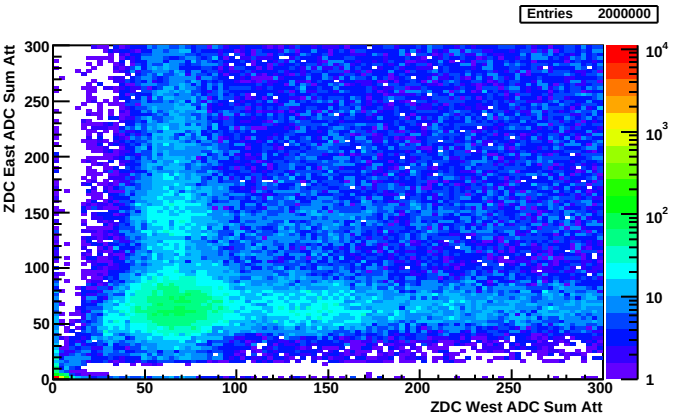

Entries 2000000
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

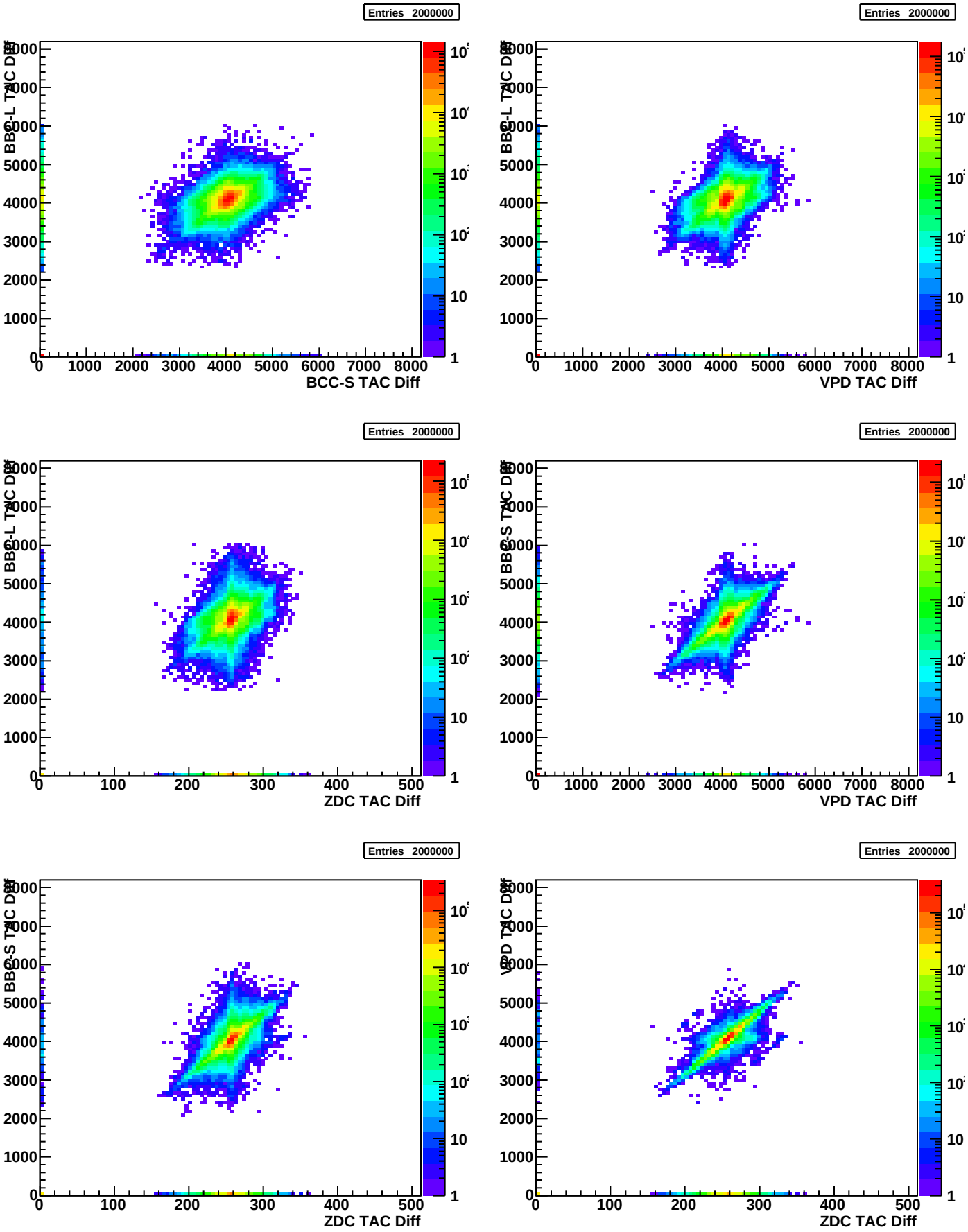

Entries 2000000
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

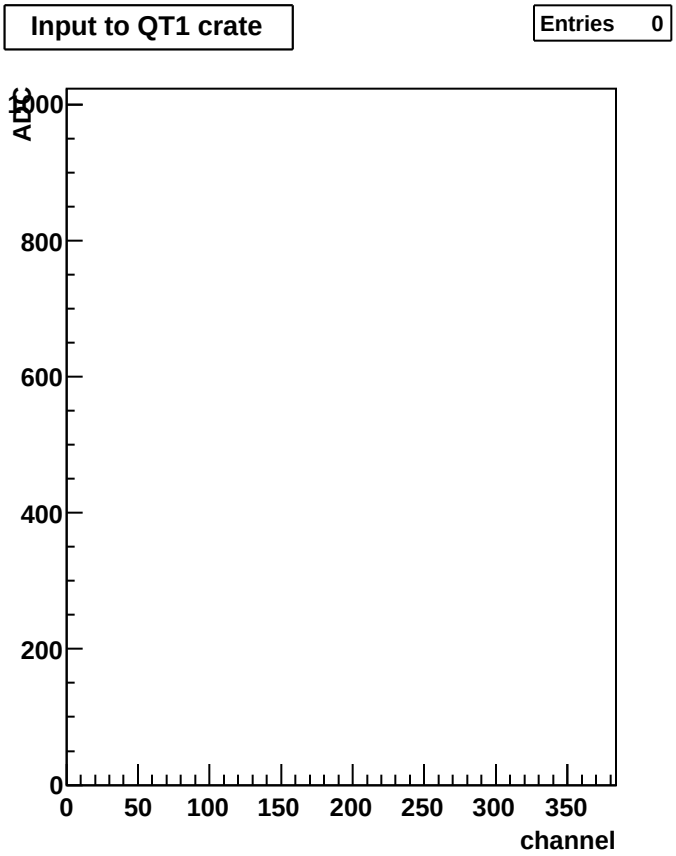

Entries 2000000
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

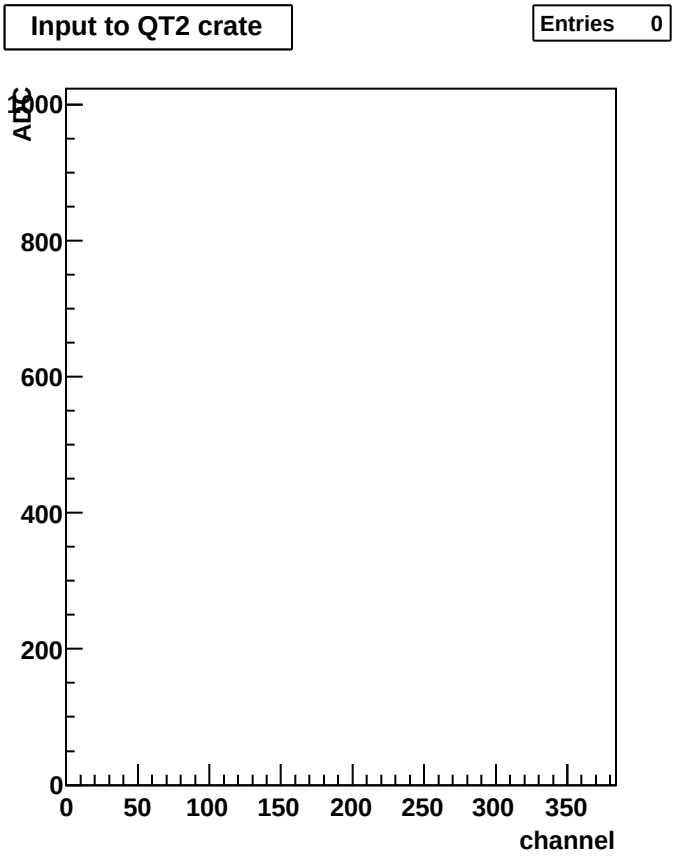

Entries 2000000
BBC-L-East ADC Sum
BBC-L-West ADC Sum

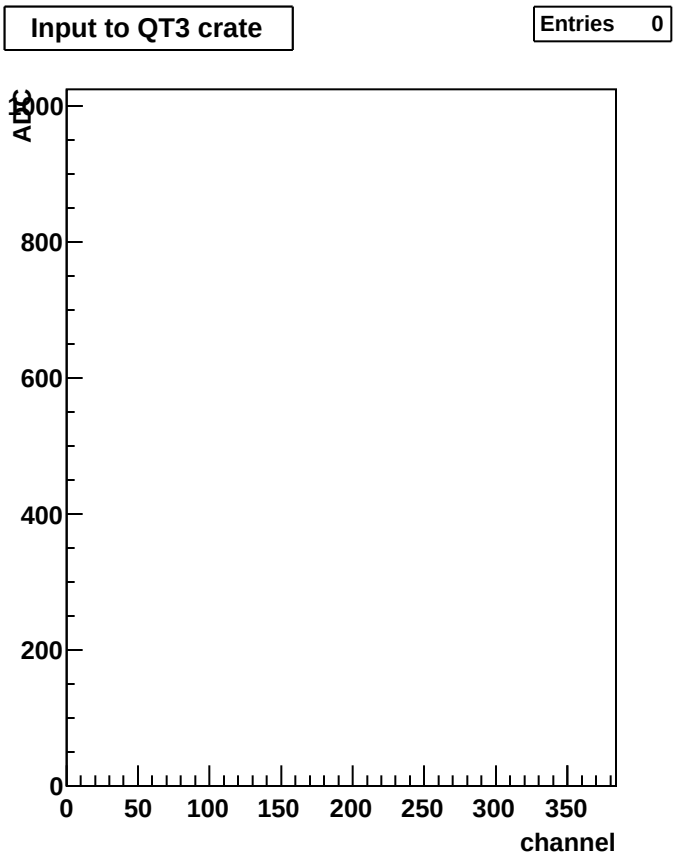

Entries 2000000
BBC-S-East ADC Sum
BBC-S-West ADC Sum

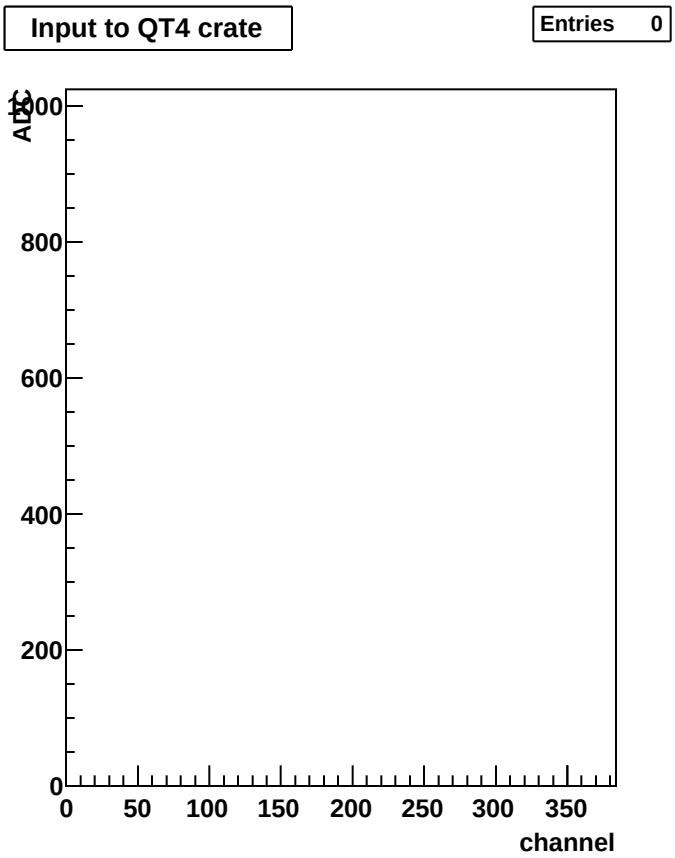

Entries 2000000
ZDC East ADC Sum Att
ZDC West ADC Sum Att

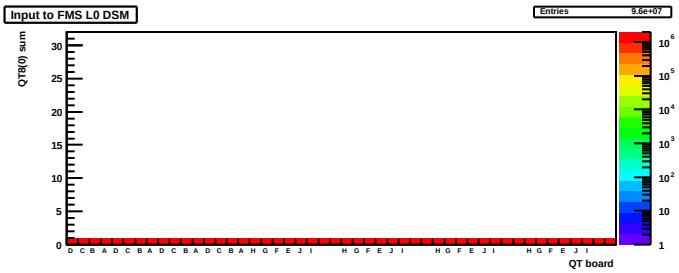

Entries 2000000
BBC-L TAC Diff
BCC-S TAC Diff
Entries 2000000
BBC-L TAC Diff
VPD TAC Diff
Entries 2000000
BBC-L TAC Diff
ZDC TAC Diff
Entries 2000000
BBC-S TAC Diff
VPD TAC Diff
Entries 2000000
BBC-S TAC Diff
ZDC TAC Diff
Entries 2000000
VPD TAC Diff
ZDC TAC Diff

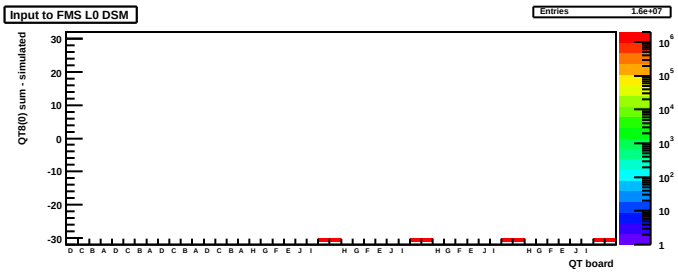

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

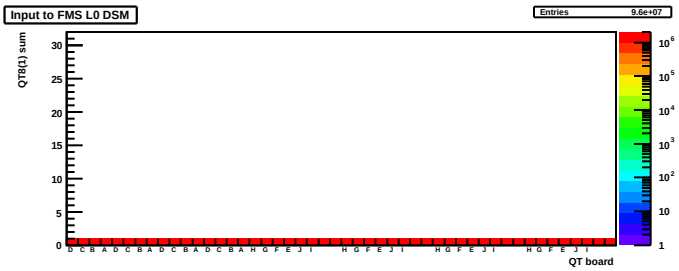

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

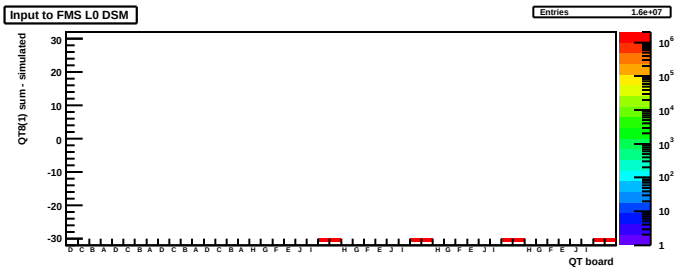

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

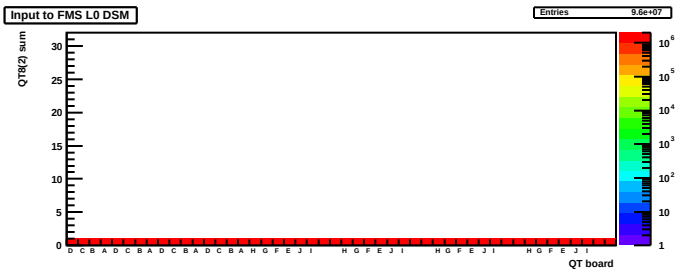

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

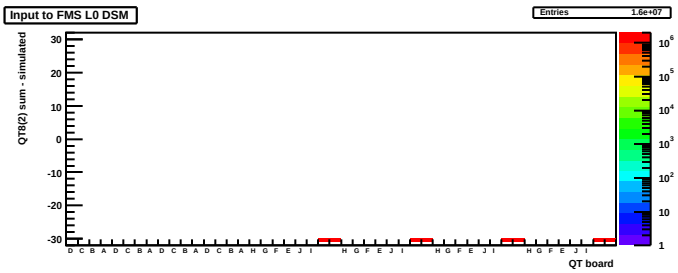

Input to FMS L0 DSM
Entries 9.6e+07
QT8(0) sum
QT board

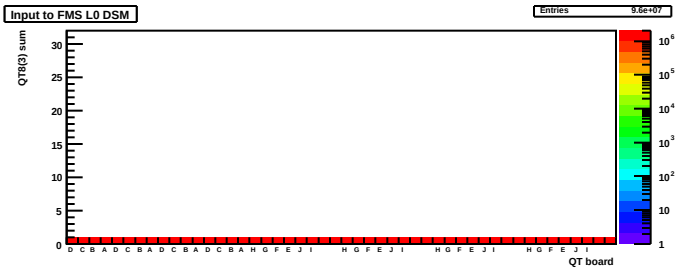

Input to FMS L0 DSM
Entries 1.6e+07
QT8(0) sum - simulated
QT board

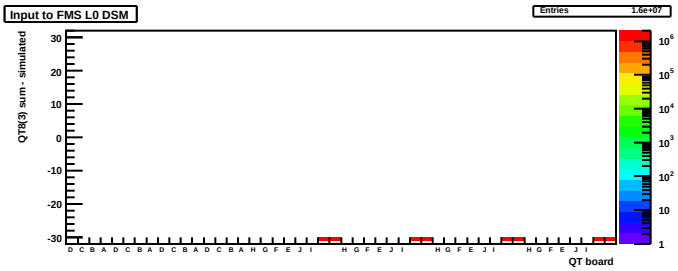

Input to FMS L0 DSM
Entries 9.6e+07
QT8(1) sum
QT board

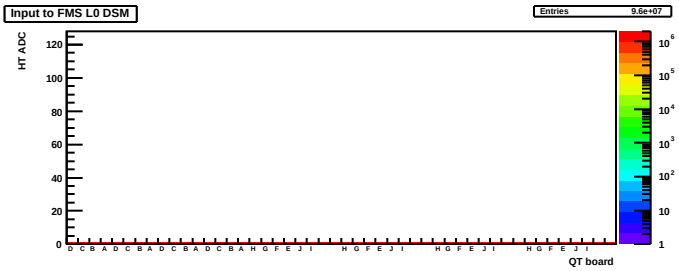

Input to FMS L0 DSM
Entries 1.6e+07
QT8(1) sum - simulated
QT board

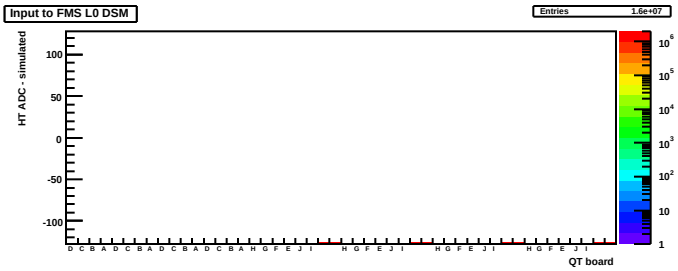

Input to FMS L0 DSM
Entries 9.6e+07
QT8(2) sum
QT board

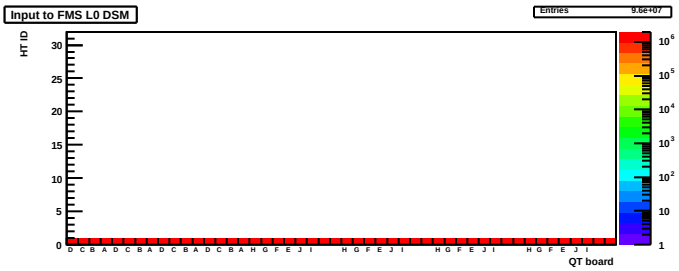

Input to FMS L0 DSM
Entries 1.6e+07
QT8(2) sum - simulated
QT board

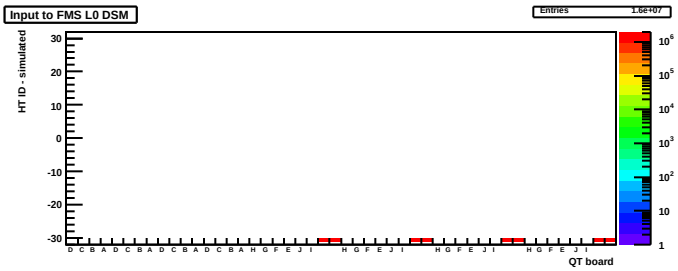

Input to FMS L0 DSM
Entries 9.6e+07
QT8(3) sum
QT board

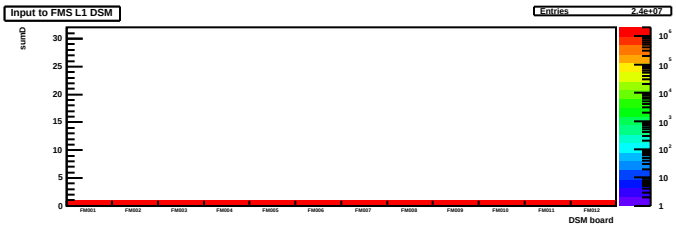

Input to FMS L0 DSM
Entries 1.6e+07
QT8(3) sum - simulated
QT board

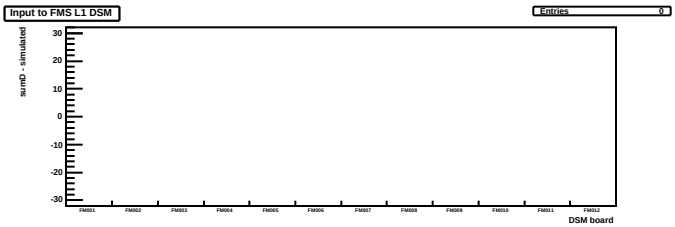

Input to FMS L0 DSM
Entries 9.6e+07
HT ADC
QT board

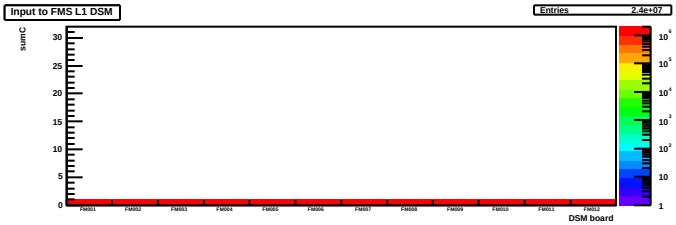

Input to FMS L0 DSM
Entries 1.6e+07
HT ADC - simulated
QT board

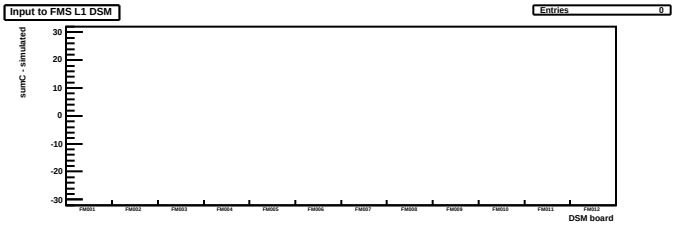

Input to FMS L0 DSM
Entries 9.6e+07
HT ID
QT board

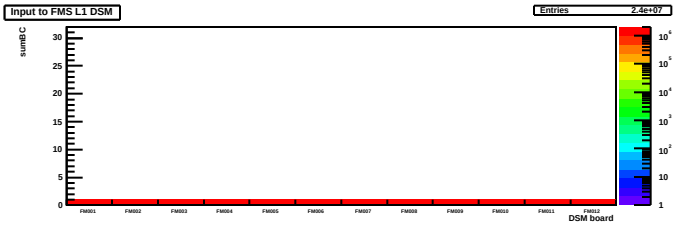

Input to FMS L0 DSM
Entries 1.6e+07
HT ID - simulated
QT board

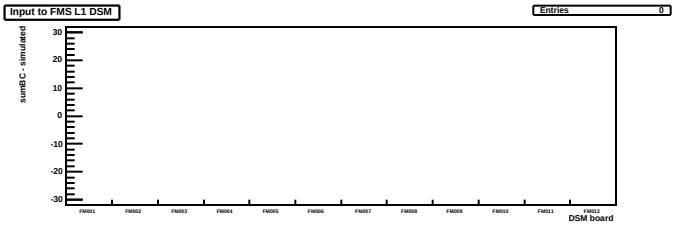

Input to FMS L1 DSM
Entries 2.4e+07
sumD
DSM board

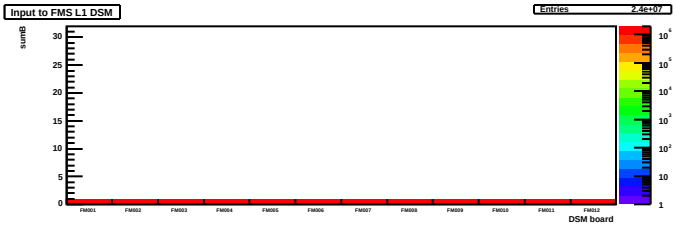

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

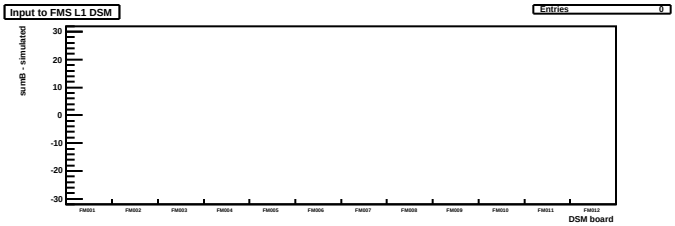

Input to FMS L1 DSM
Entries 2.4e+07
sumC
DSM board

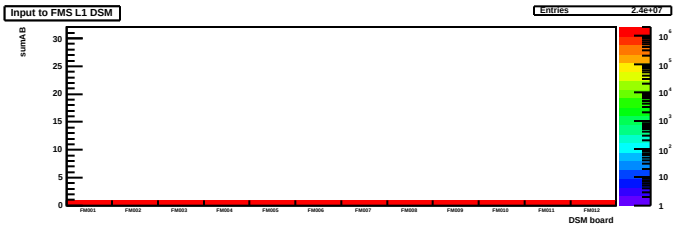

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

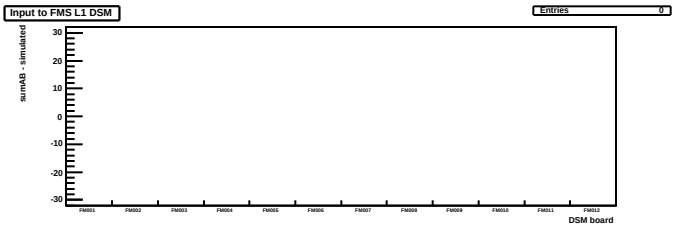

Input to FMS L1 DSM
Entries 2.4e+07
sumBC
DSM board

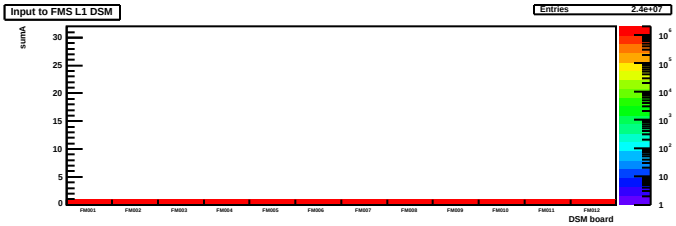

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

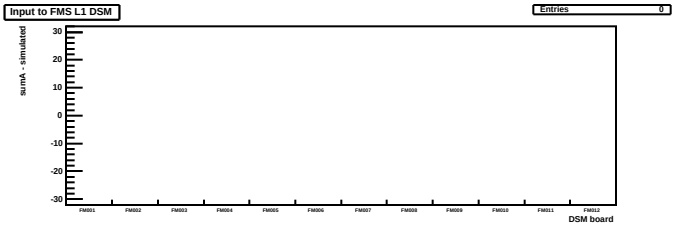

Input to FMS L1 DSM
Entries 2.4e+07
sumB
DSM board

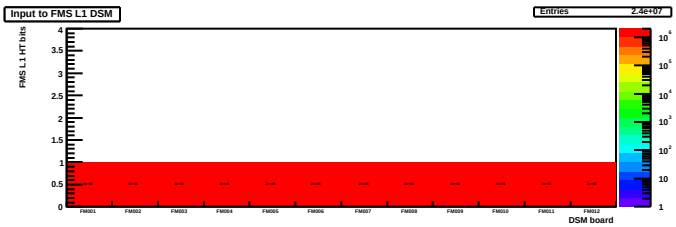

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

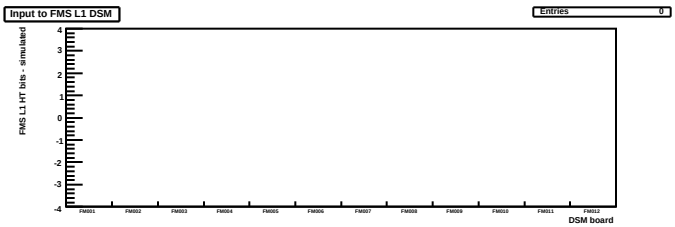

Input to FMS L1 DSM
Entries 2.4e+07
sumAB
DSM board

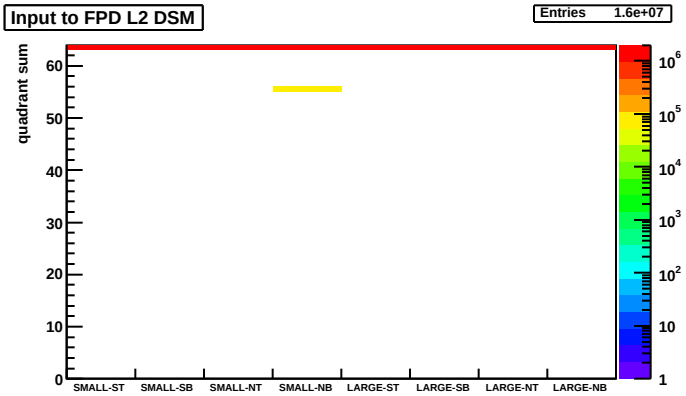

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

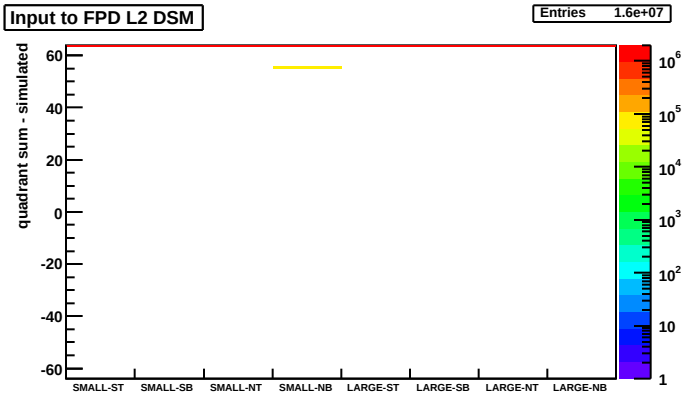

Input to FMS L1 DSM
Entries 2.4e+07
sumA
DSM board

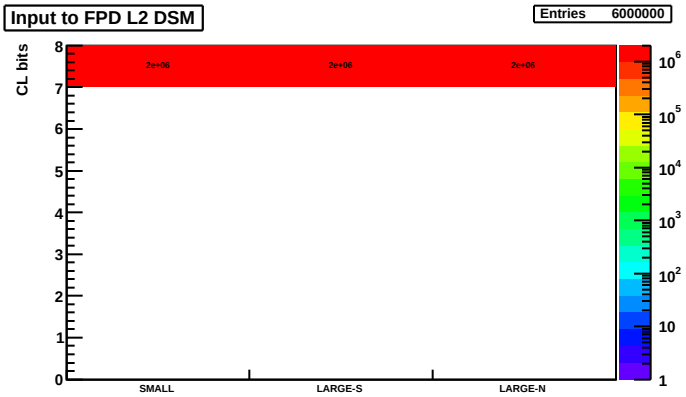

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

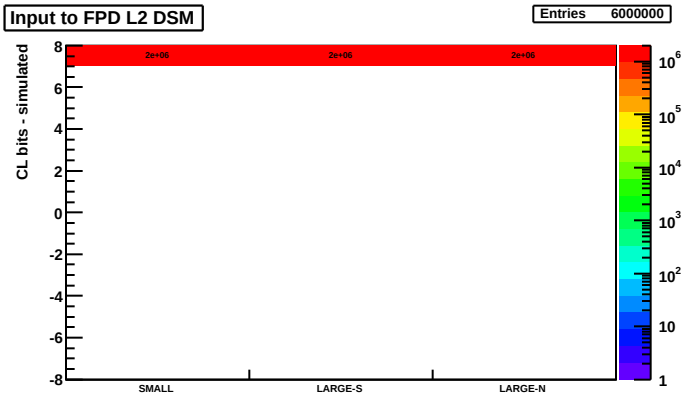

Input to FMS L1 DSM
Entries 2.4e+07
FMS L1 HT bits
DSM board

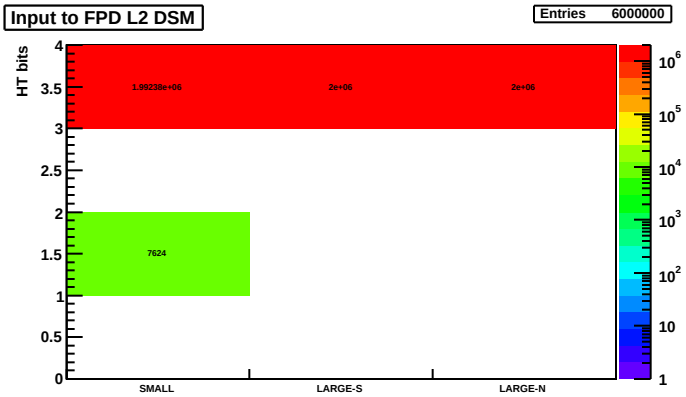

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

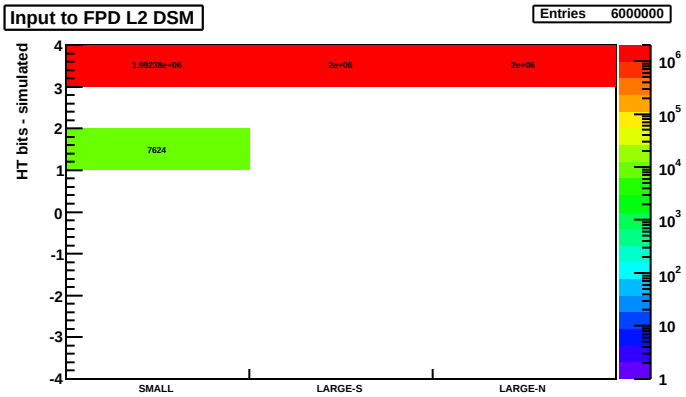

Input to FPD L2 DSM
Entries 1.6e+07
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

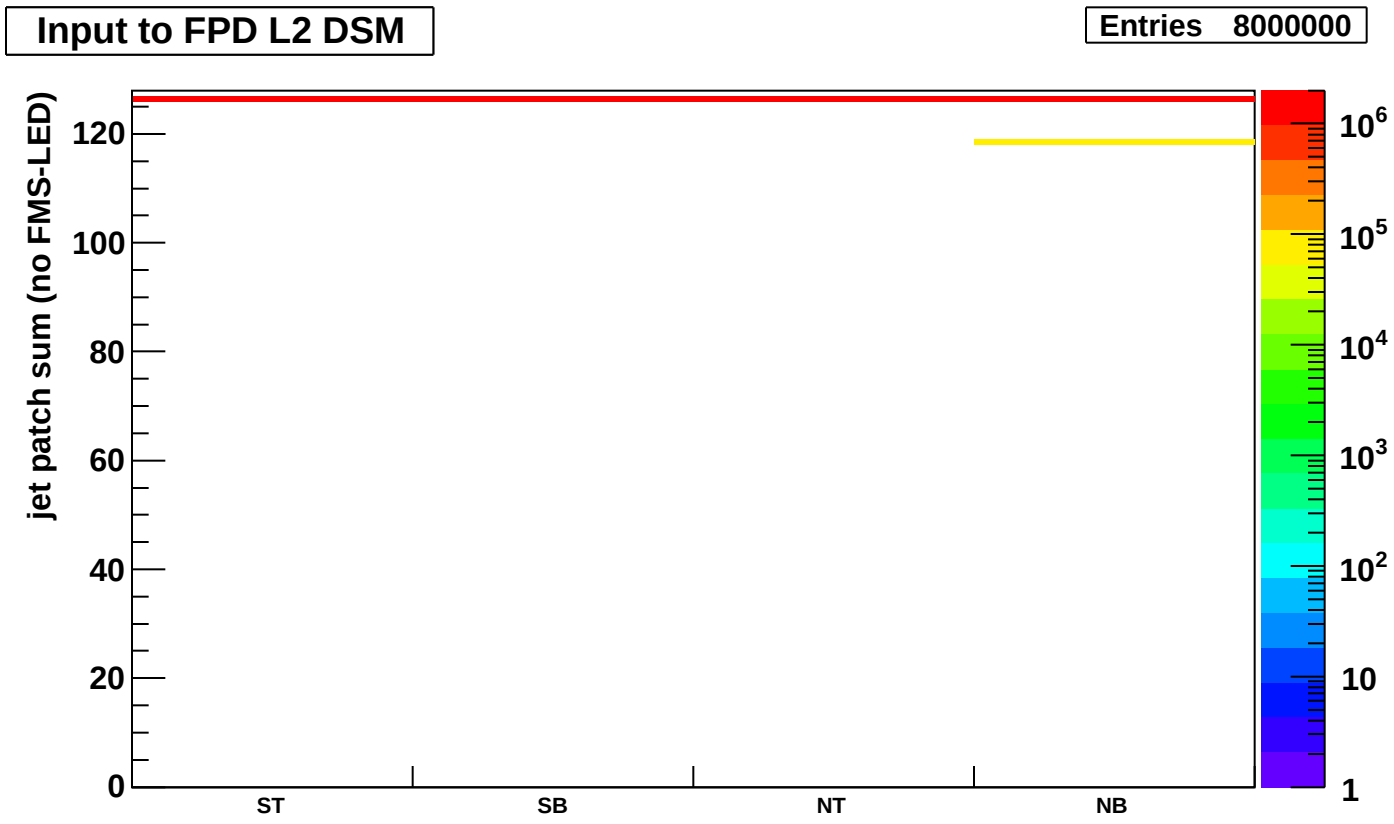

Input to FPD L2 DSM
Entries 1.6e+07
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

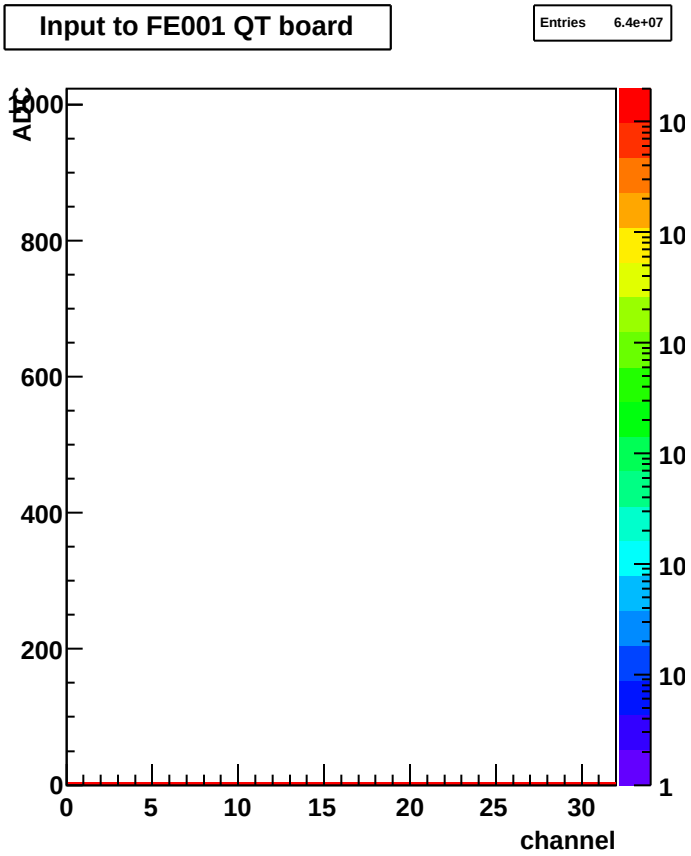

Input to FPD L2 DSM
Entries 6000000
CL bits
2e+06
2e+06
2e+06
SMALL
LARGE-S
LARGE-N

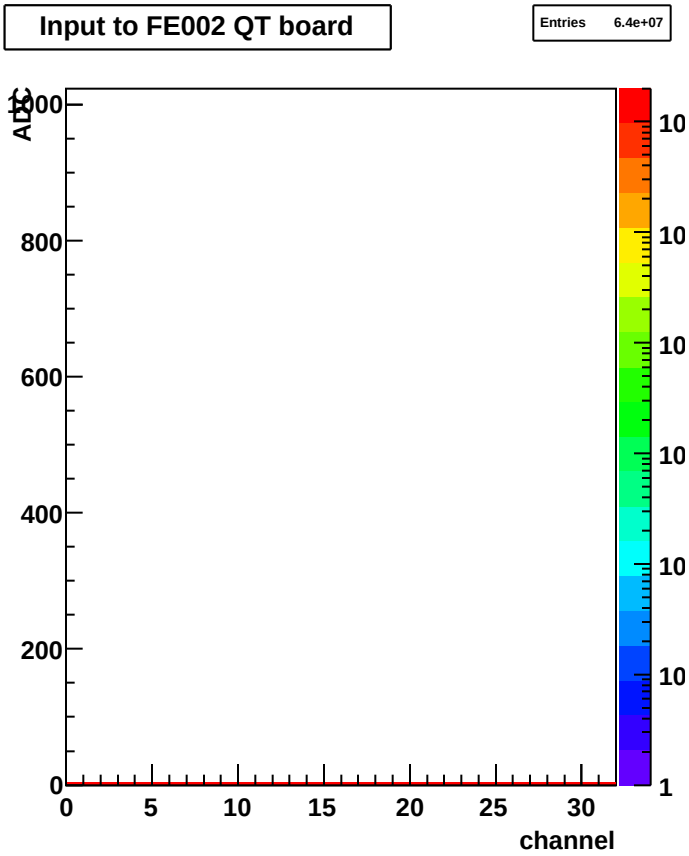

Input to FPD L2 DSM
Entries 6000000
CL bits - simulated
2e+06
2e+06
2e+06
SMALL
LARGE-S
LARGE-N

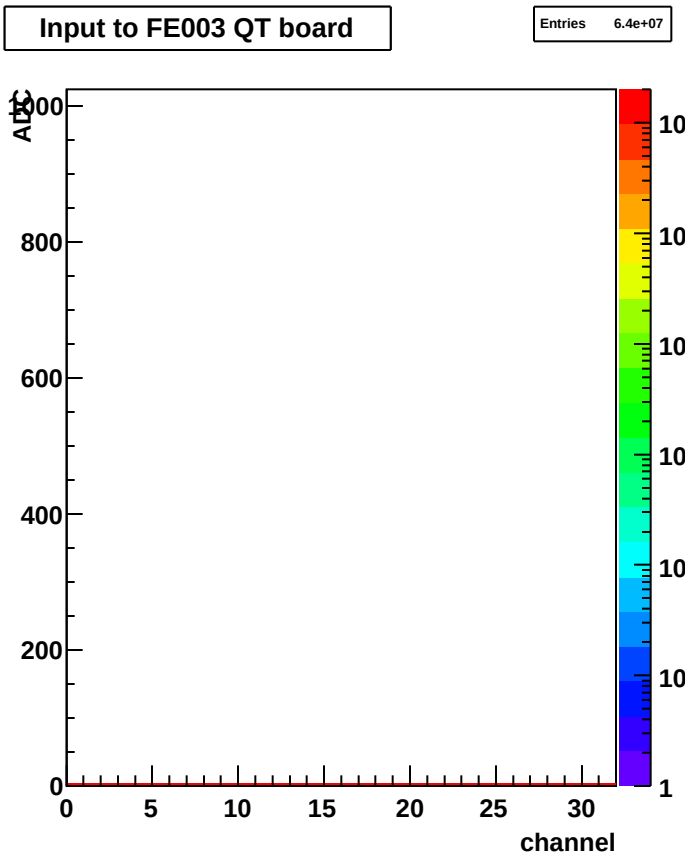

Input to FPD L2 DSM
Entries 6000000
HT bits
1.99238e+06
2e+06
2e+06
7624
SMALL
LARGE-S
LARGE-N

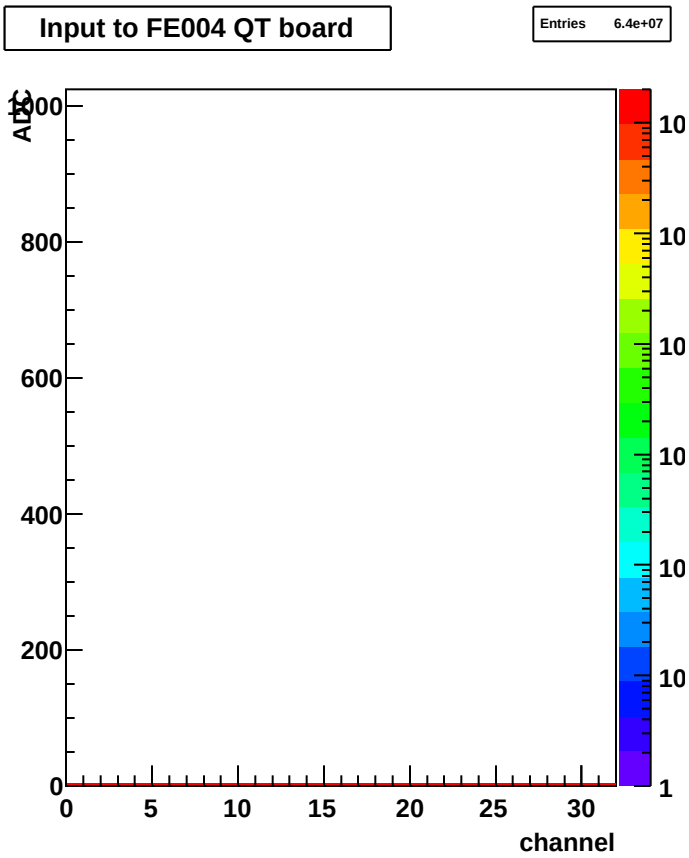

Input to FPD L2 DSM
Entries 6000000
HT bits - simulated
1.99238e+06
2e+06
2e+06
7624
SMALL
LARGE-S
LARGE-N

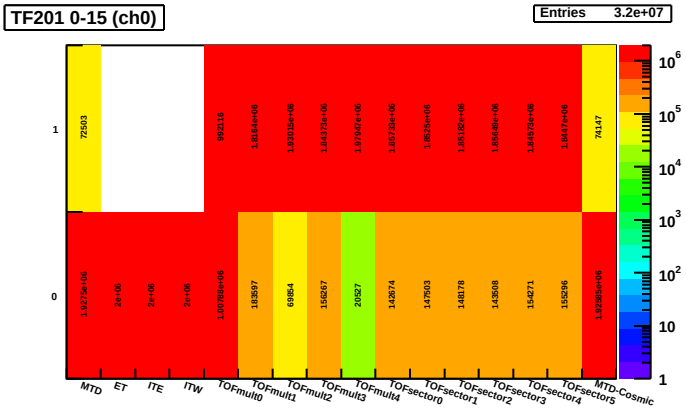

Input to FPD L2 DSM
Entries 8000000
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5
10^6

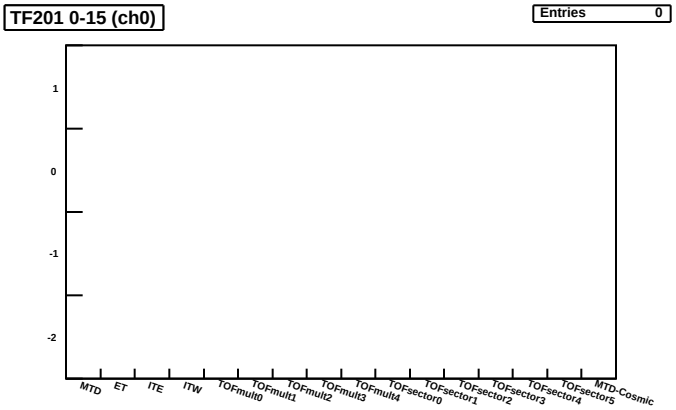

Input to FE001 QT board
Entries 6.4e+07
ADC
channel

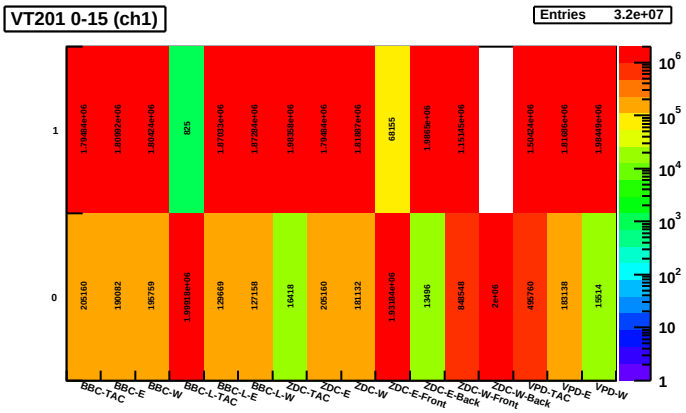

Input to FE002 QT board
Entries 6.4e+07
ADC
channel

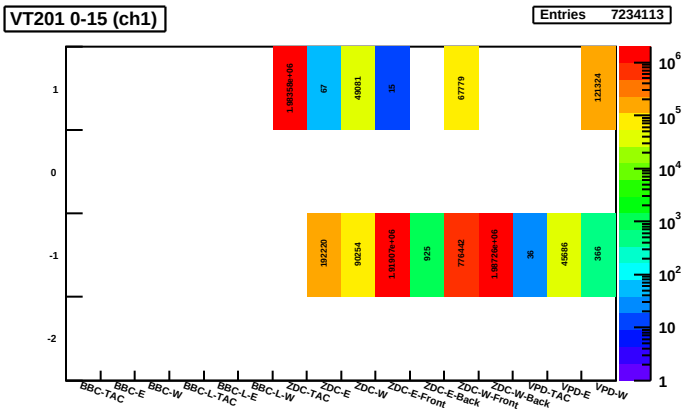

Input to FE003 QT board
Entries 6.4e+07
ADC
channel

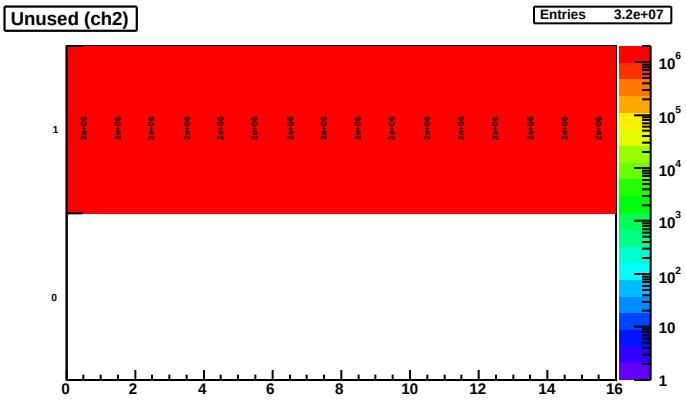

Input to FE004 QT board
Entries 6.4e+07
ADC
channel

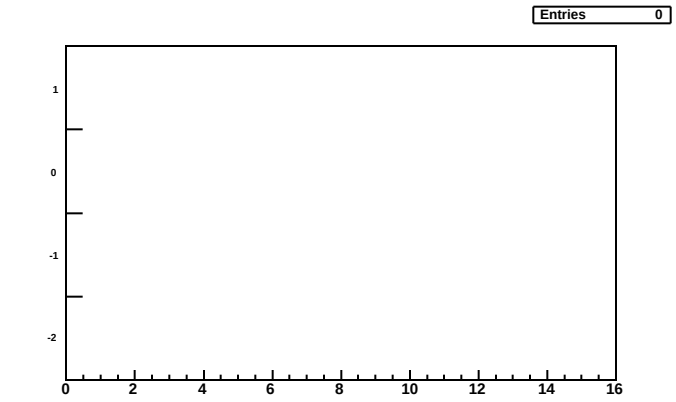

TF201 0-15 (ch0)
Entries 3.2e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

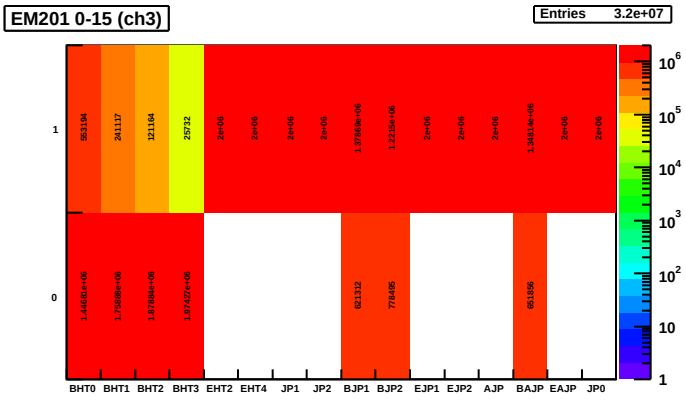

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

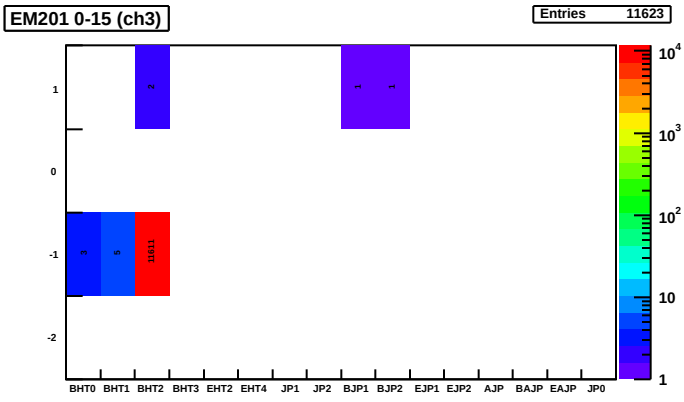

VT201 0-15 (ch1)
Entries 3.2e+07
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

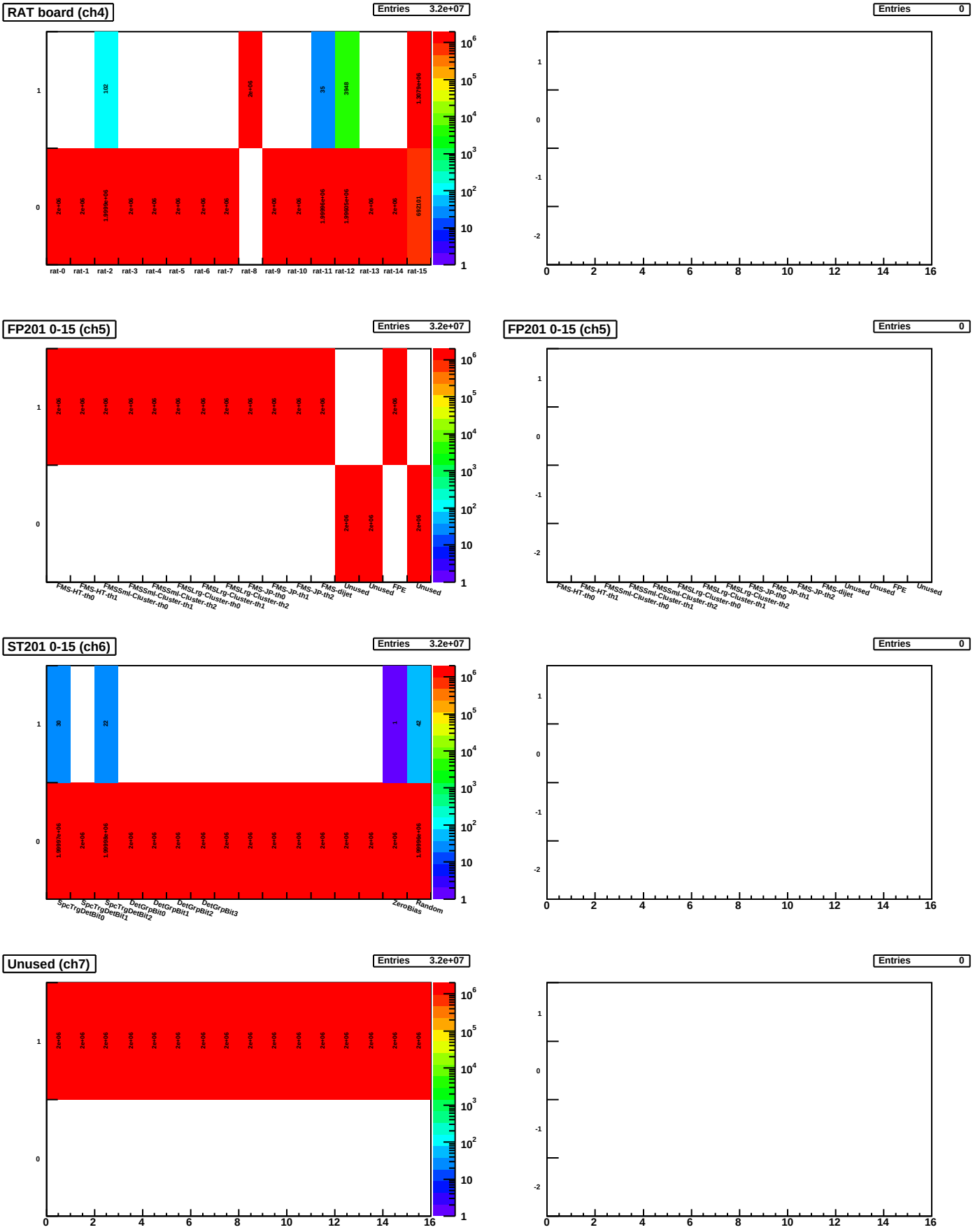

VT201 0-15 (ch1)
Entries 7234113
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 3.2e+07


Entries 0


EM201 0-15 (ch3)
Entries 3.2e+07
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0


EM201 0-15 (ch3)
Entries 11623
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0


RAT board (ch4)
Entries 3.2e+07
Entries 0
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15
FP201 0-15 (ch5)
Entries 3.2e+07
Entries 0
FMS-HT-th0 FMS-HT-th1 FMSSml-Cluster-th0 FMSSml-Cluster-th1 FMSSml-Cluster-th2 FMSLrg-Cluster-th0 FMSLrg-Cluster-th1 FMSLrg-Cluster-th2 FMS-JP-th0 FMS-JP-th1 FMS-JP-th2 FMS-dijet Unused Unused FPE Unused
ST201 0-15 (ch6)
Entries 3.2e+07
Entries 0
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random
Unused (ch7)
Entries 3.2e+07
Entries 0