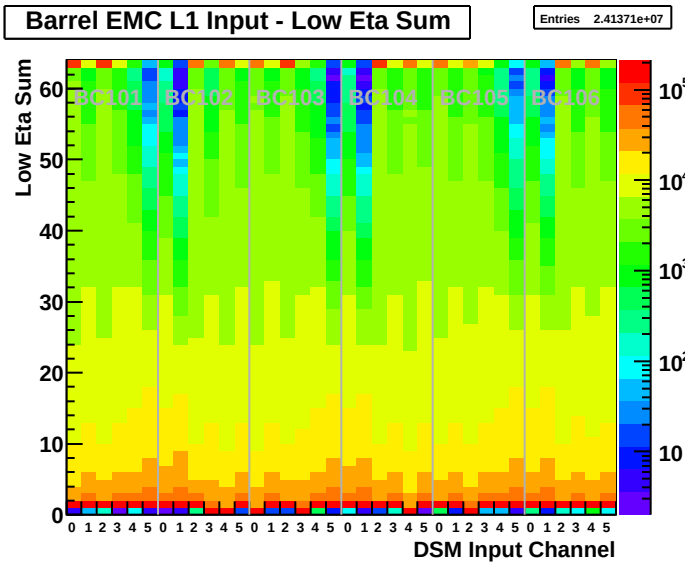
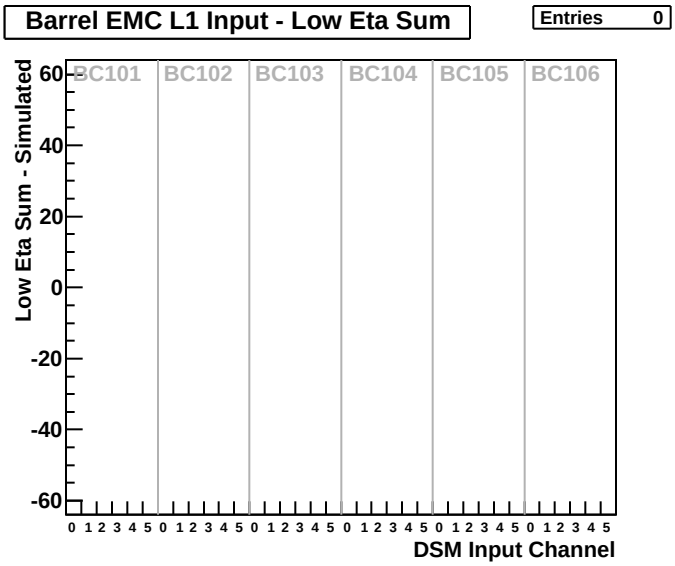
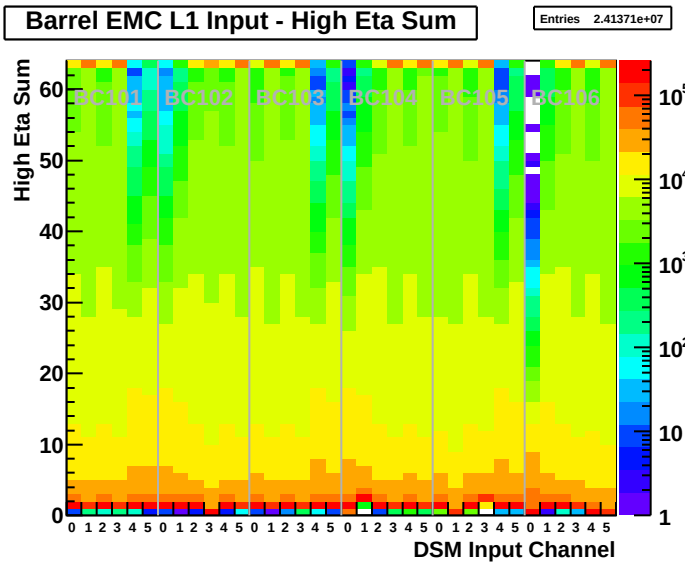
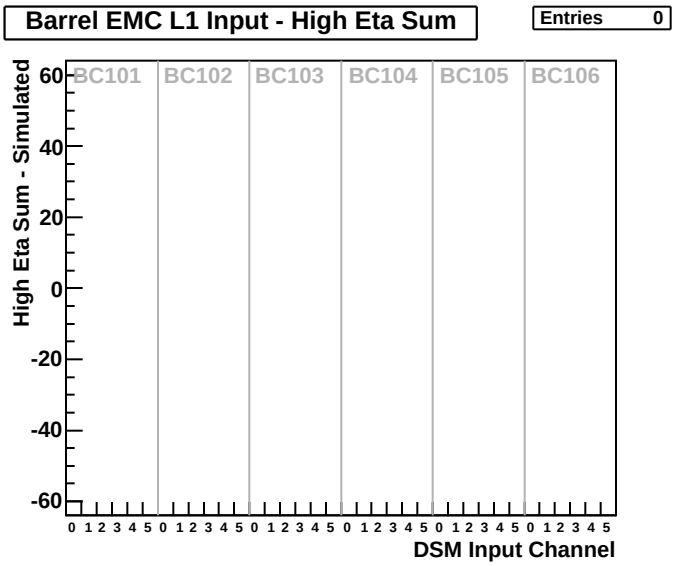
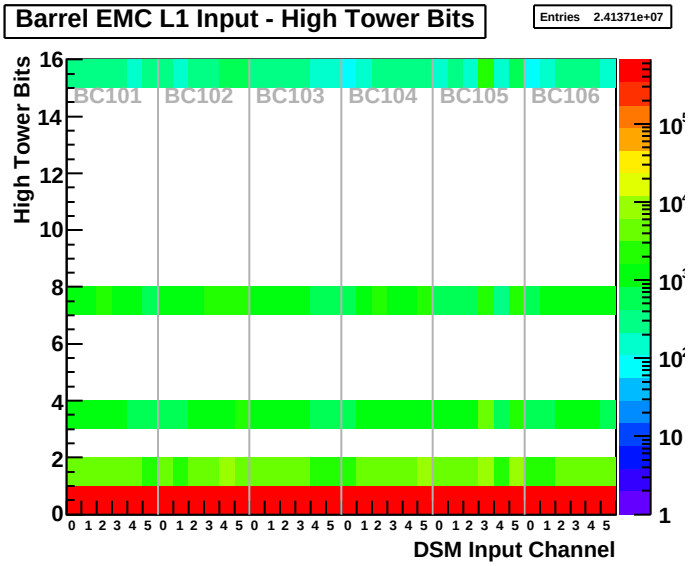
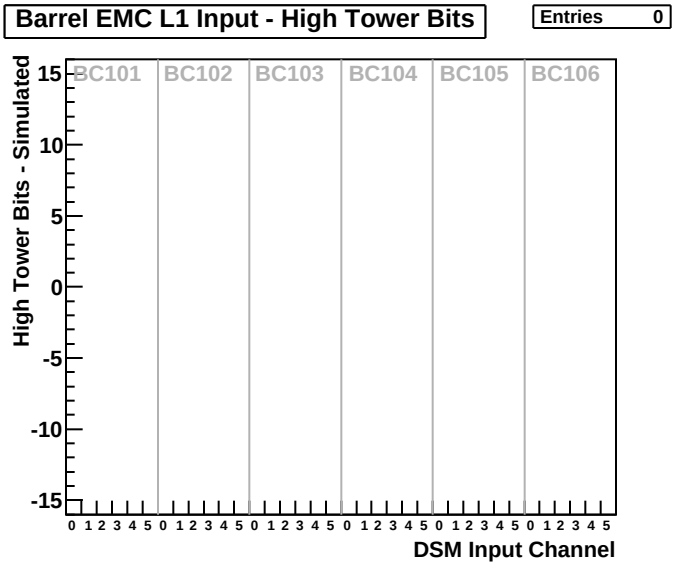

Barrel EMC L1 Input - Low Eta Sum
Entries 2.41371e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

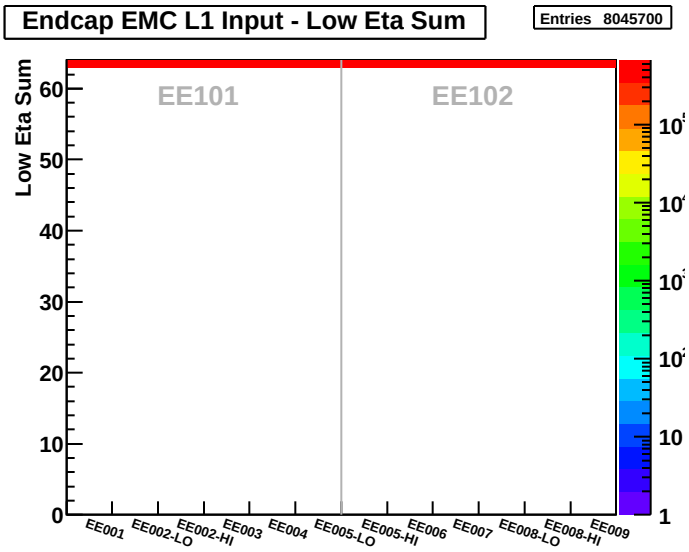

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

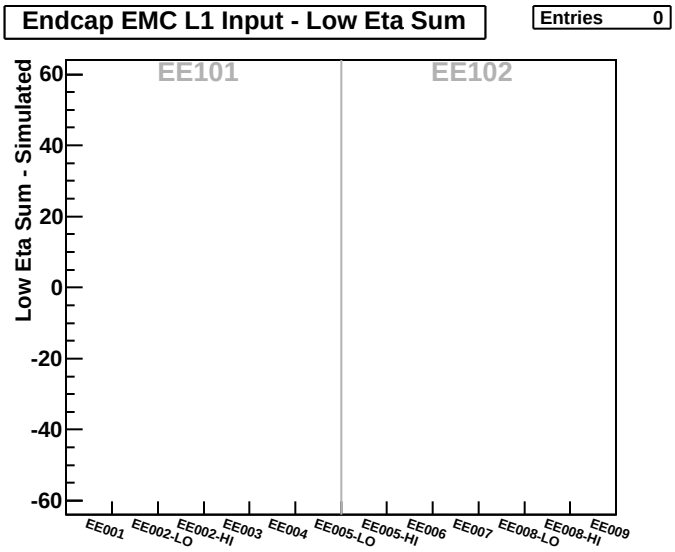

Barrel EMC L1 Input - High Eta Sum
Entries 2.41371e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

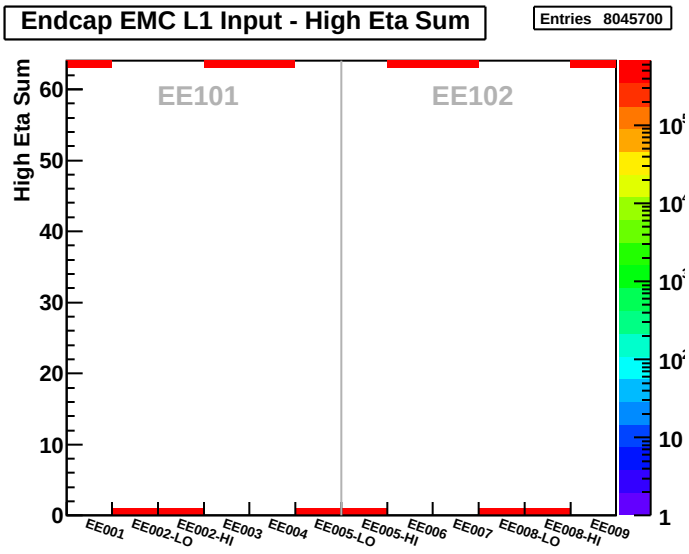

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

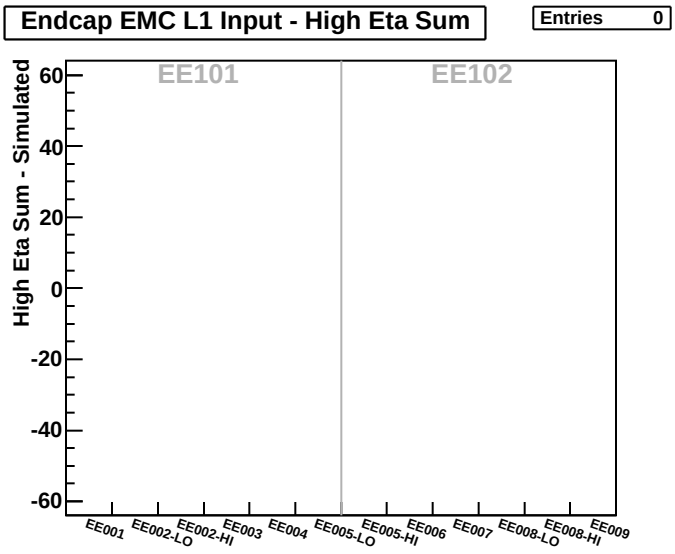

Barrel EMC L1 Input - High Tower Bits
Entries 2.41371e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

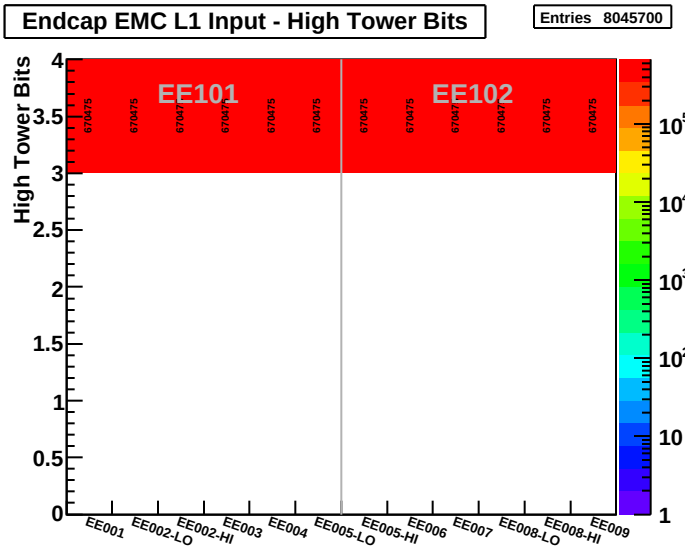

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

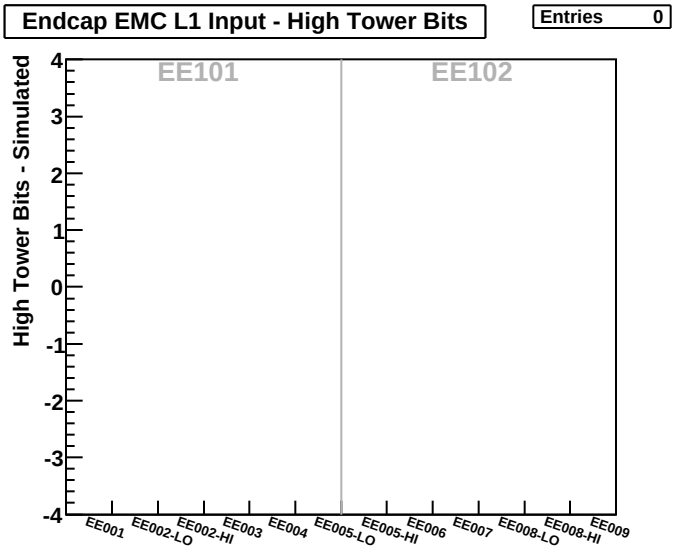

Endcap EMC L1 Input - Low Eta Sum
Entries 8045700
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

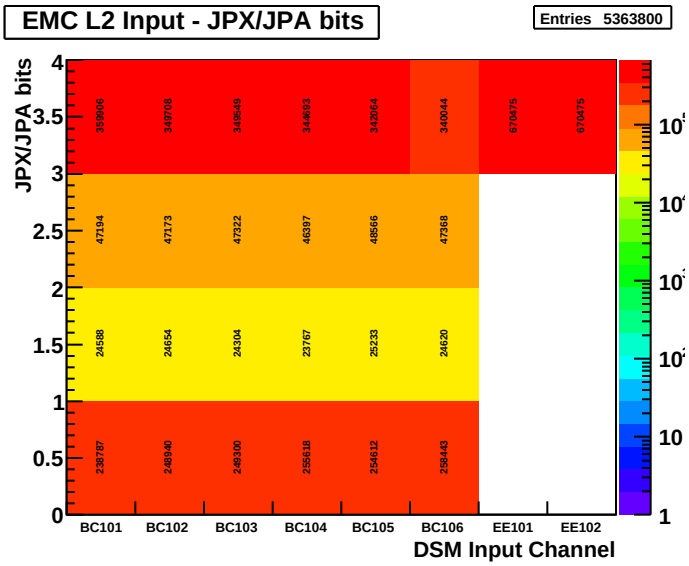

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

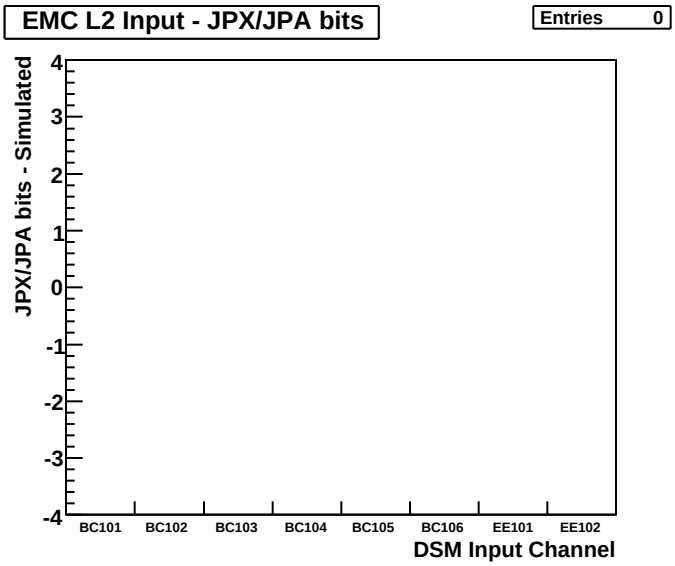

Endcap EMC L1 Input - High Eta Sum
Entries 8045700
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

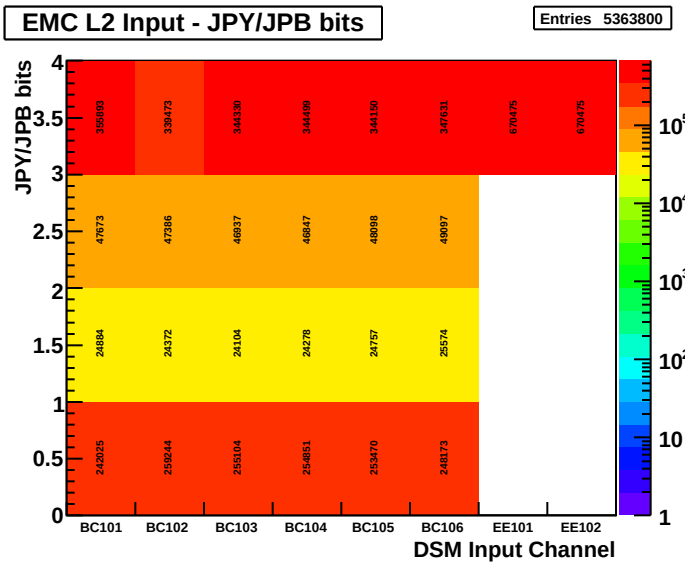

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

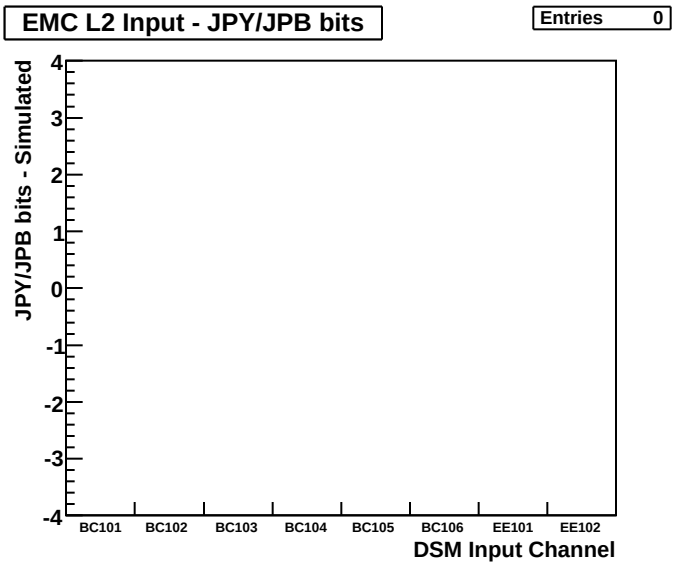

Endcap EMC L1 Input - High Tower Bits
Entries 8045700
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

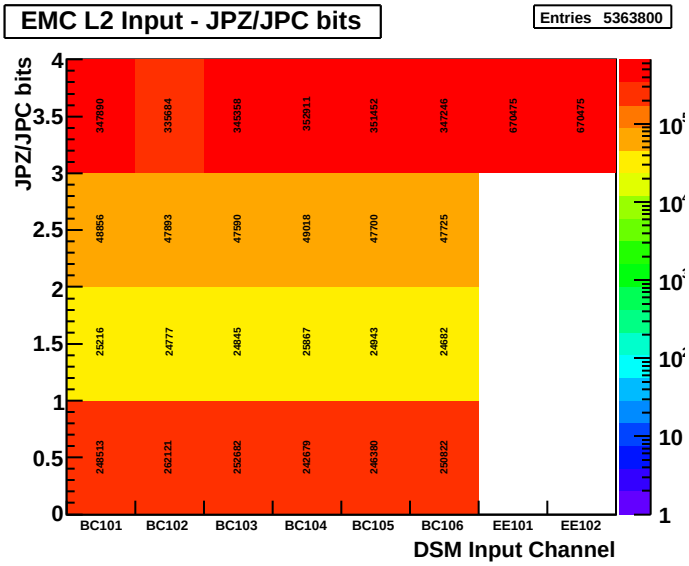

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

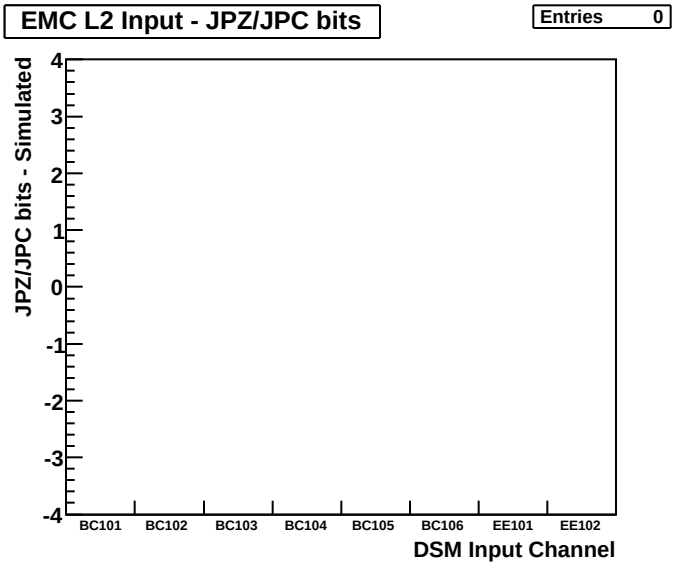

EMC L2 Input - JPX/JPA bits
Entries 5363800
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
355906 349708 349549 344693 342064 340044 670475 670475
47194 47173 47322 46397 48566 47368
24588 24654 24304 23767 25233 24620
238767 248940 249300 255618 254612 258443

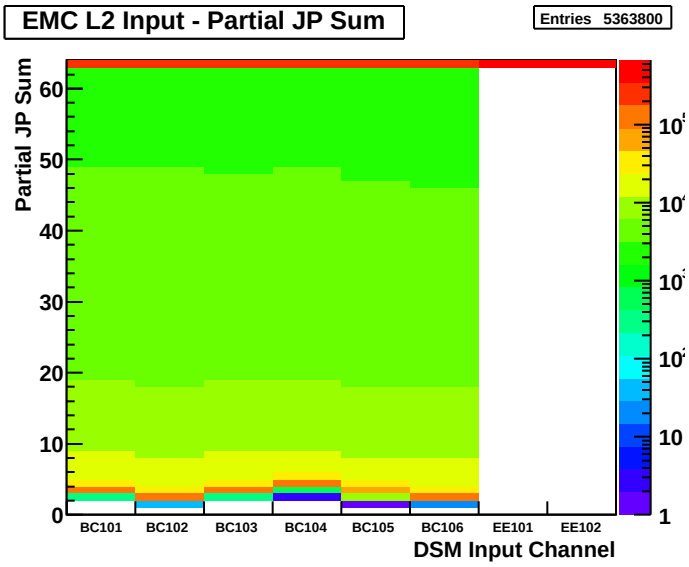

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

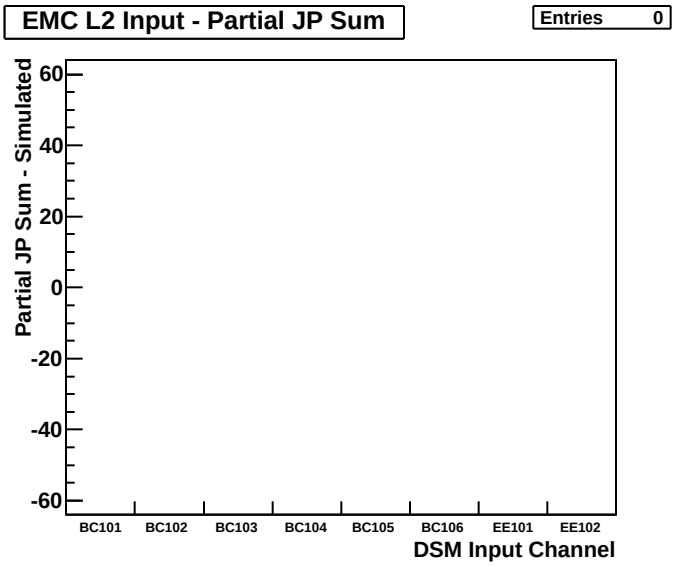

EMC L2 Input - JPY/JPB bits
Entries 5363800
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
355893 339473 344330 344499 344150 347631 670475 670475
47673 47386 46937 46847 48098 49097
24884 24372 24104 24278 24757 25574
242025 259244 255104 254851 253470 248173

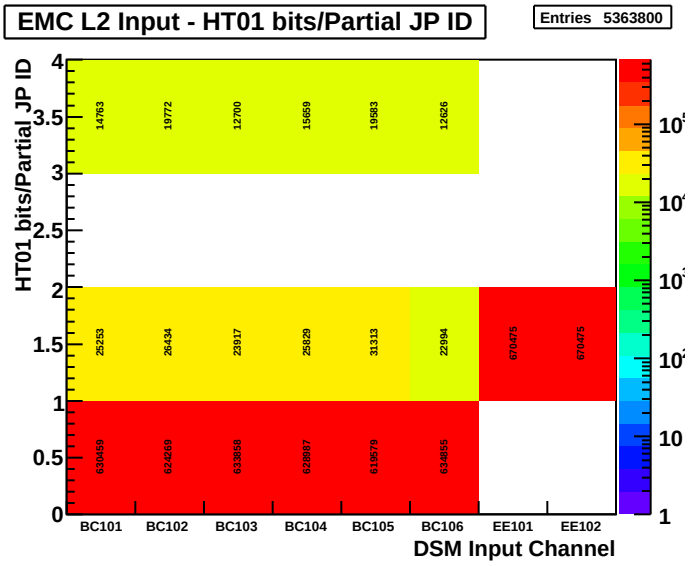

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

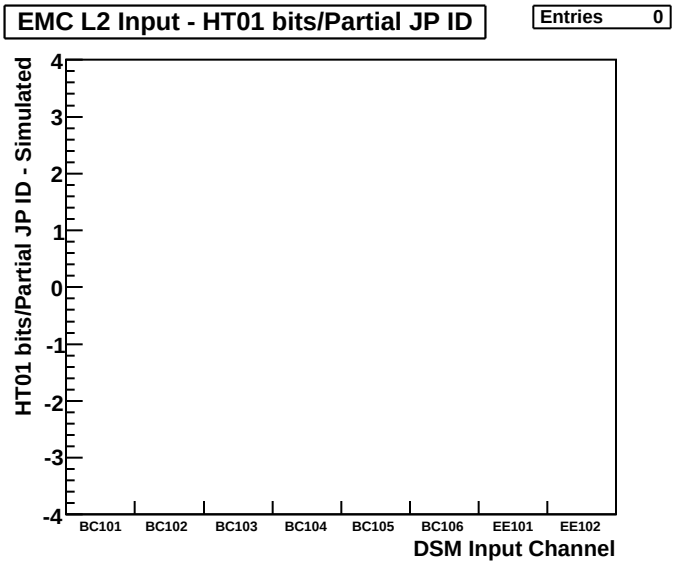

EMC L2 Input - JPZ/JPC bits
Entries 5363800
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
347690 335684 343358 352911 351452 347246 670475 670475
48856 47893 47590 49018 47700 47725
25216 24777 24845 25867 24943 24682
248513 262121 252682 242679 246380 250822

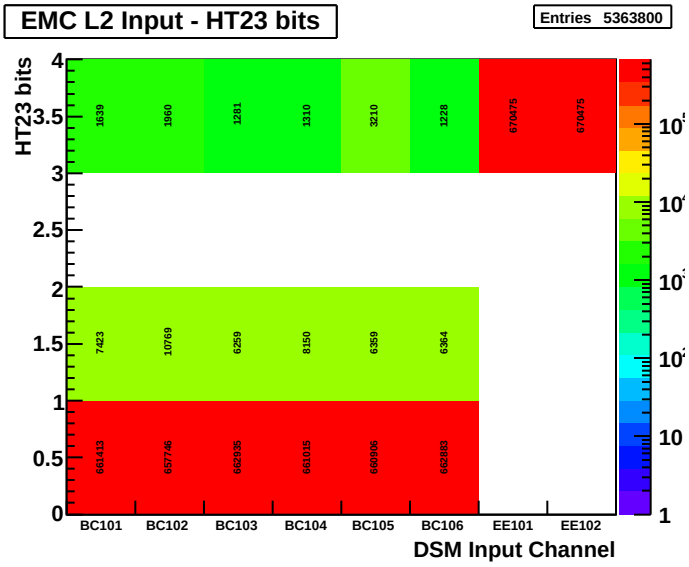

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

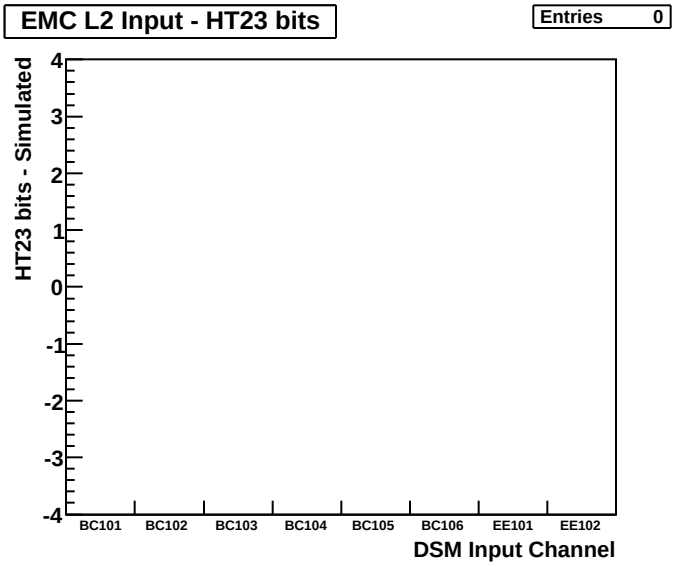

EMC L2 Input - Partial JP Sum
Entries 5363800
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

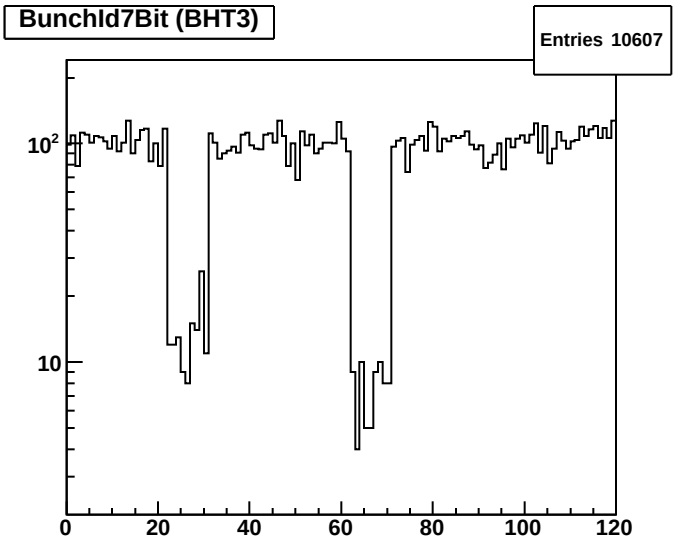

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

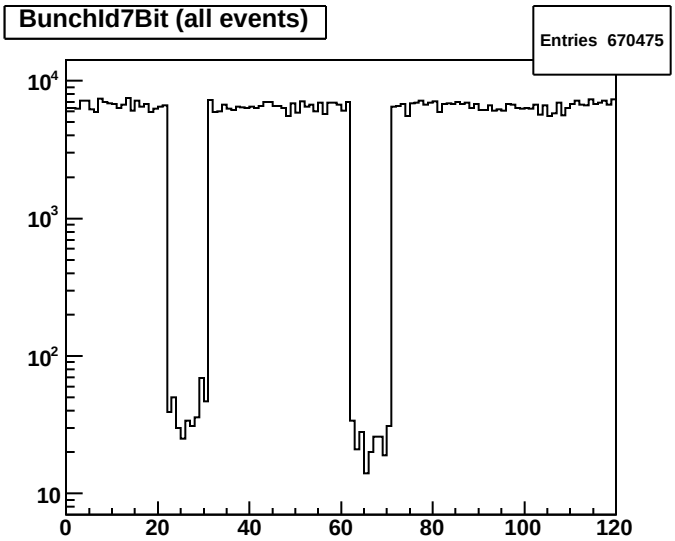

EMC L2 Input - HT01 bits/Partial JP ID
Entries 5363800
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

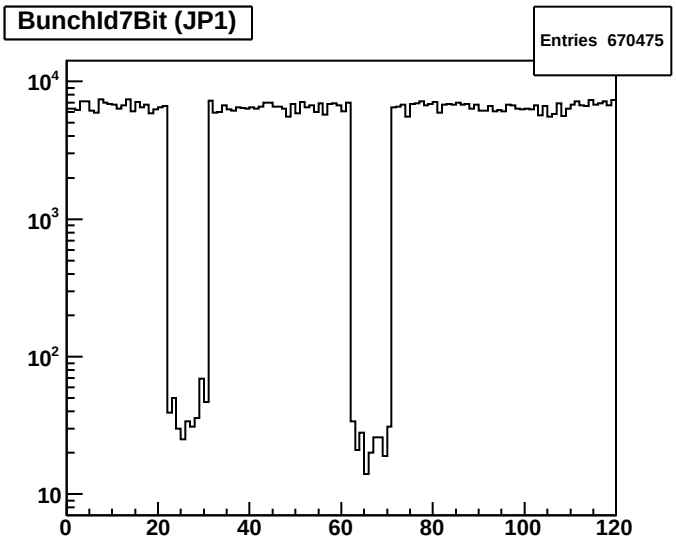

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

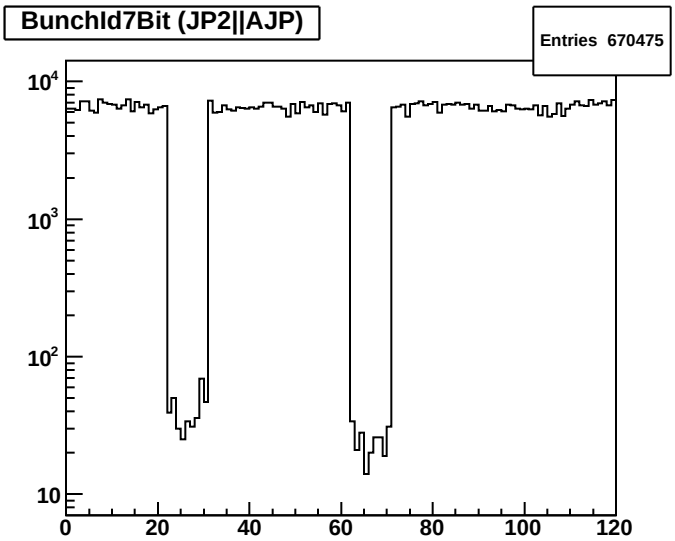

EMC L2 Input - HT23 bits
Entries 5363800
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

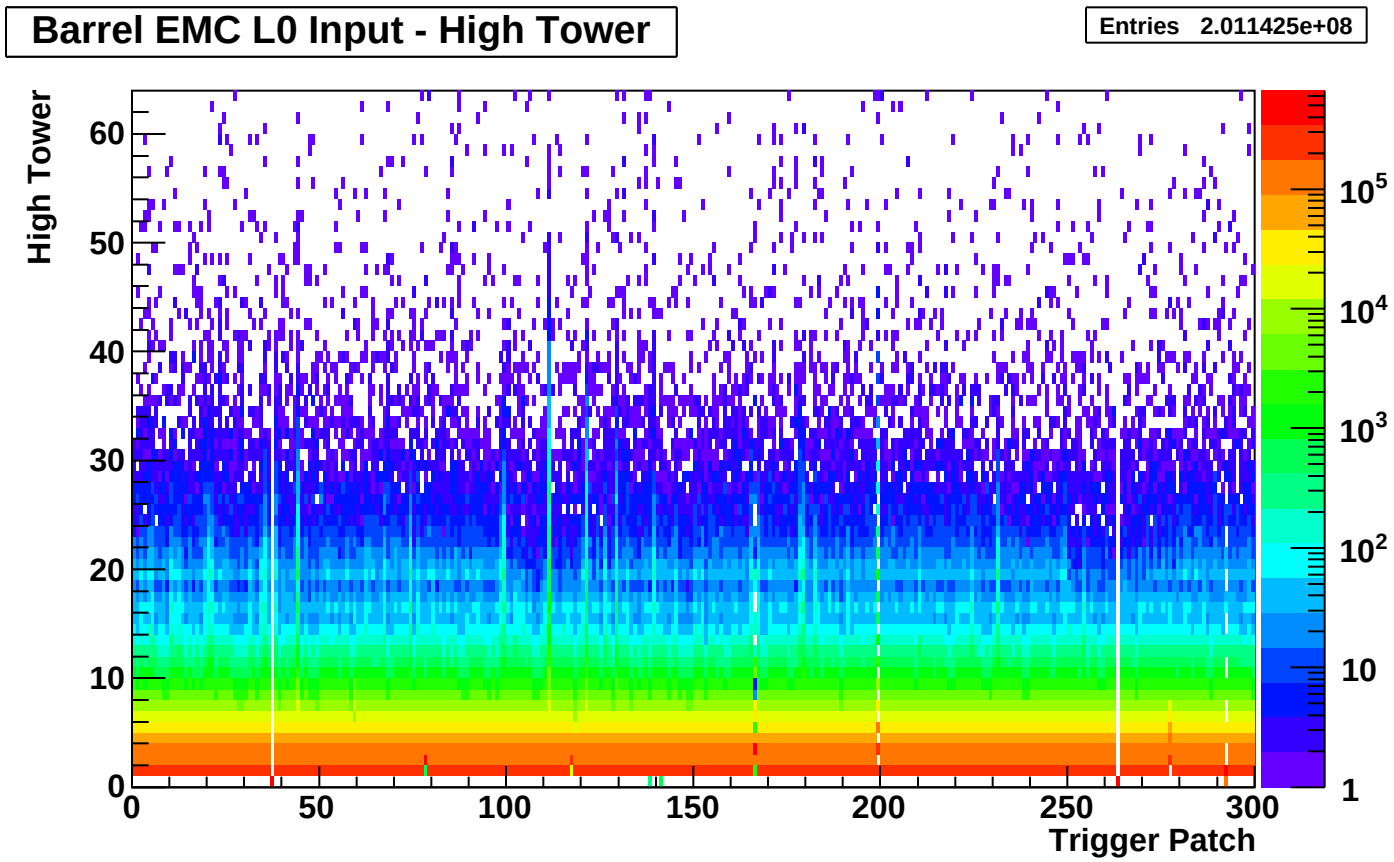

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

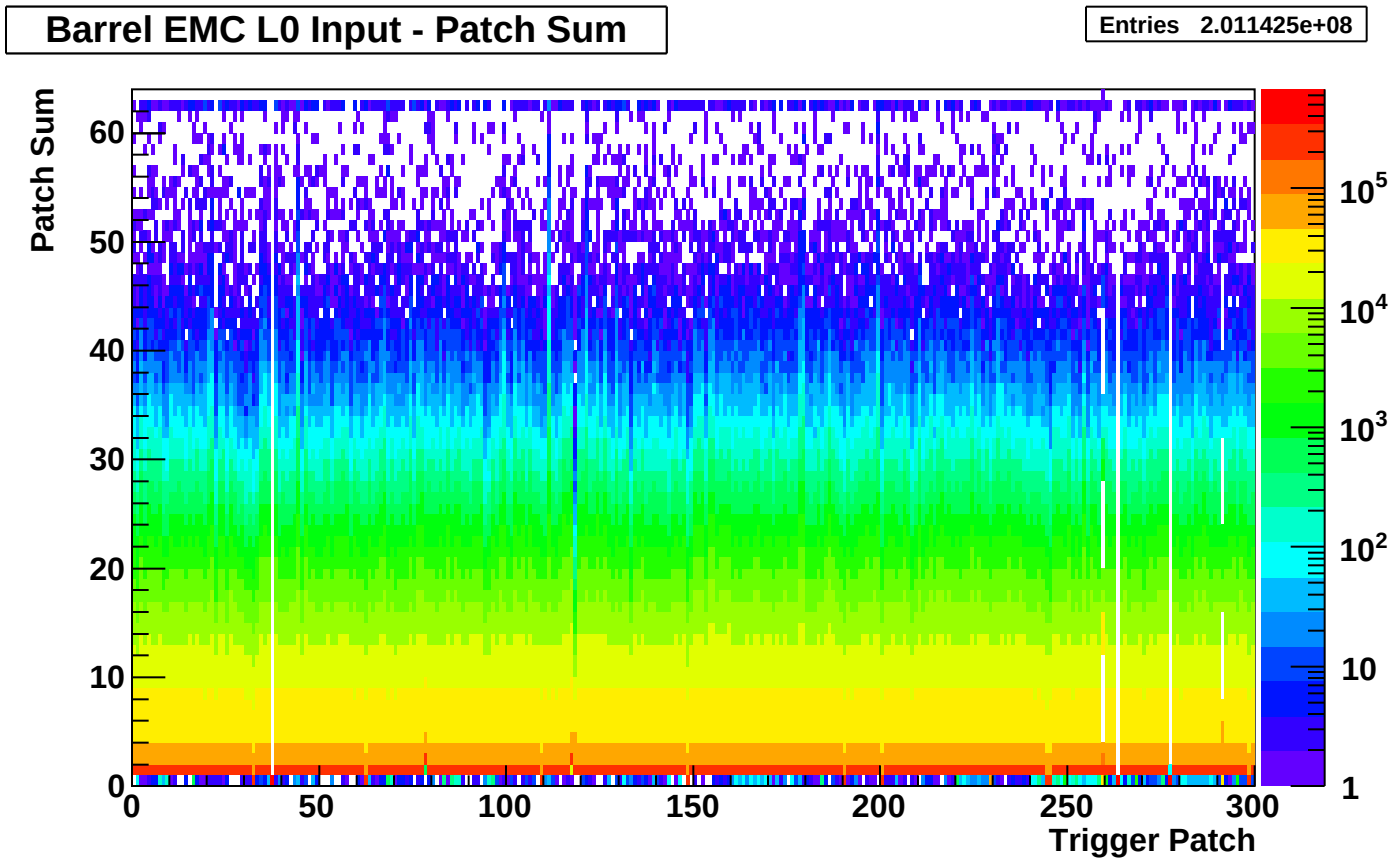

BunchId7Bit (BHT3)
Entries 10607
10^2
10
0
20
40
60
80
100
120

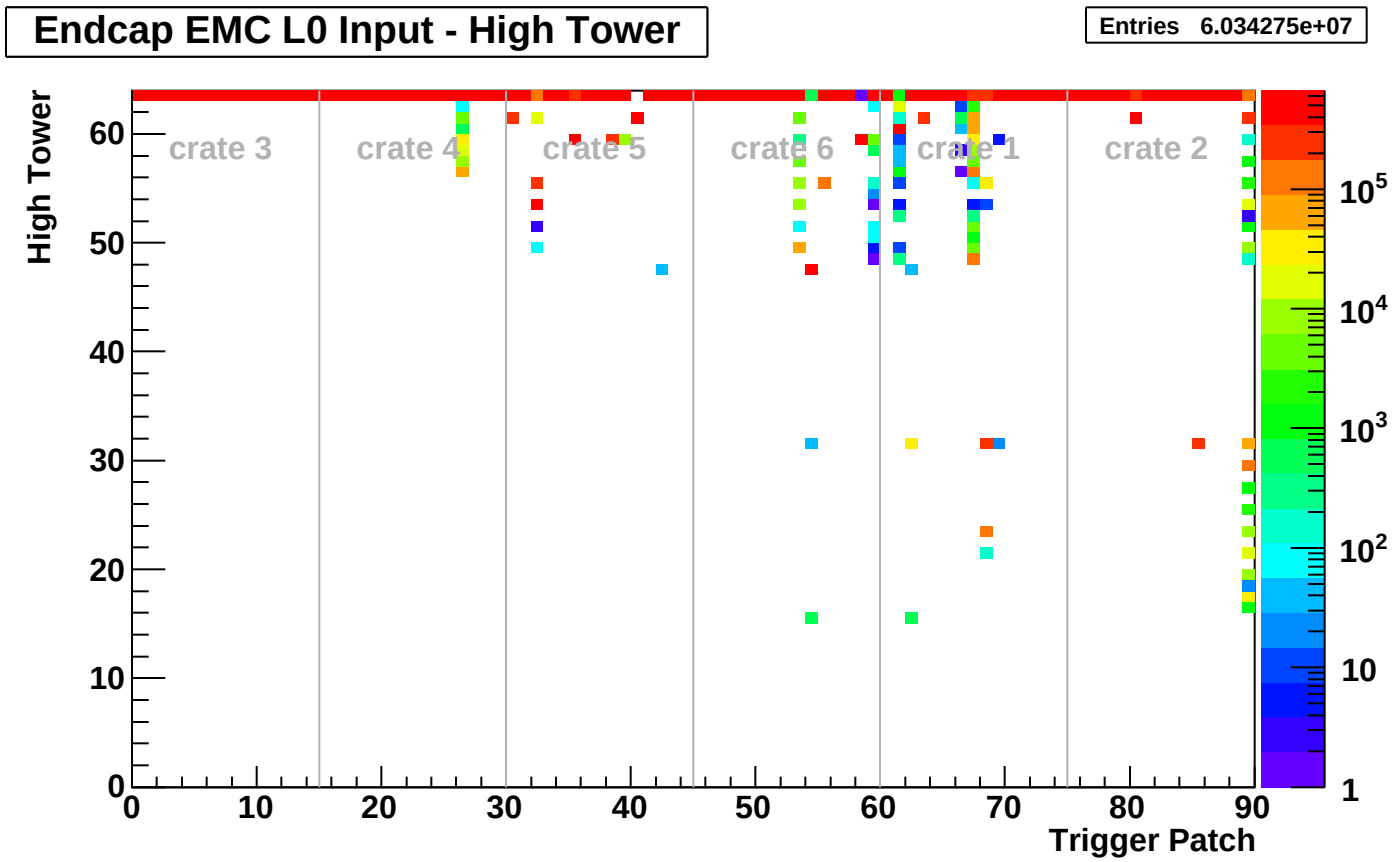

BunchId7Bit (all events)
Entries 670475
10^4
10^3
10^2
10
0
20
40
60
80
100
120

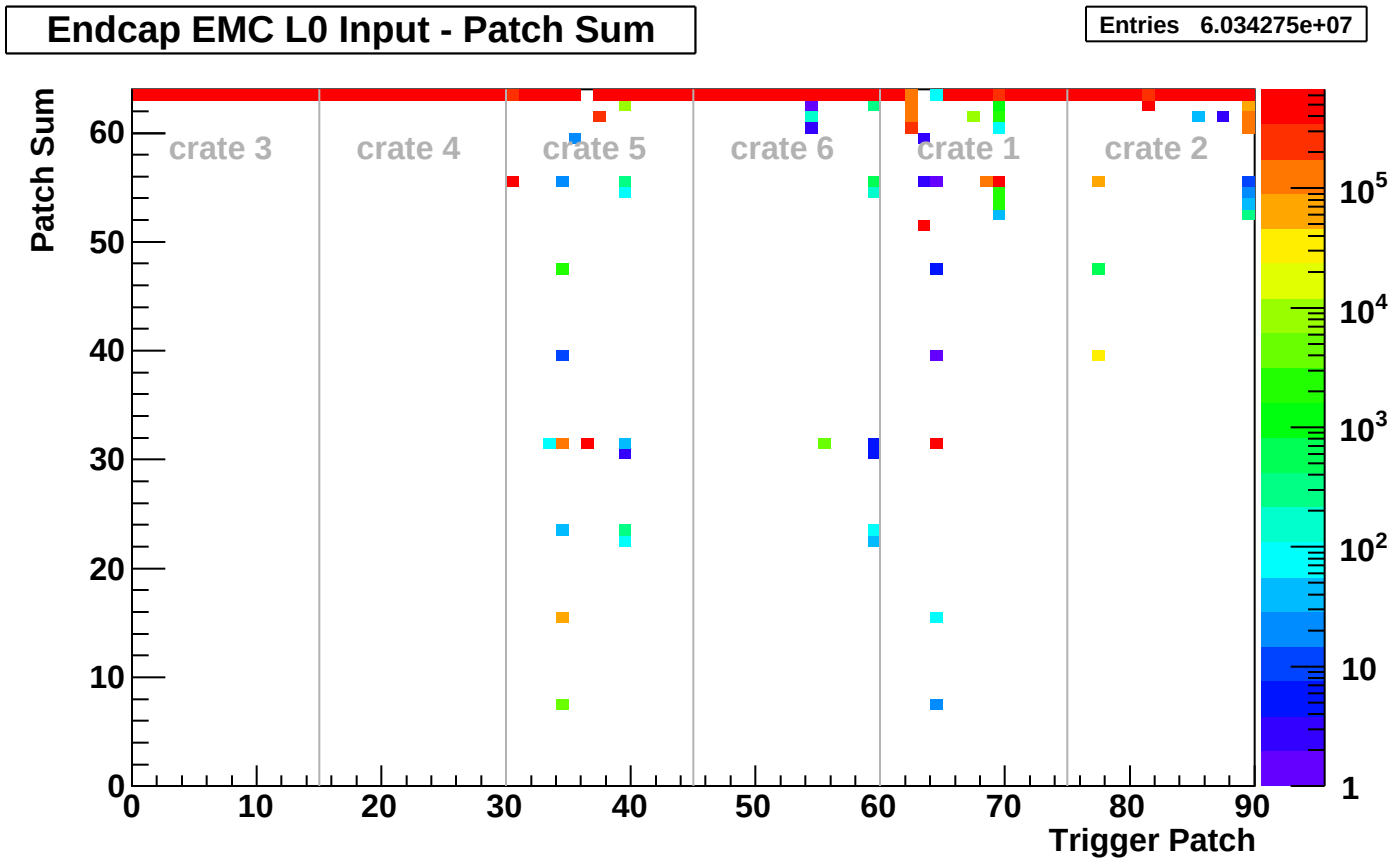

BunchId7Bit (JP1)
Entries 670475
10^4
10^3
10^2
10
0
20
40
60
80
100
120

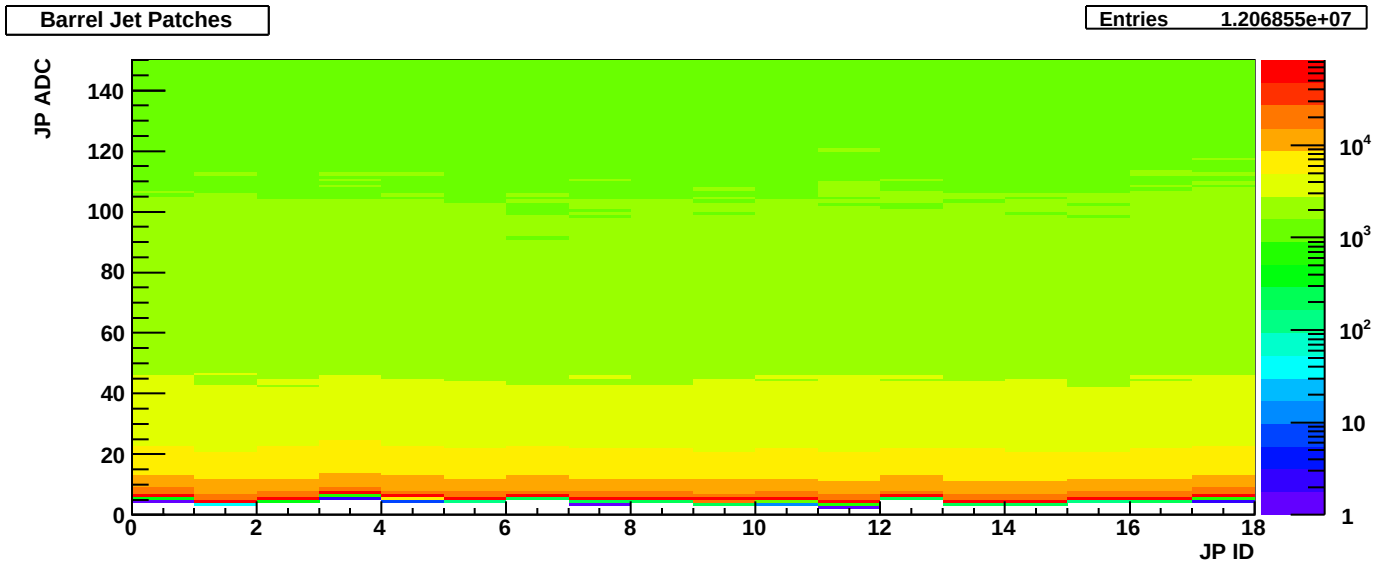

BunchId7Bit (JP2||AJP)
Entries 670475
10^4
10^3
10^2
10
0
20
40
60
80
100
120

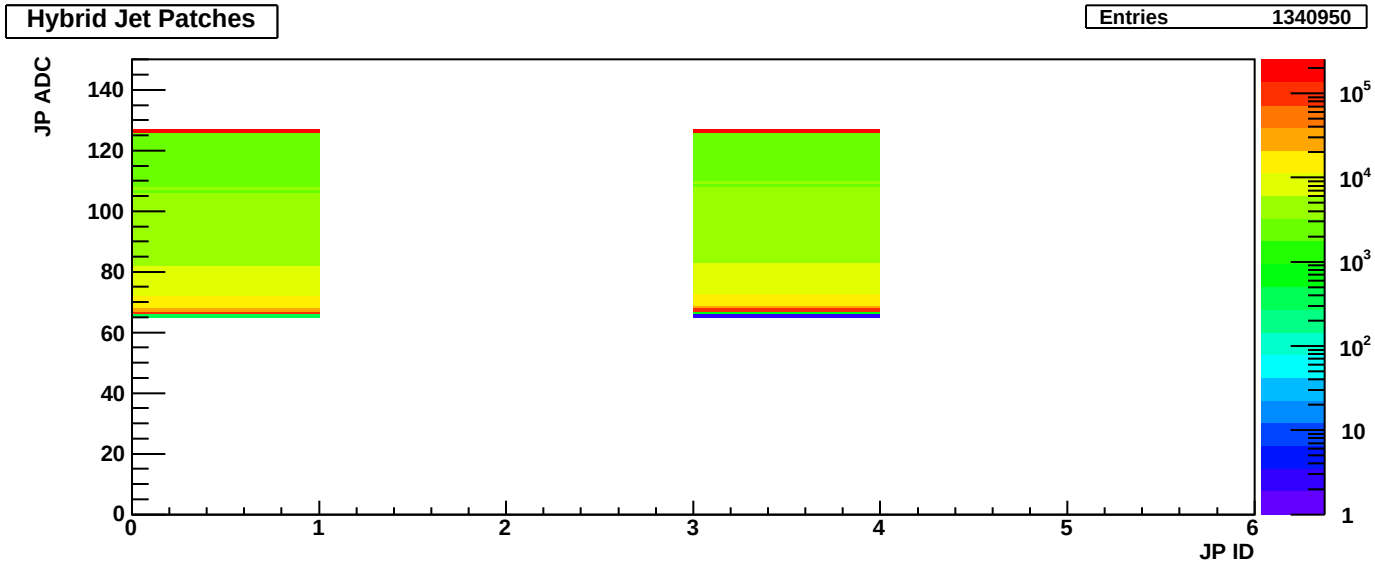

Barrel EMC L0 Input - High Tower
Entries 2.011425e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

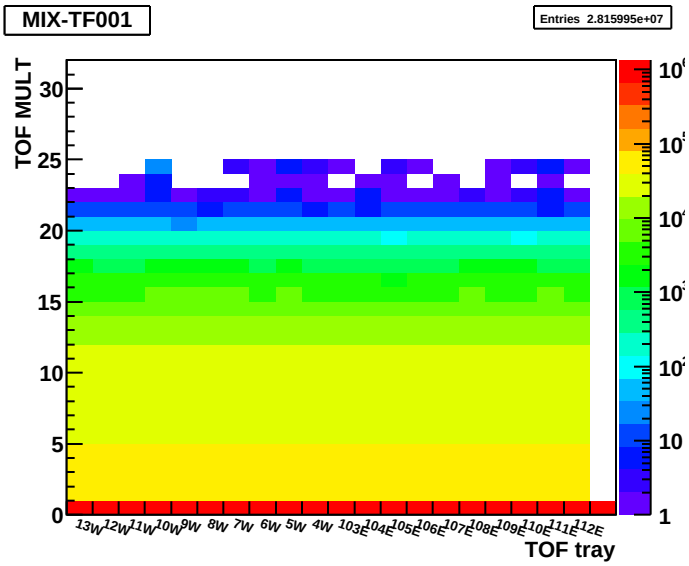

Barrel EMC L0 Input - Patch Sum
Entries 2.011425e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

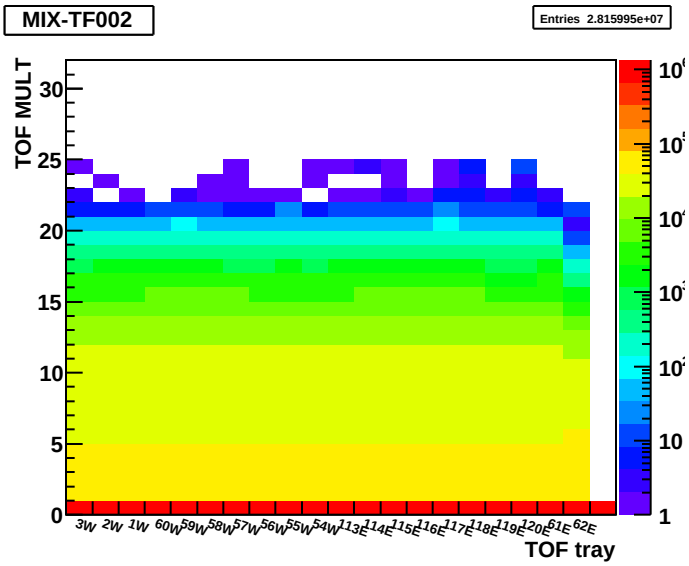

Endcap EMC L0 Input - High Tower
Entries 6.034275e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

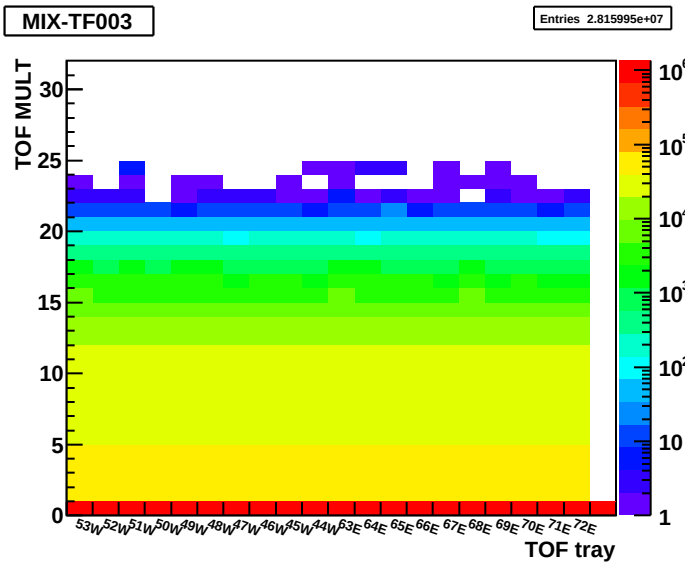

Endcap EMC L0 Input - Patch Sum
Entries 6.034275e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

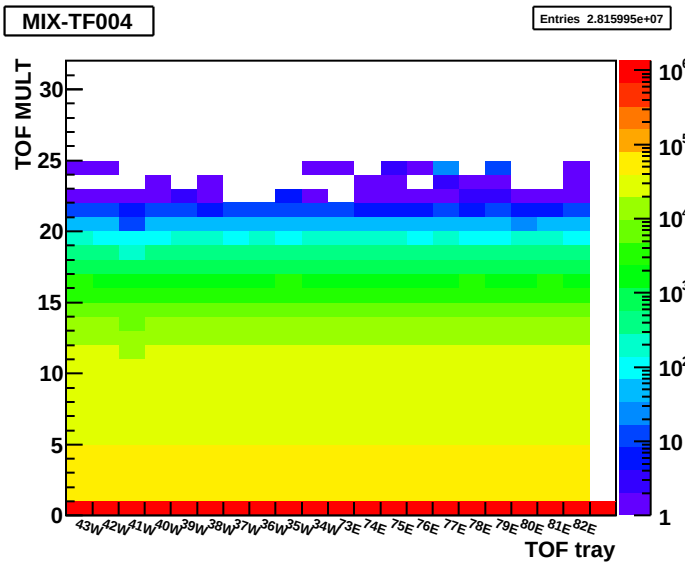

Barrel Jet Patches
Entries 1.206855e+07
JP ADC
JP ID

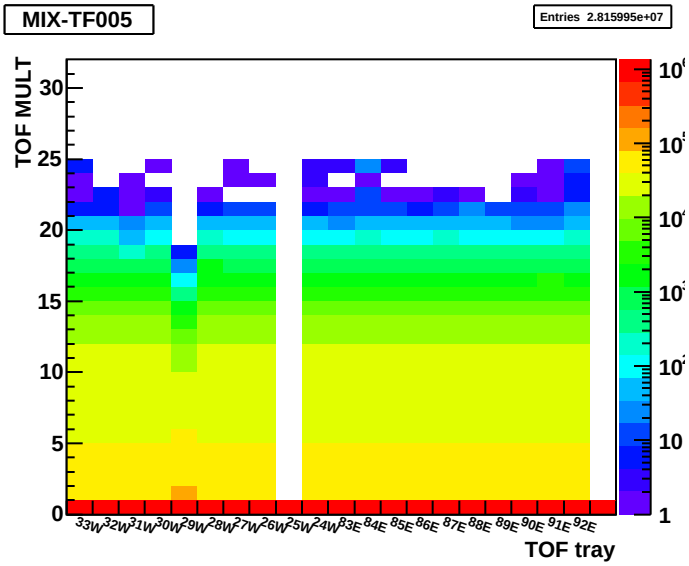

Hybrid Jet Patches
Entries 1340950
JP ADC
JP ID

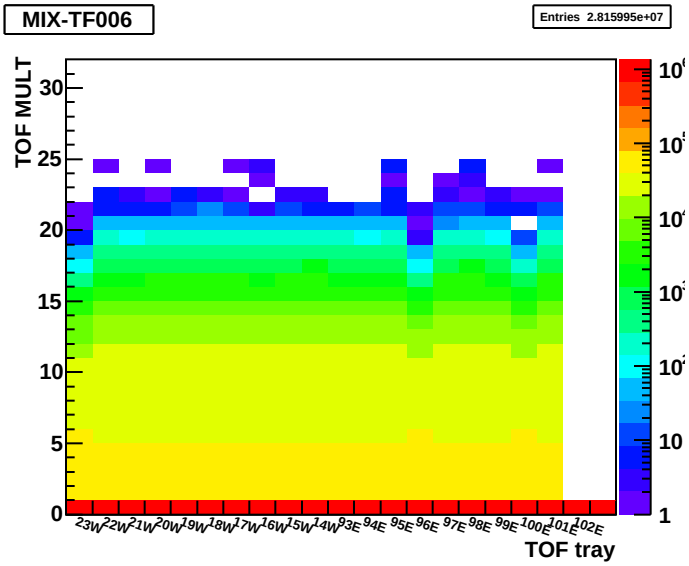

MIX-TF001
Entries 2.815995e+07
TOF MULT
TOF tray

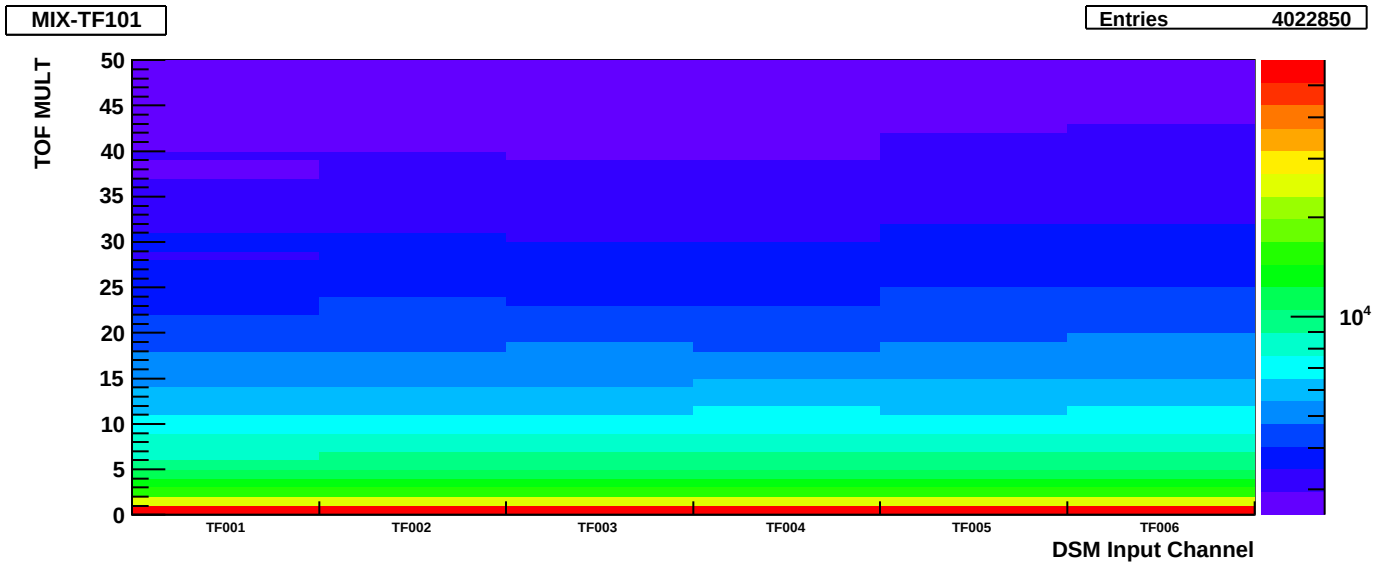

MIX-TF002
Entries 2.815995e+07
TOF MULT
TOF tray

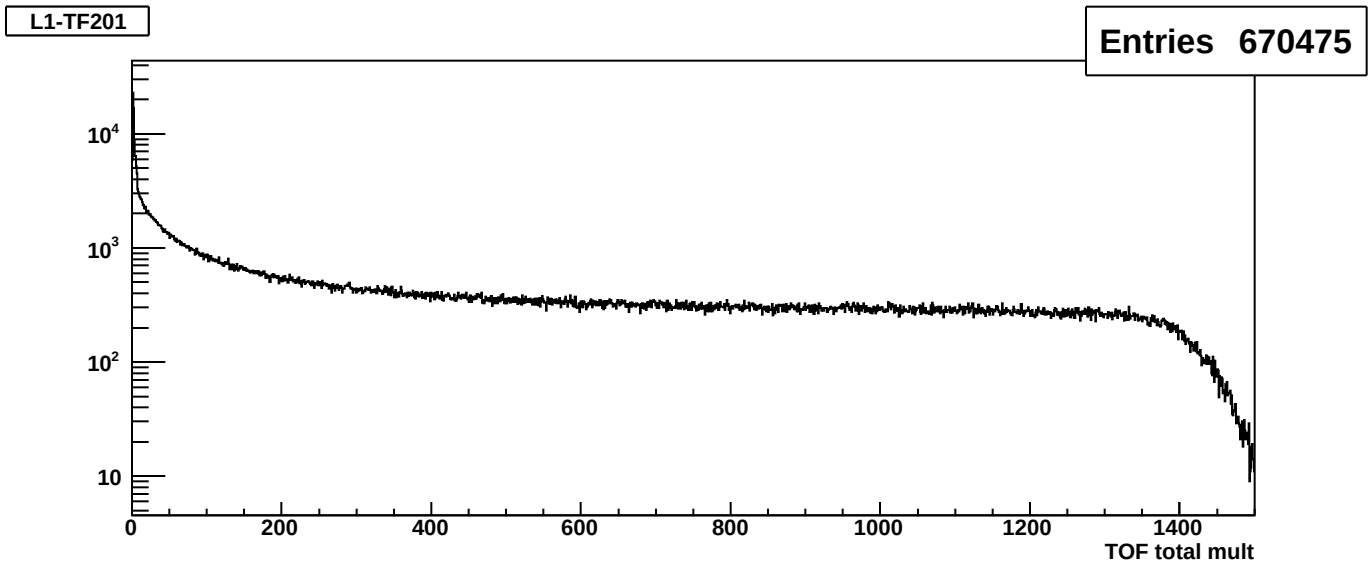

MIX-TF003
Entries 2.815995e+07
TOF MULT
TOF tray

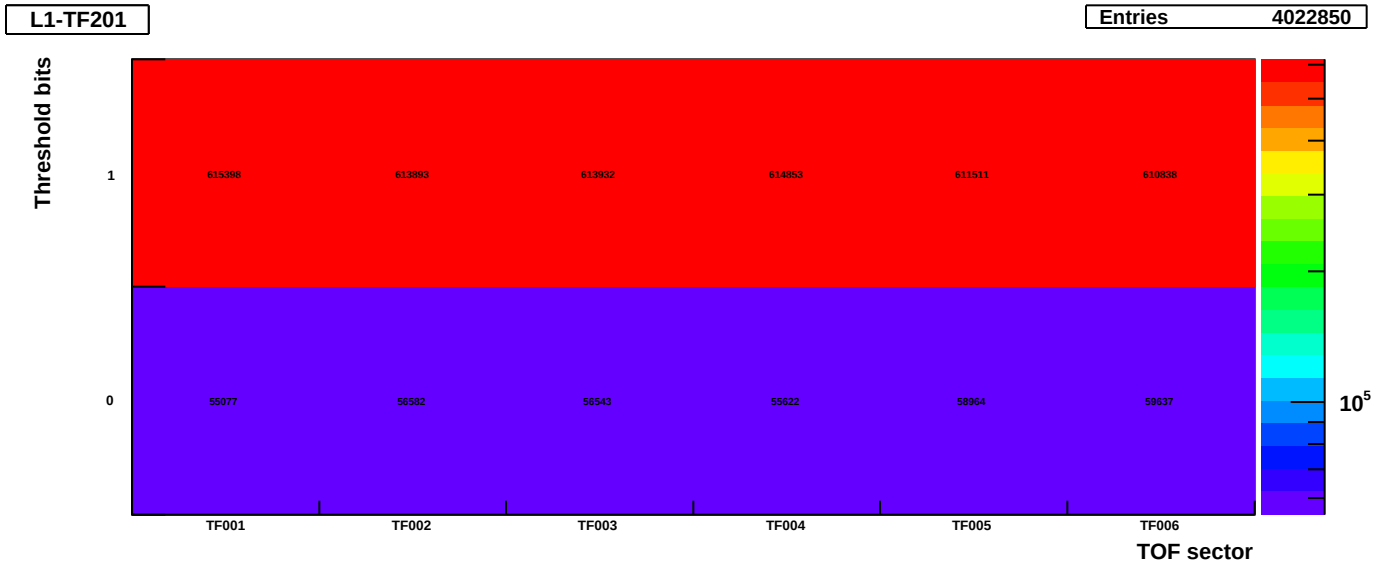

MIX-TF004
Entries 2.815995e+07
TOF MULT
TOF tray

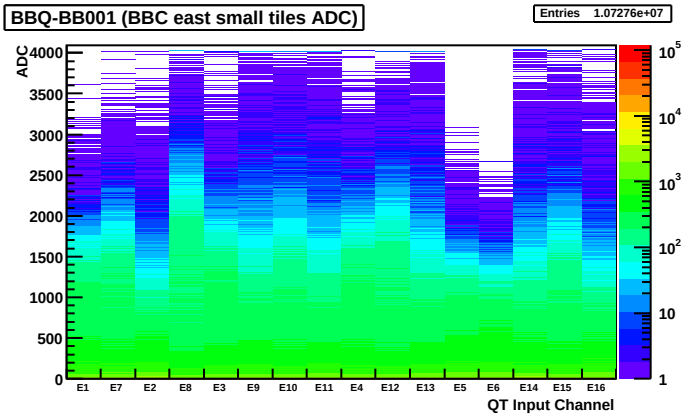

MIX-TF005
Entries 2.815995e+07
TOF MULT
TOF tray

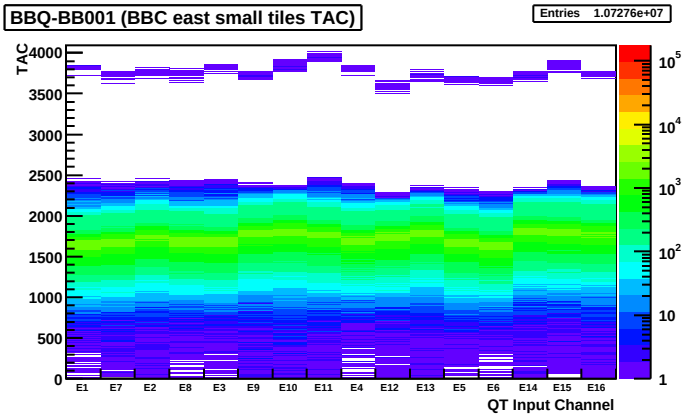

MIX-TF006
Entries 2.815995e+07
TOF MULT
TOF tray

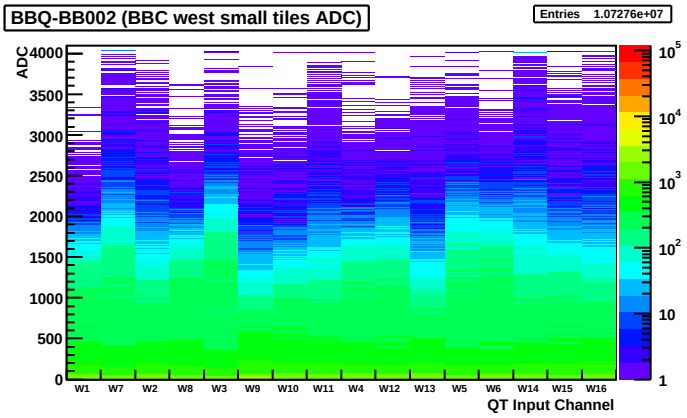

MIX-TF101
Entries 4022850
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

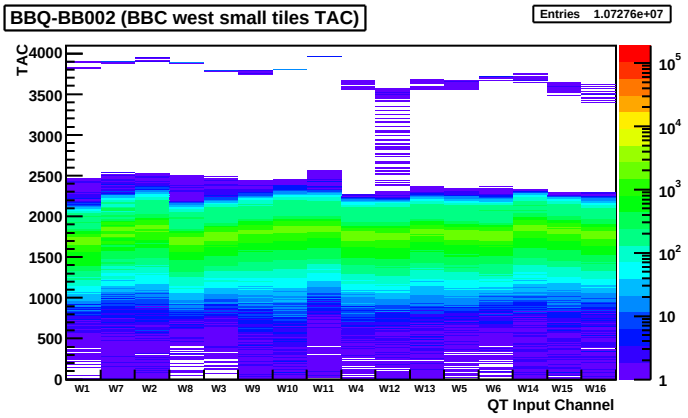

L1-TF201
Entries 670475
10^4
10^3
10^2
10
0
200
400
600
800
1000
1200
1400
TOF total mult

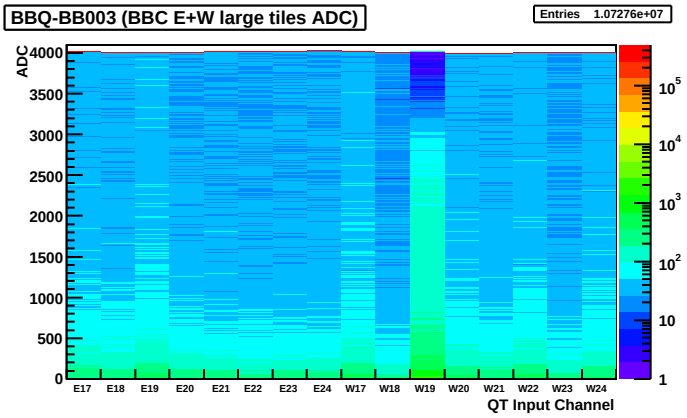

L1-TF201
Entries 4022850
Threshold bits
1
0
615398
613893
613932
614853
611511
610838
55077
56582
56543
55622
58964
59637
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

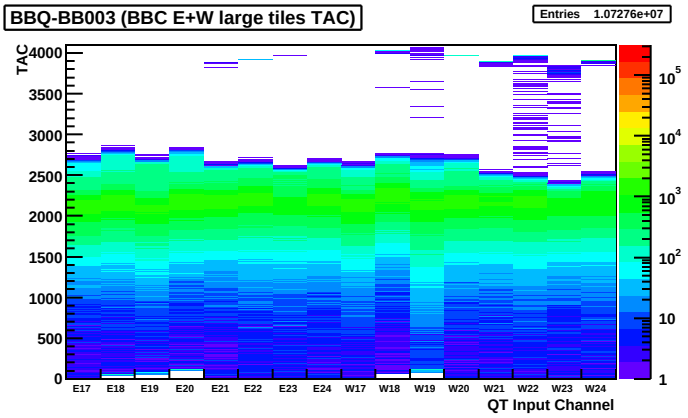

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.07276e+07
ADC
QT Input Channel

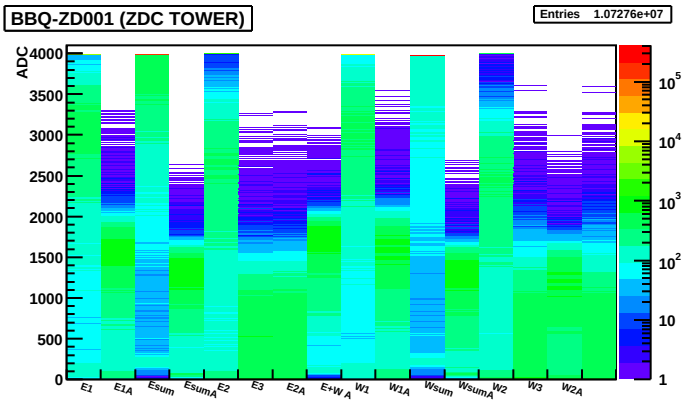

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.07276e+07
TAC
QT Input Channel

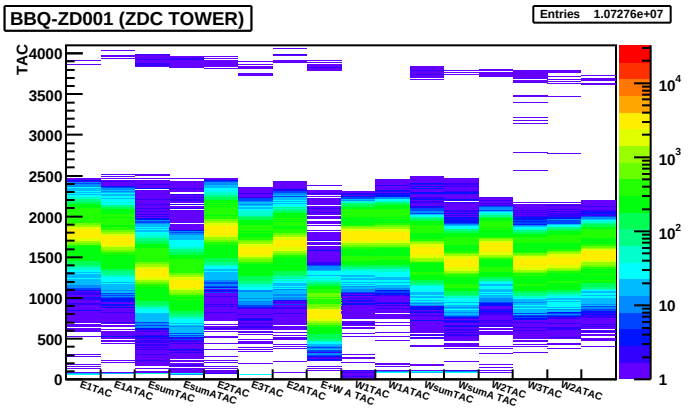

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.07276e+07
ADC
QT Input Channel

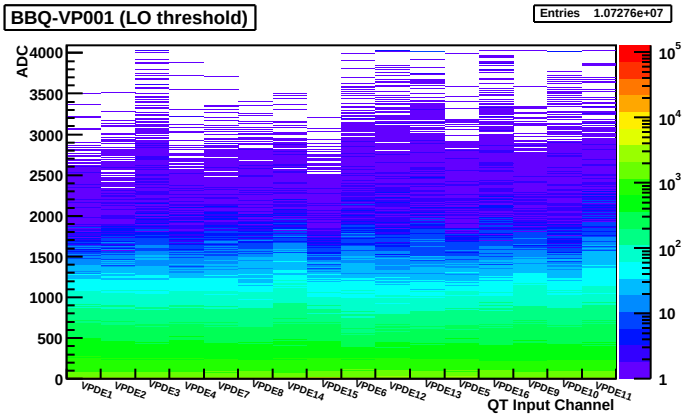

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.07276e+07
TAC
QT Input Channel

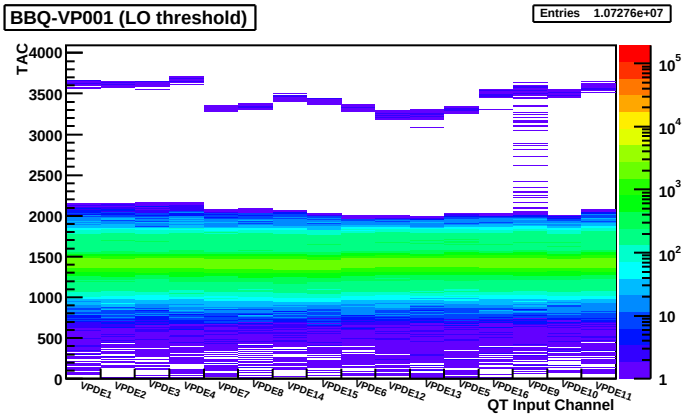

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.07276e+07
ADC
QT Input Channel

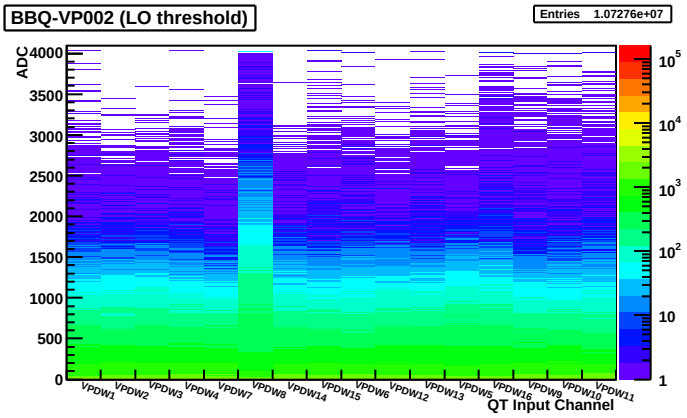

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.07276e+07
TAC
QT Input Channel

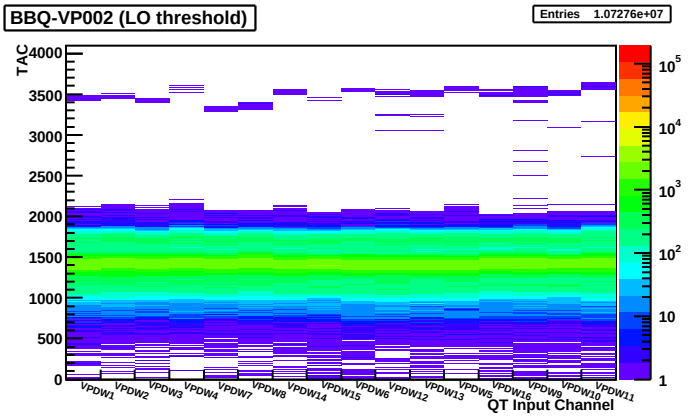

BBQ-ZD001 (ZDC TOWER)
Entries 1.07276e+07
ADC

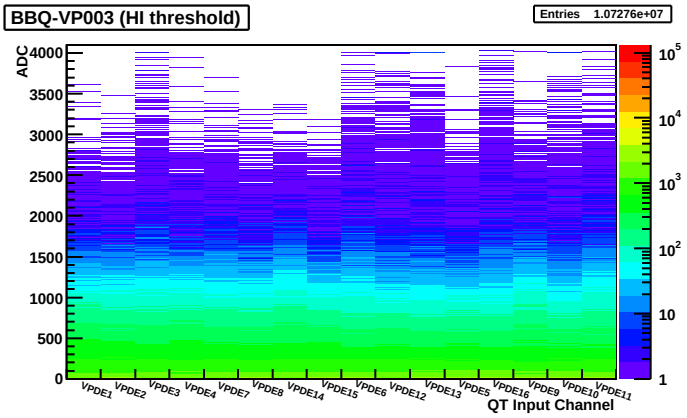

BBQ-ZD001 (ZDC TOWER)
Entries 1.07276e+07
TAC

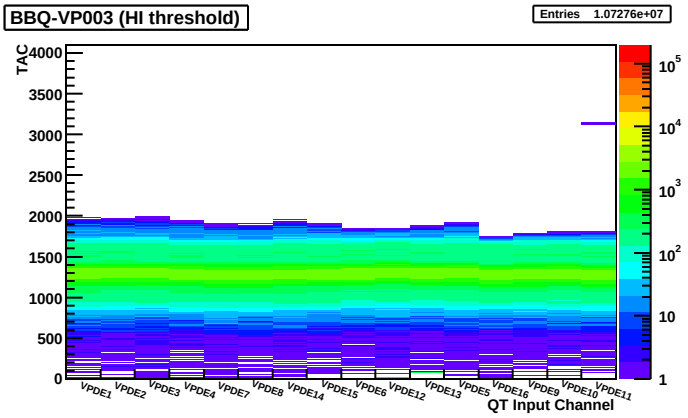

BBQ-VP001 (LO threshold)
Entries 1.07276e+07
ADC
QT Input Channel

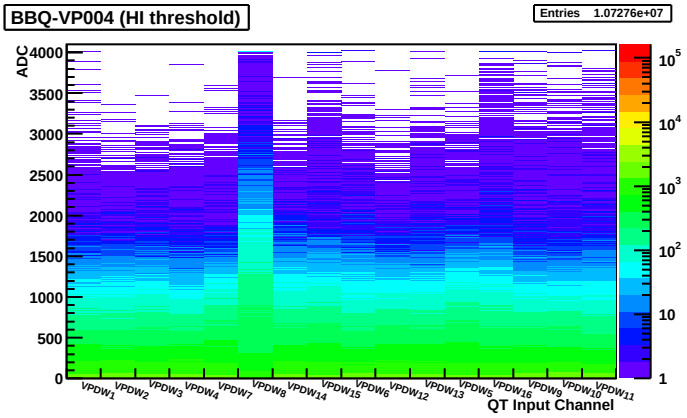

BBQ-VP001 (LO threshold)
Entries 1.07276e+07
TAC
QT Input Channel

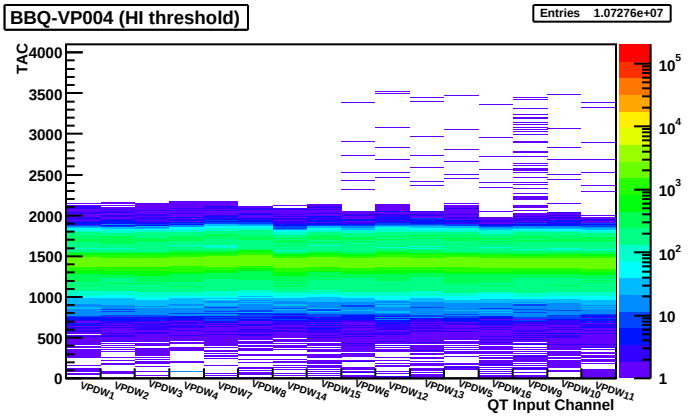

BBQ-VP002 (LO threshold)
Entries 1.07276e+07
ADC
QT Input Channel

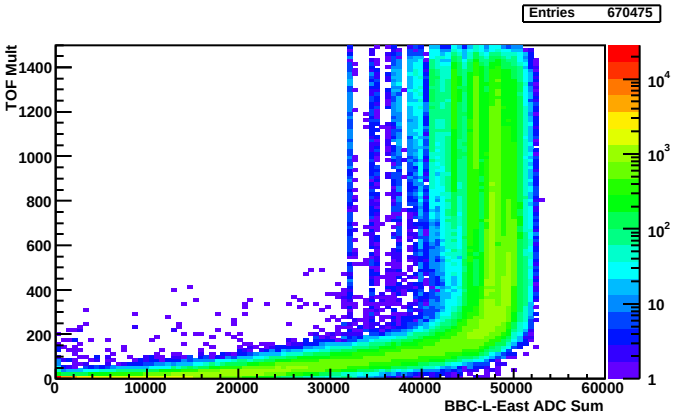

BBQ-VP002 (LO threshold)
Entries 1.07276e+07
TAC
QT Input Channel

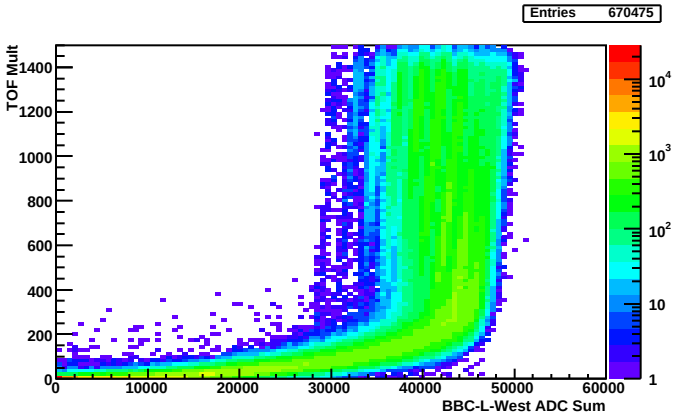

BBQ-VP003 (HI threshold)
Entries 1.07276e+07
ADC
QT Input Channel

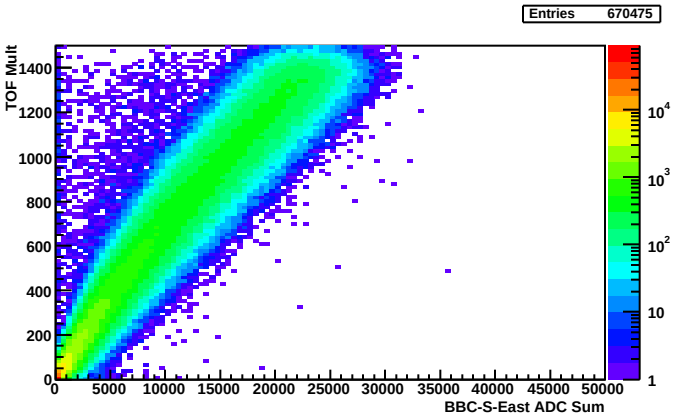

BBQ-VP003 (HI threshold)
Entries 1.07276e+07
TAC
QT Input Channel

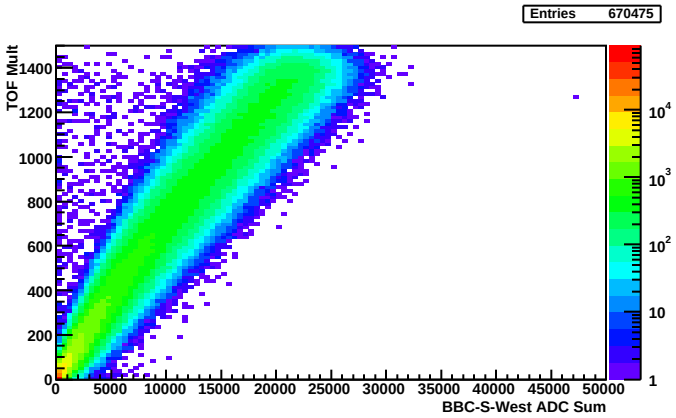

BBQ-VP004 (HI threshold)
Entries 1.07276e+07
ADC
QT Input Channel

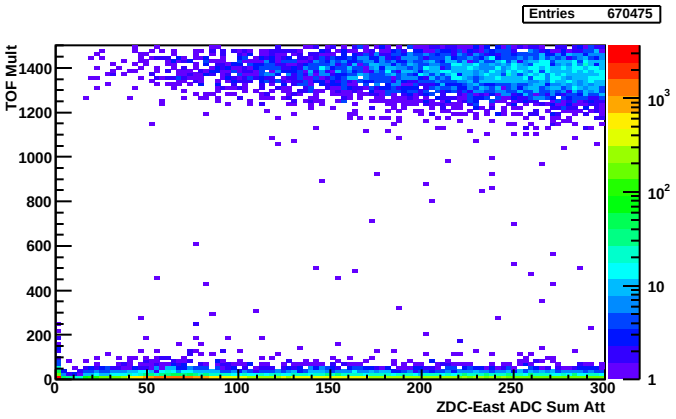

BBQ-VP004 (HI threshold)
Entries 1.07276e+07
TAC
QT Input Channel

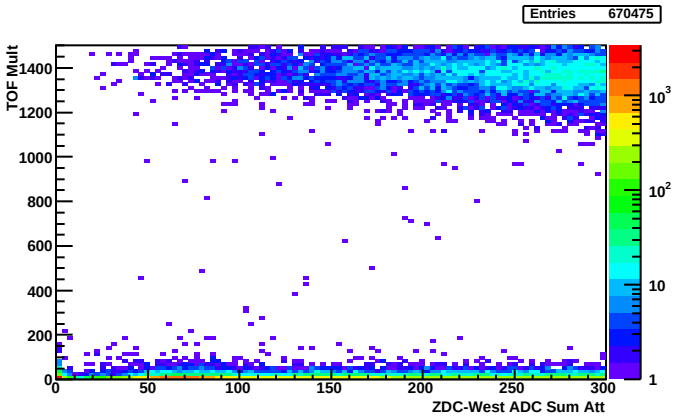

Entries 670475
TOF Mult
BBC-L-East ADC Sum

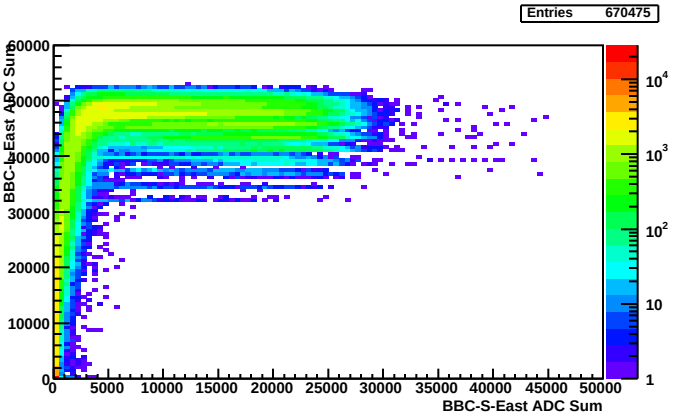

Entries 670475
TOF Mult
BBC-L-West ADC Sum

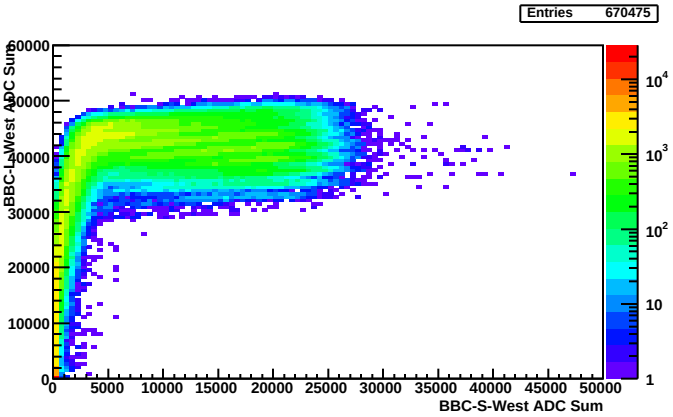

Entries 670475
TOF Mult
BBC-S-East ADC Sum

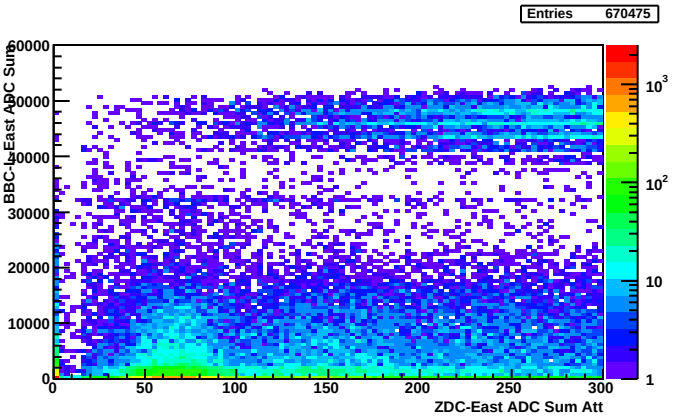

Entries 670475
TOF Mult
BBC-S-West ADC Sum

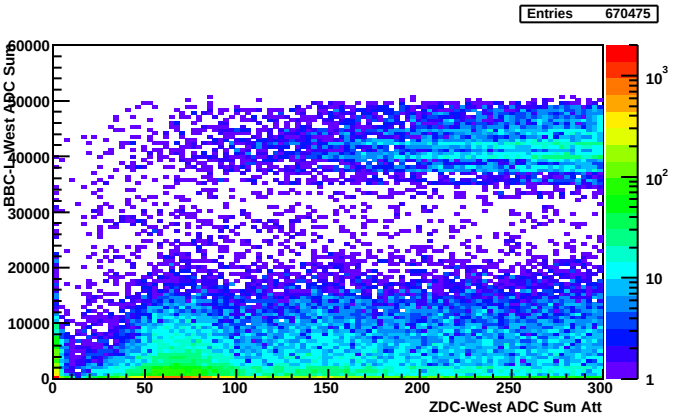

Entries 670475
TOF Mult
ZDC-East ADC Sum Att


Entries 670475
TOF Mult
ZDC-West ADC Sum Att


Entries 670475
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 670475
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 670475
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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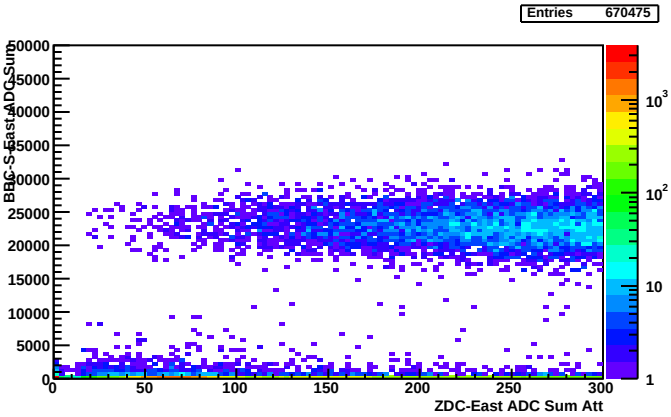
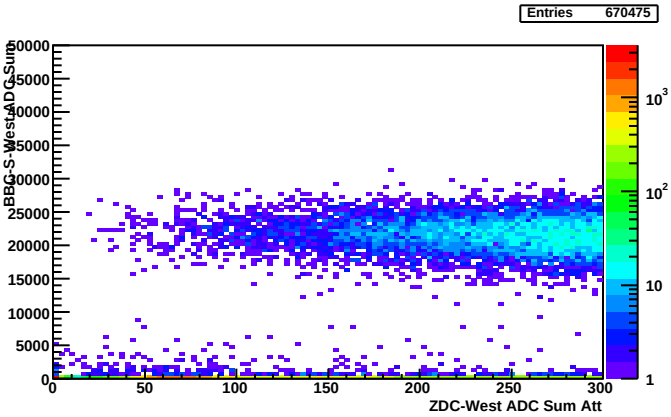
BBC-L-West ADC Sum
ZDC-West ADC Sum Att


Entries 670475
BBC-S-East ADC Sum
ZDC-East ADC Sum Att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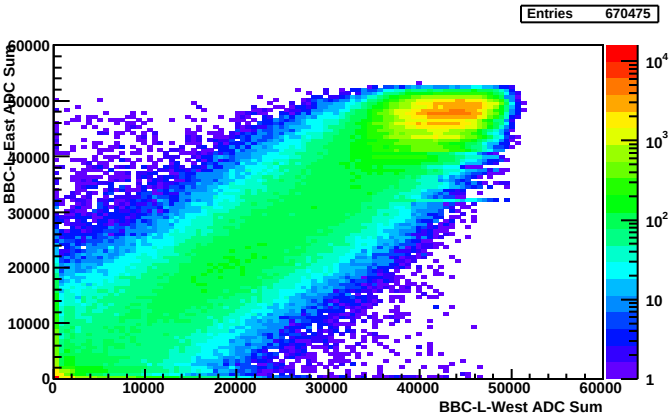
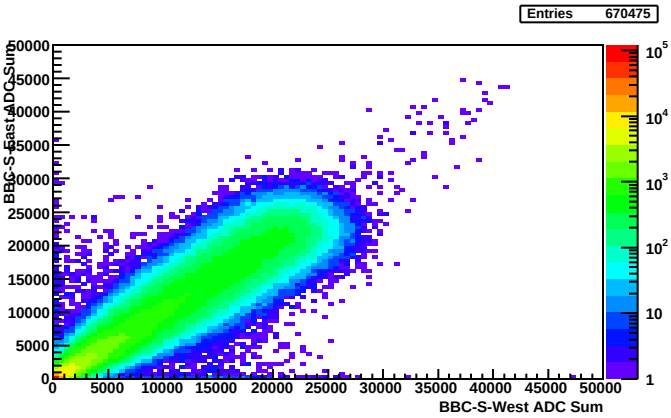
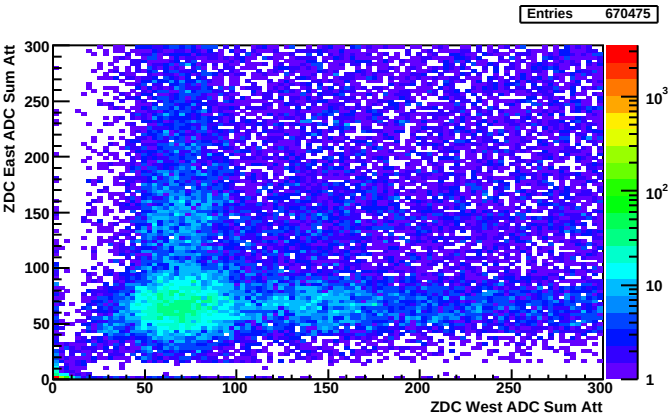
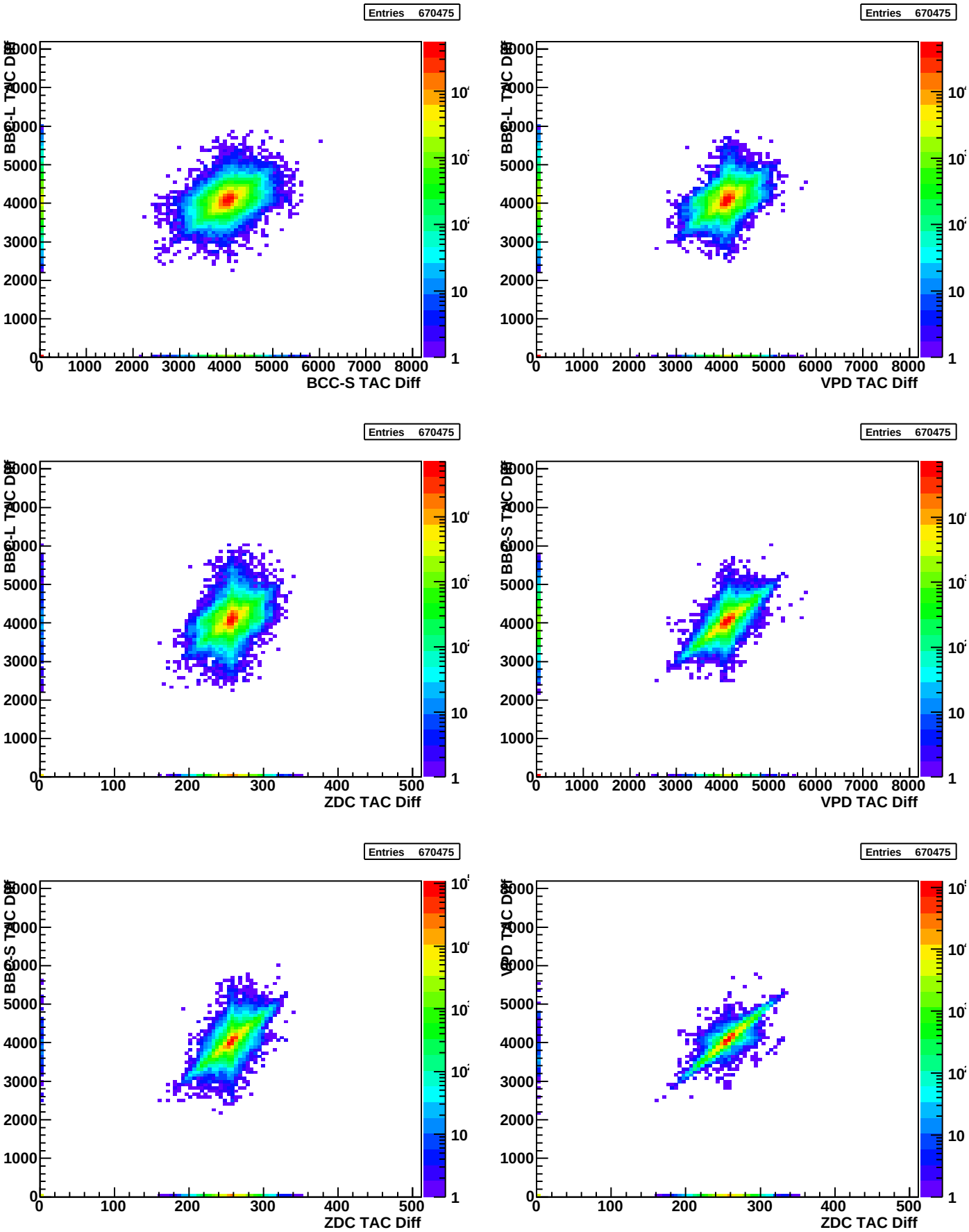
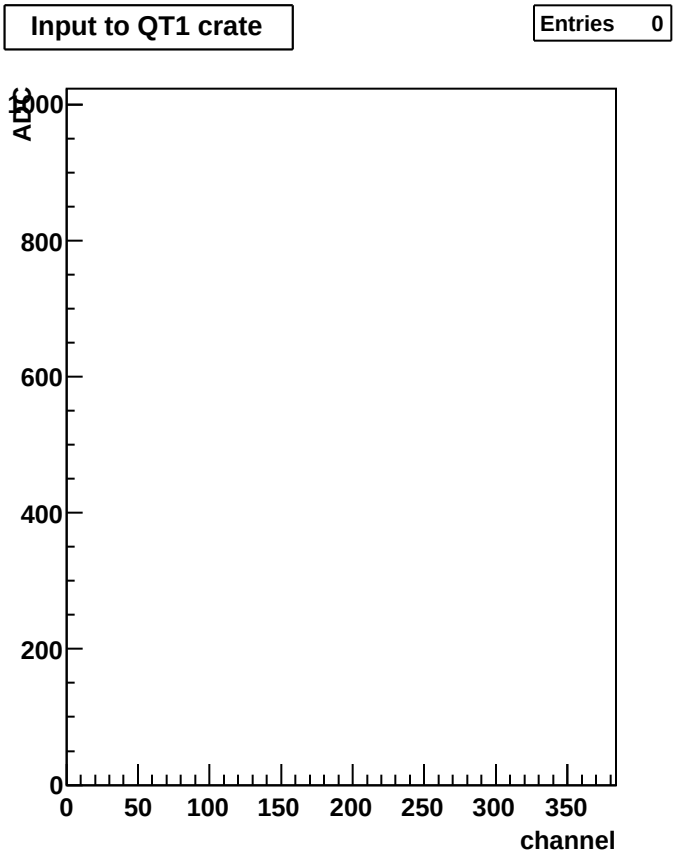
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

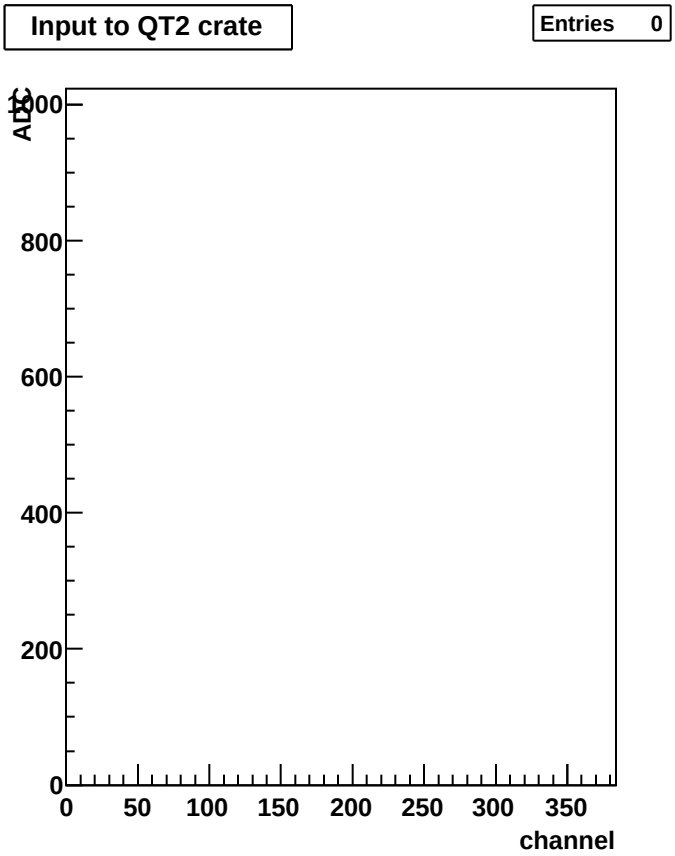

Entries 670475
BBC-L-East ADC Sum
BBC-L-West ADC Sum

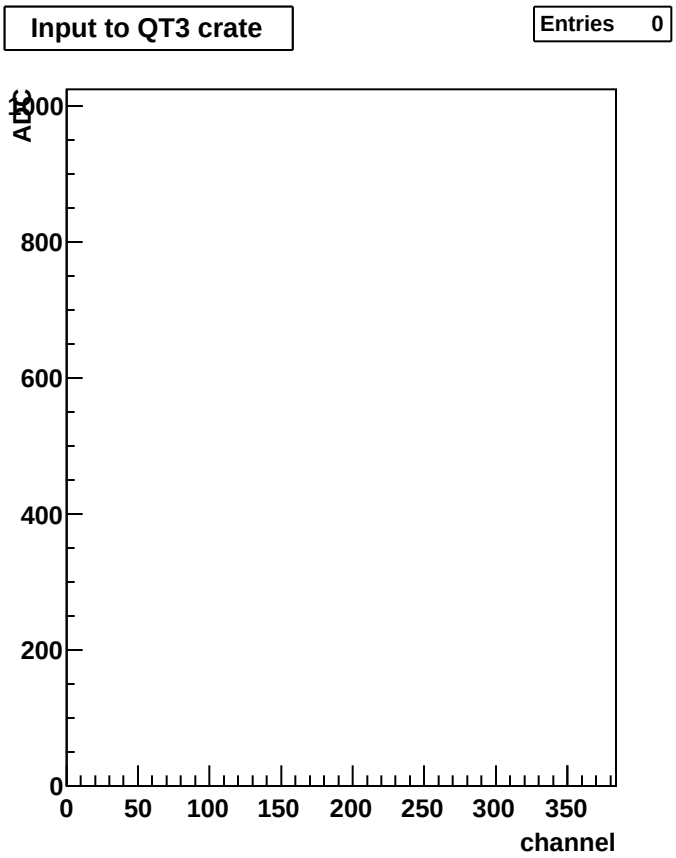

Entries 670475
BBC-S-East ADC Sum
BBC-S-West ADC Sum

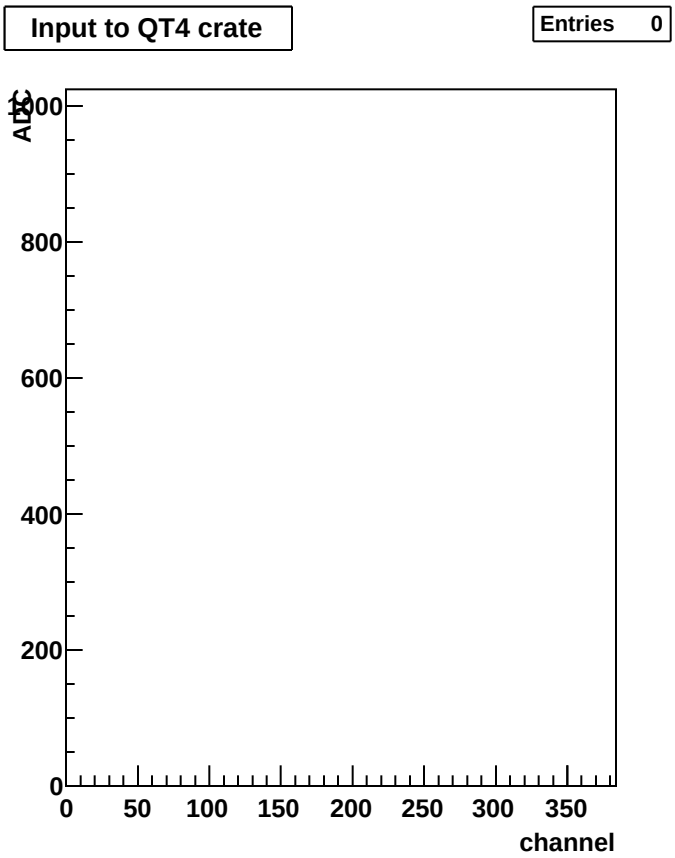

Entries 670475
ZDC East ADC Sum Att
ZDC West ADC Sum Att

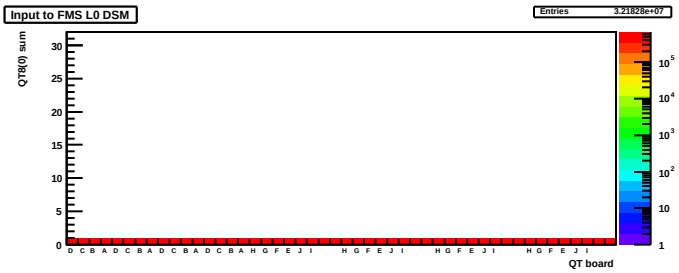

Entries 670475
BBC-L TAC Diff
BCC-S TAC Diff
Entries 670475
BBC-L TAC Diff
VPD TAC Diff
Entries 670475
BBC-L TAC Diff
ZDC TAC Diff
Entries 670475
BBC-S TAC Diff
VPD TAC Diff
Entries 670475
BBC-S TAC Diff
ZDC TAC Diff
Entries 670475
VPD TAC Diff
ZDC TAC Diff

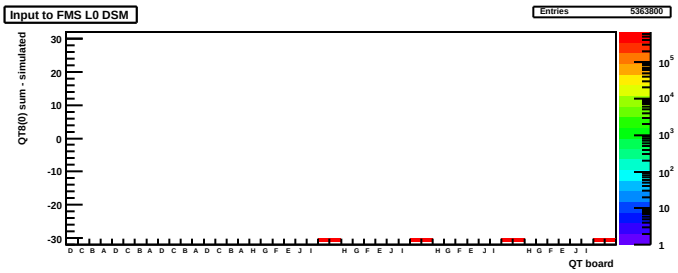

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

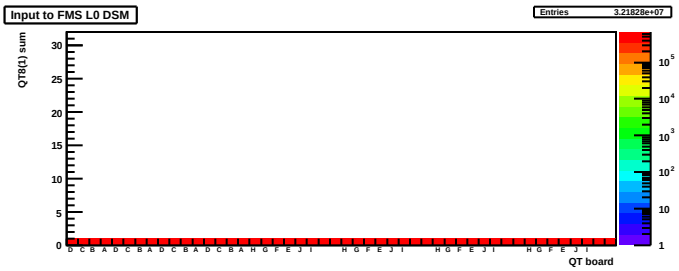

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

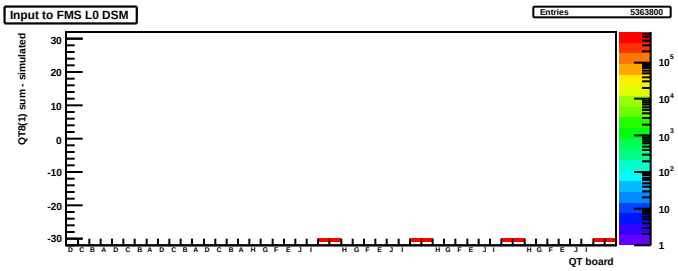

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

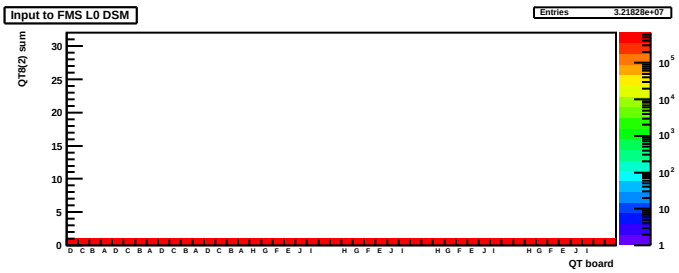

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

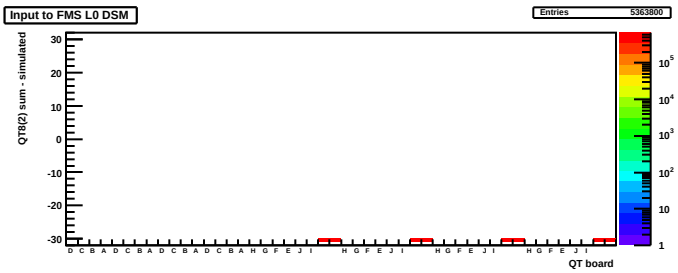

Input to FMS L0 DSM
Entries 3.21828e+07
QT8(0) sum
QT board

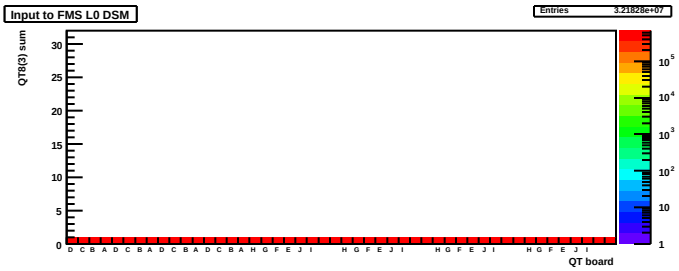

Input to FMS L0 DSM
Entries 5363800
QT8(0) sum - simulated
QT board

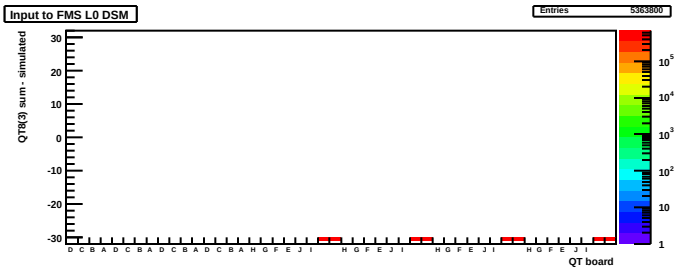

Input to FMS L0 DSM
Entries 3.21828e+07
QT8(1) sum
QT board

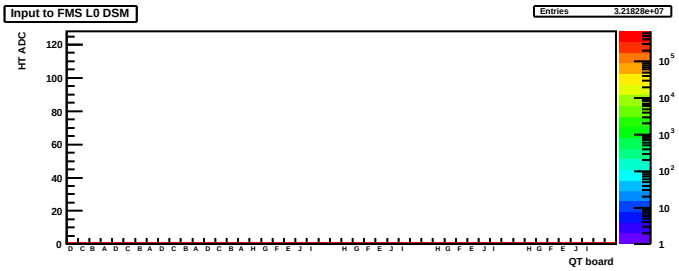

Input to FMS L0 DSM
Entries 5363800
QT8(1) sum - simulated
QT board

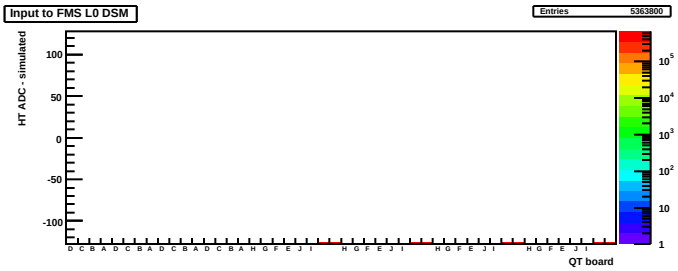

Input to FMS L0 DSM
Entries 3.21828e+07
QT8(2) sum
QT board

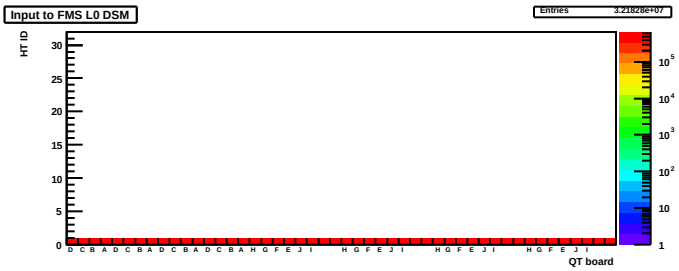

Input to FMS L0 DSM
Entries 5363800
QT8(2) sum - simulated
QT board

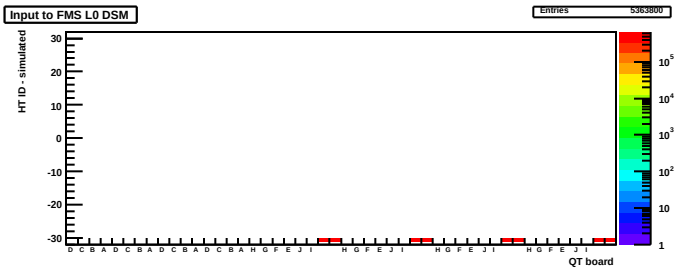

Input to FMS L0 DSM
Entries 3.21828e+07
QT8(3) sum
QT board

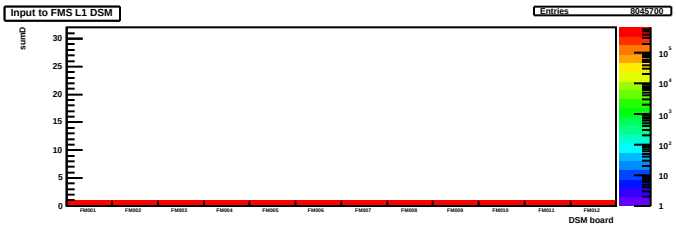

Input to FMS L0 DSM
Entries 5363800
QT8(3) sum - simulated
QT board

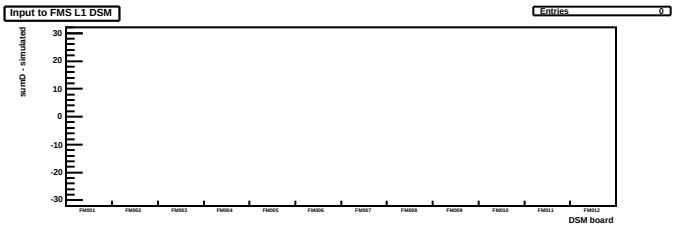

Input to FMS L0 DSM
Entries 3.21828e+07
HT ADC
QT board

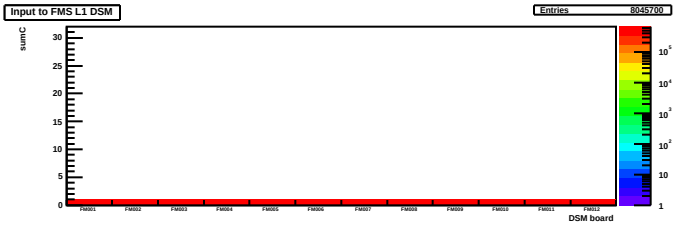

Input to FMS L0 DSM
Entries 5363800
HT ADC - simulated
QT board

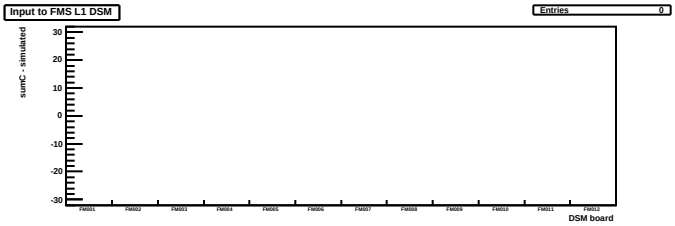

Input to FMS L0 DSM
Entries 3.21828e+07
HT ID
QT board

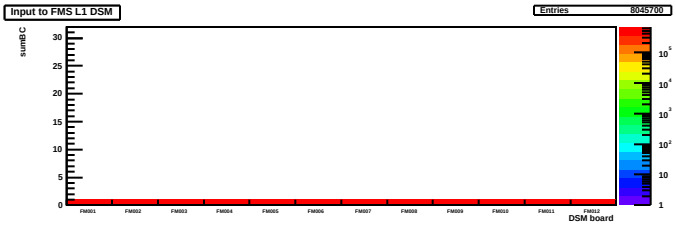

Input to FMS L0 DSM
Entries 5363800
HT ID - simulated
QT board

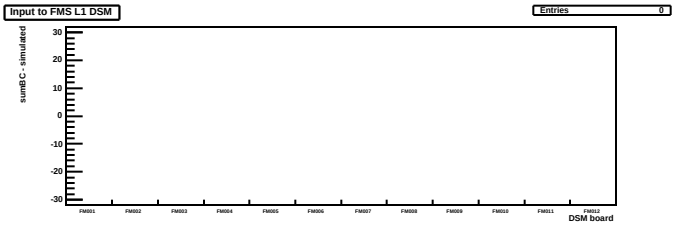

Input to FMS L1 DSM
Entries 8045700
sumD
DSM board

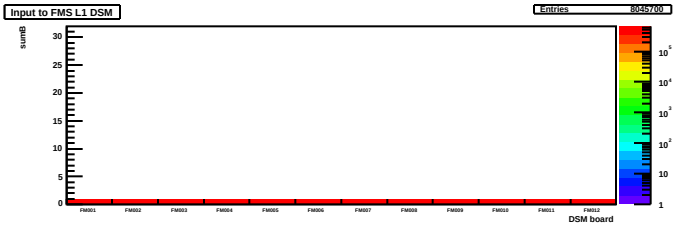

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

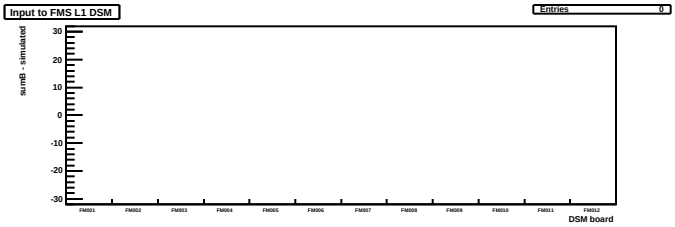

Input to FMS L1 DSM
Entries 8045700
sumC
DSM board

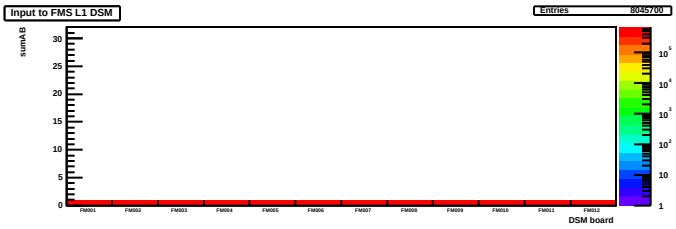

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

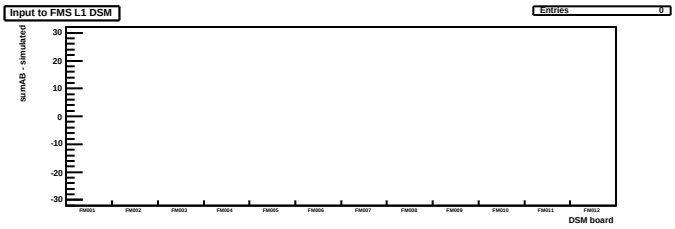

Input to FMS L1 DSM
Entries 8045700
sumBC
DSM board

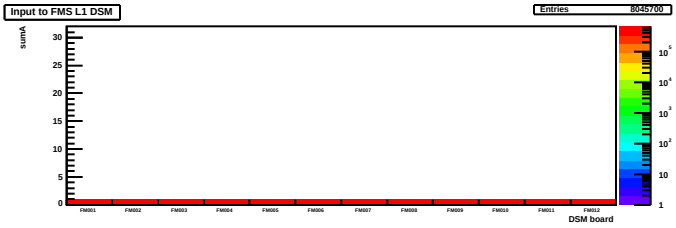

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

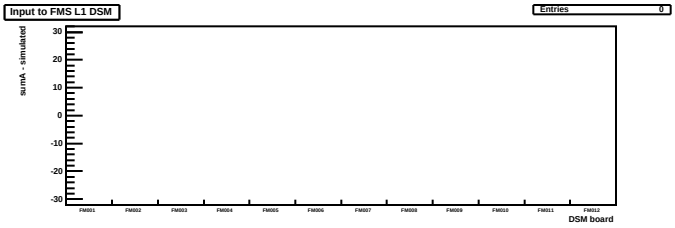

Input to FMS L1 DSM
Entries 8045700
sumB
DSM board

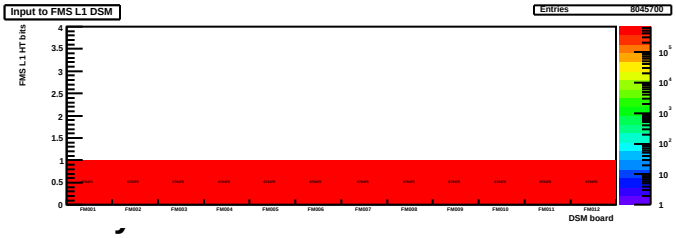

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

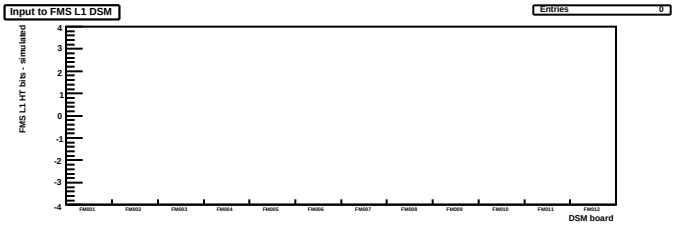

Input to FMS L1 DSM
Entries 8045700
sumAB
DSM board

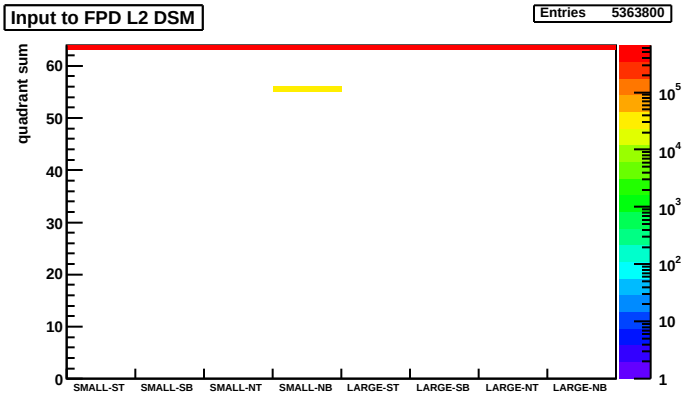

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

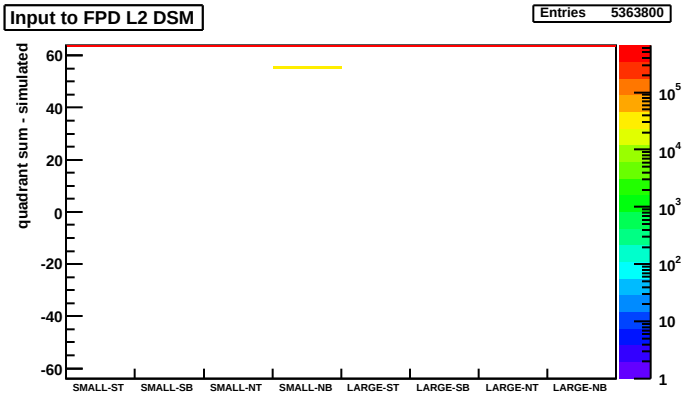

Input to FMS L1 DSM
Entries 8045700
sumA
DSM board

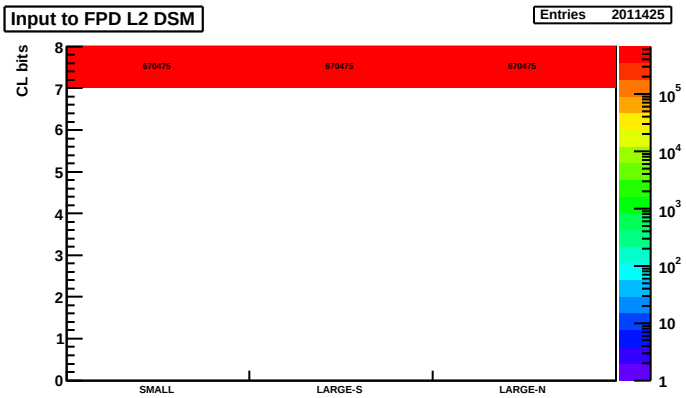

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

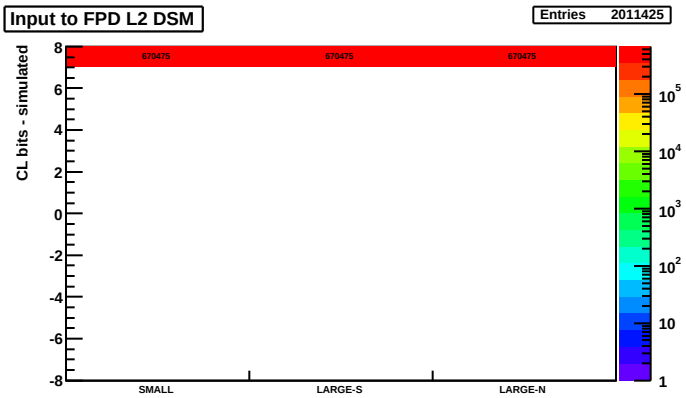

Input to FMS L1 DSM
Entries 8045700
FMS L1 HT bits
DSM board

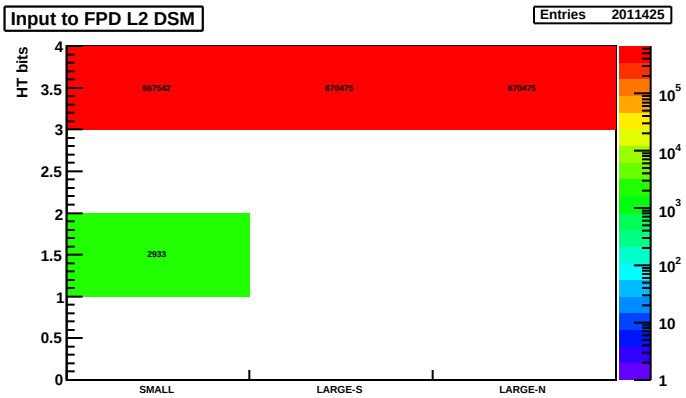

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

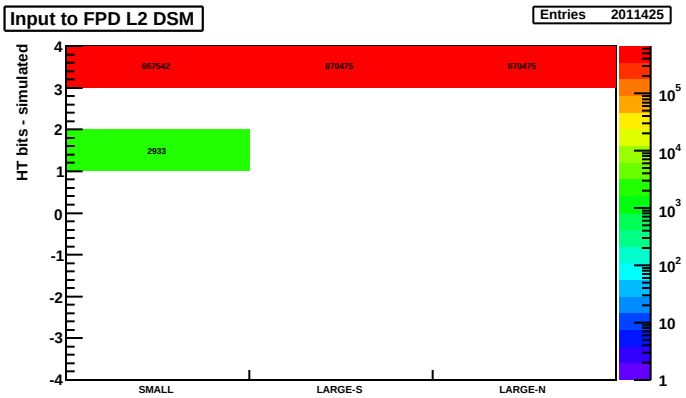

Input to FPD L2 DSM
Entries 5363800
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

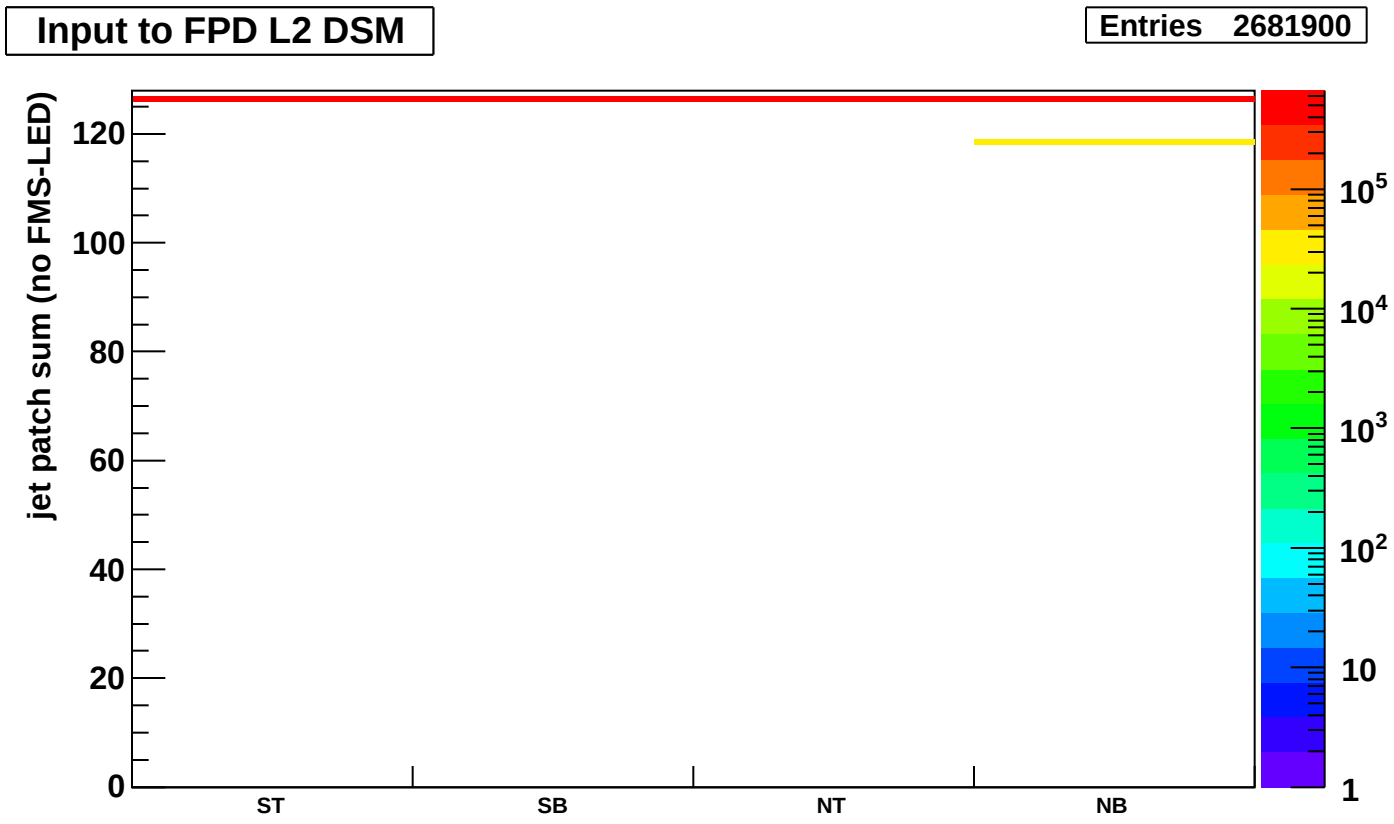

Input to FPD L2 DSM
Entries 5363800
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

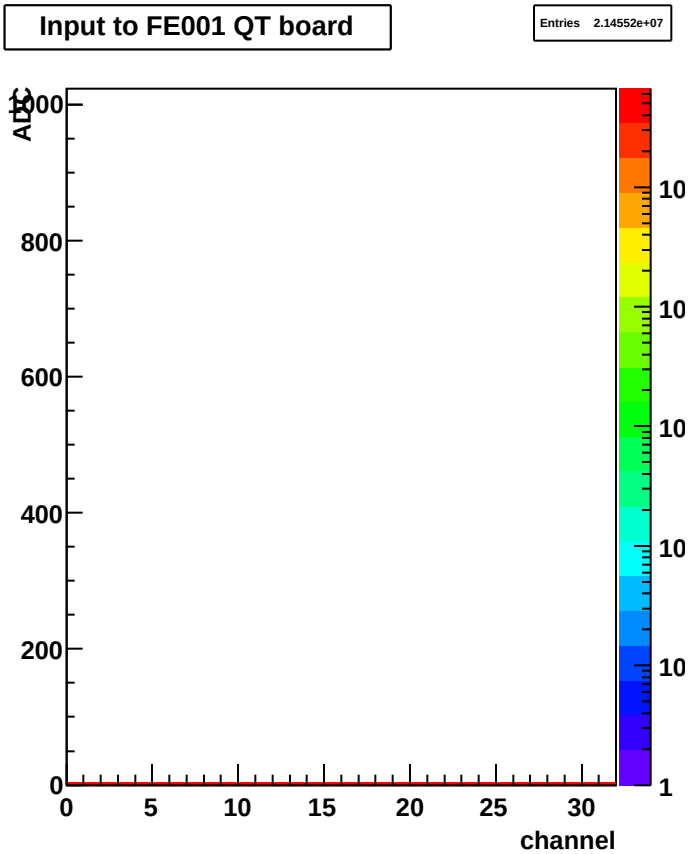

Input to FPD L2 DSM
Entries 2011425
CL bits
670475
670475
670475
SMALL
LARGE-S
LARGE-N

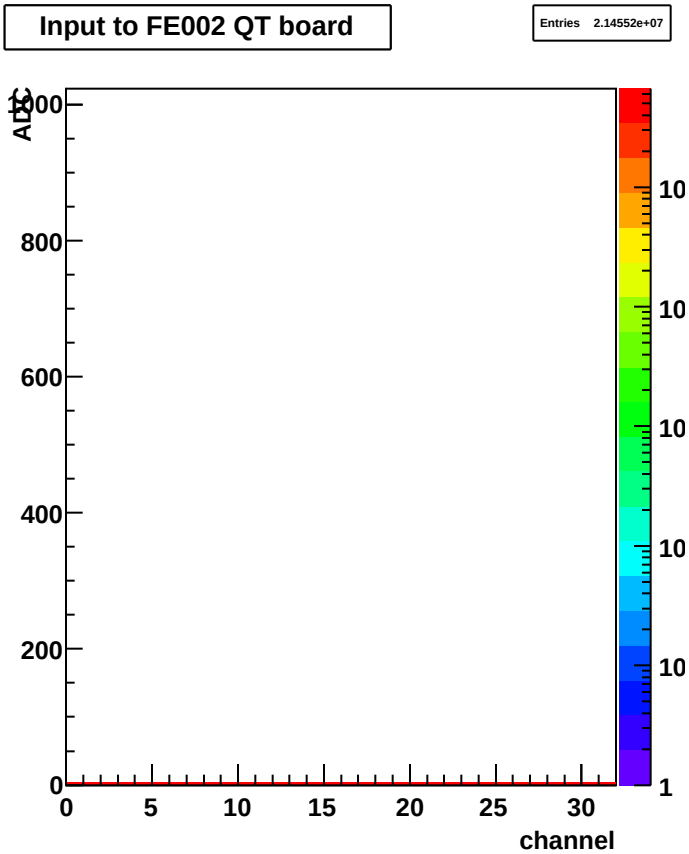

Input to FPD L2 DSM
Entries 2011425
CL bits - simulated
670475
670475
670475
SMALL
LARGE-S
LARGE-N

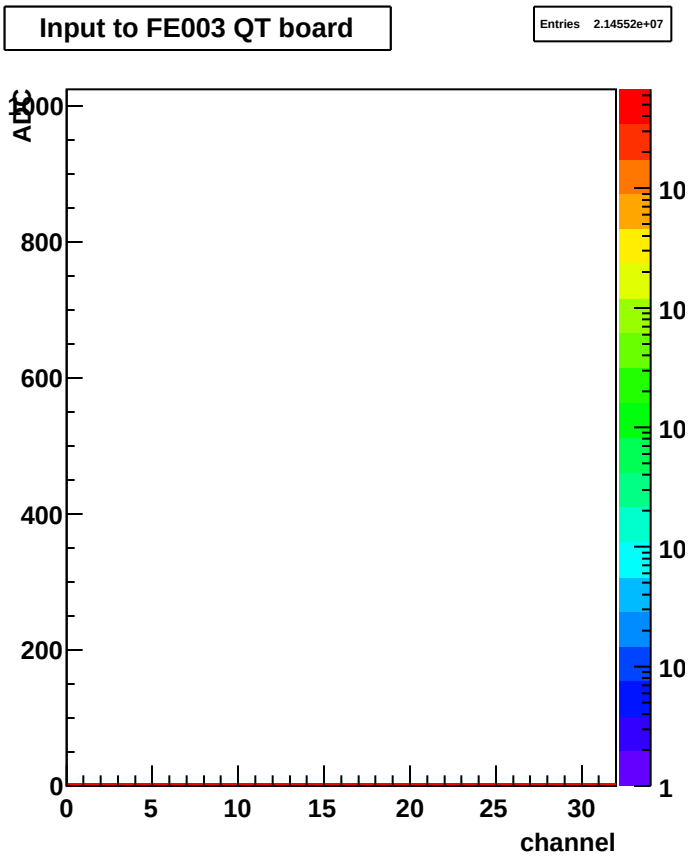

Input to FPD L2 DSM
Entries 2011425
HT bits
667542
670475
670475
2933
SMALL
LARGE-S
LARGE-N

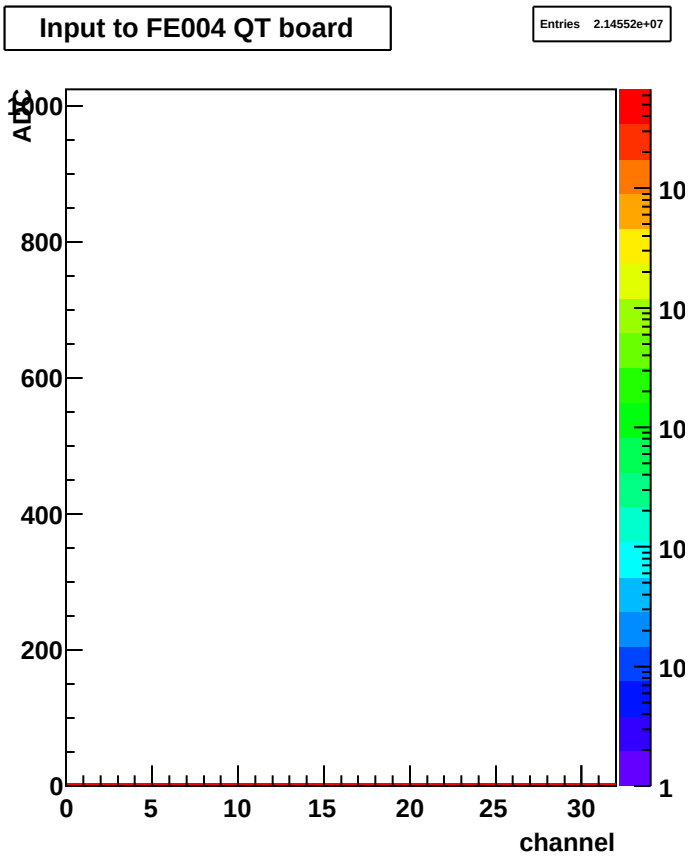

Input to FPD L2 DSM
Entries 2011425
HT bits - simulated
667542
670475
670475
2933
SMALL
LARGE-S
LARGE-N

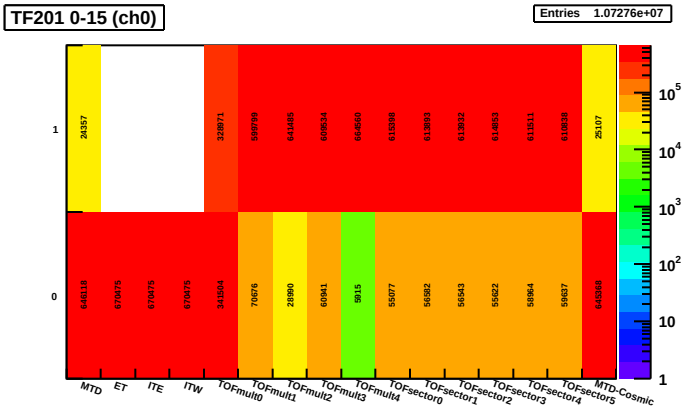

Input to FPD L2 DSM
Entries 2681900
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

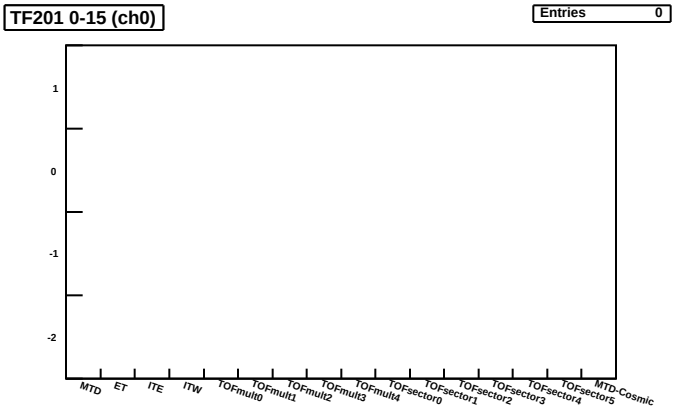

Input to FE001 QT board
Entries 2.14552e+07
ADC
channel

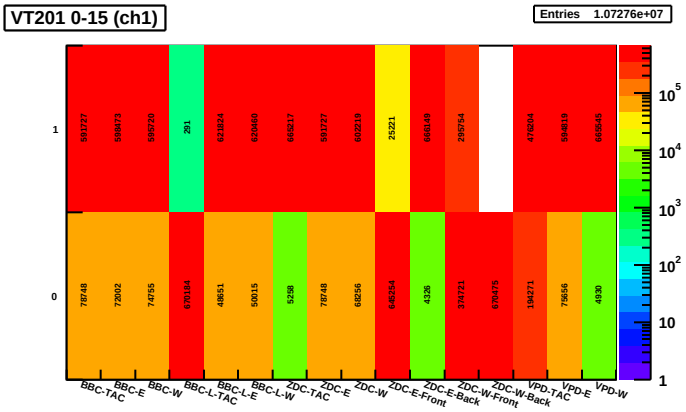

Input to FE002 QT board
Entries 2.14552e+07
ADC
channel

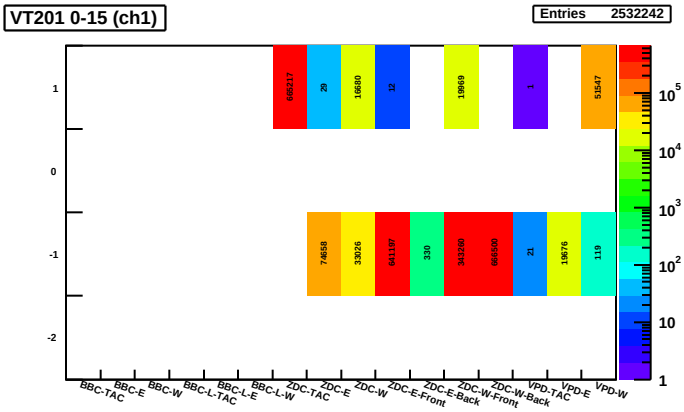

Input to FE003 QT board
Entries 2.14552e+07
ADC
channel

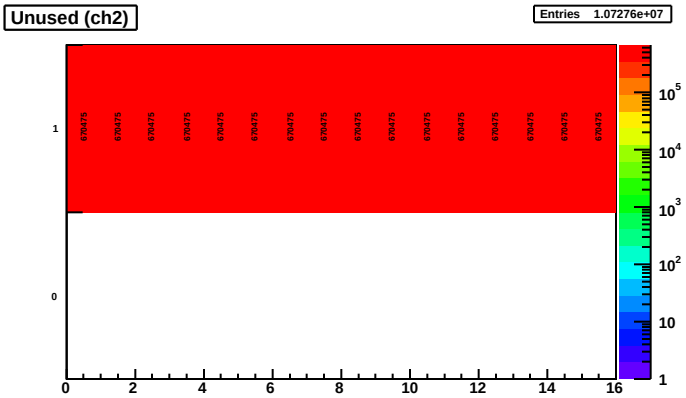

Input to FE004 QT board
Entries 2.14552e+07
ADC
channel

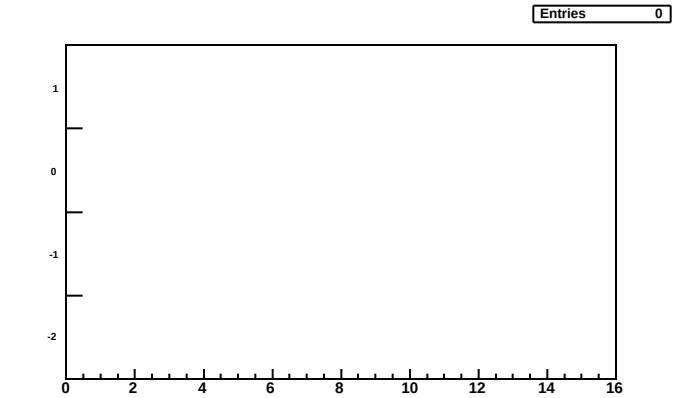

TF201 0-15 (ch0)
Entries 1.07276e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

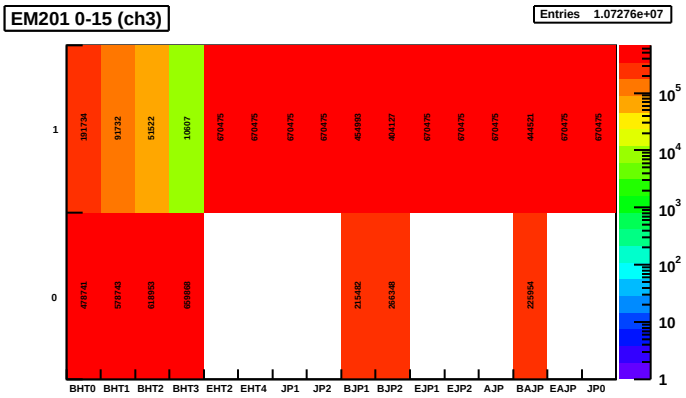

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

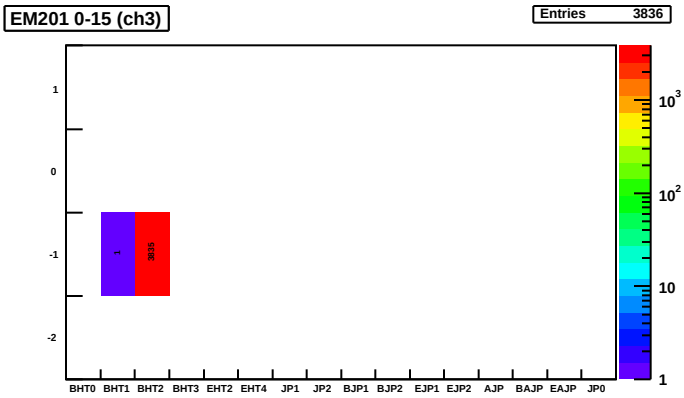

VT201 0-15 (ch1)
Entries 1.07276e+07
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

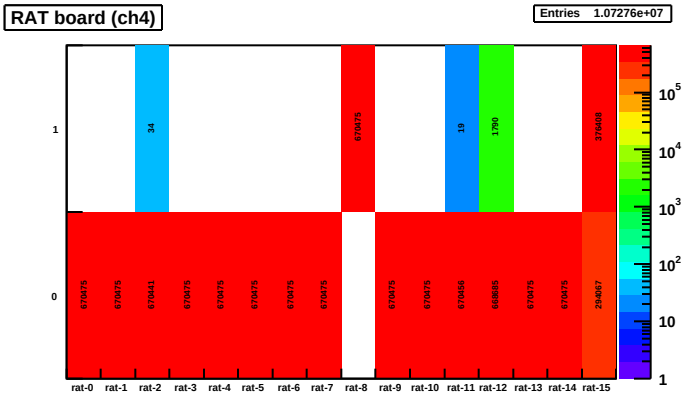

VT201 0-15 (ch1)
Entries 2532242
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

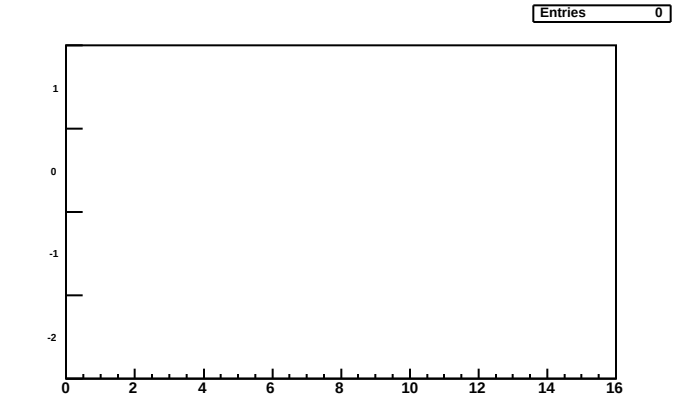

Unused (ch2)
Entries 1.07276e+07

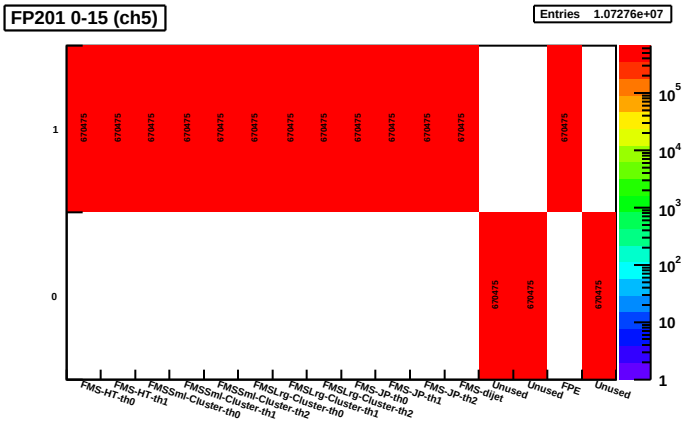

Entries 0

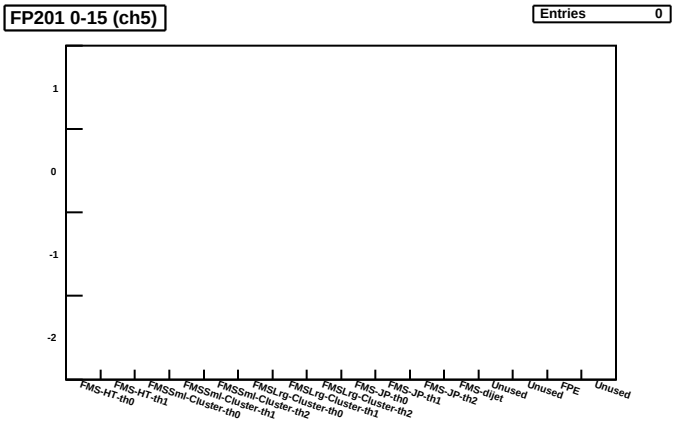

EM201 0-15 (ch3)
Entries 1.07276e+07
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

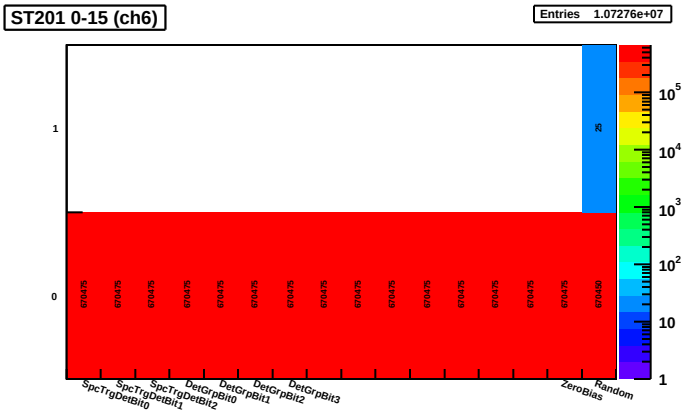

EM201 0-15 (ch3)
Entries 3836
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

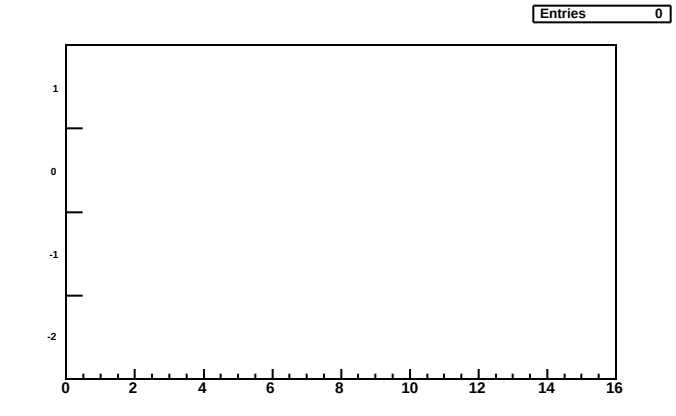

RAT board (ch4)
Entries 1.07276e+07
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

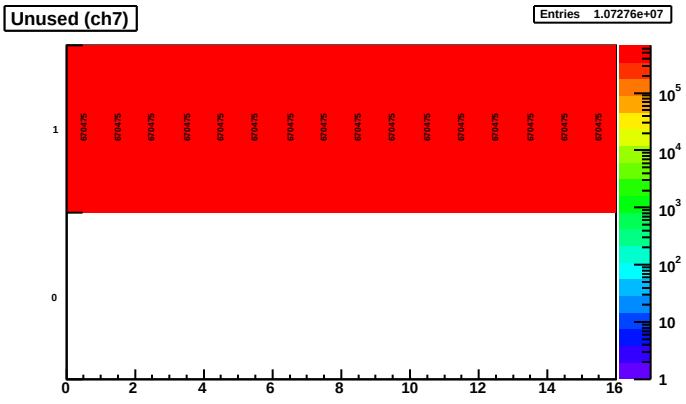

Entries 0

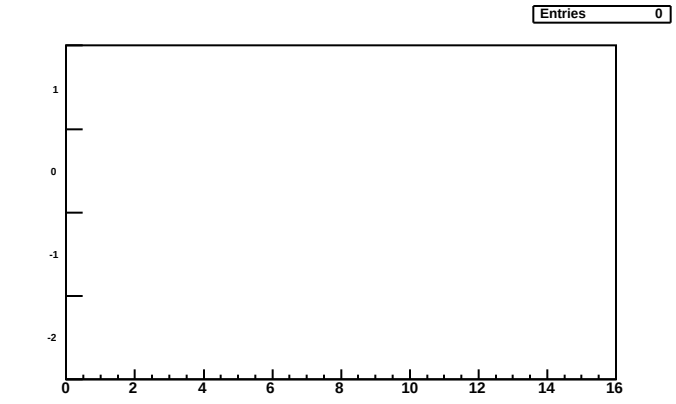

FP201 0-15 (ch5)
Entries 1.07276e+07
FMS-HT-th0
FMS-HT-th1
FMSSml-Cluster-th0
FMSSml-Cluster-th1
FMSSml-Cluster-th2
FMSLrg-Cluster-th0
FMSLrg-Cluster-th1
FMSLrg-Cluster-th2
FMS-JP-th0
FMS-JP-th1
FMS-JP-th2
FMS-diJet
Unused
Unused
FPE
Unused


FP201 0-15 (ch5)
Entries 0
FMS-HT-th0
FMS-HT-th1
FMSSml-Cluster-th0
FMSSml-Cluster-th1
FMSSml-Cluster-th2
FMSLrg-Cluster-th0
FMSLrg-Cluster-th1
FMSLrg-Cluster-th2
FMS-JP-th0
FMS-JP-th1
FMS-JP-th2
FMS-diJet
Unused
Unused
FPE
Unused


ST201 0-15 (ch6)
Entries 1.07276e+07
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetGrpBit0
DetGrpBit1
DetGrpBit2
DetGrpBit3
ZeroBias
Random


Entries 0


Unused (ch7)
Entries 1.07276e+07


Entries 0