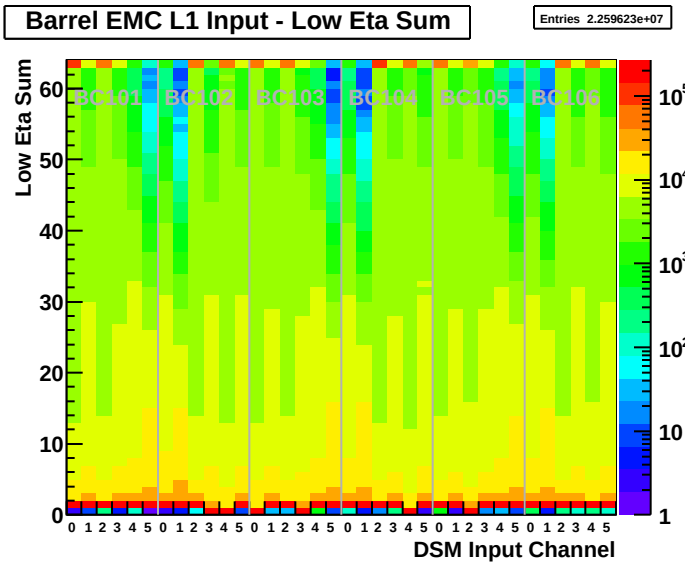
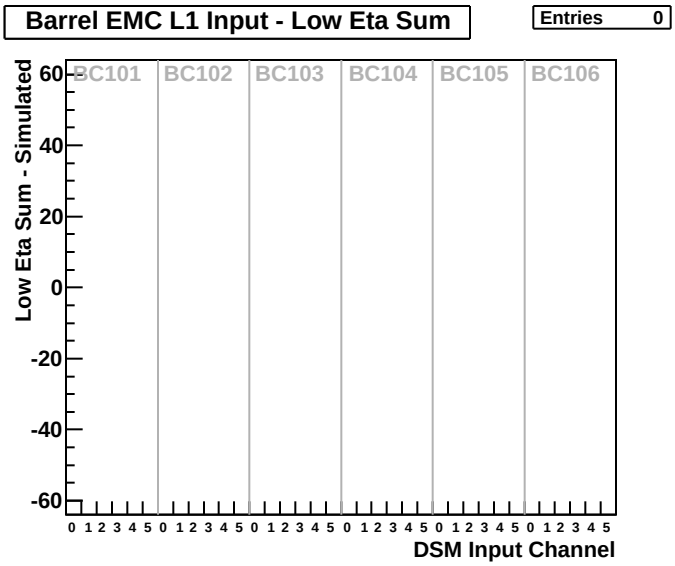
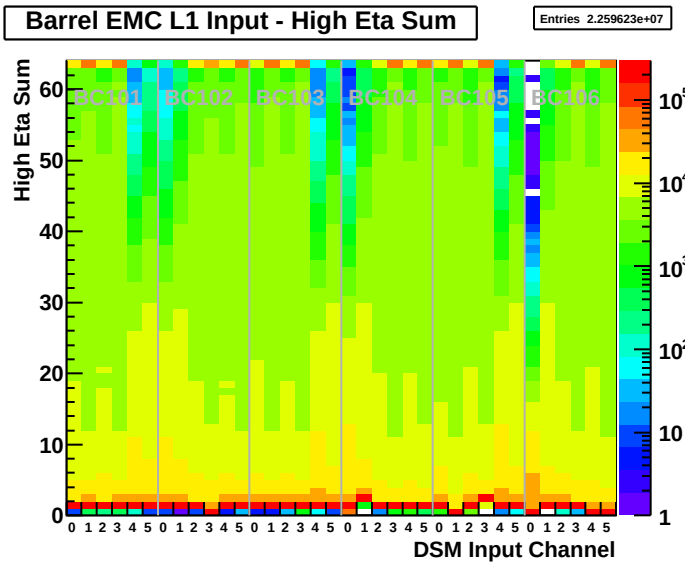
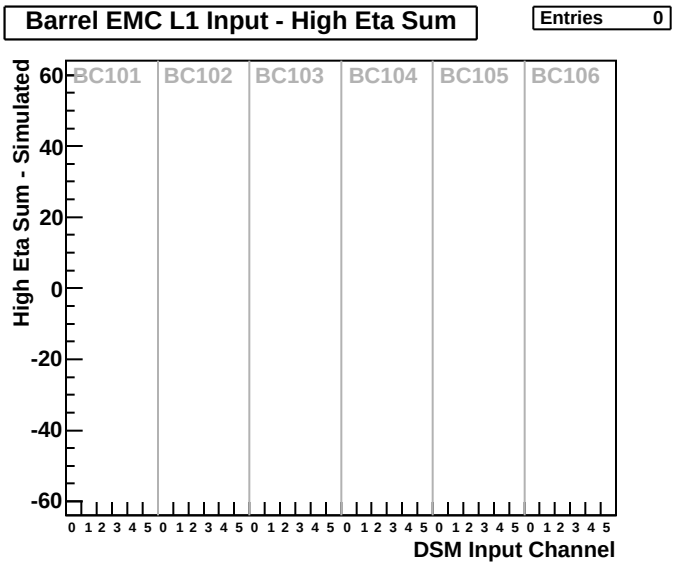
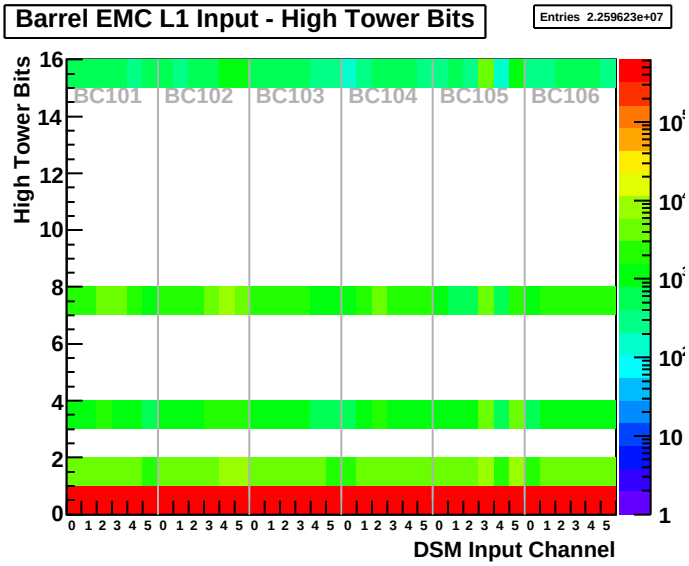
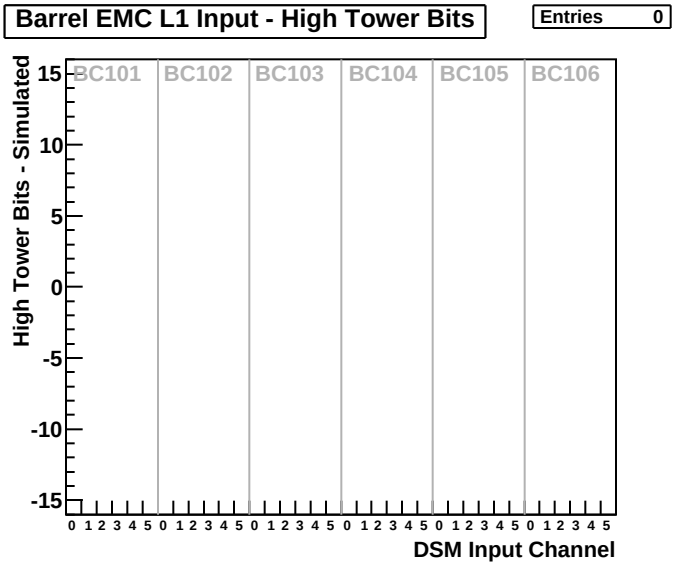

Barrel EMC L1 Input - Low Eta Sum
Entries 2.259623e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

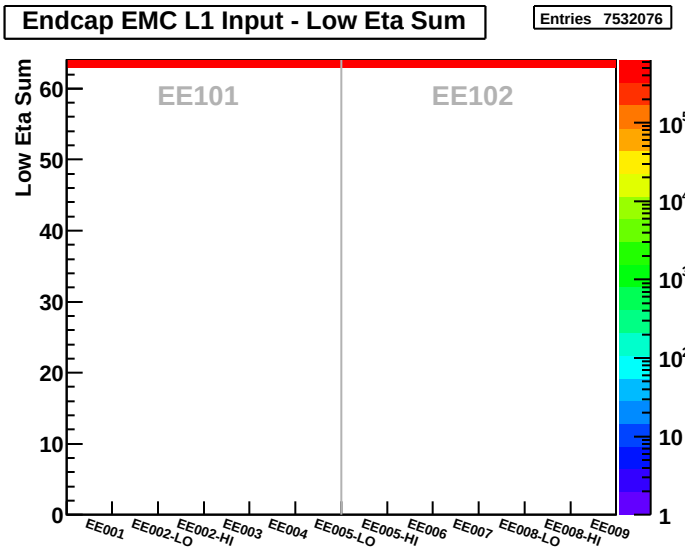

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

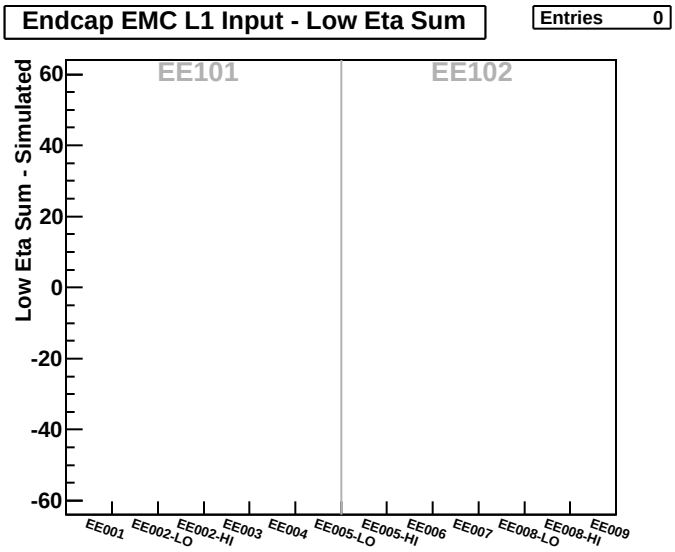

Barrel EMC L1 Input - High Eta Sum
Entries 2.259623e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

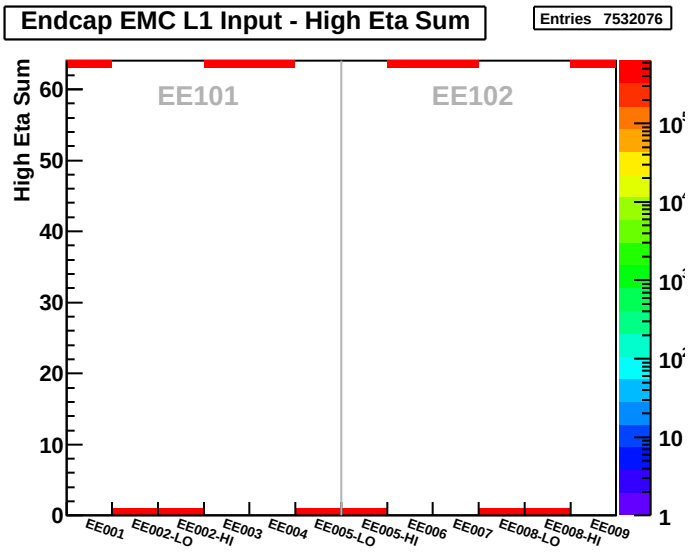

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

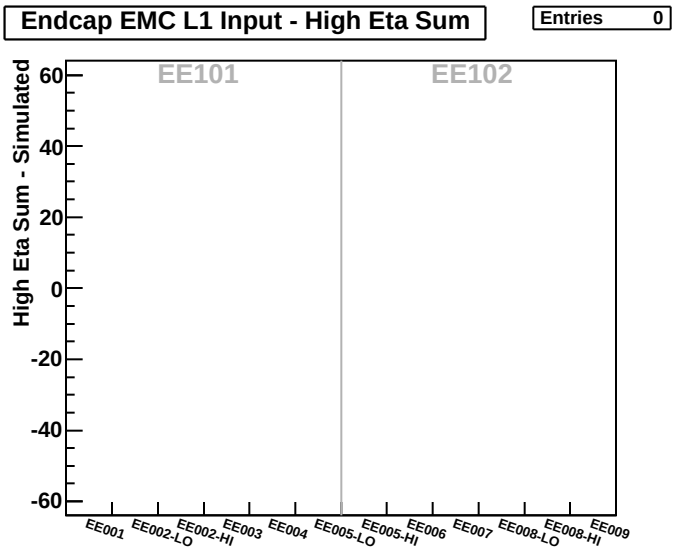

Barrel EMC L1 Input - High Tower Bits
Entries 2.259623e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

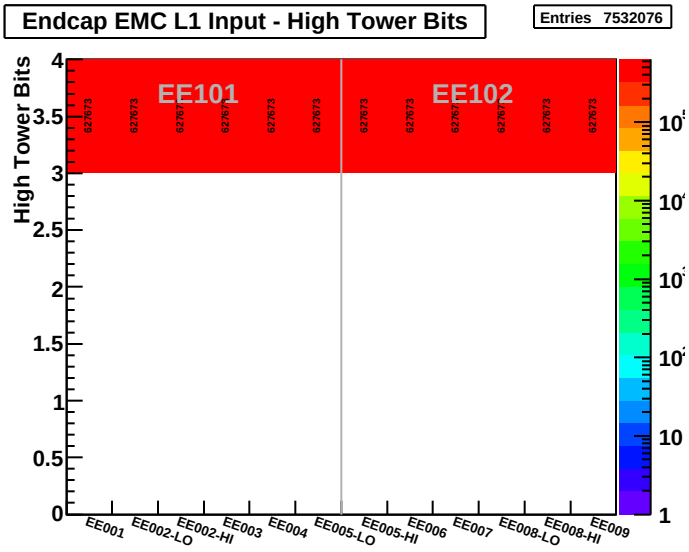

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

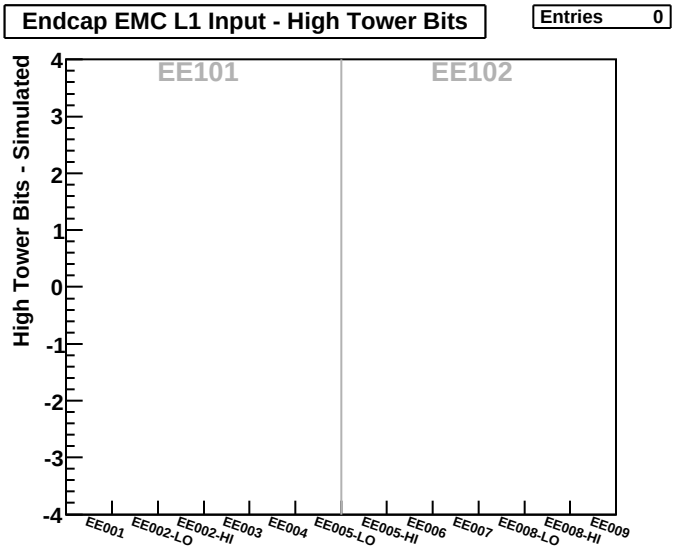

Endcap EMC L1 Input - Low Eta Sum
Entries 7532076
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

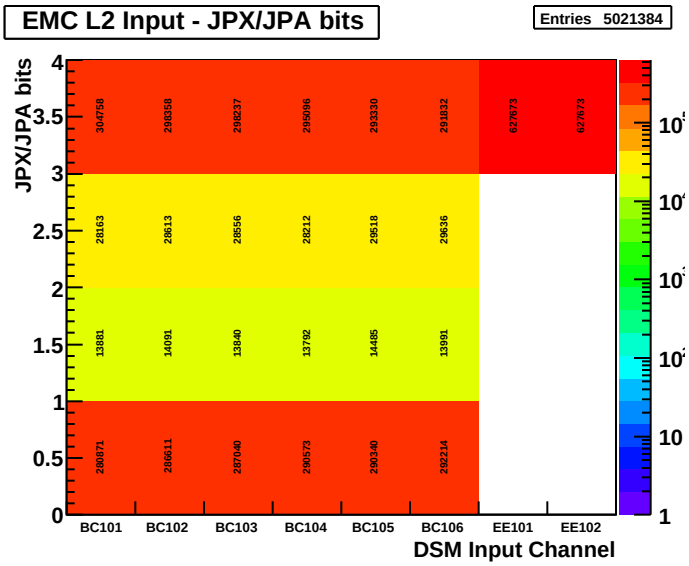

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

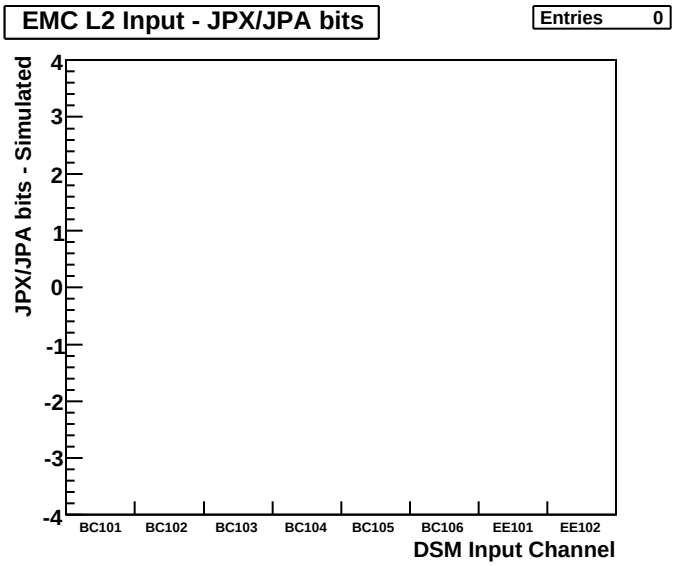

Endcap EMC L1 Input - High Eta Sum
Entries 7532076
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

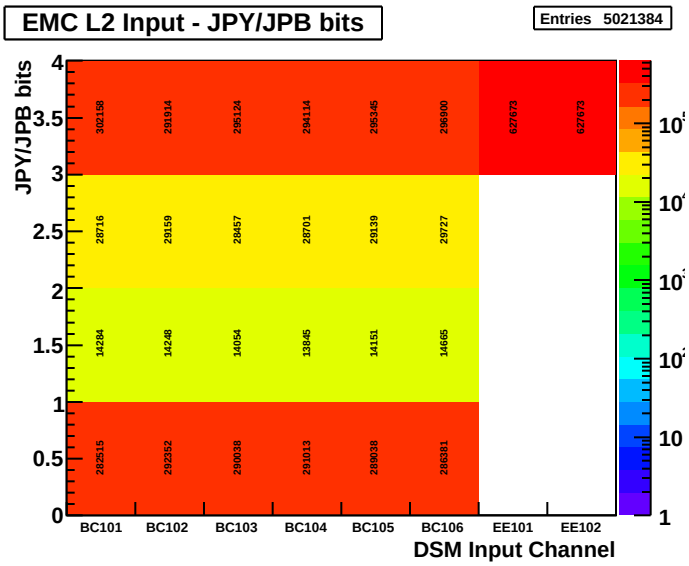

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

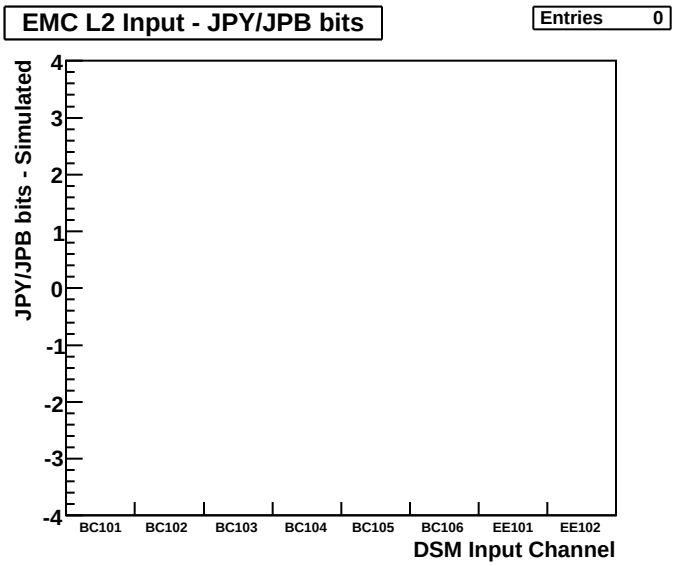

Endcap EMC L1 Input - High Tower Bits
Entries 7532076
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

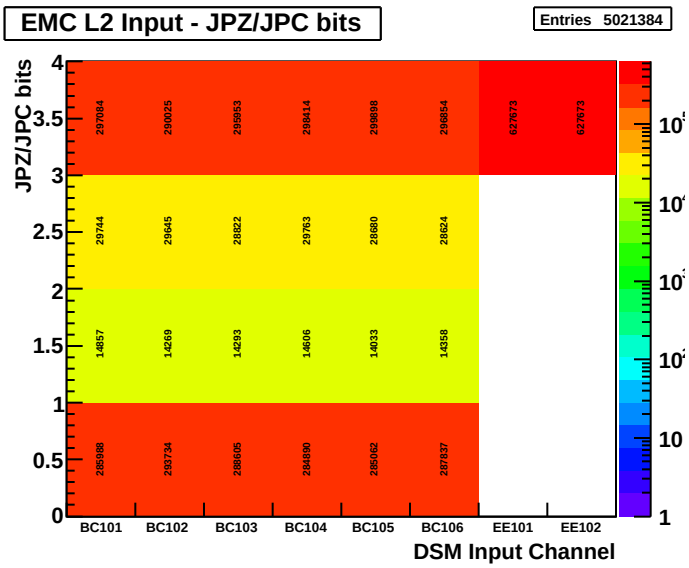

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

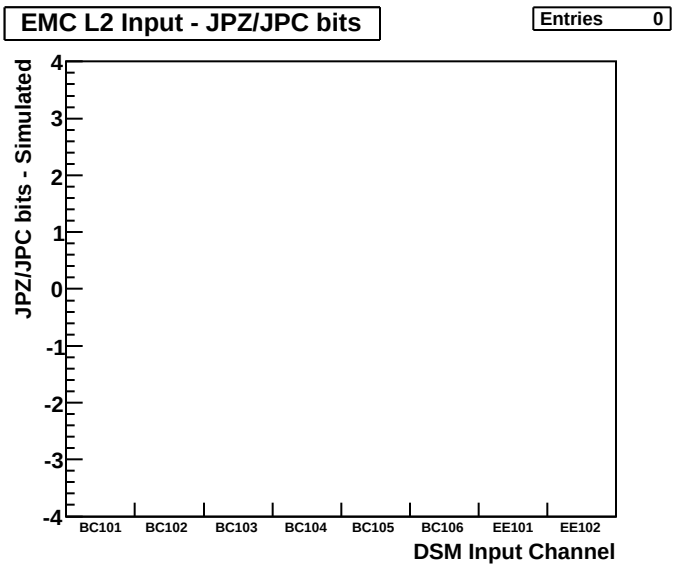

EMC L2 Input - JPX/JPA bits
Entries 5021384
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
304758 298358 298237 295696 293330 291832 627673 627673
28163 28613 28556 28212 29518 29636
13881 14091 13840 13792 14485 13991
280871 286611 287040 290573 290340 292214

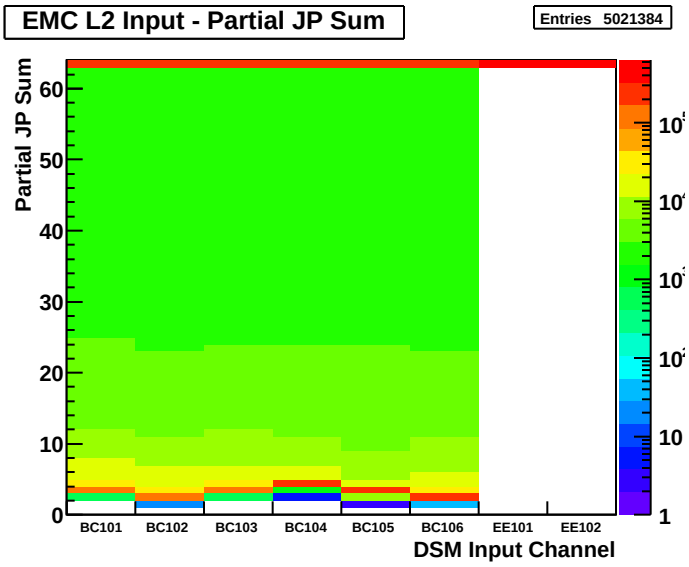

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

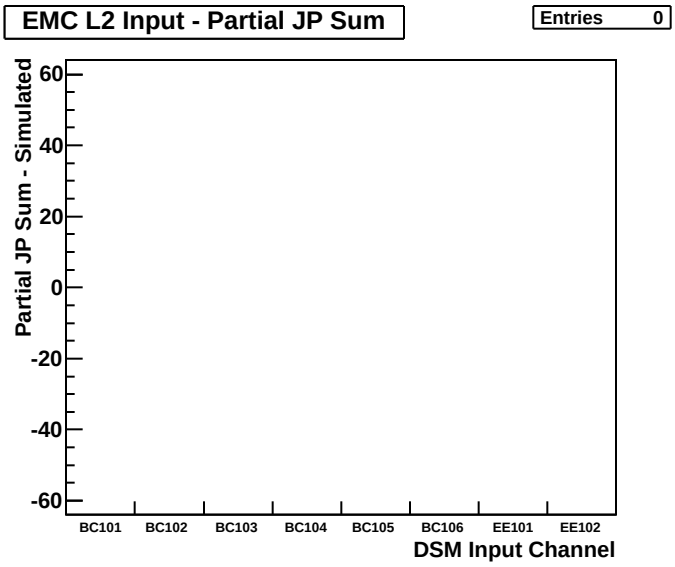

EMC L2 Input - JPY/JPB bits
Entries 5021384
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
302158 291914 295124 294114 295345 296900 627673 627673
28716 29159 28457 28701 29139 29727
14284 14248 14054 13845 14151 14665
282515 292352 290038 291013 289038 286381

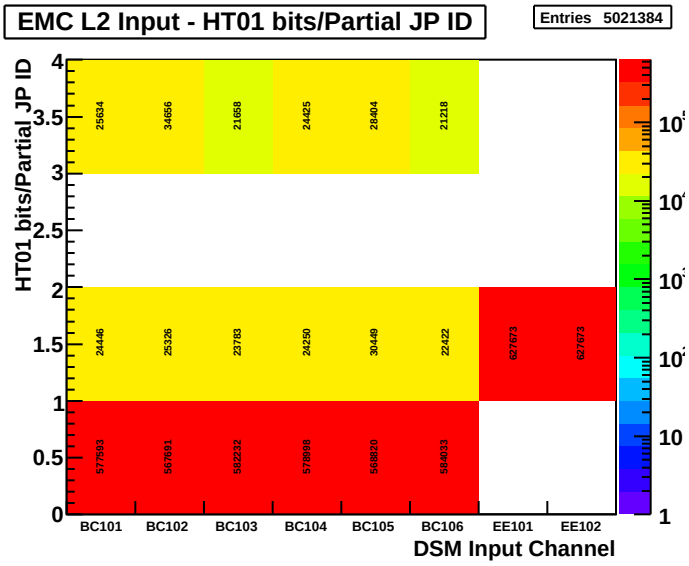

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

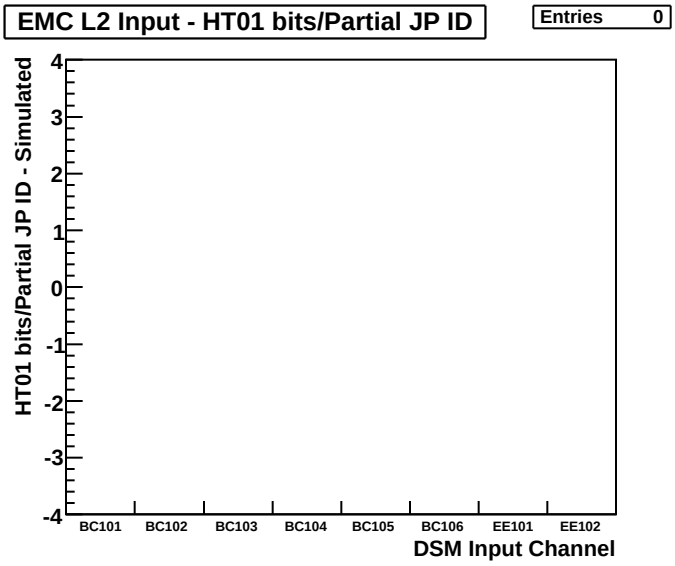

EMC L2 Input - JPZ/JPC bits
Entries 5021384
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
297084 290025 293953 298414 299898 296854 627673 627673
29744 29645 28822 29763 28680 28624
14857 14269 14293 14606 14033 14358
285988 293734 288605 284890 285062 287837

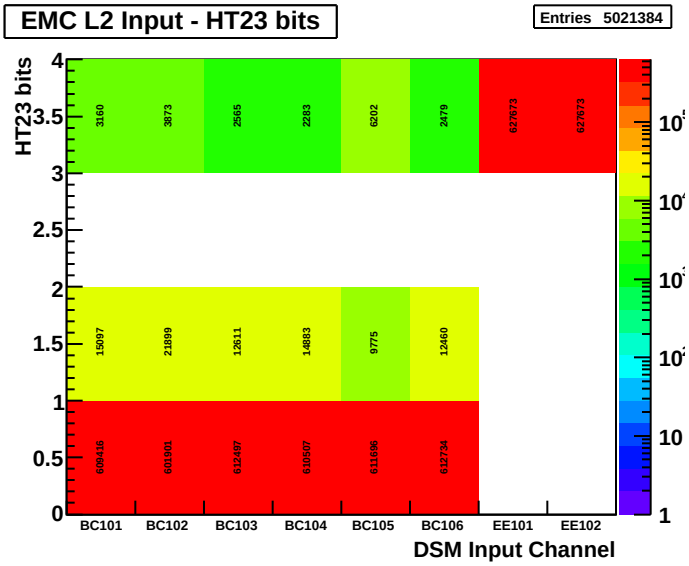

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

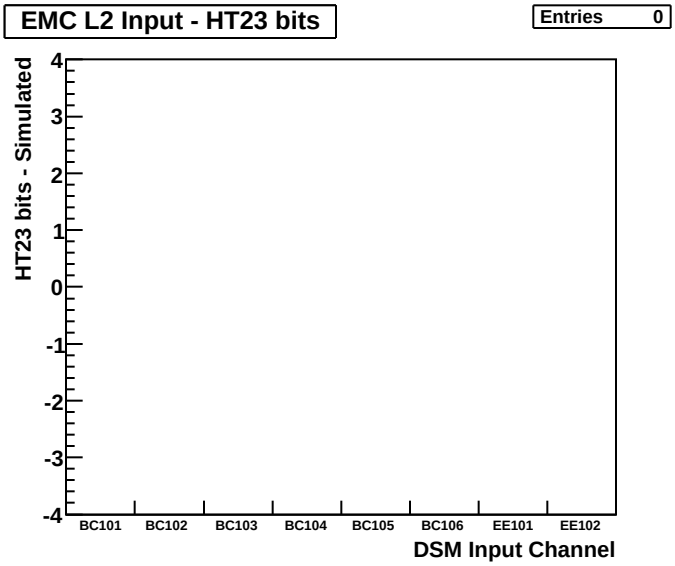

EMC L2 Input - Partial JP Sum
Entries 5021384
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

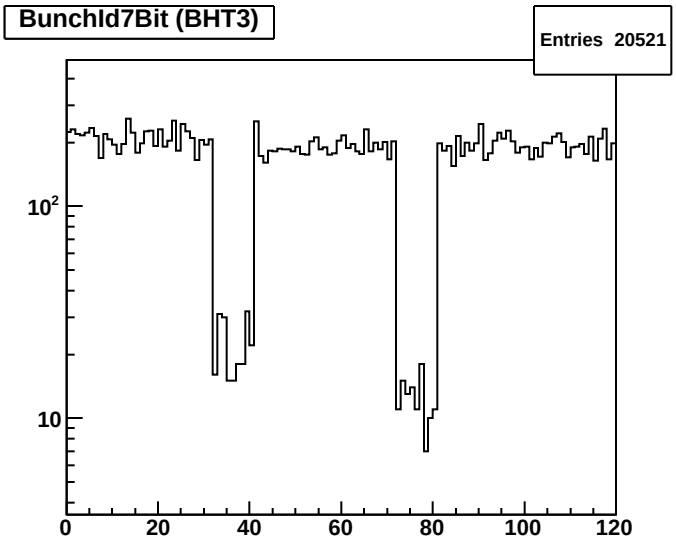

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

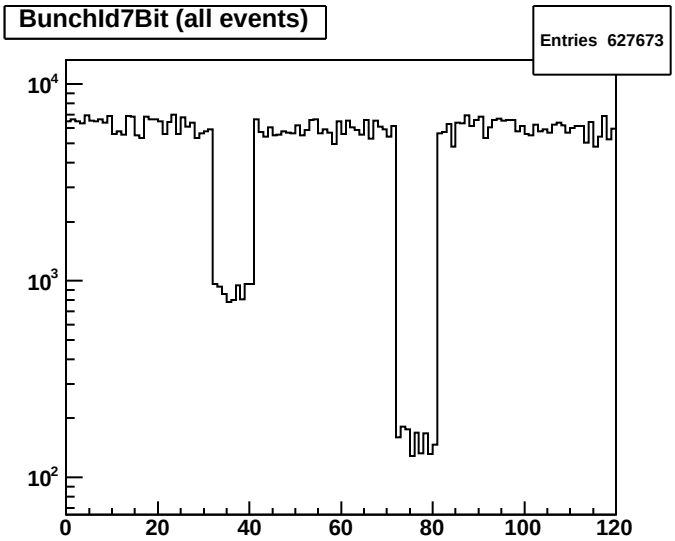

EMC L2 Input - HT01 bits/Partial JP ID
Entries 5021384
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

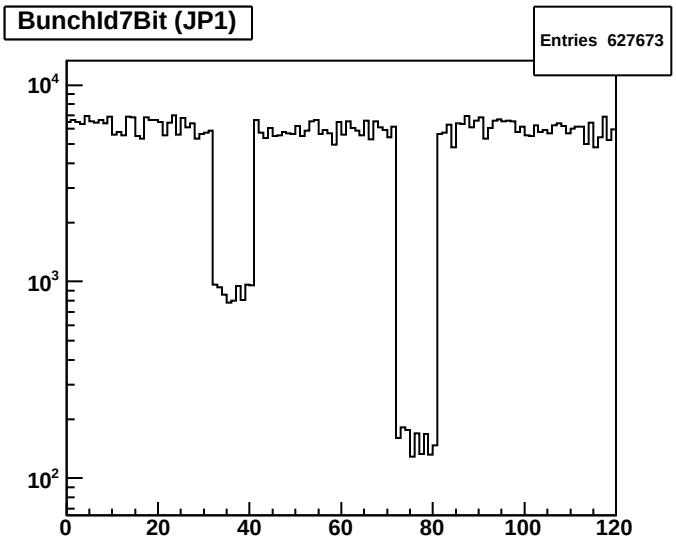

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

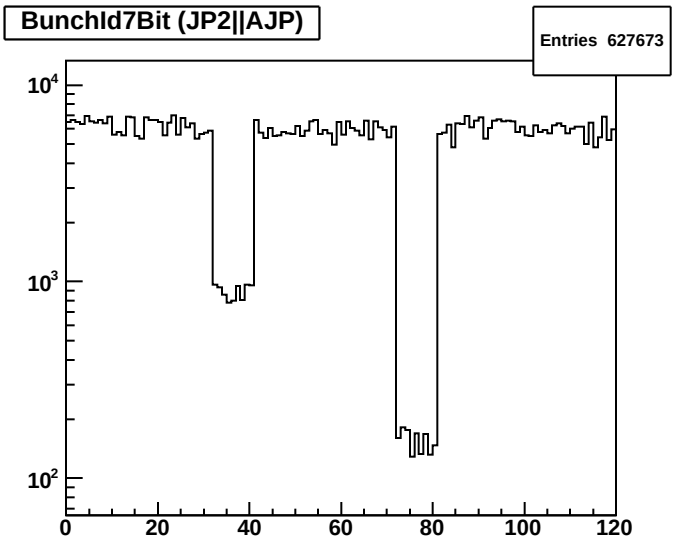

EMC L2 Input - HT23 bits
Entries 5021384
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

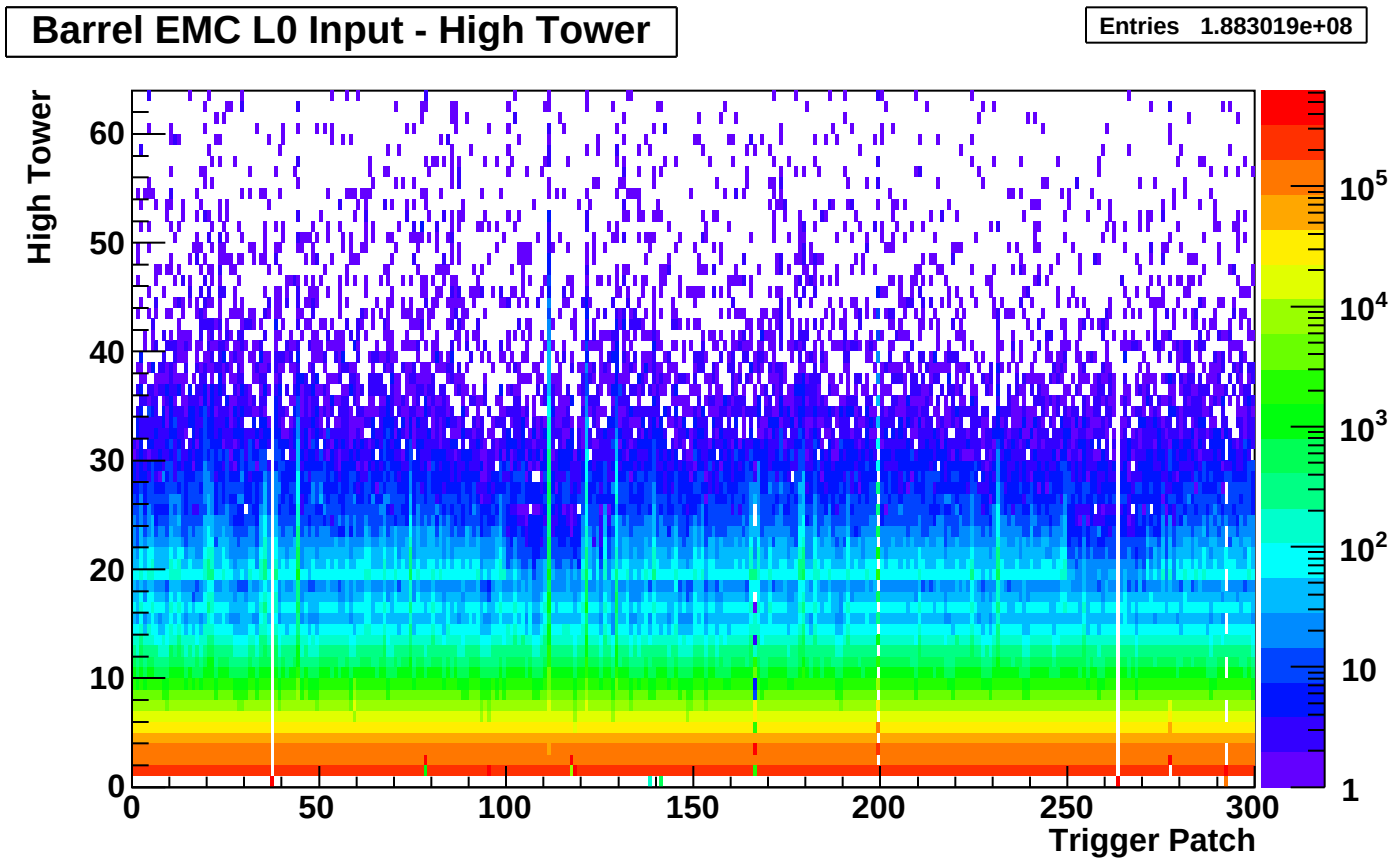

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

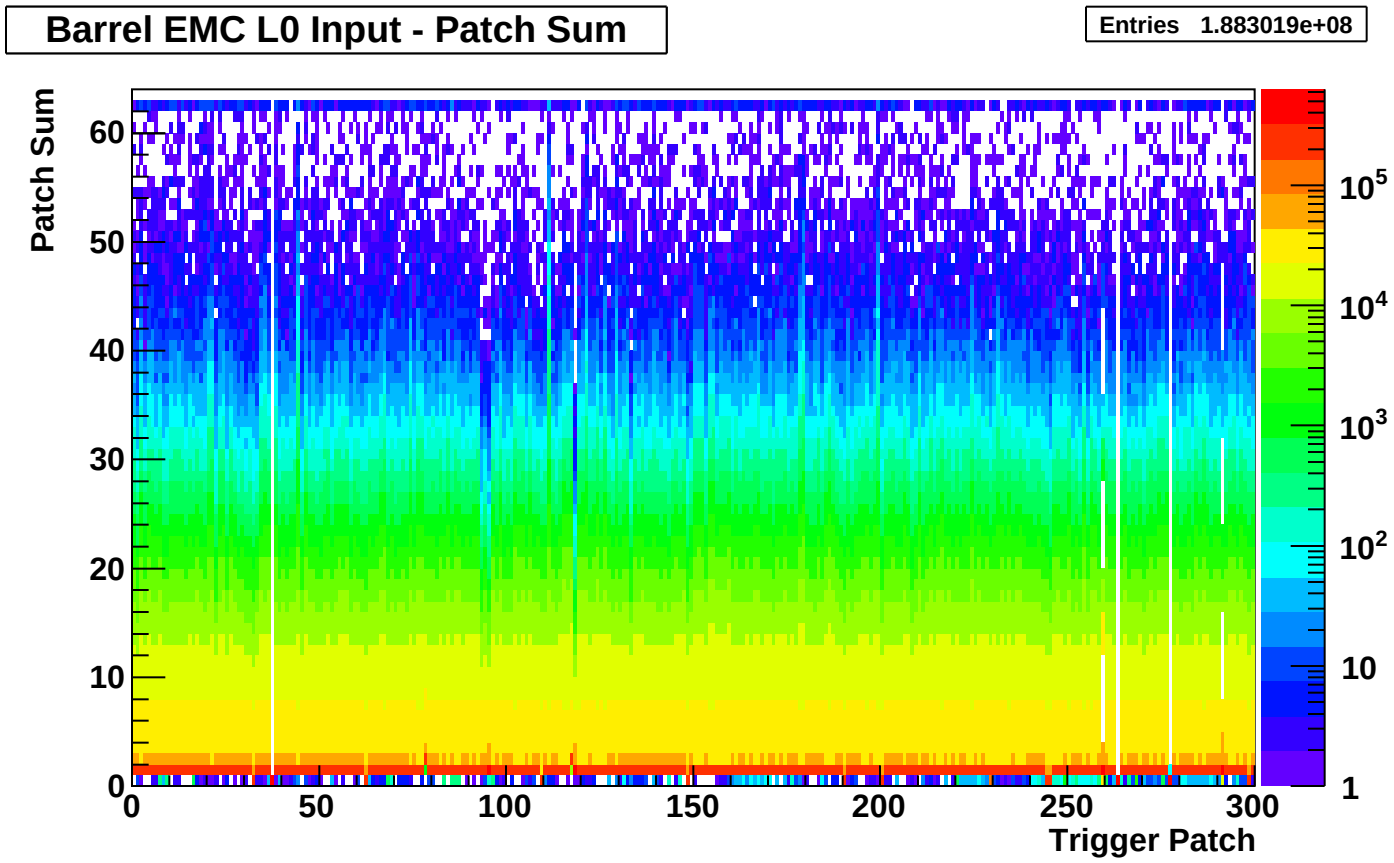

BunchId7Bit (BHT3)
Entries 20521
10^2
10
0
20
40
60
80
100
120

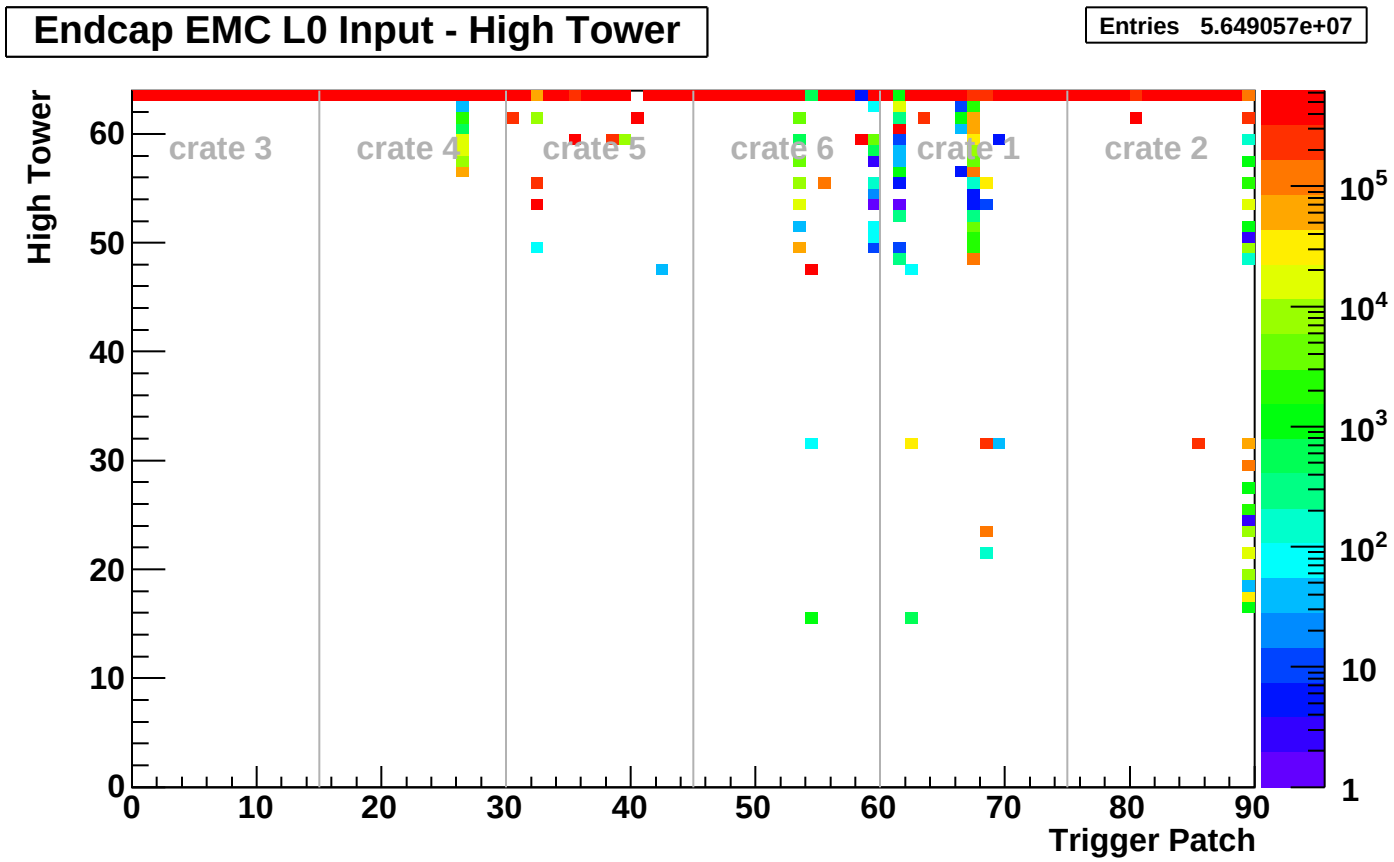

BunchId7Bit (all events)
Entries 627673
10^4
10^3
10^2
0
20
40
60
80
100
120

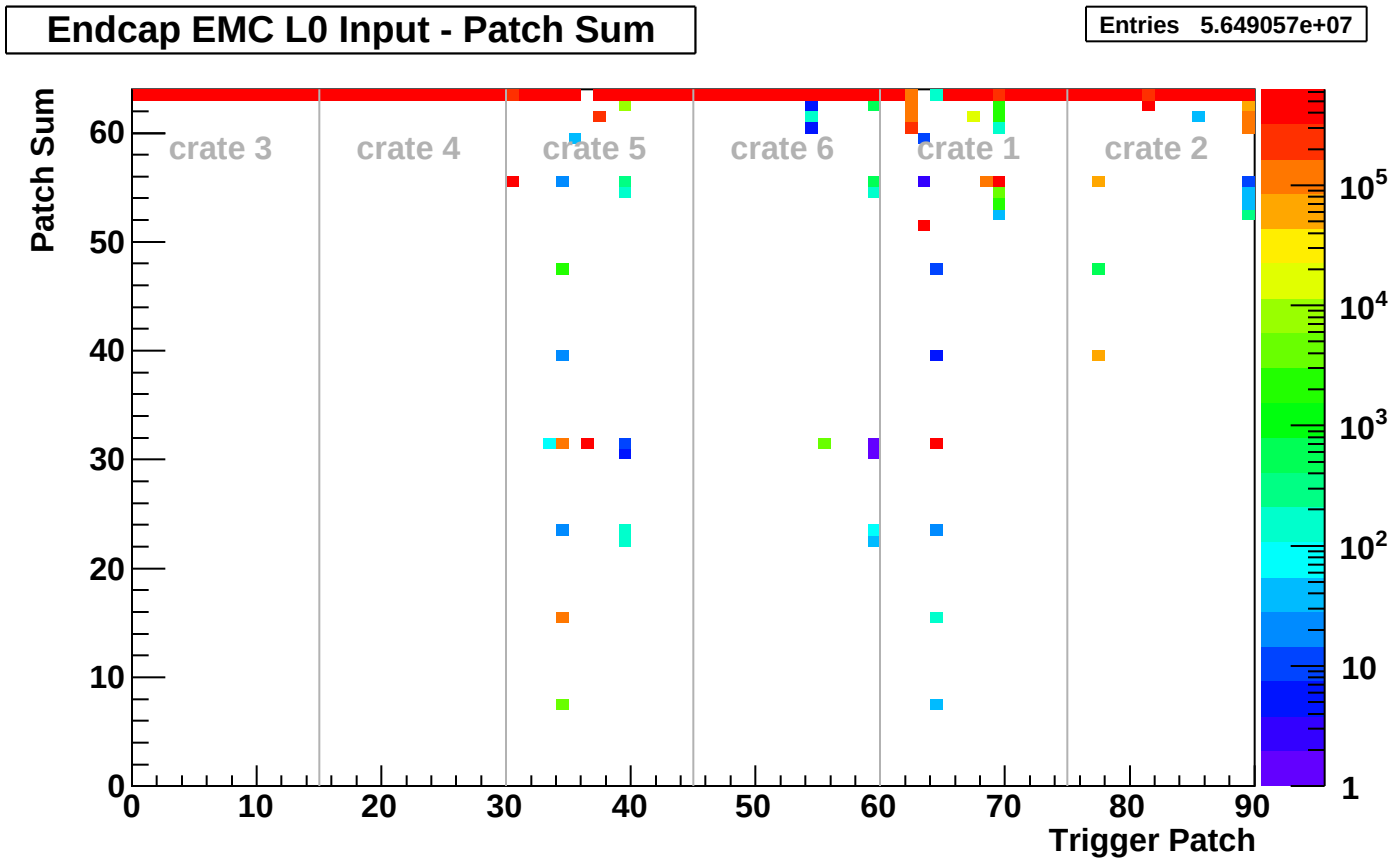

BunchId7Bit (JP1)
Entries 627673
10^4
10^3
10^2
0
20
40
60
80
100
120

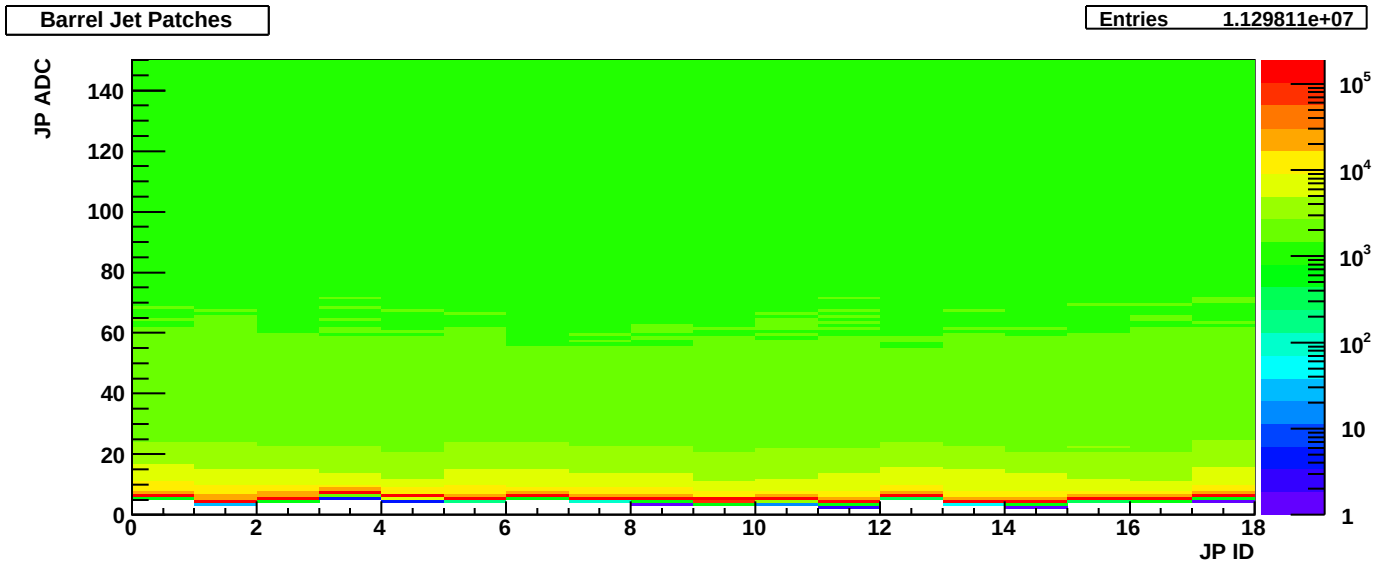

BunchId7Bit (JP2||AJP)
Entries 627673
10^4
10^3
10^2
0
20
40
60
80
100
120

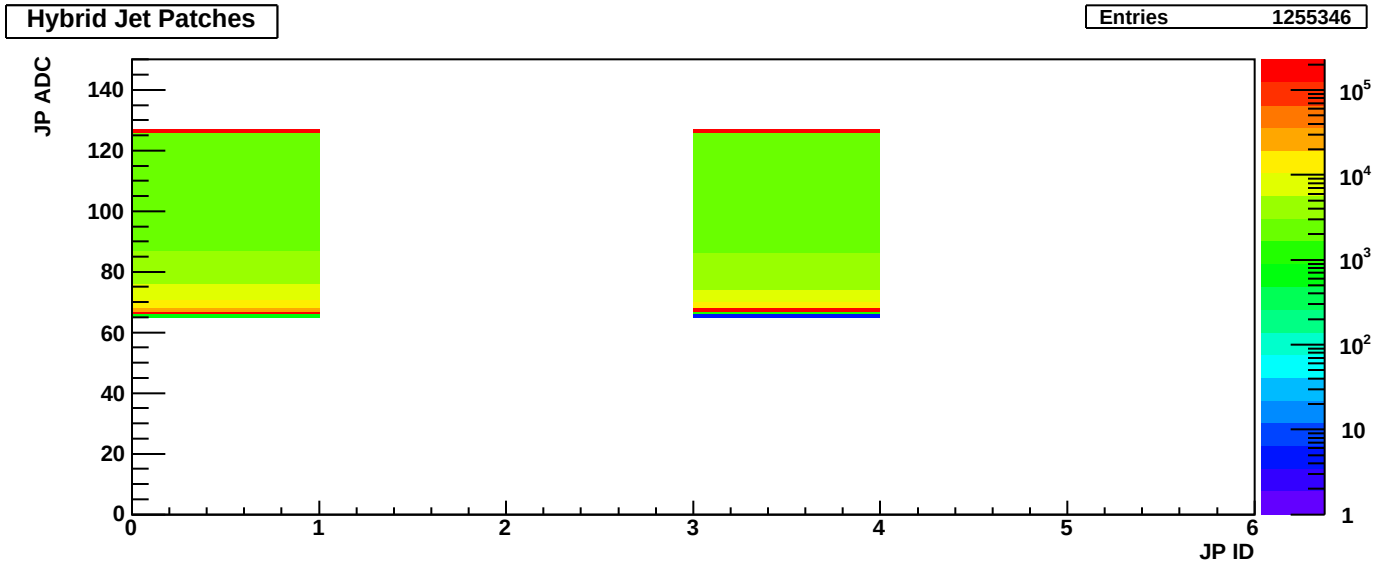

Barrel EMC L0 Input - High Tower
Entries 1.883019e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

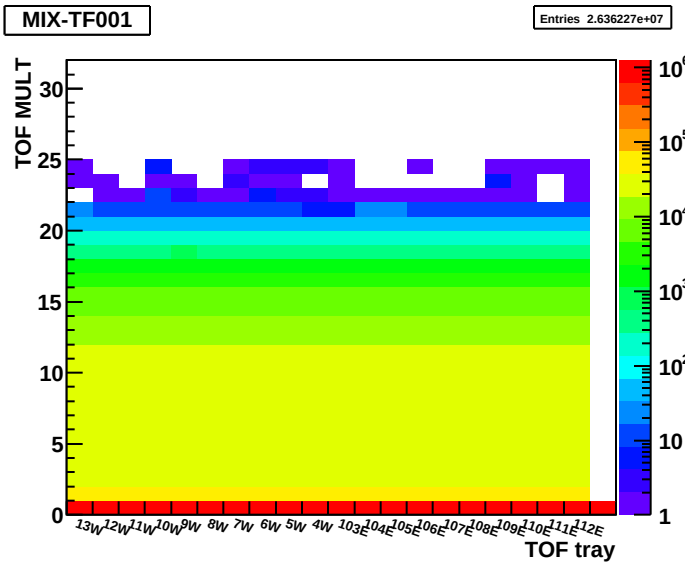

Barrel EMC L0 Input - Patch Sum
Entries 1.883019e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

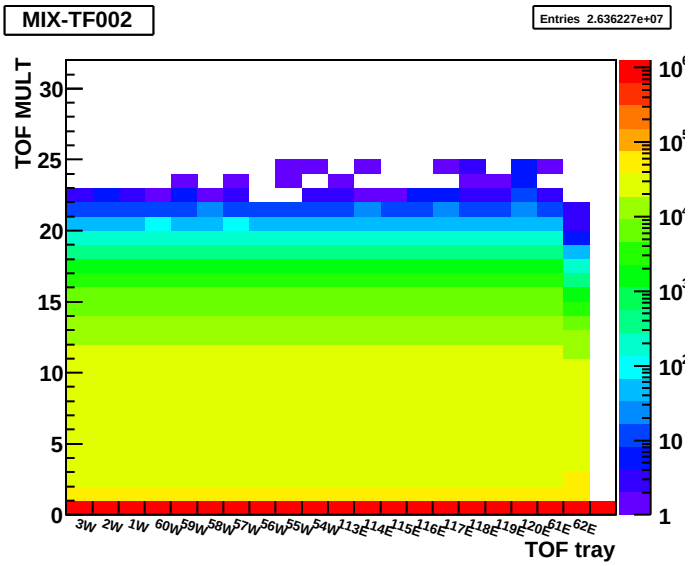

Endcap EMC L0 Input - High Tower
Entries 5.649057e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

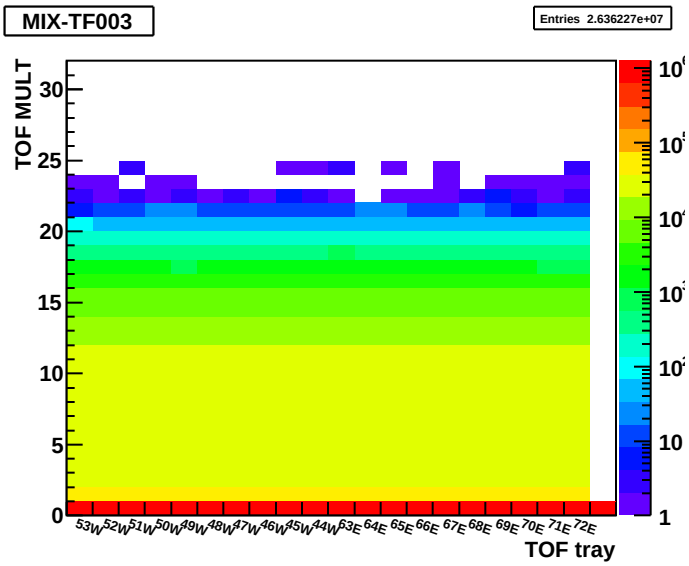

Endcap EMC L0 Input - Patch Sum
Entries 5.649057e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

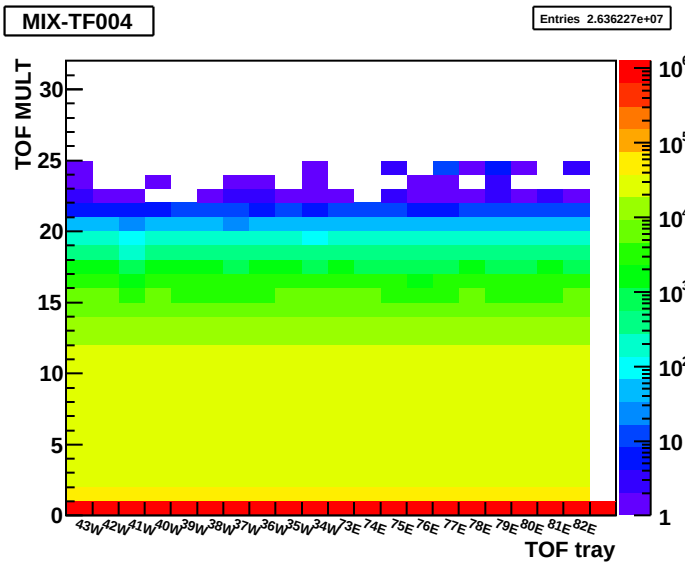

Barrel Jet Patches
Entries 1.129811e+07
JP ADC
JP ID

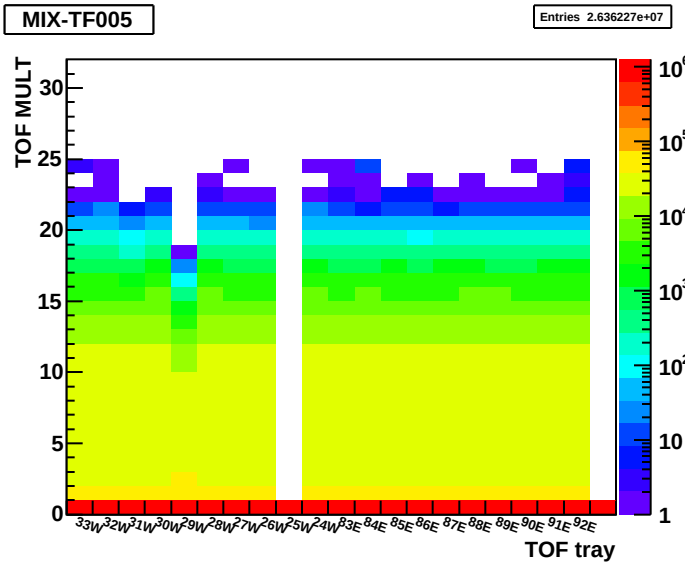

Hybrid Jet Patches
Entries 1255346
JP ADC
JP ID

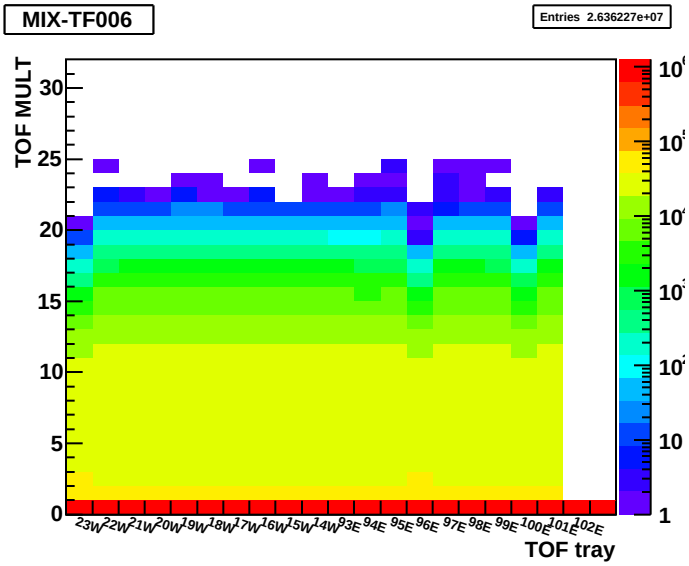

MIX-TF001
Entries 2.636227e+07
TOF MULT
TOF tray

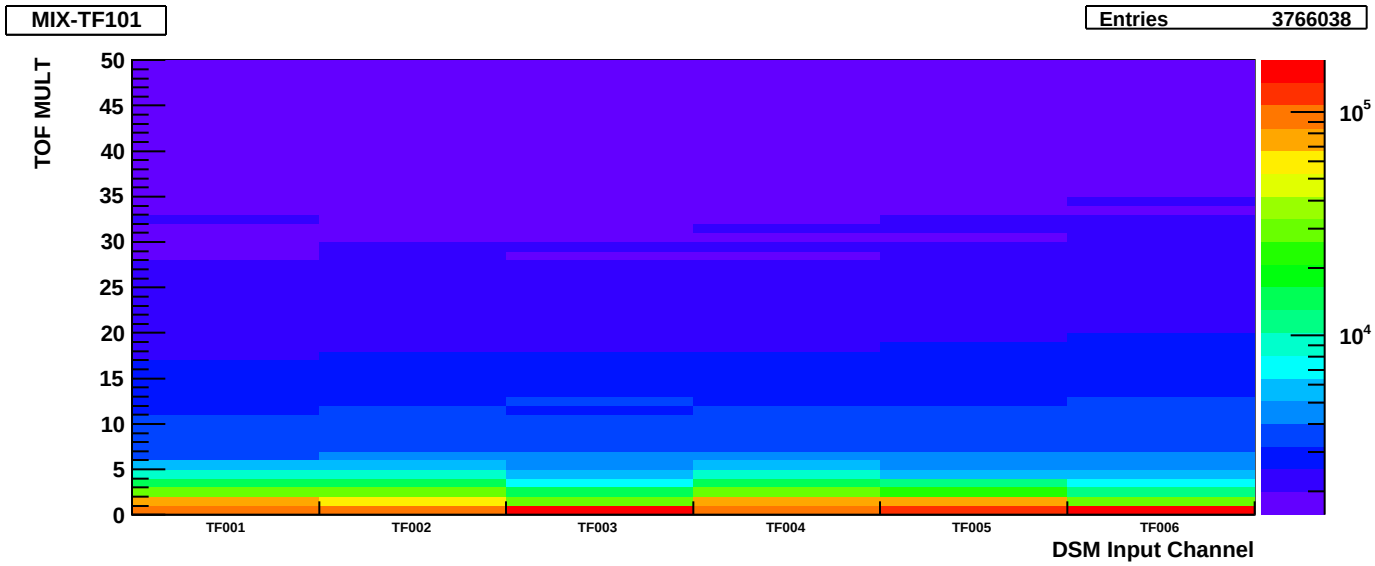

MIX-TF002
Entries 2.636227e+07
TOF MULT
TOF tray

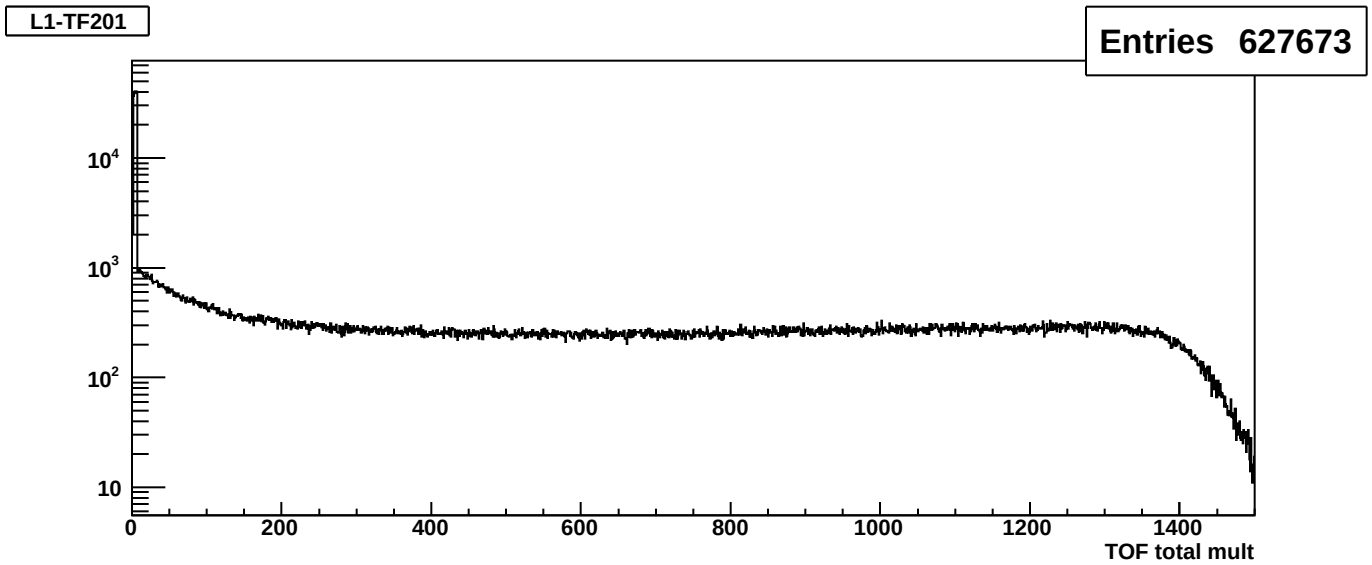

MIX-TF003
Entries 2.636227e+07
TOF MULT
TOF tray

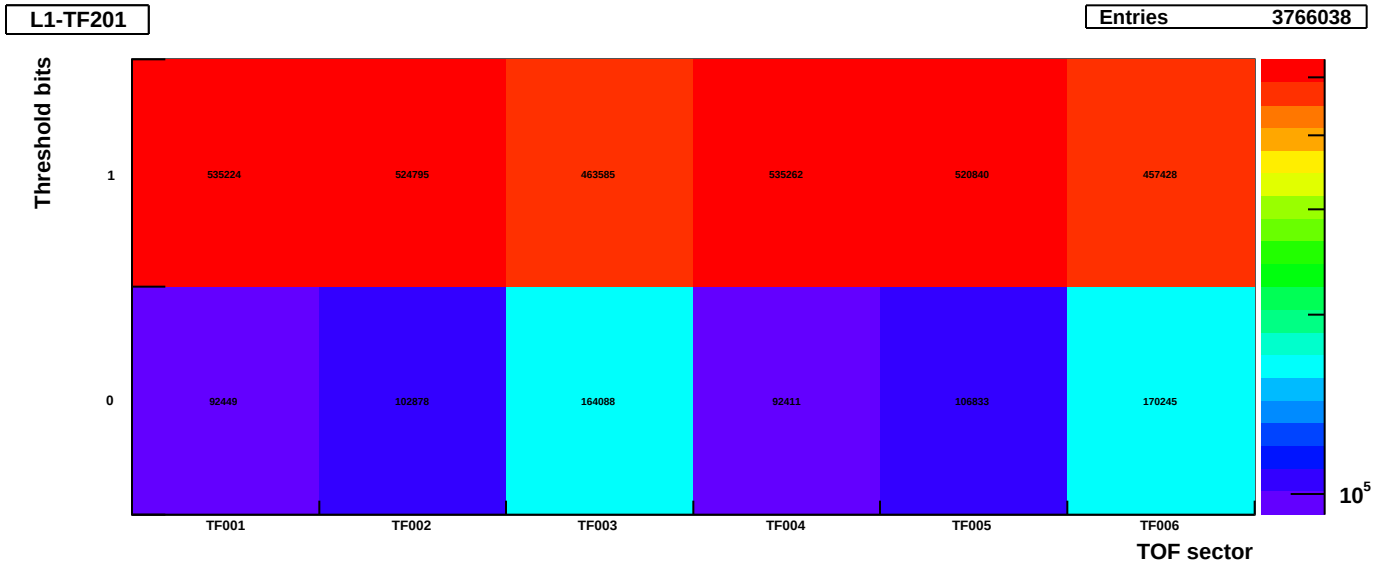

MIX-TF004
Entries 2.636227e+07
TOF MULT
TOF tray

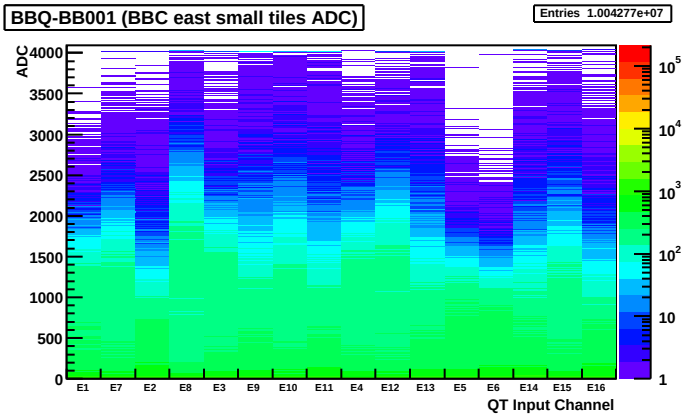

MIX-TF005
Entries 2.636227e+07
TOF MULT
TOF tray

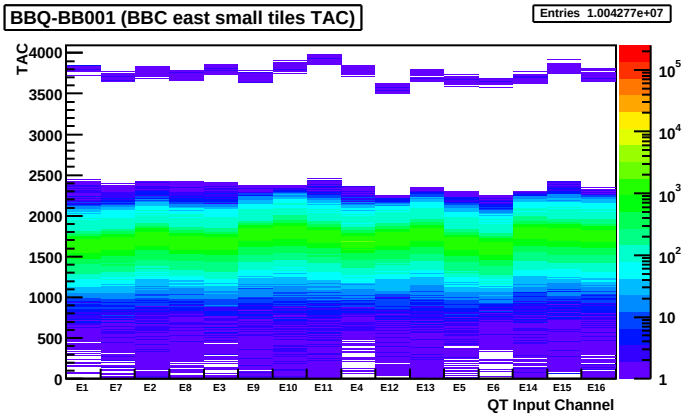

MIX-TF006
Entries 2.636227e+07
TOF MULT
TOF tray

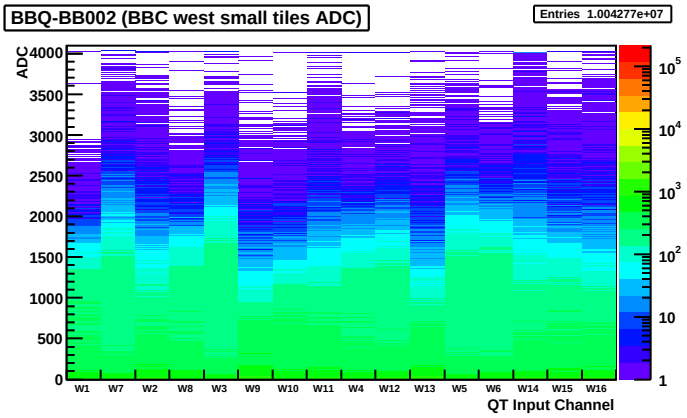

MIX-TF101
Entries 3766038
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4

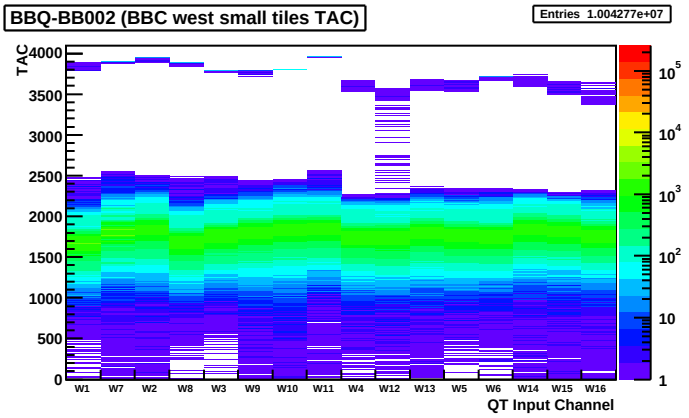

L1-TF201
Entries 627673
10^4
10^3
10^2
10
0
200
400
600
800
1000
1200
1400
TOF total mult

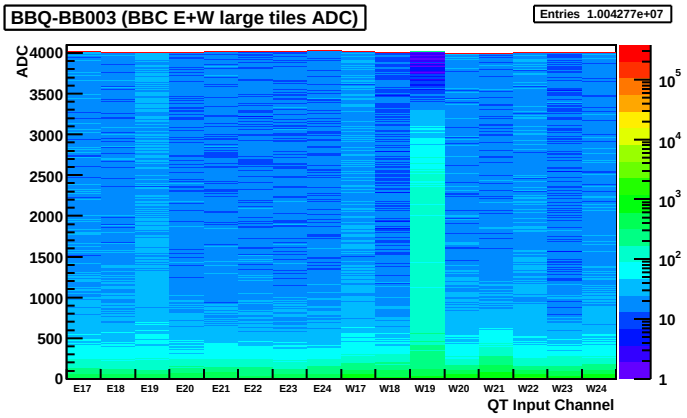

L1-TF201
Entries 3766038
Threshold bits
1
0
535224
524795
463585
535262
520840
457428
92449
102878
164088
92411
106833
170245
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

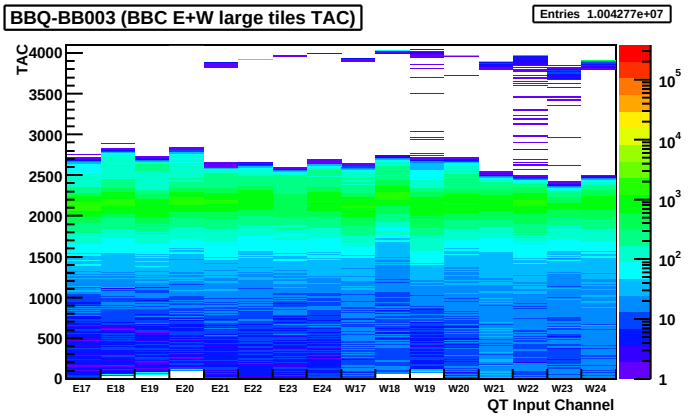

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.004277e+07
ADC
QT Input Channel

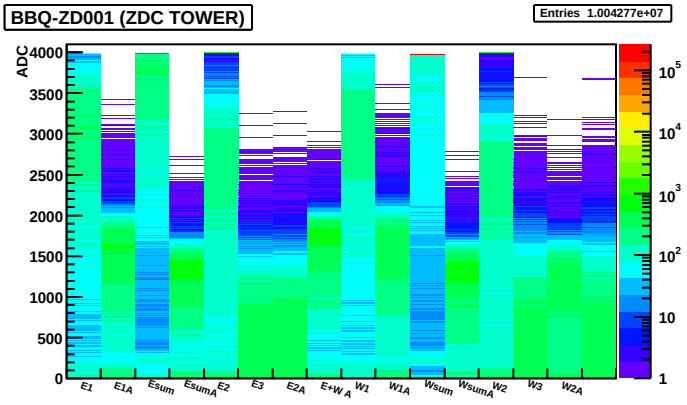

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.004277e+07
TAC
QT Input Channel

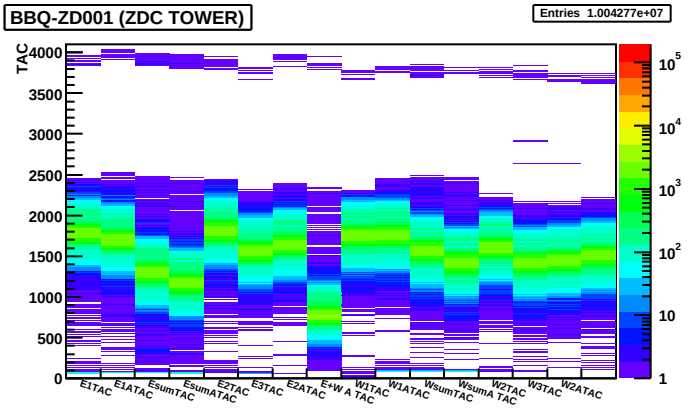

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.004277e+07
ADC
QT Input Channel

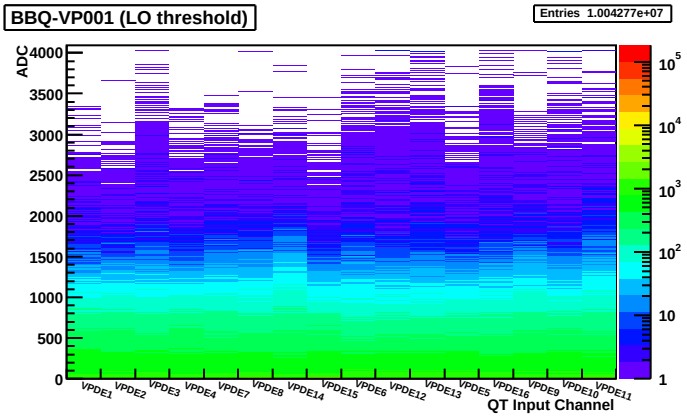

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.004277e+07
TAC
QT Input Channel

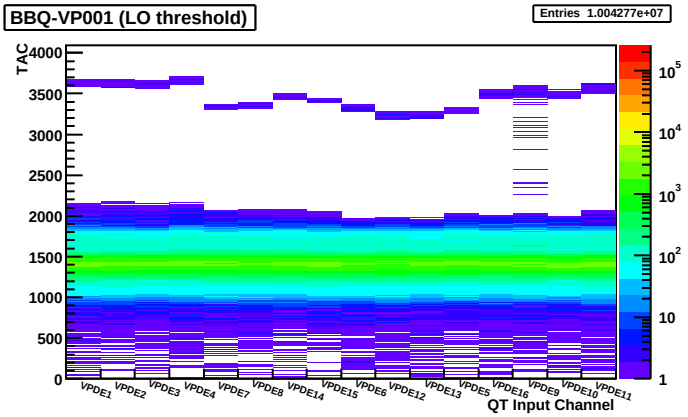

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.004277e+07
ADC
QT Input Channel

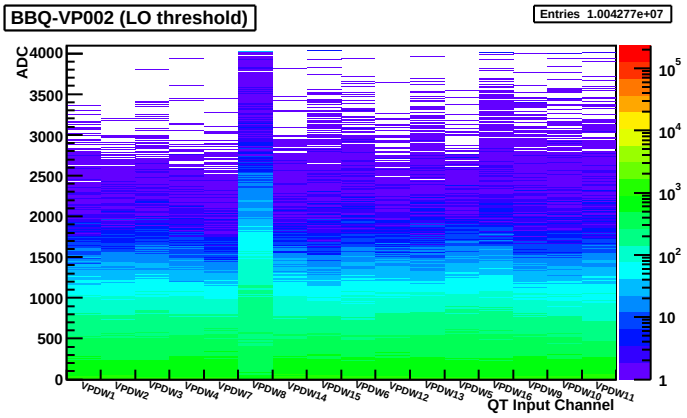

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.004277e+07
TAC
QT Input Channel

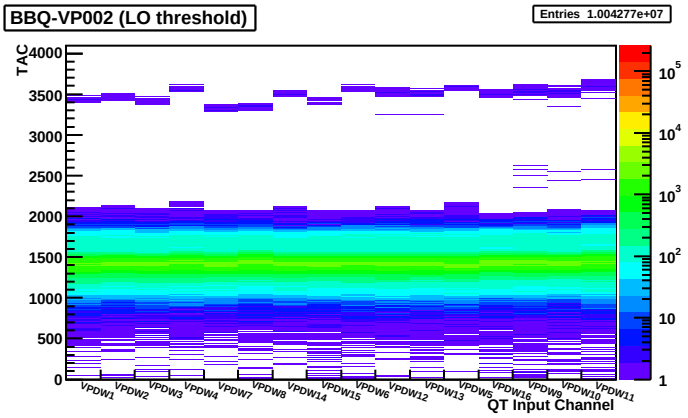

BBQ-ZD001 (ZDC TOWER)
Entries 1.004277e+07
ADC

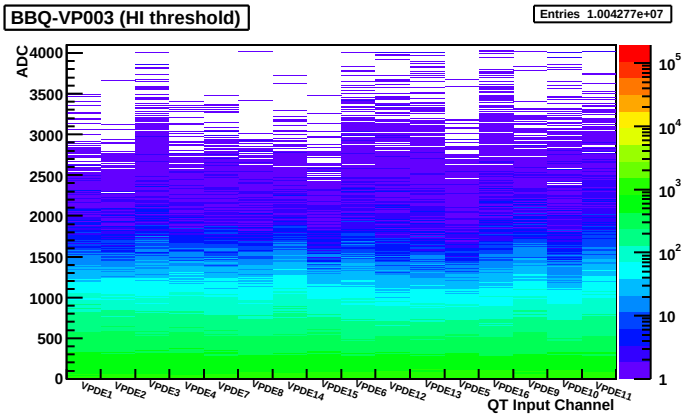

BBQ-ZD001 (ZDC TOWER)
Entries 1.004277e+07
TAC

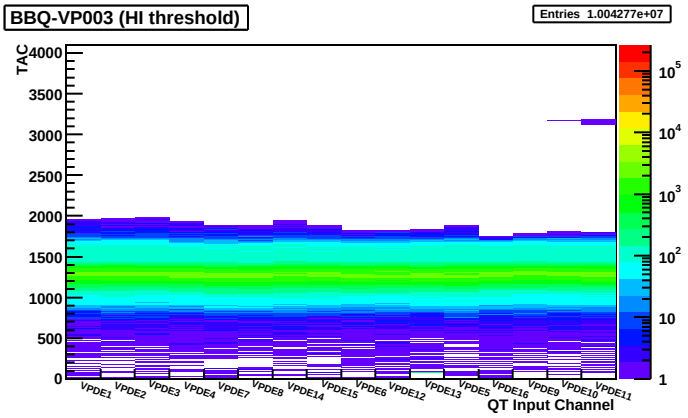

BBQ-VP001 (LO threshold)
Entries 1.004277e+07
ADC
QT Input Channel

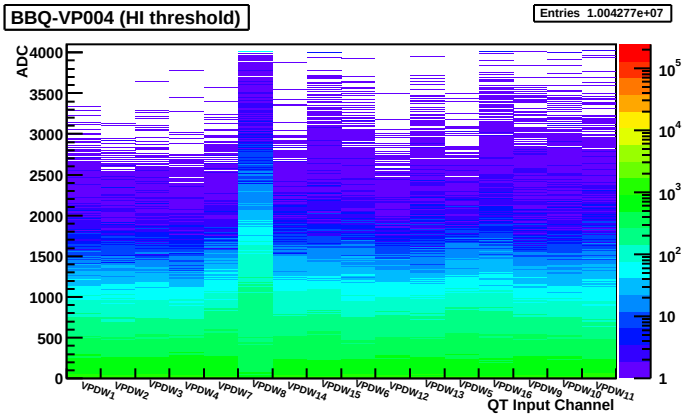

BBQ-VP001 (LO threshold)
Entries 1.004277e+07
TAC
QT Input Channel

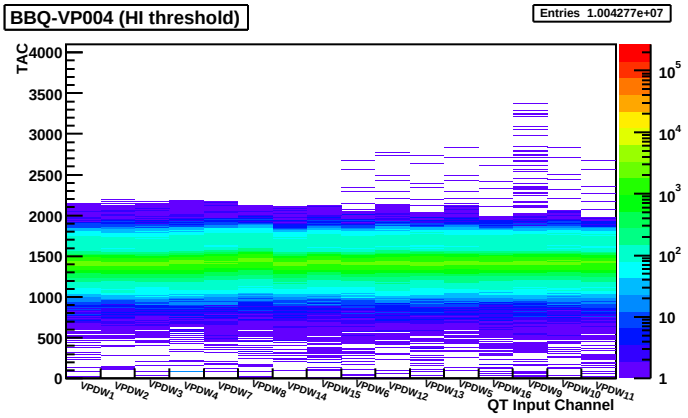

BBQ-VP002 (LO threshold)
Entries 1.004277e+07
ADC
QT Input Channel

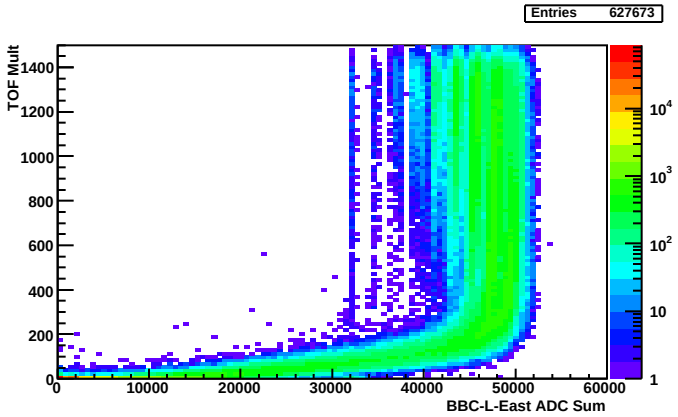

BBQ-VP002 (LO threshold)
Entries 1.004277e+07
TAC
QT Input Channel

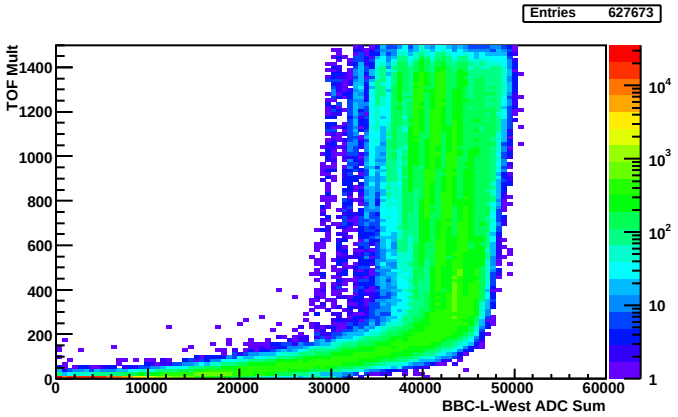

BBQ-VP003 (HI threshold)
Entries 1.004277e+07
ADC
QT Input Channel

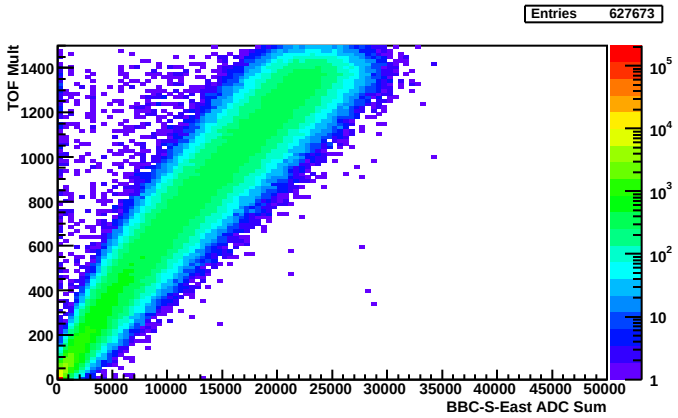

BBQ-VP003 (HI threshold)
Entries 1.004277e+07
TAC
QT Input Channel

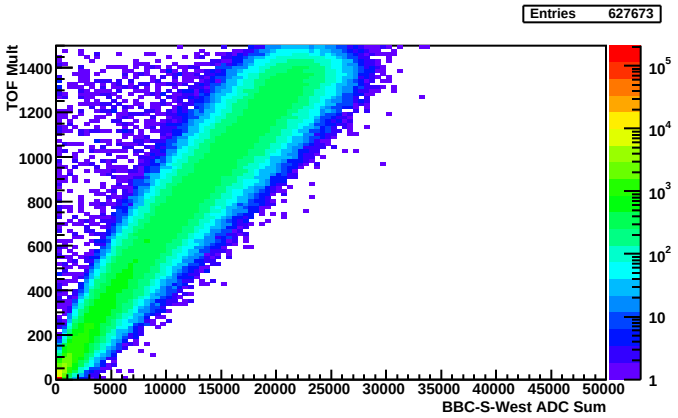

BBQ-VP004 (HI threshold)
Entries 1.004277e+07
ADC
QT Input Channel

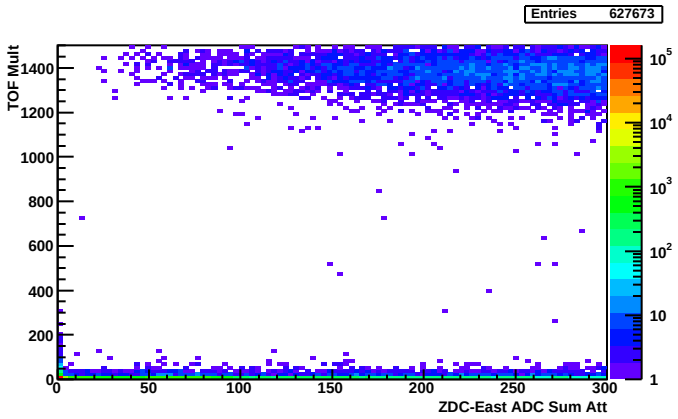

BBQ-VP004 (HI threshold)
Entries 1.004277e+07
TAC
QT Input Channel

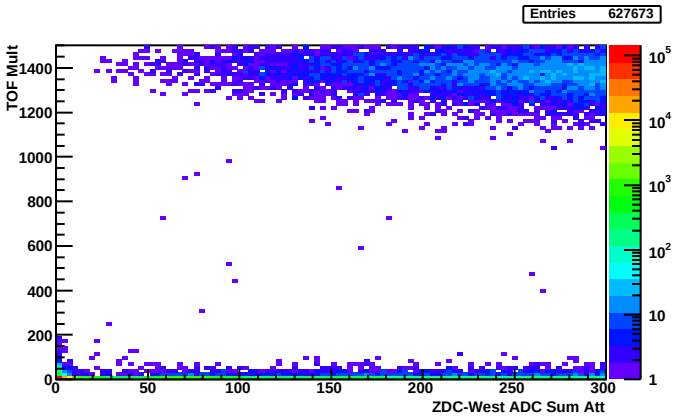

Entries 627673
TOF Mult
BBC-L-East ADC Sum

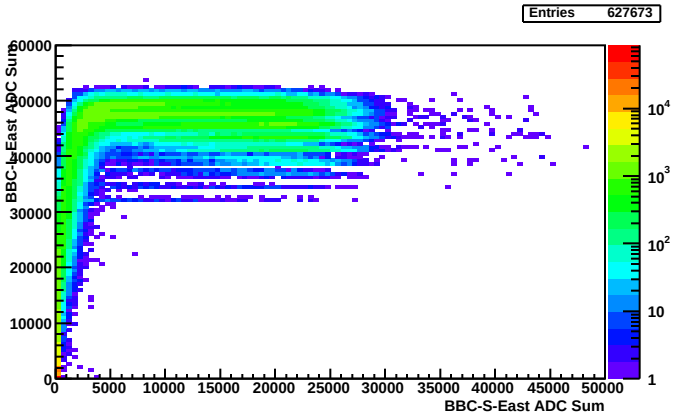

Entries 627673
TOF Mult
BBC-L-West ADC Sum

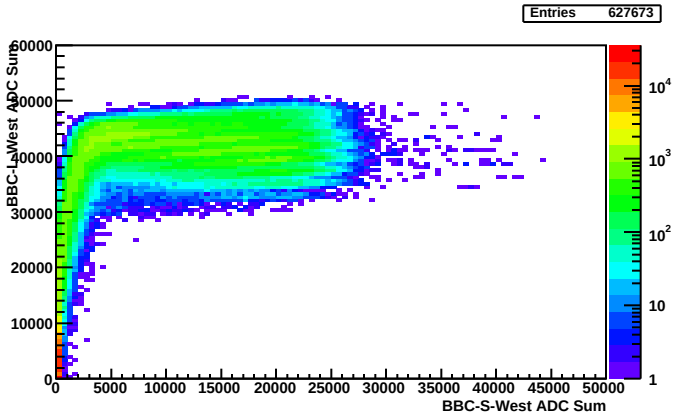

Entries 627673
TOF Mult
BBC-S-East ADC Sum

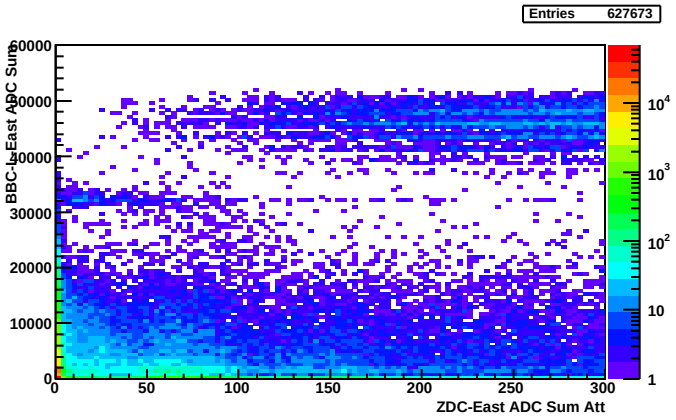

Entries 627673
TOF Mult
BBC-S-West ADC Sum

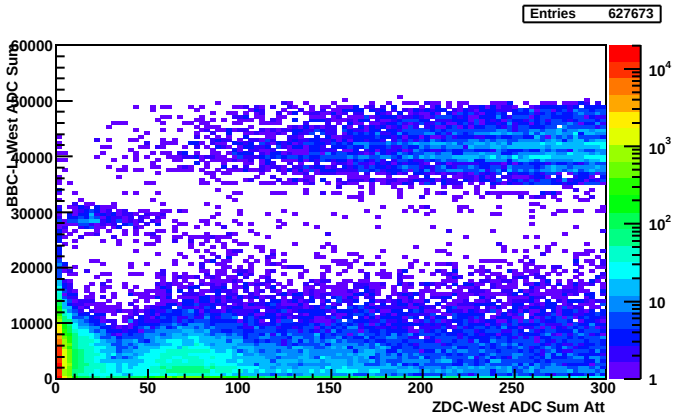

Entries 627673
TOF Mult
ZDC-East ADC Sum Att

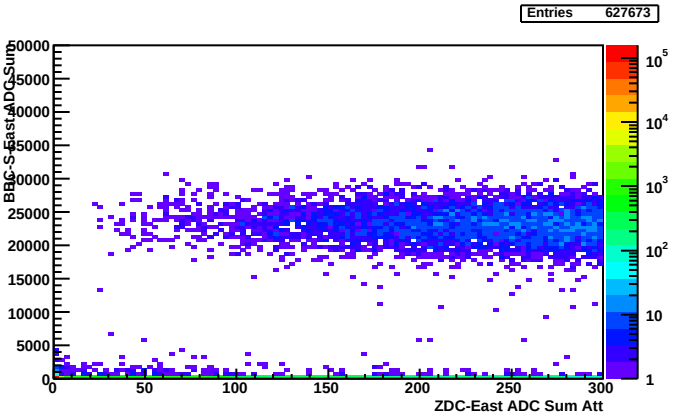

Entries 627673
TOF Mult
ZDC-West ADC Sum Att

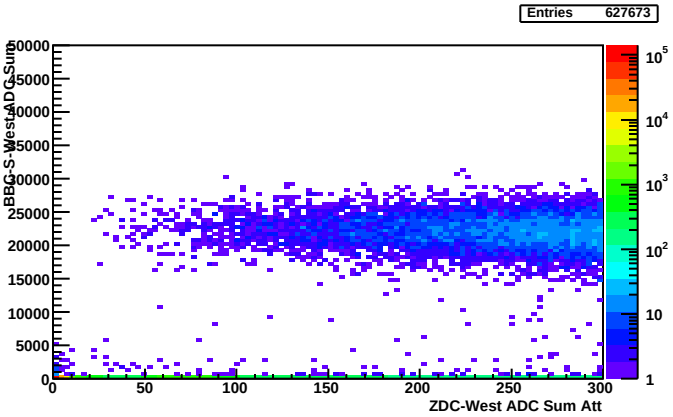

Entries 627673
BBC-L-East ADC Sum
BBC-S-East ADC Sum

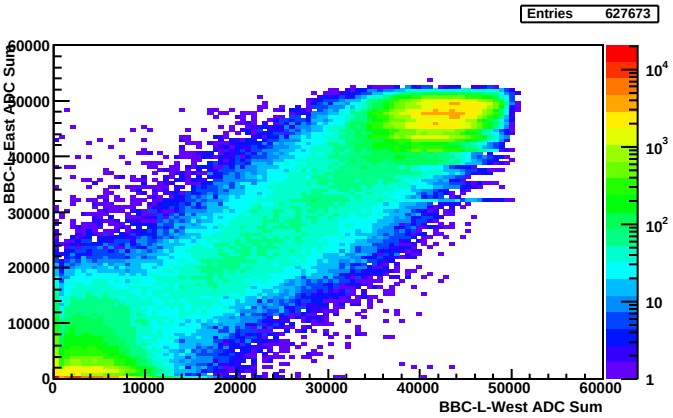

Entries 627673
BBC-L-West ADC Sum
BBC-S-West ADC Sum

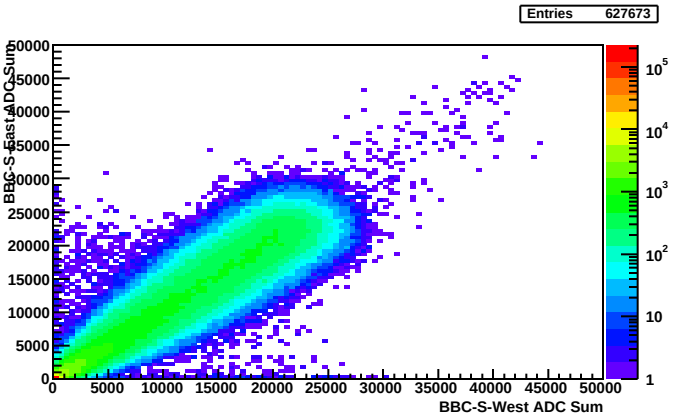

Entries 627673
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

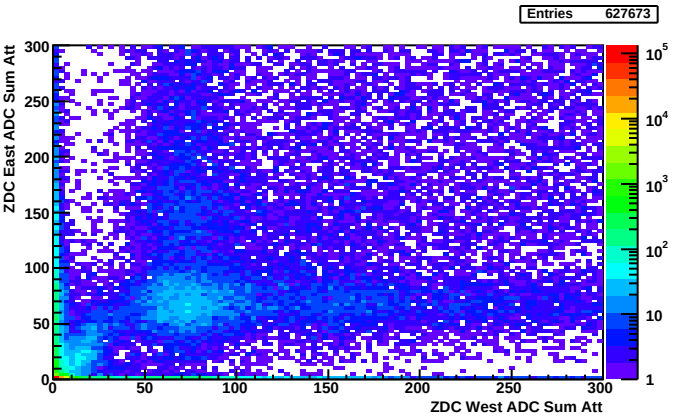

Entries 627673
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

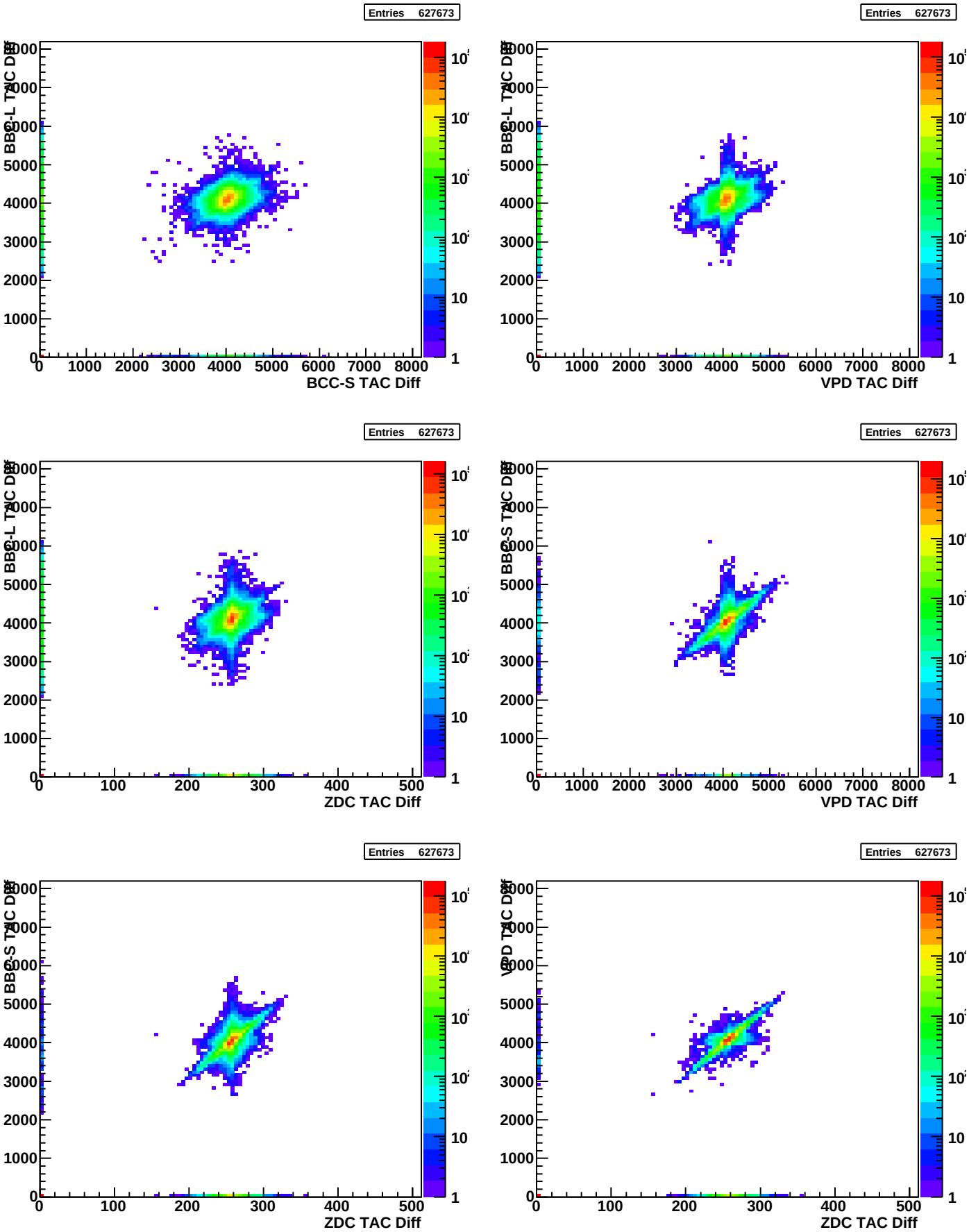

Entries 627673
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

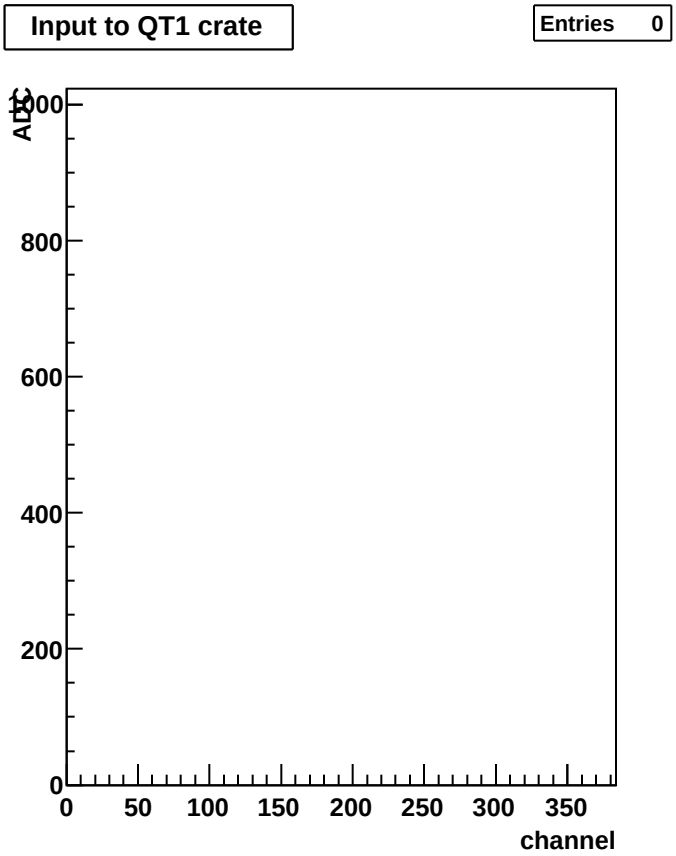

Entries 627673
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

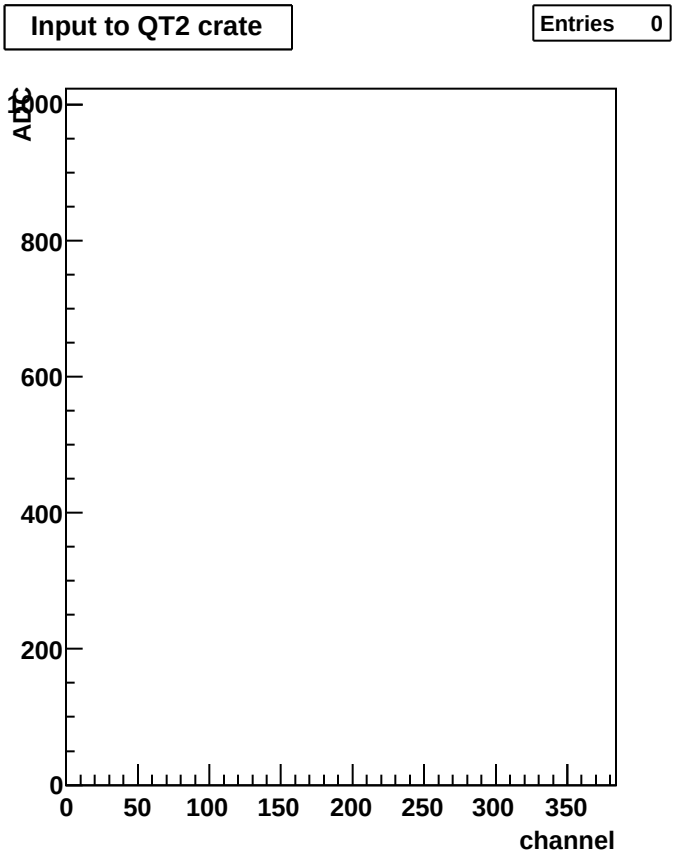

Entries 627673
BBC-L-East ADC Sum
BBC-L-West ADC Sum

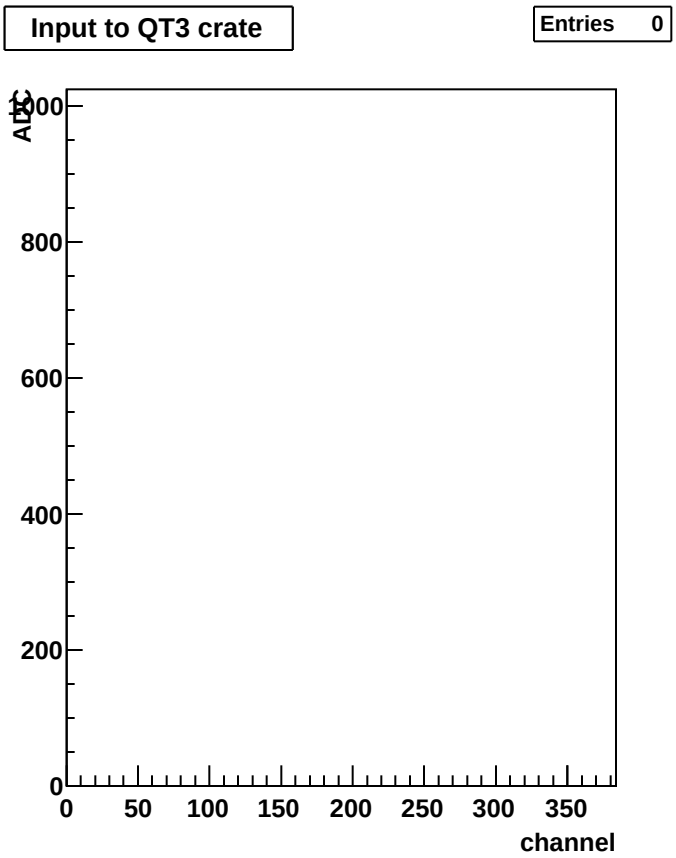

Entries 627673
BBC-S-East ADC Sum
BBC-S-West ADC Sum

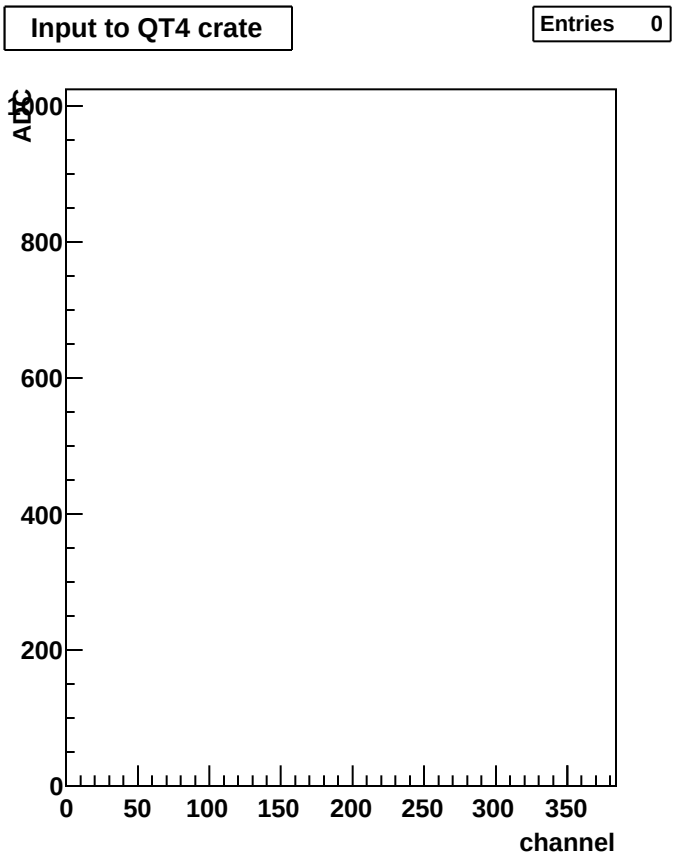

Entries 627673
ZDC East ADC Sum Att
ZDC West ADC Sum Att

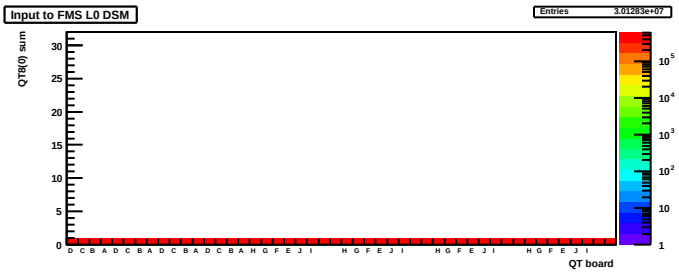

Entries 627673
BBC-L TAC Diff
BCC-S TAC Diff
Entries 627673
BBC-L TAC Diff
VPD TAC Diff
Entries 627673
BBC-L TAC Diff
ZDC TAC Diff
Entries 627673
BBC-S TAC Diff
VPD TAC Diff
Entries 627673
BBC-S TAC Diff
ZDC TAC Diff
Entries 627673
VPD TAC Diff
ZDC TAC Diff

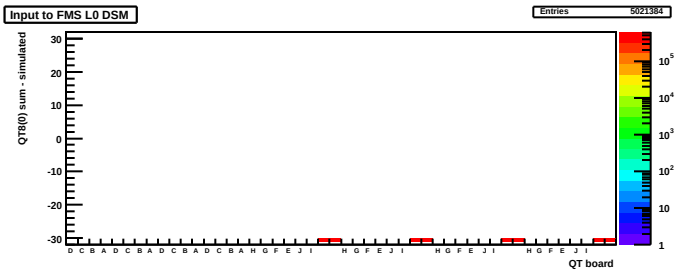

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

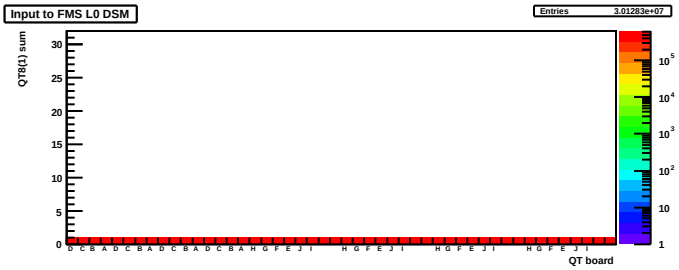

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

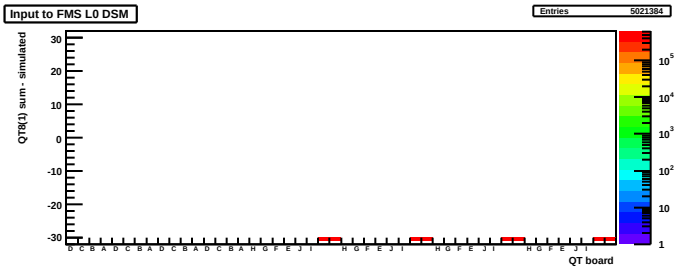

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

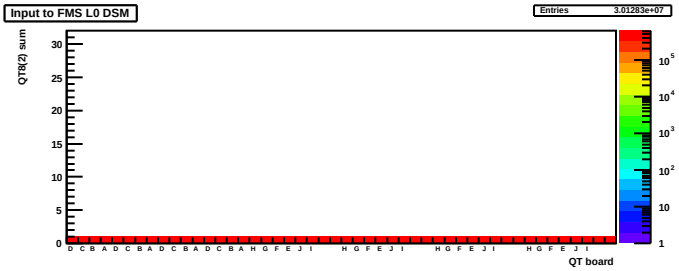

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

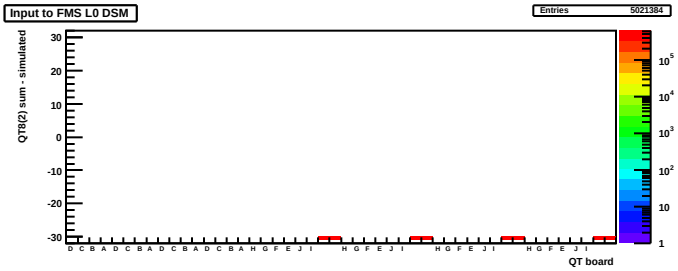

Input to FMS L0 DSM
Entries 3.01283e+07
QT8(0) sum
QT board

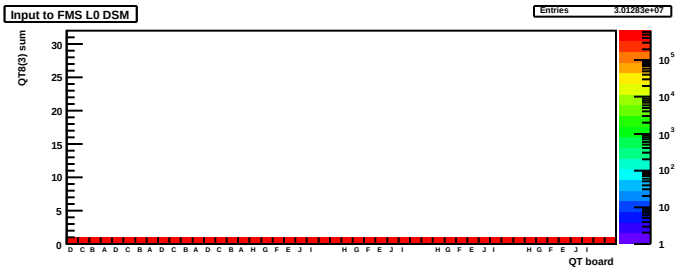

Input to FMS L0 DSM
Entries 5021384
QT8(0) sum - simulated
QT board

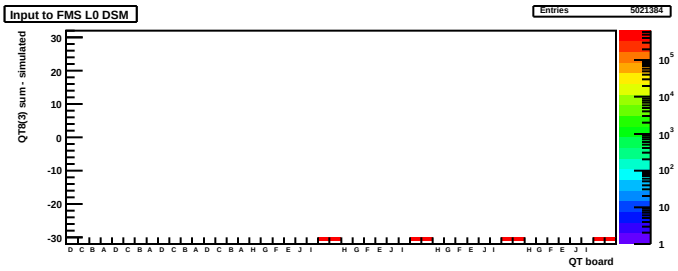

Input to FMS L0 DSM
Entries 3.01283e+07
QT8(1) sum
QT board

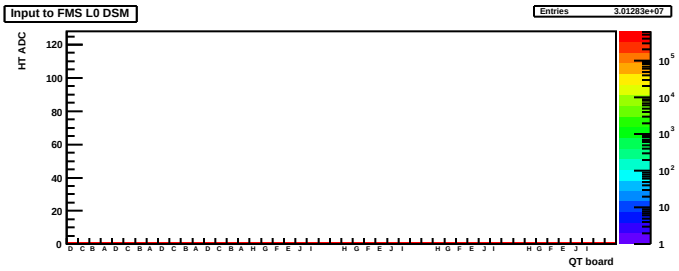

Input to FMS L0 DSM
Entries 5021384
QT8(1) sum - simulated
QT board

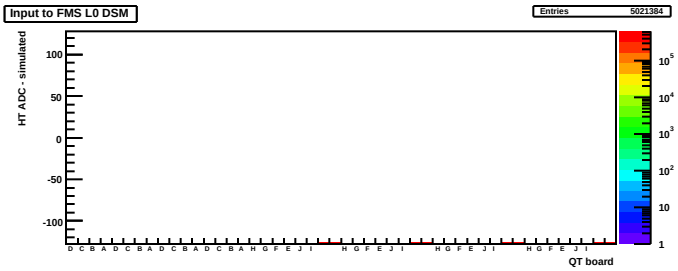

Input to FMS L0 DSM
Entries 3.01283e+07
QT8(2) sum
QT board

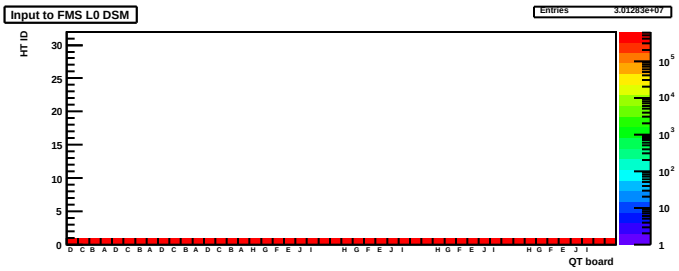

Input to FMS L0 DSM
Entries 5021384
QT8(2) sum - simulated
QT board

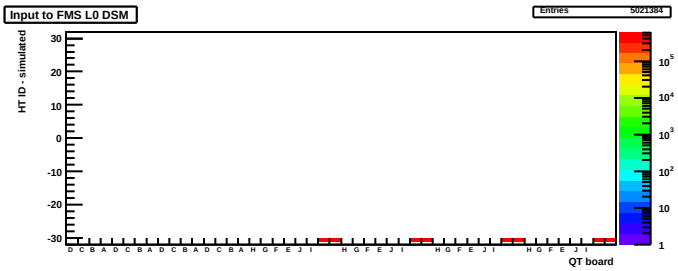

Input to FMS L0 DSM
Entries 3.01283e+07
QT8(3) sum
QT board

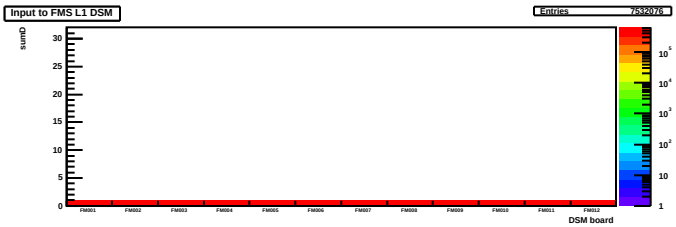

Input to FMS L0 DSM
Entries 5021384
QT8(3) sum - simulated
QT board

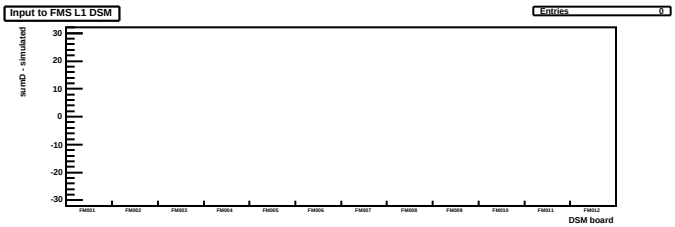

Input to FMS L0 DSM
Entries 3.01283e+07
HT ADC
QT board

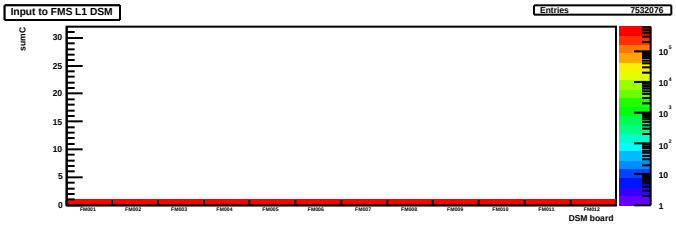

Input to FMS L0 DSM
Entries 5021384
HT ADC - simulated
QT board

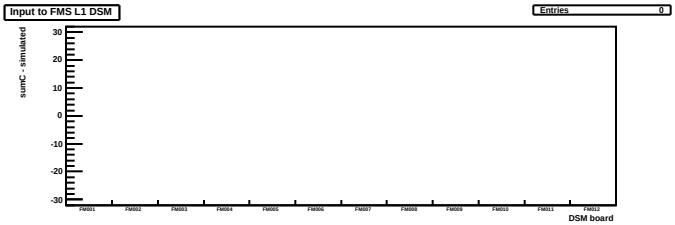

Input to FMS L0 DSM
Entries 3.01283e+07
HT ID
QT board

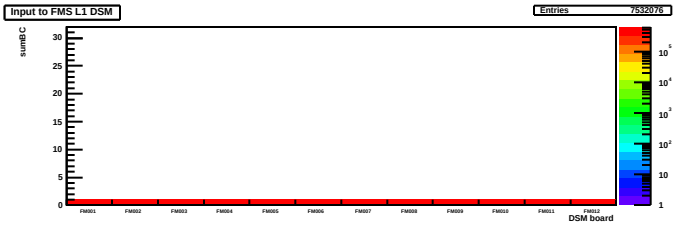

Input to FMS L0 DSM
Entries 5021384
HT ID - simulated
QT board

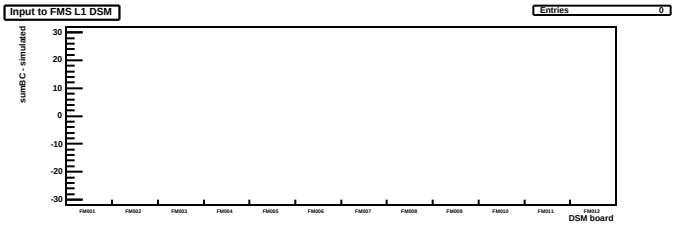

Input to FMS L1 DSM
Entries 7532076
sumD
DSM board

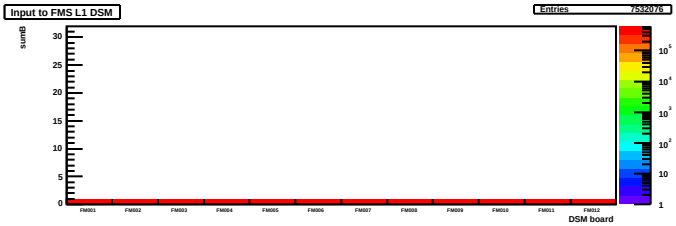

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

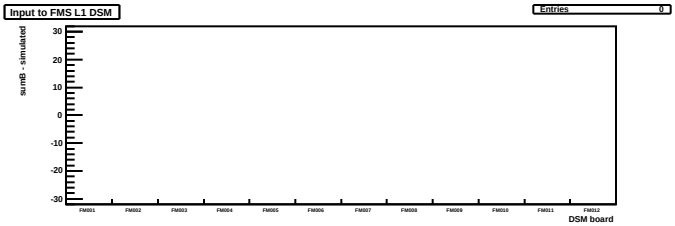

Input to FMS L1 DSM
Entries 7532076
sumC
DSM board

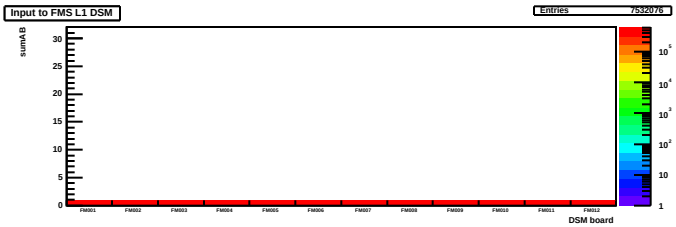

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

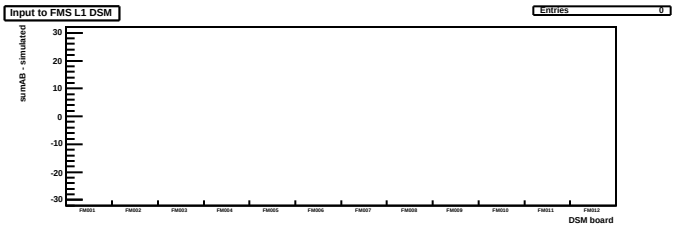

Input to FMS L1 DSM
Entries 7532076
sumBC
DSM board

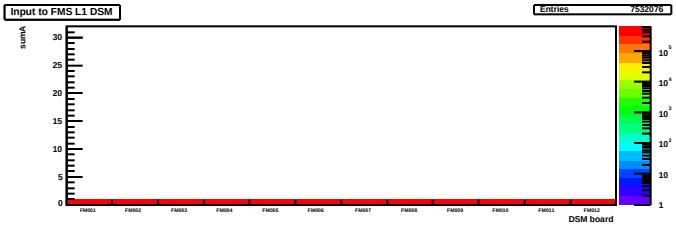

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

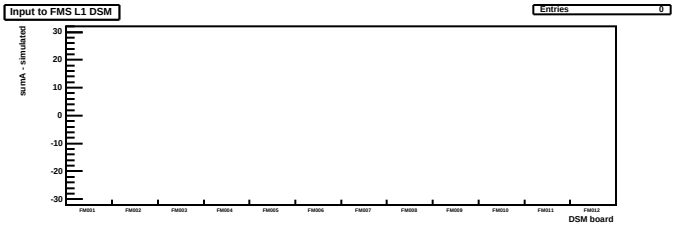

Input to FMS L1 DSM
Entries 7532076
sumB
DSM board

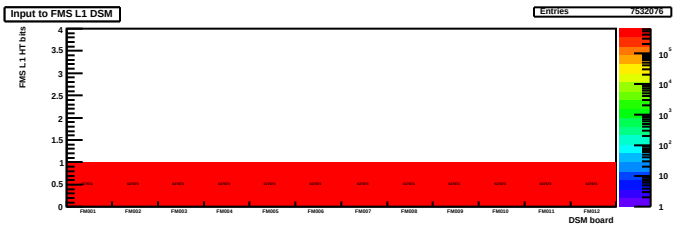

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

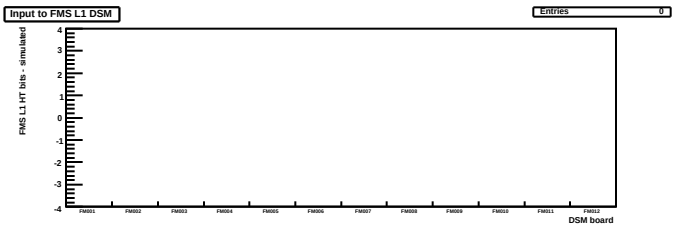

Input to FMS L1 DSM
Entries 7532076
sumAB
DSM board

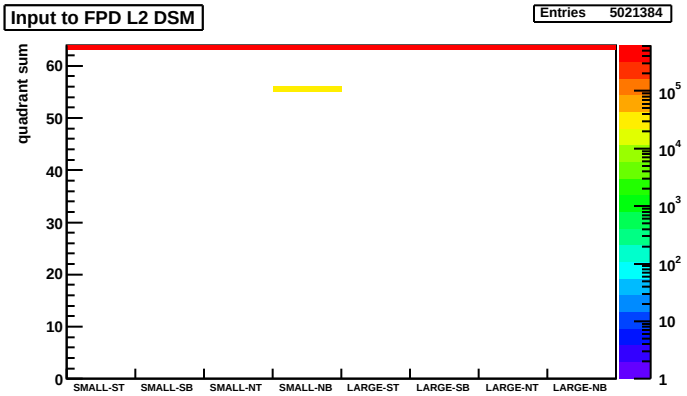

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

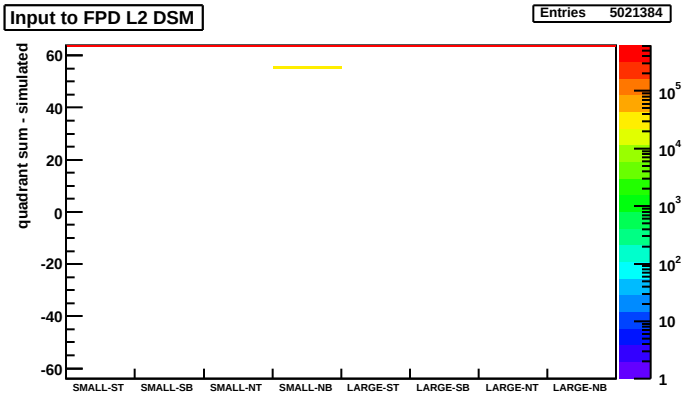

Input to FMS L1 DSM
Entries 7532076
sumA
DSM board

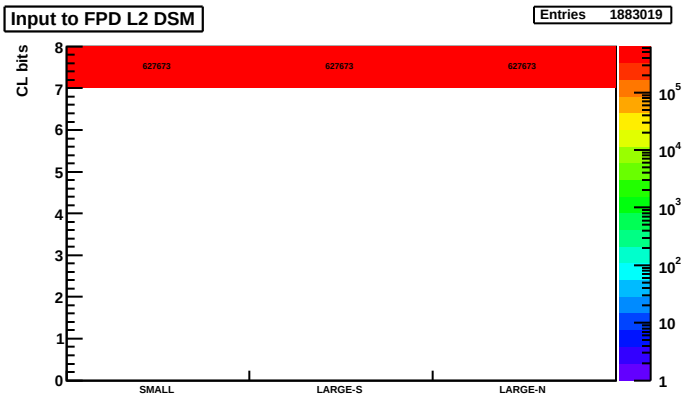

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

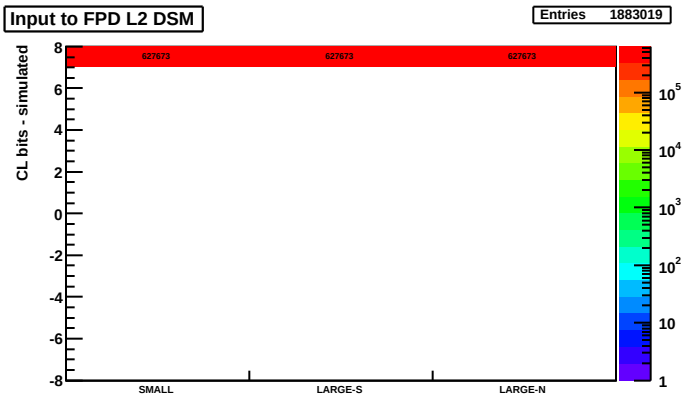

Input to FMS L1 DSM
Entries 7532076
FMS L1 HT bits
DSM board

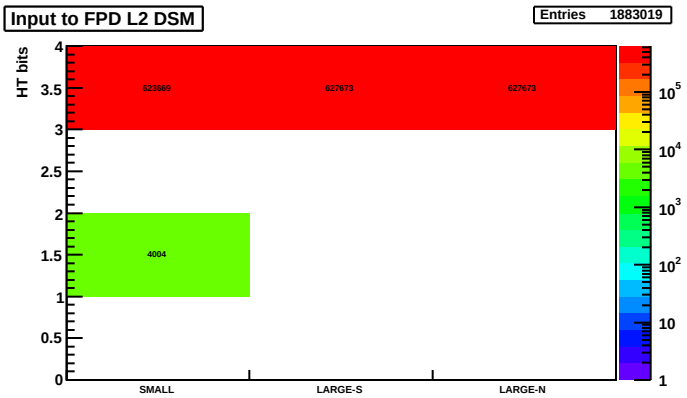

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

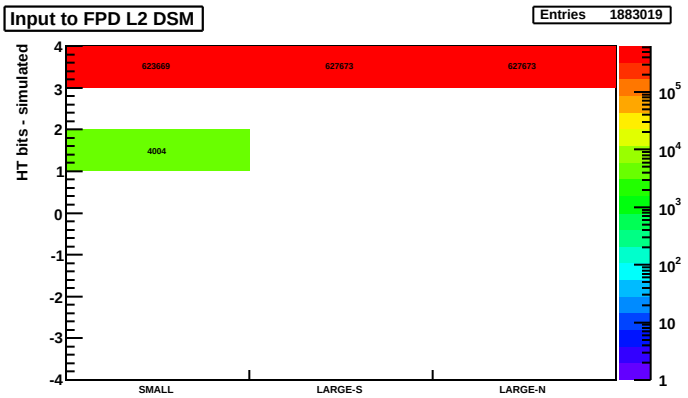

Input to FPD L2 DSM
Entries 5021384
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

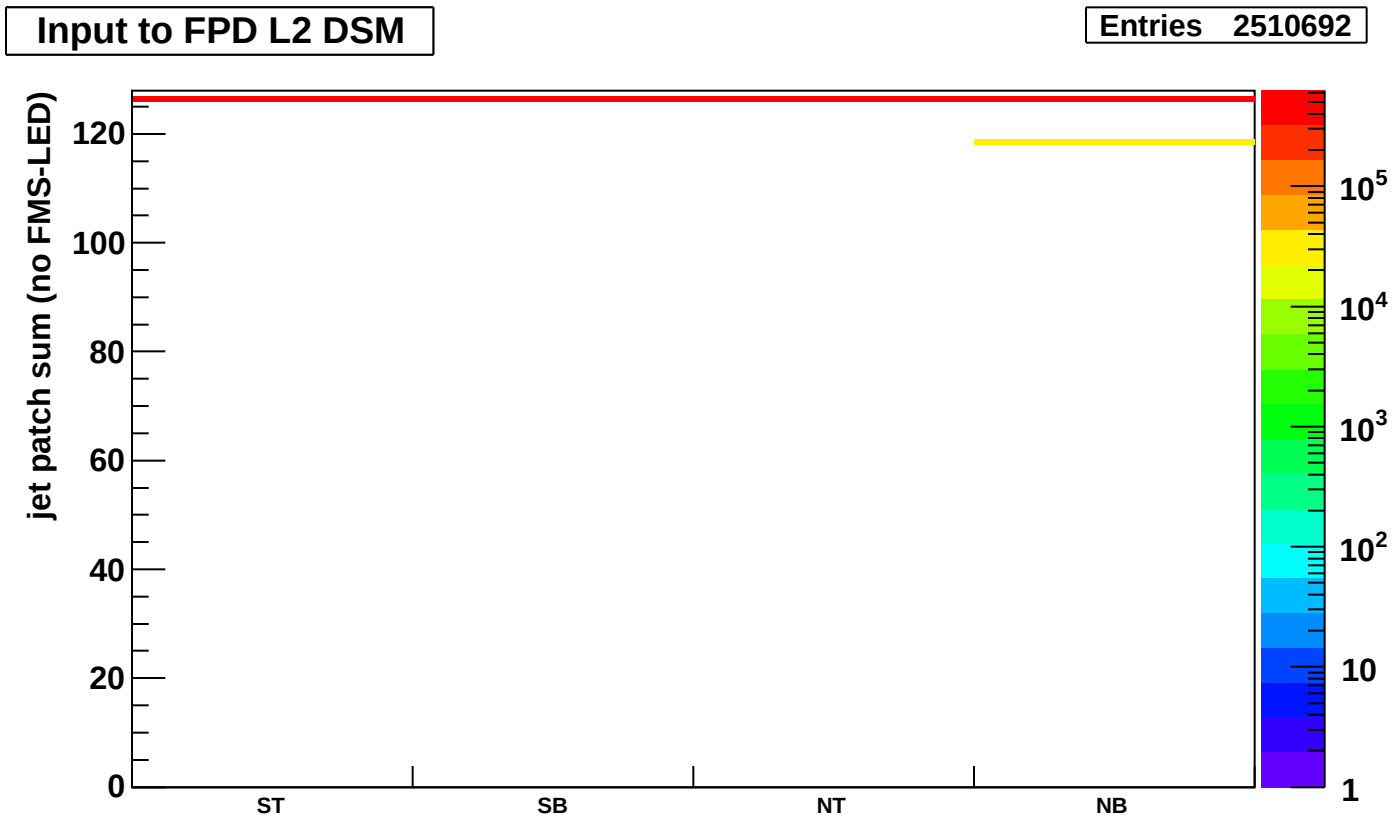

Input to FPD L2 DSM
Entries 5021384
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

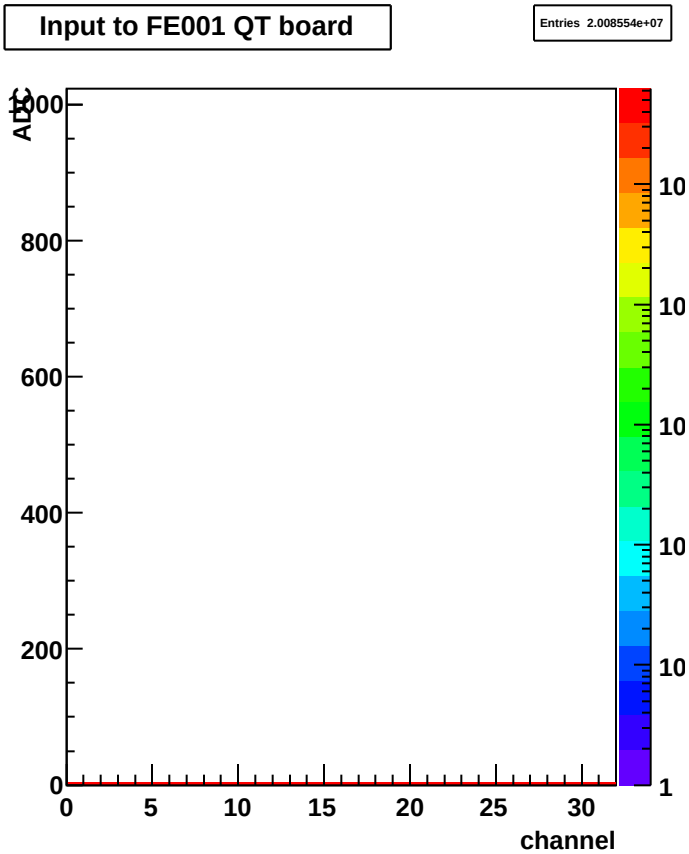

Input to FPD L2 DSM
Entries 1883019
CL bits
627673
627673
627673
SMALL
LARGE-S
LARGE-N

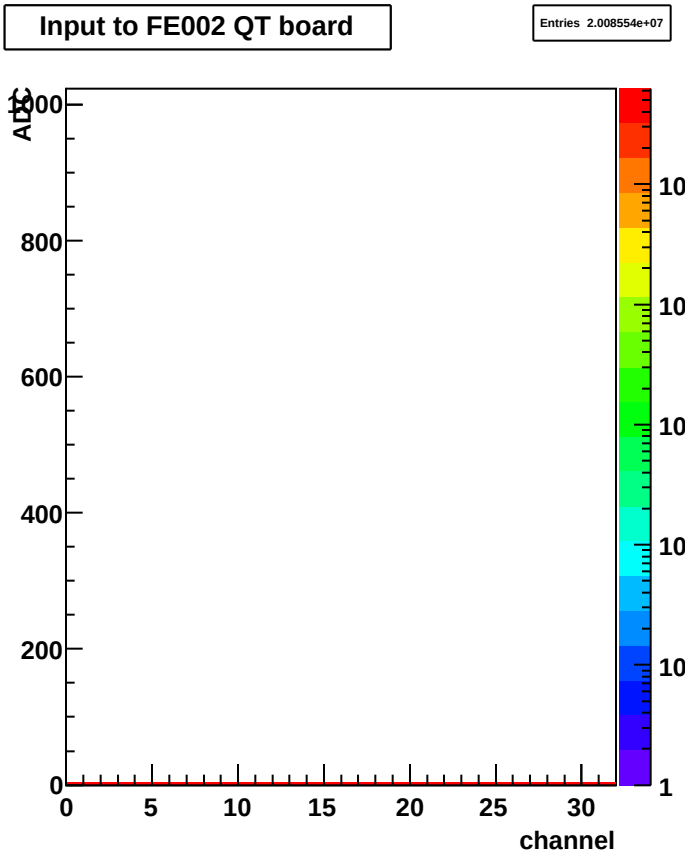

Input to FPD L2 DSM
Entries 1883019
CL bits - simulated
627673
627673
627673
SMALL
LARGE-S
LARGE-N

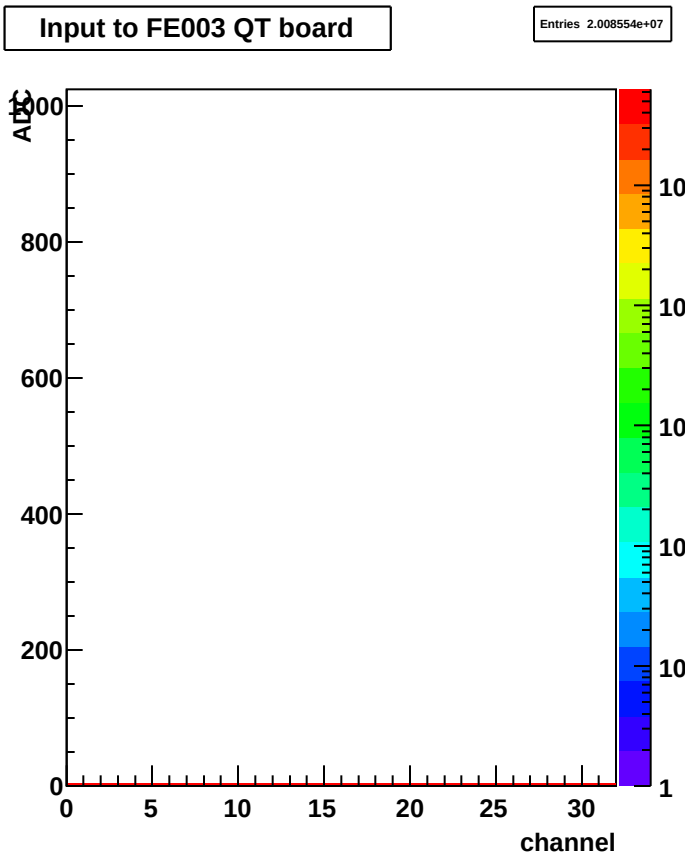

Input to FPD L2 DSM
Entries 1883019
HT bits
623669
627673
627673
4004
SMALL
LARGE-S
LARGE-N

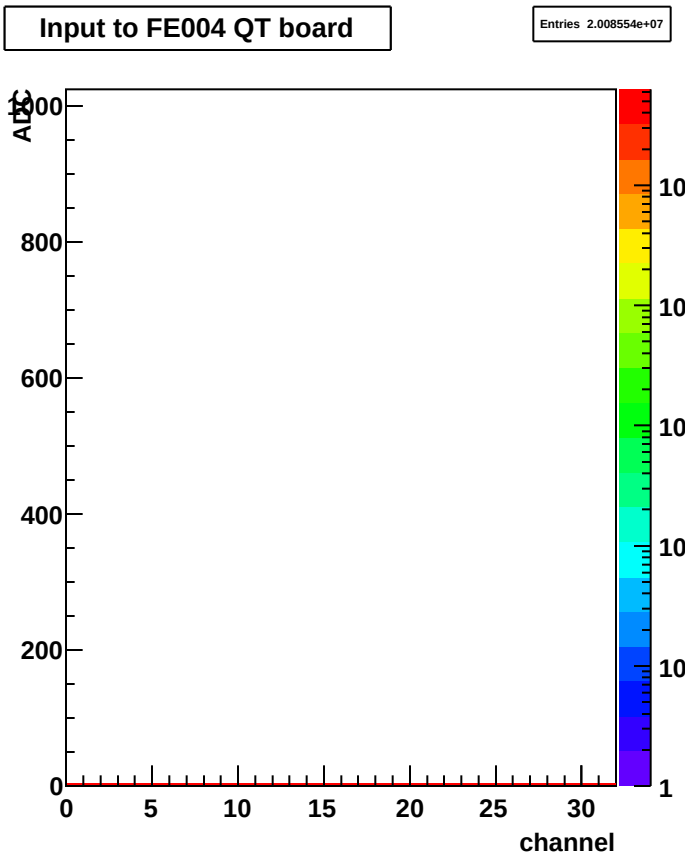

Input to FPD L2 DSM
Entries 1883019
HT bits - simulated
623669
627673
627673
4004
SMALL
LARGE-S
LARGE-N

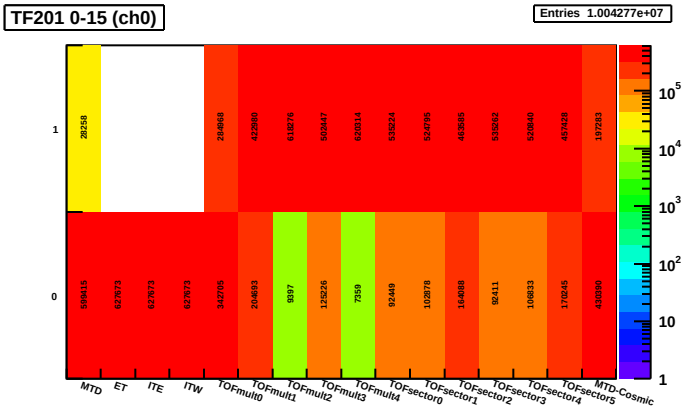

Input to FPD L2 DSM
Entries 2510692
jet patch sum (no FMS-LED)
120
100
80
60
40
20
0
ST
SB
NT
NB
10^5
10^4
10^3
10^2
10
1

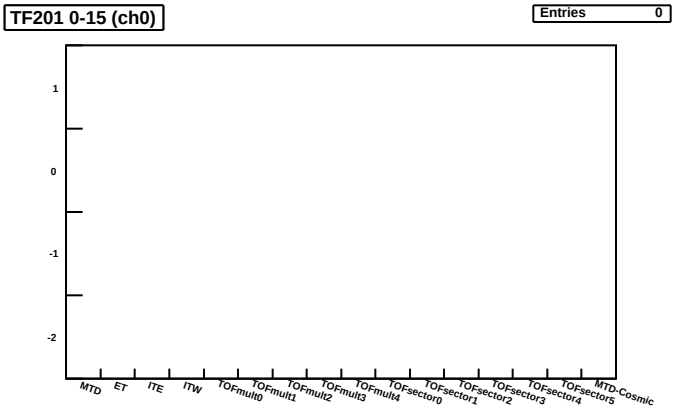

Input to FE001 QT board
Entries 2.008554e+07
ADC
channel

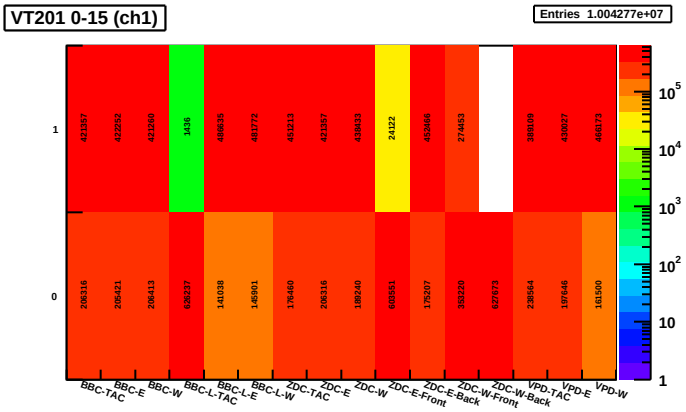

Input to FE002 QT board
Entries 2.008554e+07
ADC
channel

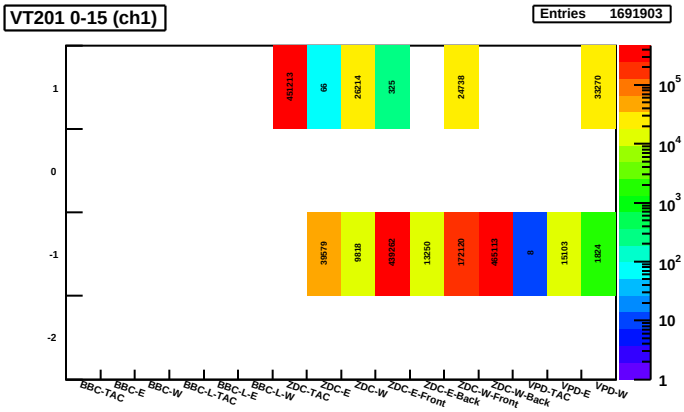

Input to FE003 QT board
Entries 2.008554e+07
ADC
channel

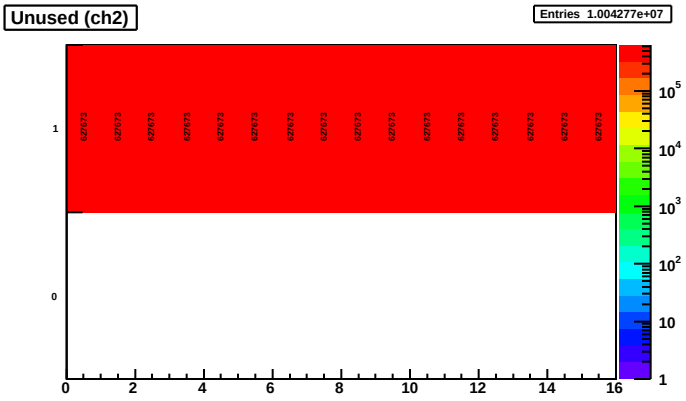

Input to FE004 QT board
Entries 2.008554e+07
ADC
channel

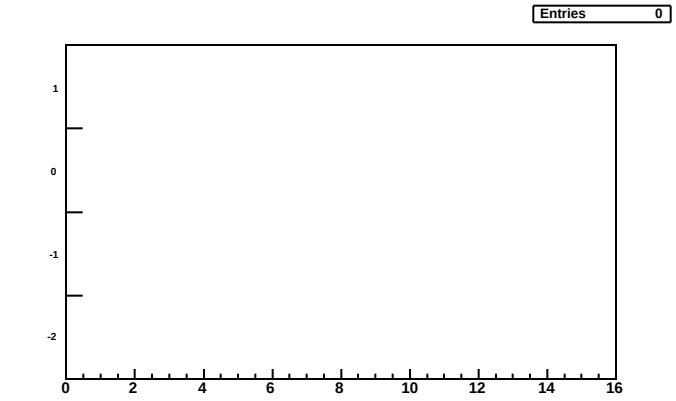

TF201 0-15 (ch0)
Entries 1.004277e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

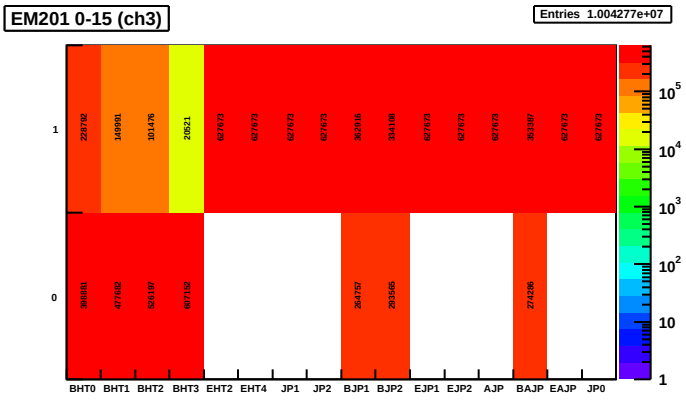

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

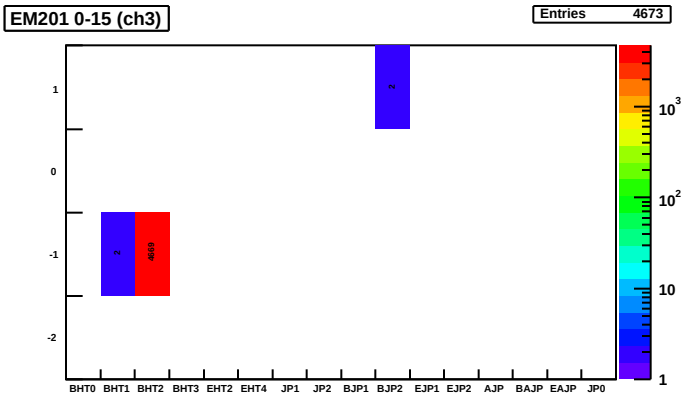

VT201 0-15 (ch1)
Entries 1.004277e+07
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 1691903
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

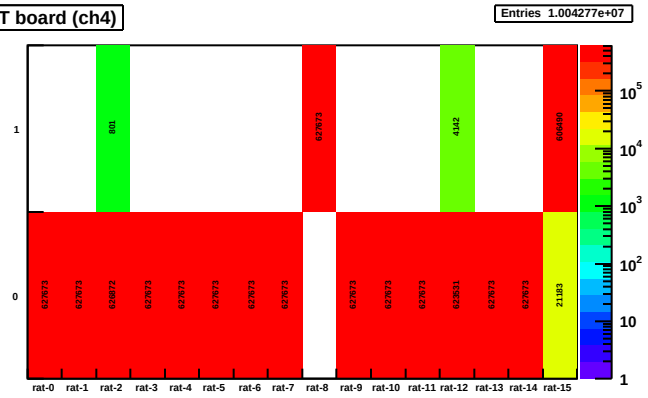

Unused (ch2)
Entries 1.004277e+07

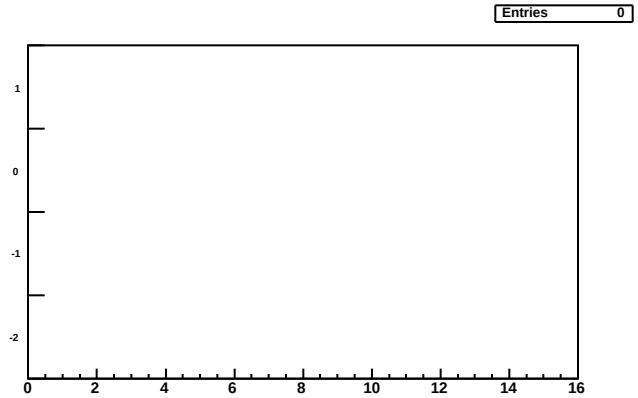

Entries 0


EM201 0-15 (ch3)
Entries 1.004277e+07
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

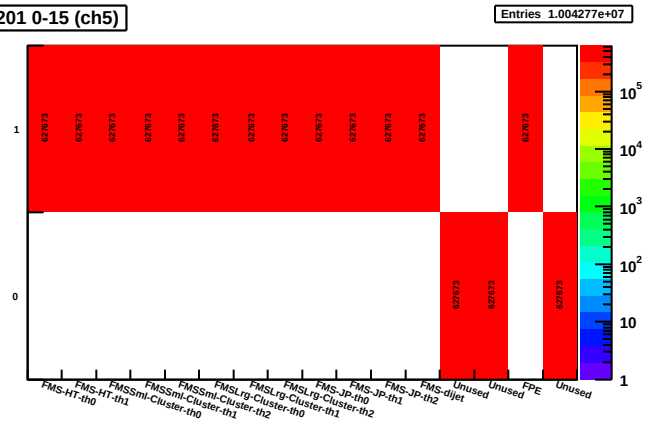

EM201 0-15 (ch3)
Entries 4673
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

RAT board (ch4)

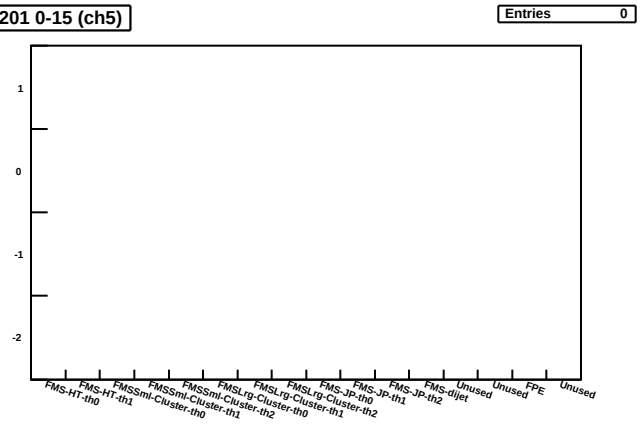




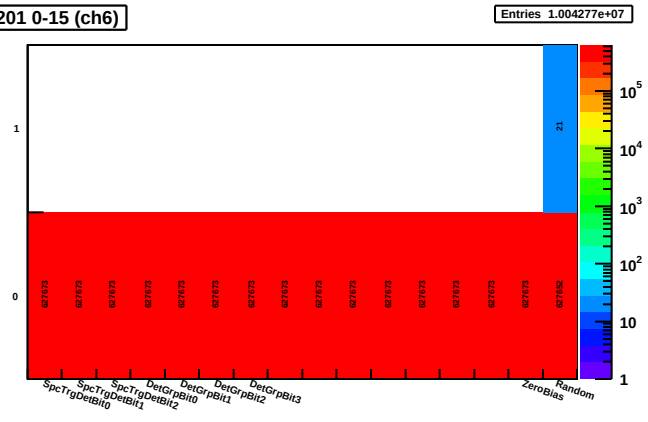
FP201 0-15 (ch5)

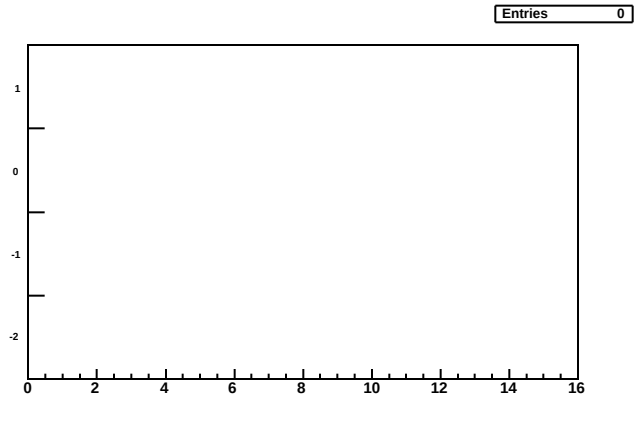
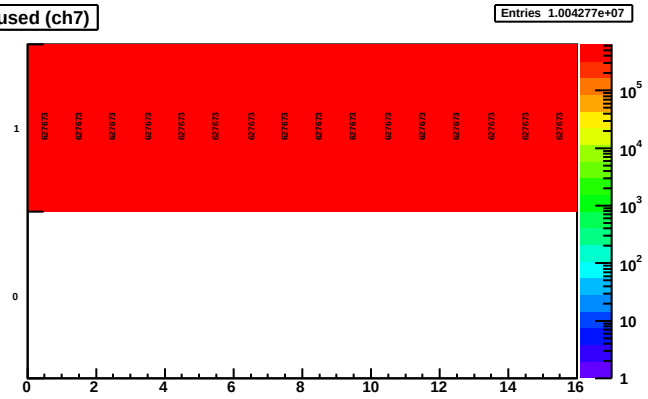
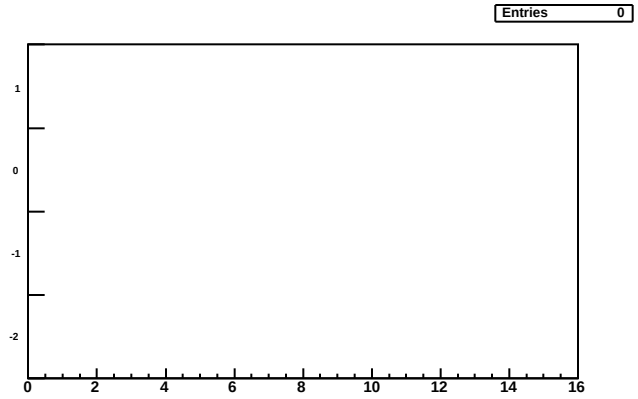


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)