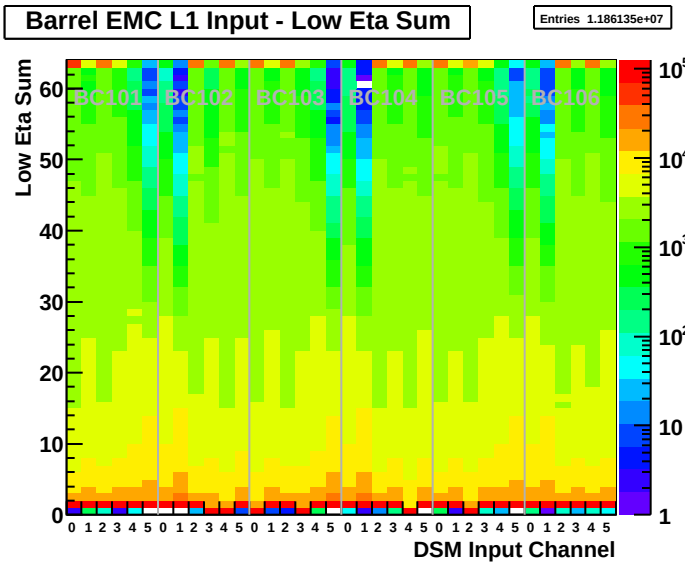
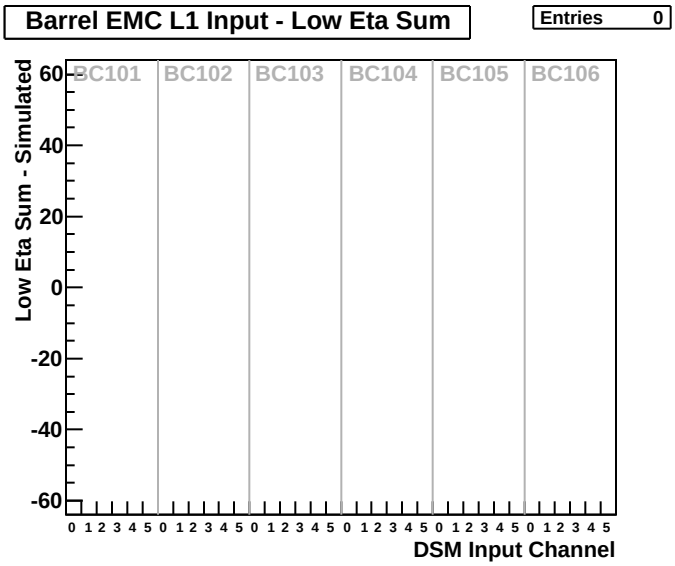
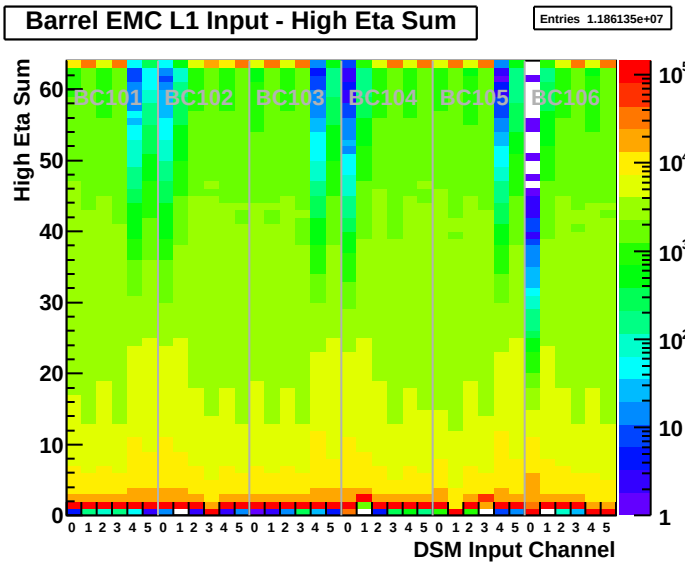
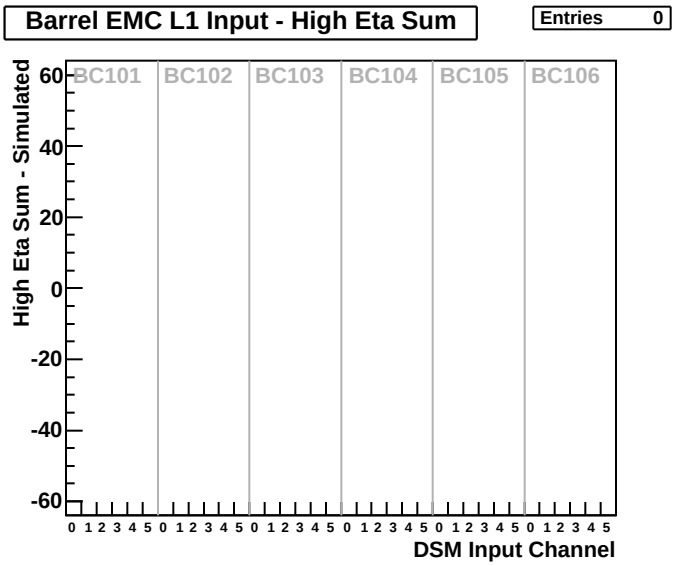
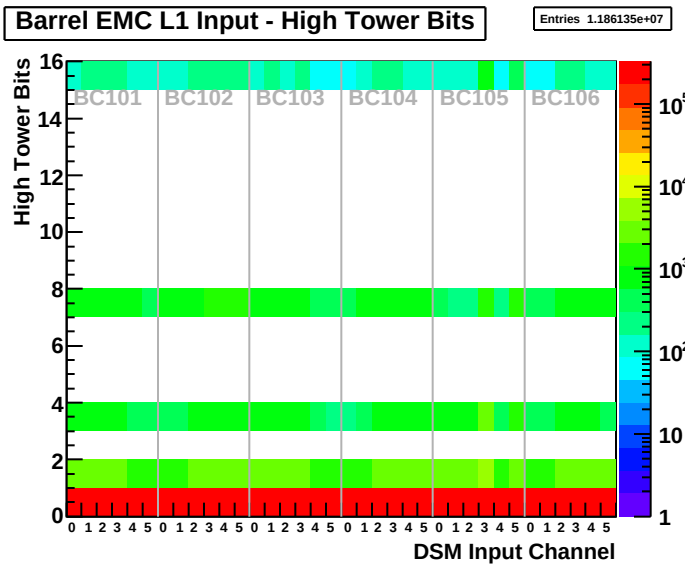
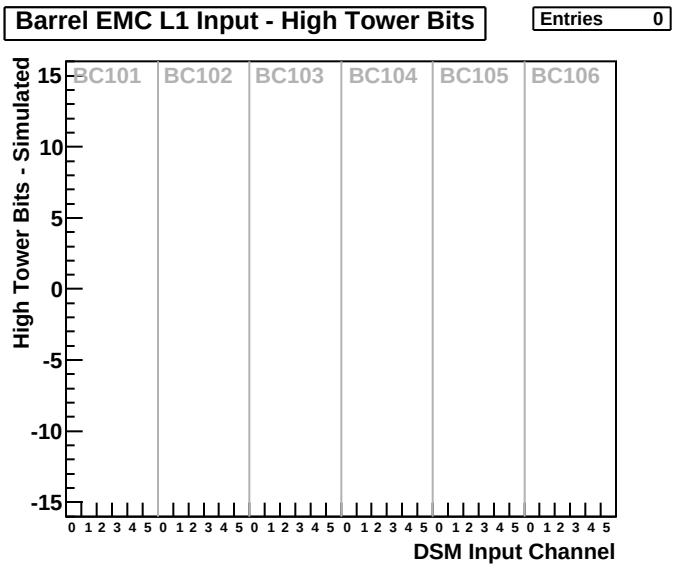

Barrel EMC L1 Input - Low Eta Sum
Entries 1.186135e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

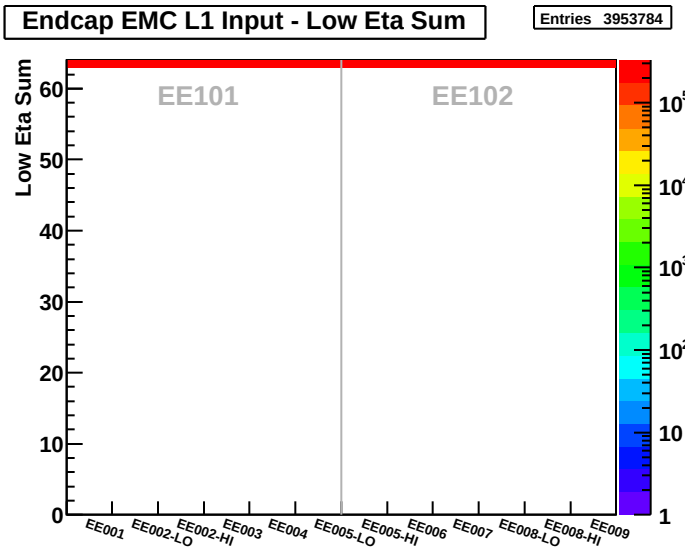

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

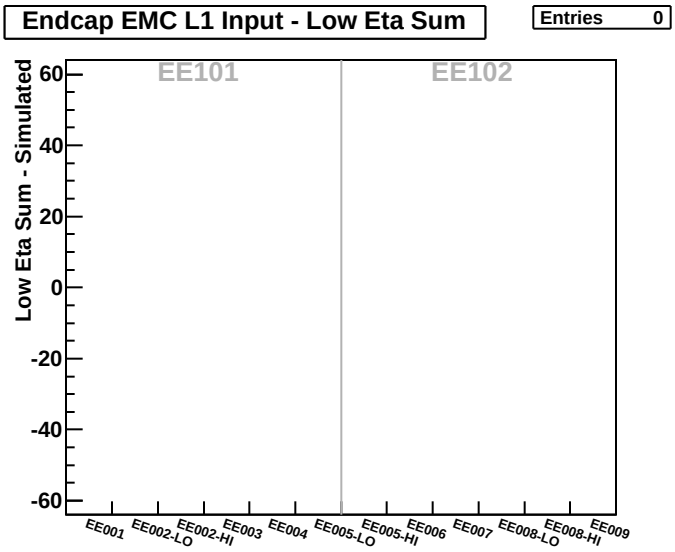

Barrel EMC L1 Input - High Eta Sum
Entries 1.186135e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

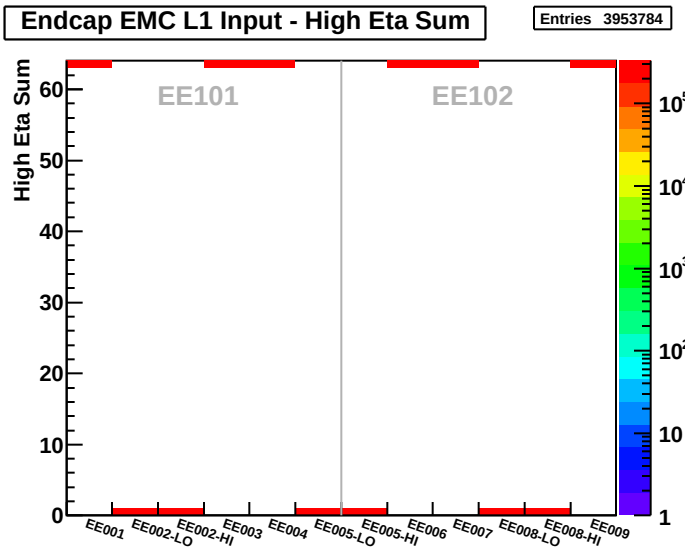

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

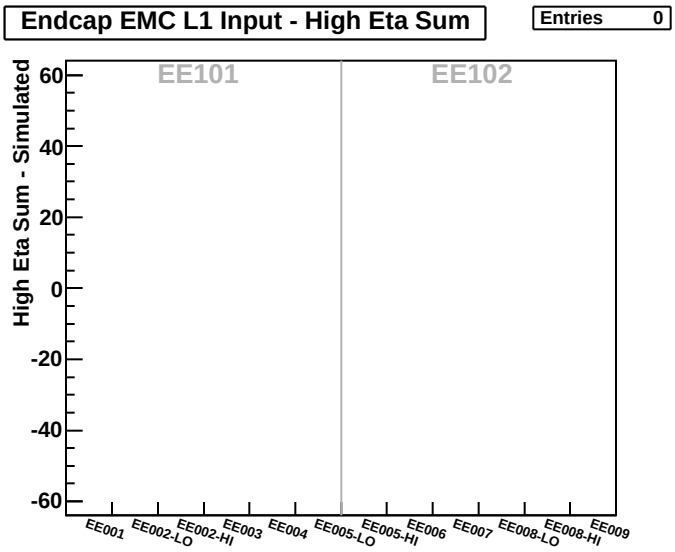

Barrel EMC L1 Input - High Tower Bits
Entries 1.186135e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

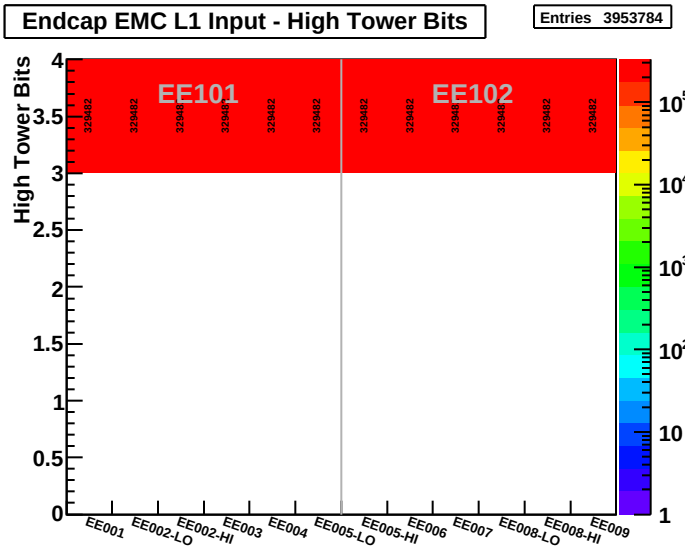

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

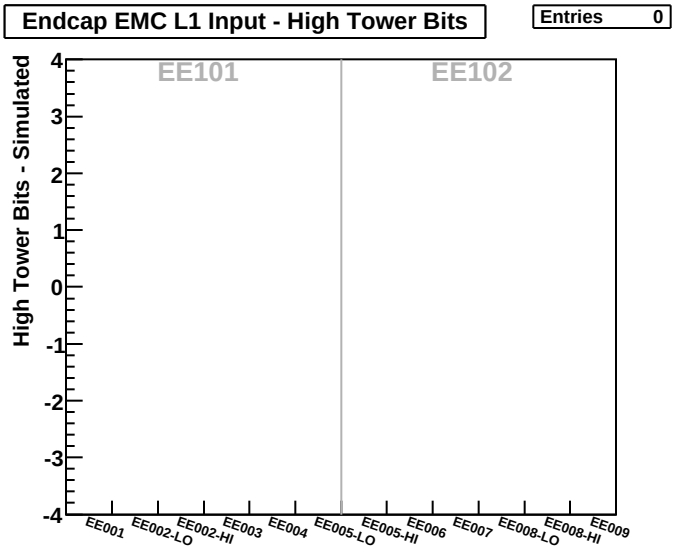

Endcap EMC L1 Input - Low Eta Sum
Entries 3953784
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

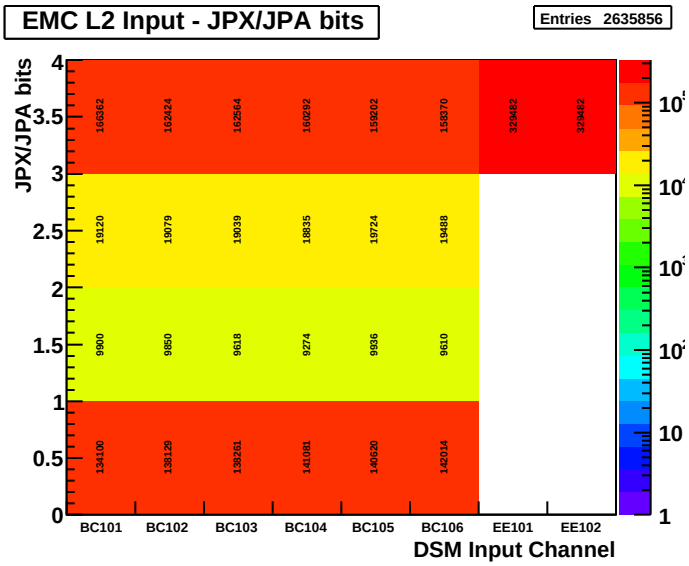

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

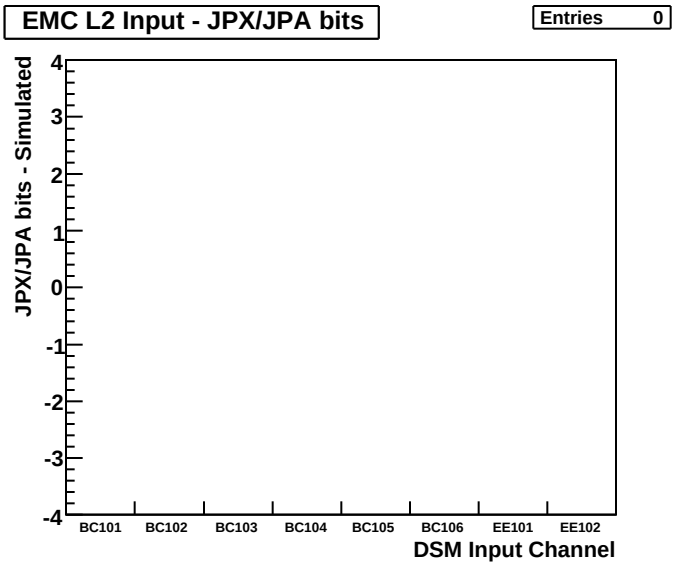

Endcap EMC L1 Input - High Eta Sum
Entries 3953784
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

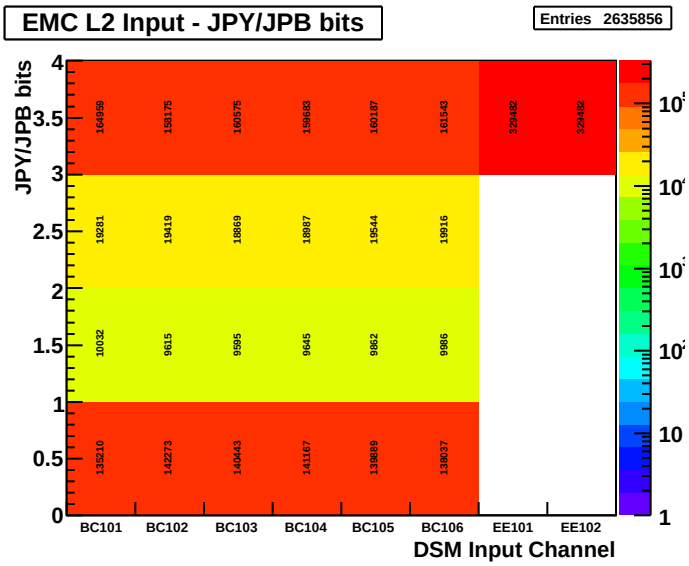

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

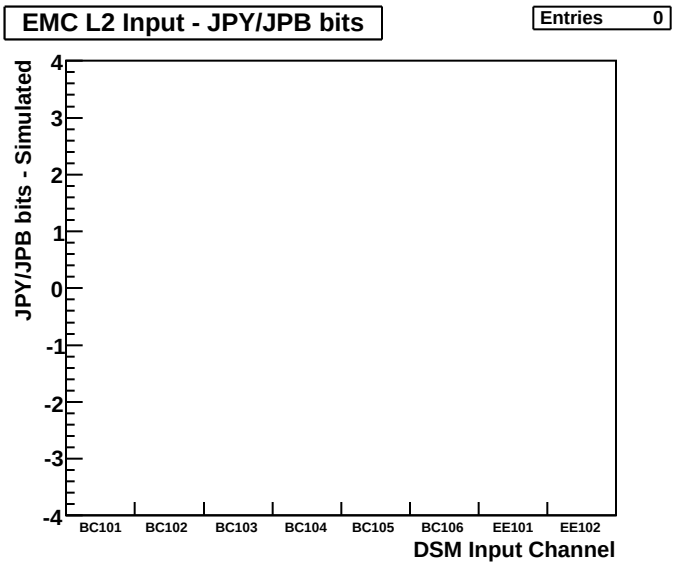

Endcap EMC L1 Input - High Tower Bits
Entries 3953784
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

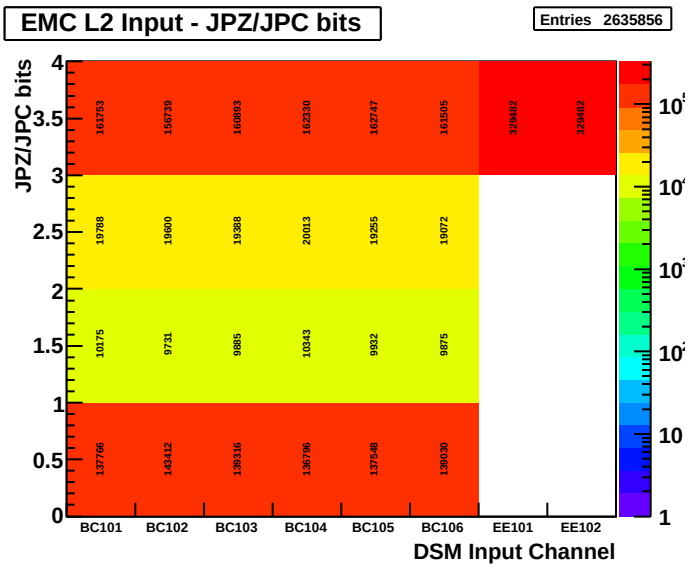

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

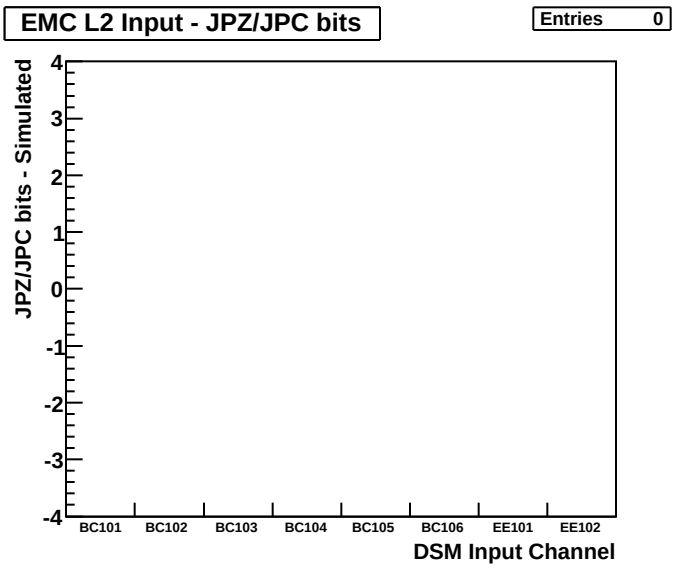

EMC L2 Input - JPX/JPA bits
Entries 2635856
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
166362 162424 162564 160292 159202 158370 329482 329482
19120 19079 19039 18835 19724 19488
9900 9850 9618 9274 9936 9610
134100 138129 138261 141081 140620 142014

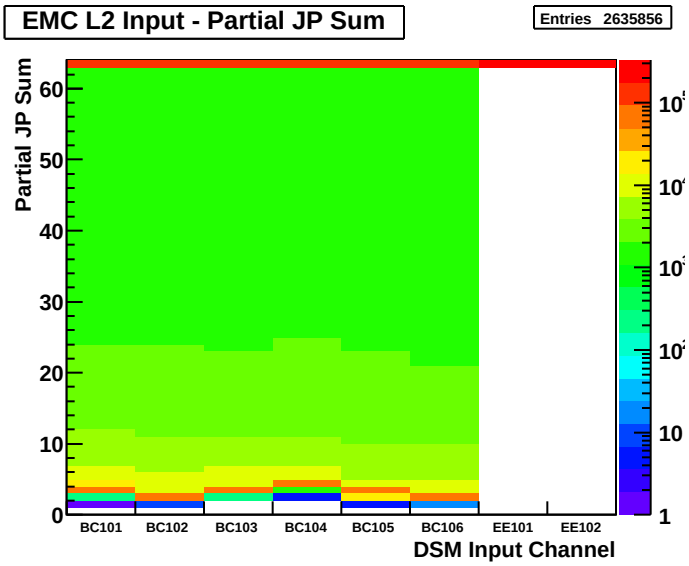

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

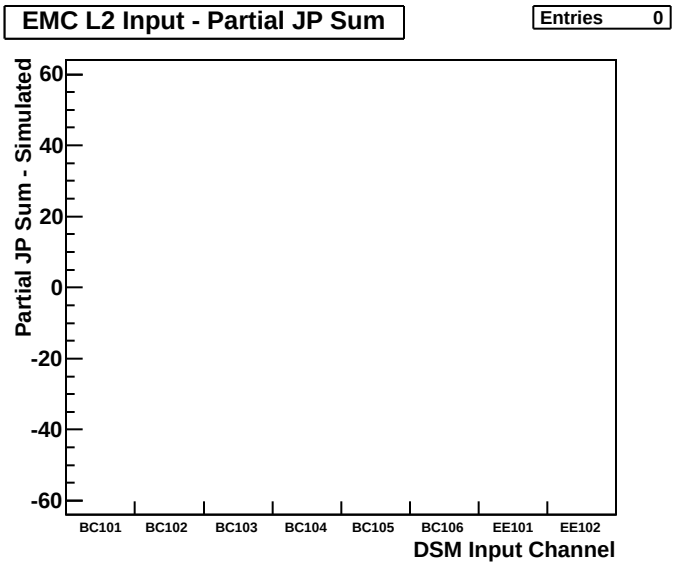

EMC L2 Input - JPY/JPB bits
Entries 2635856
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
164959 158175 160575 159683 160187 161543 329482 329482
19281 19419 18869 18987 19544 19916
10032 9615 9595 9645 9862 9986
138210 142273 140443 141167 139889 138037

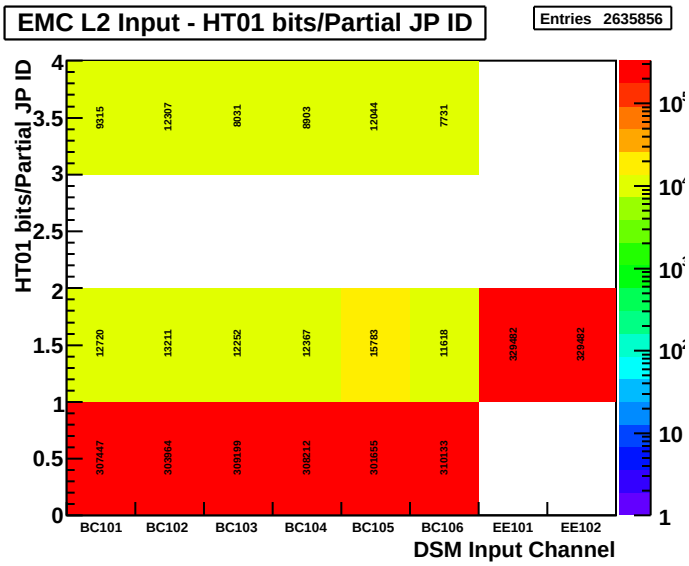

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

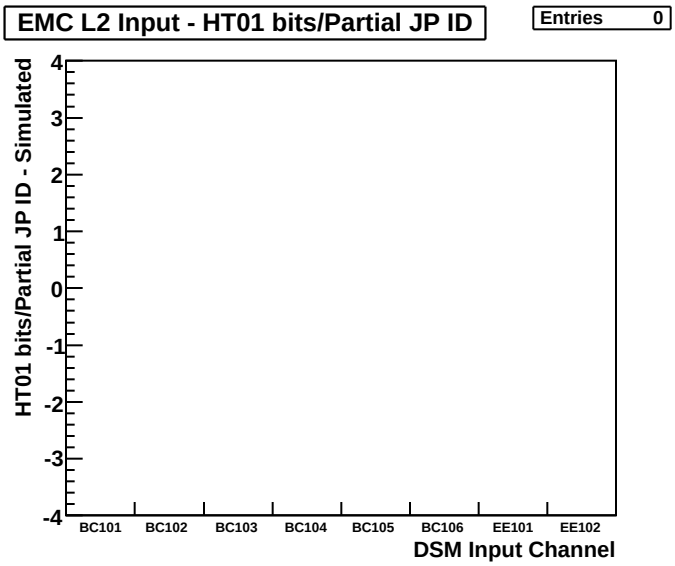

EMC L2 Input - JPZ/JPC bits
Entries 2635856
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
161753 156739 160893 162330 162747 161505 329482 329482
19788 19600 19388 20013 19255 19072
10175 9731 9885 10343 9932 9875
137766 143412 139316 136796 137548 139030

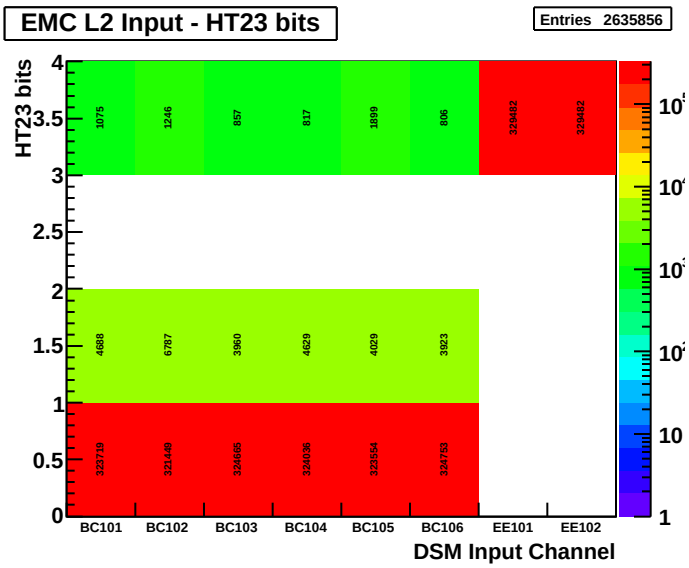

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

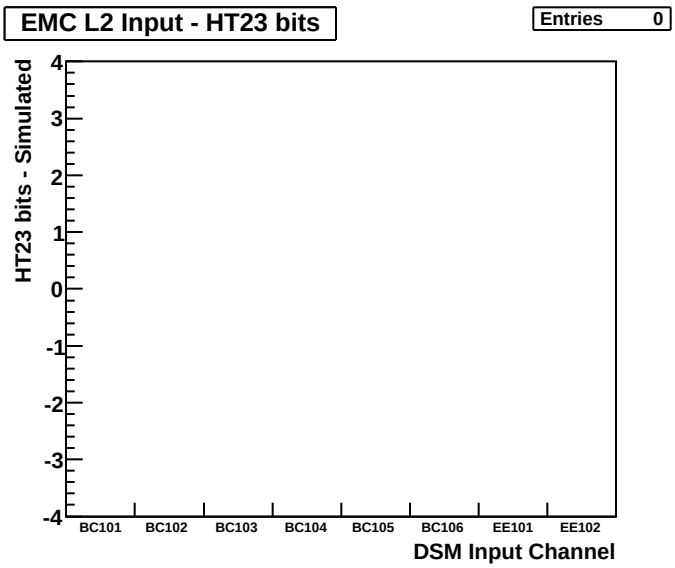

EMC L2 Input - Partial JP Sum
Entries 2635856
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

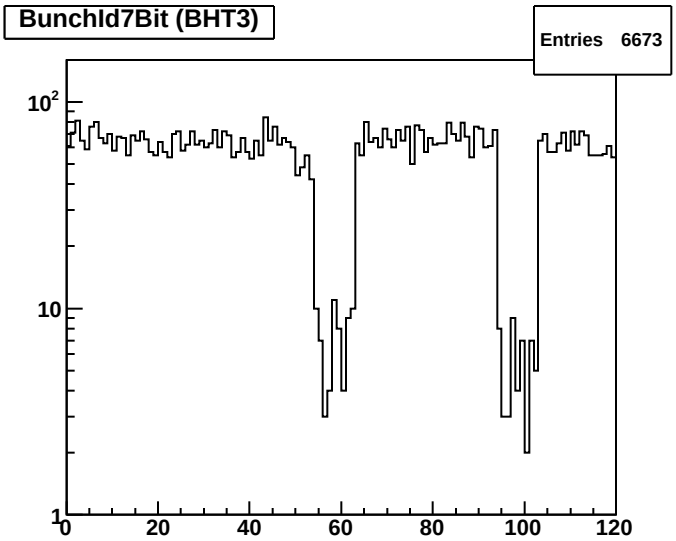

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

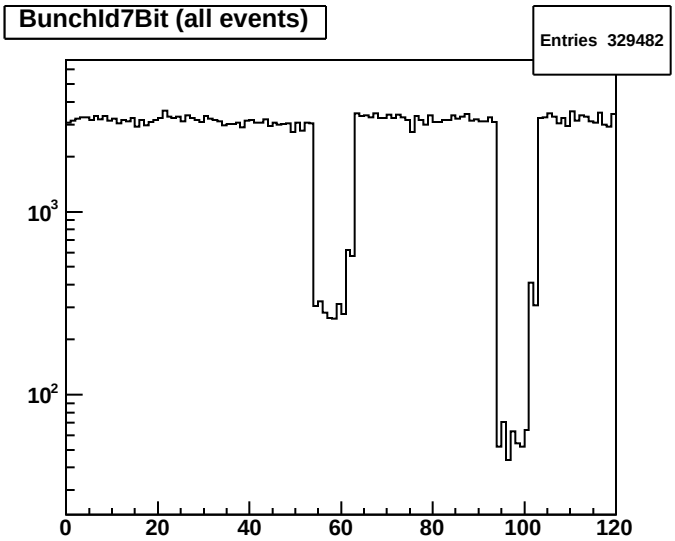

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2635856
HT01 bits/Partial JP ID
9315 12307 8031 8903 12044 7731
12720 13211 12252 12367 15783 11618 329482 329482
307447 303864 309199 308212 301655 310133
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

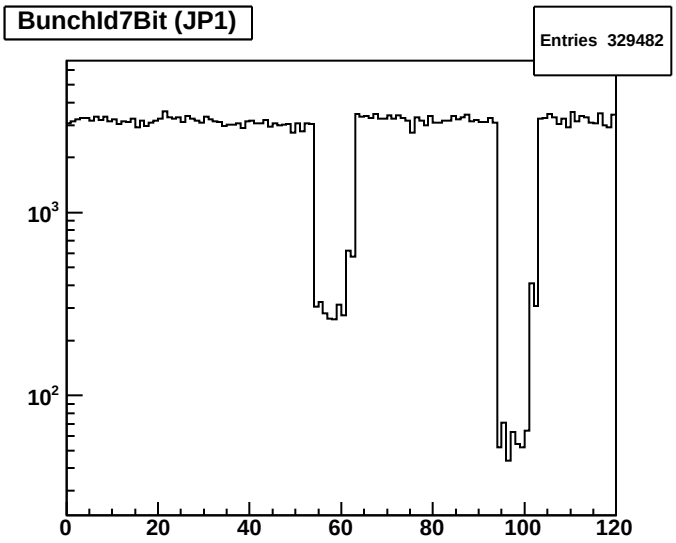

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

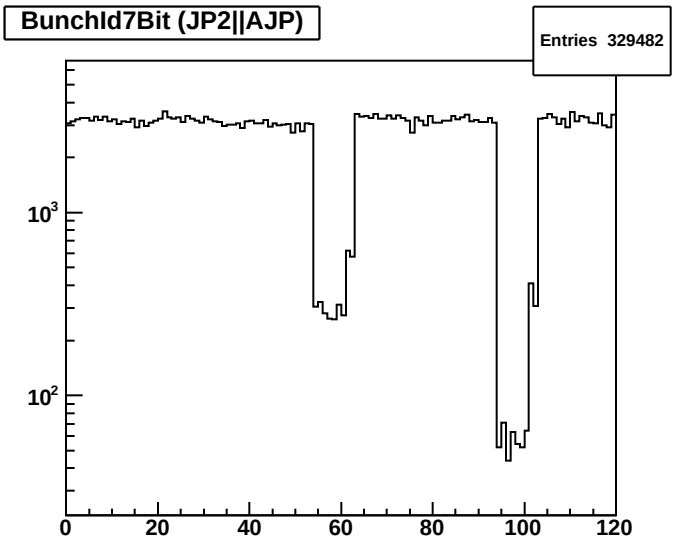

EMC L2 Input - HT23 bits
Entries 2635856
HT23 bits
1075 1246 857 817 1099 806 329482 329482
4688 6787 3960 4629 4029 3923
323719 321449 324665 324036 323554 324753
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

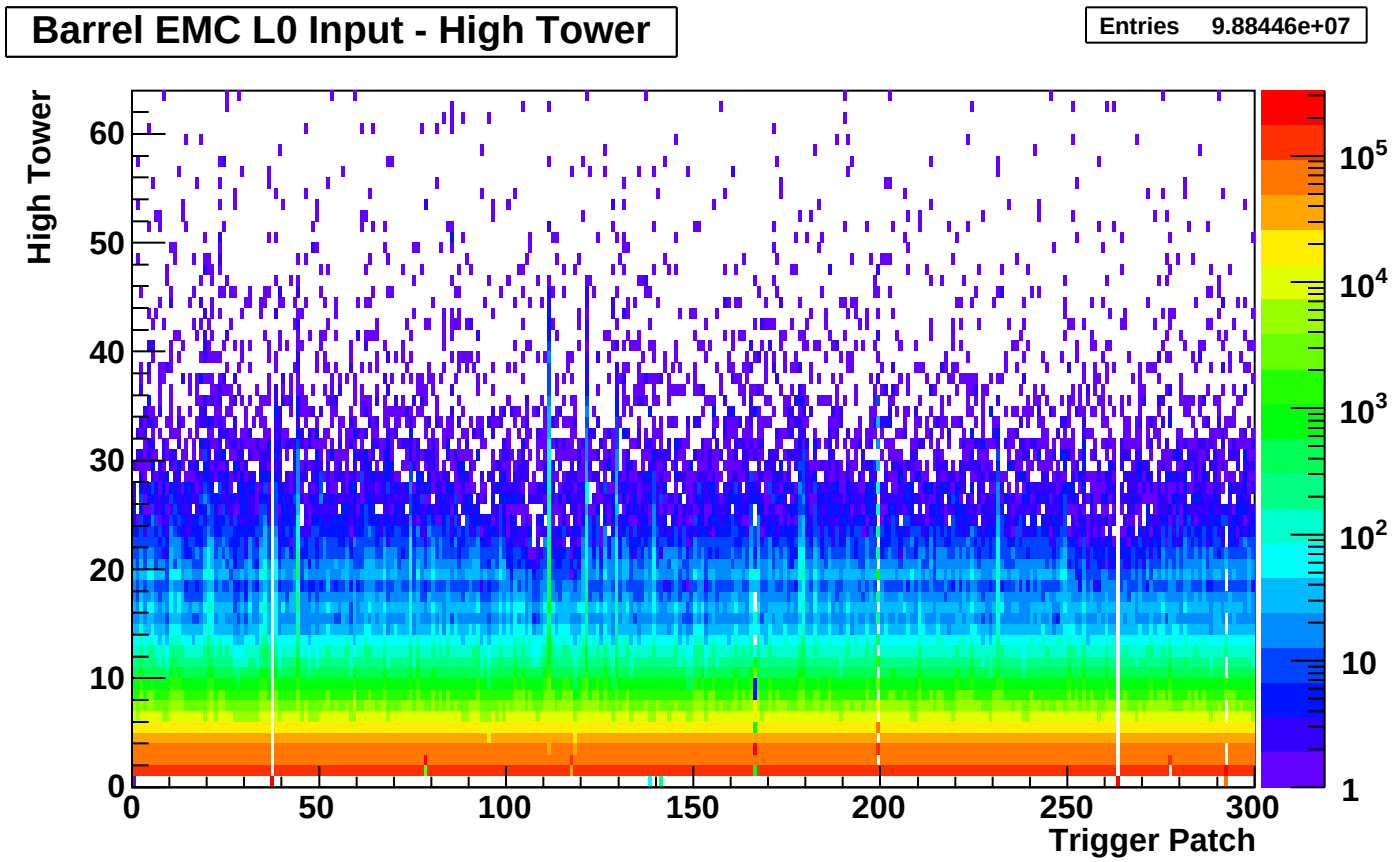

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

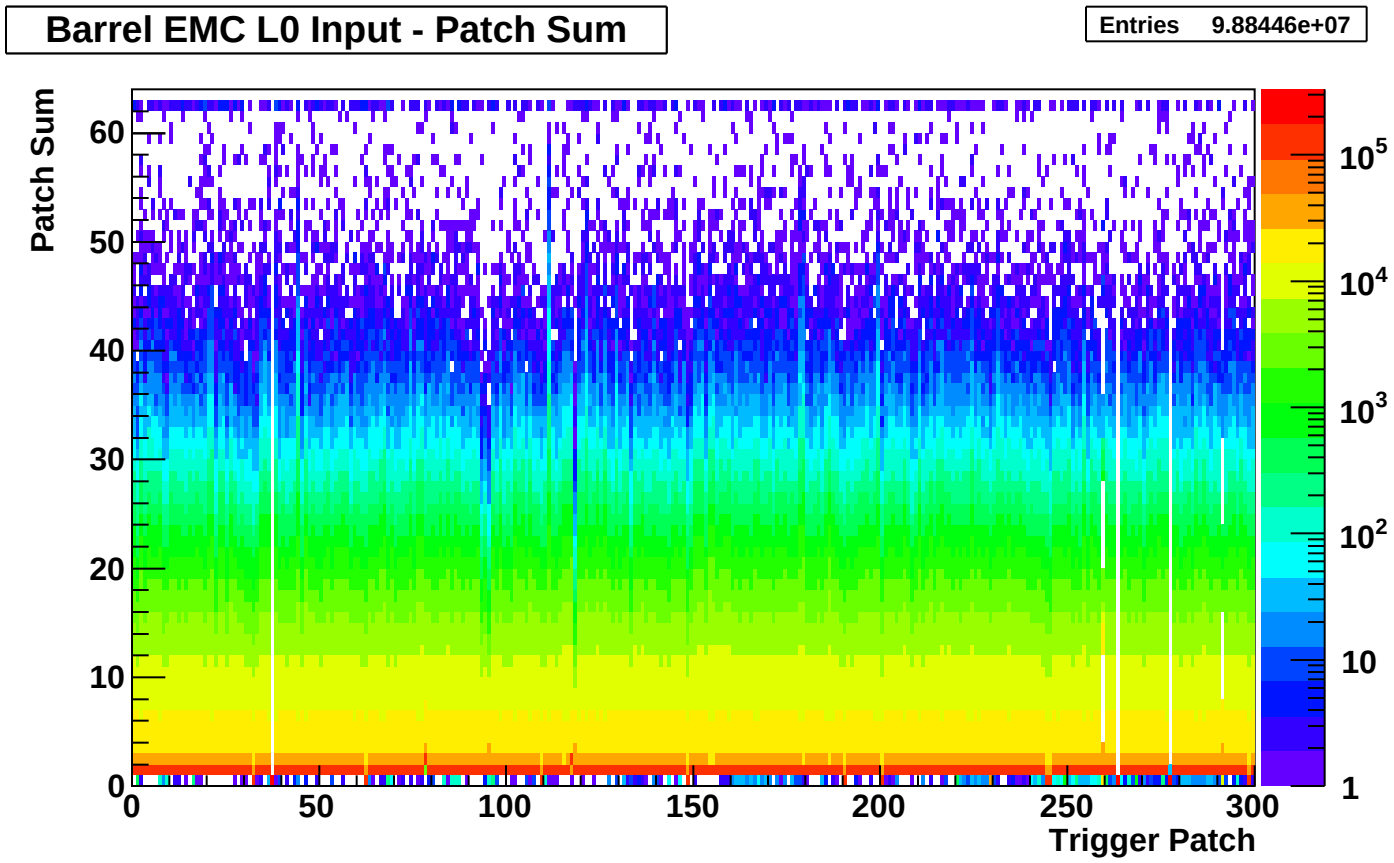

BunchId7Bit (BHT3)
Entries 6673
10^2
10
1
0
20
40
60
80
100
120

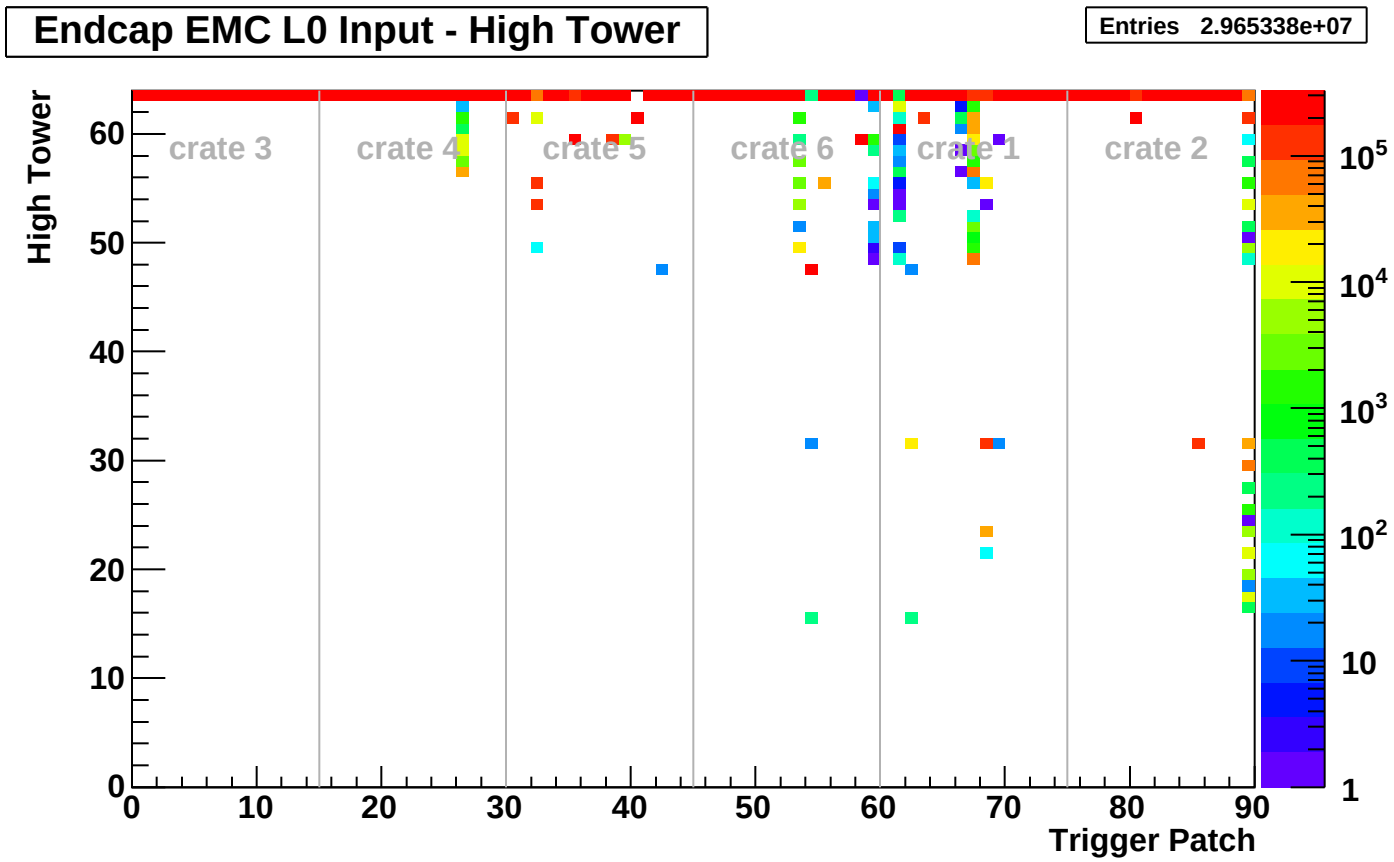

BunchId7Bit (all events)
Entries 329482
10^3
10^2
0
20
40
60
80
100
120

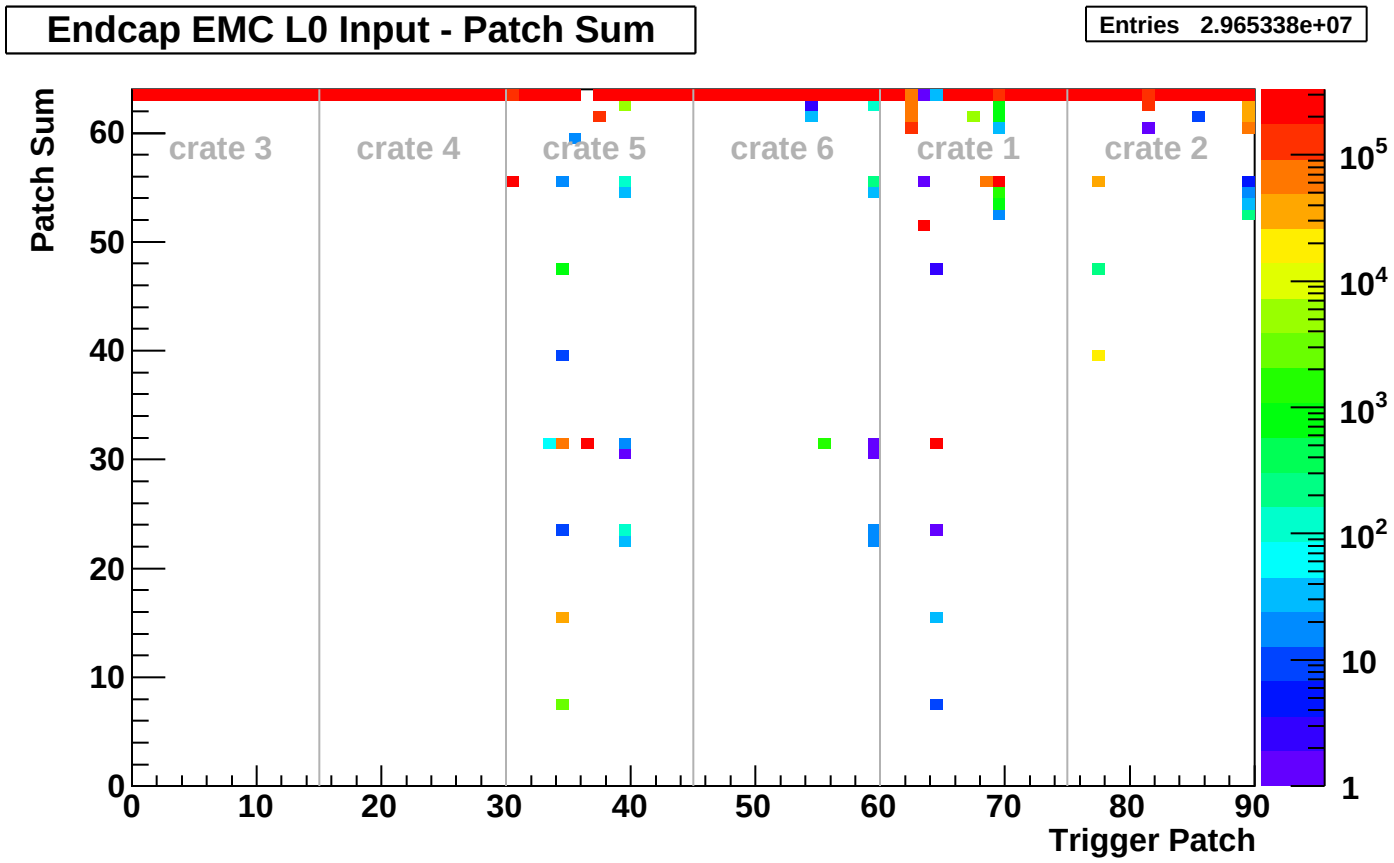

BunchId7Bit (JP1)
Entries 329482
10^3
10^2
0
20
40
60
80
100
120

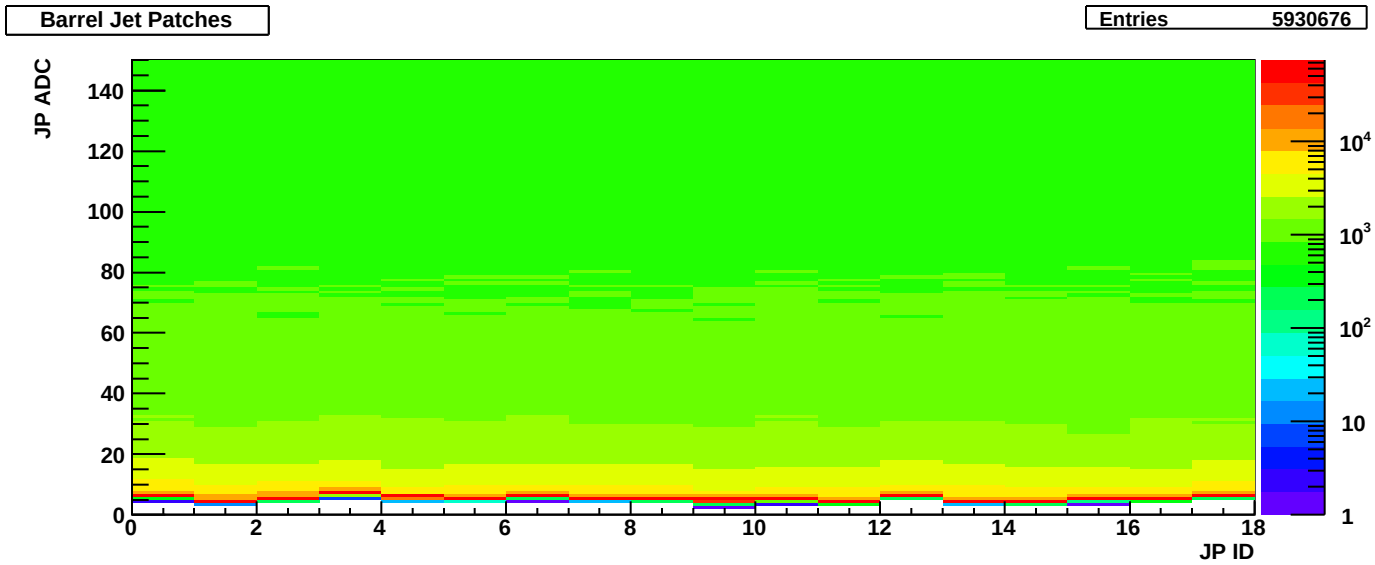

BunchId7Bit (JP2||AJP)
Entries 329482
10^3
10^2
0
20
40
60
80
100
120

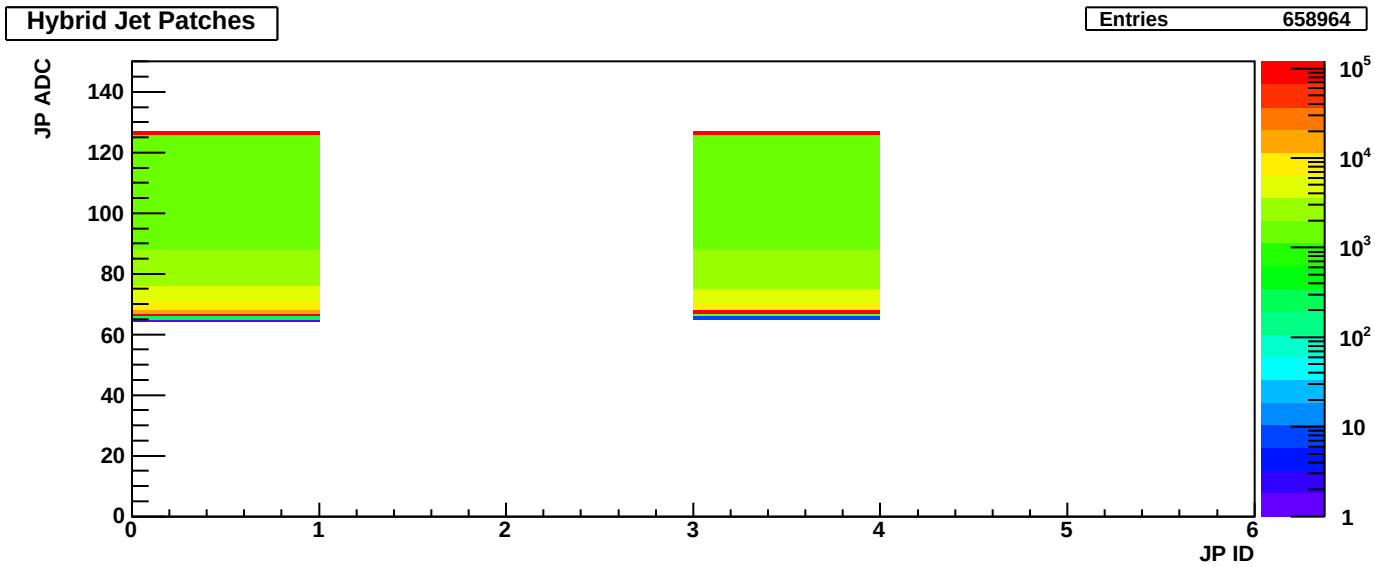

Barrel EMC L0 Input - High Tower
Entries 9.88446e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

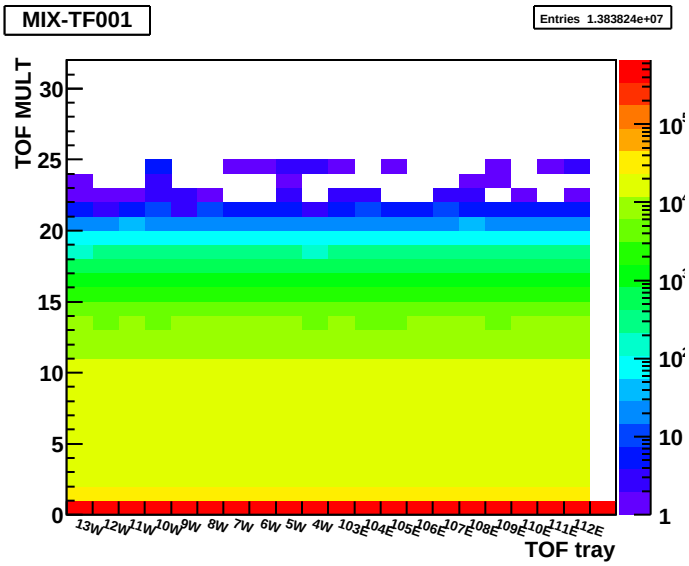

Barrel EMC L0 Input - Patch Sum
Entries 9.88446e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

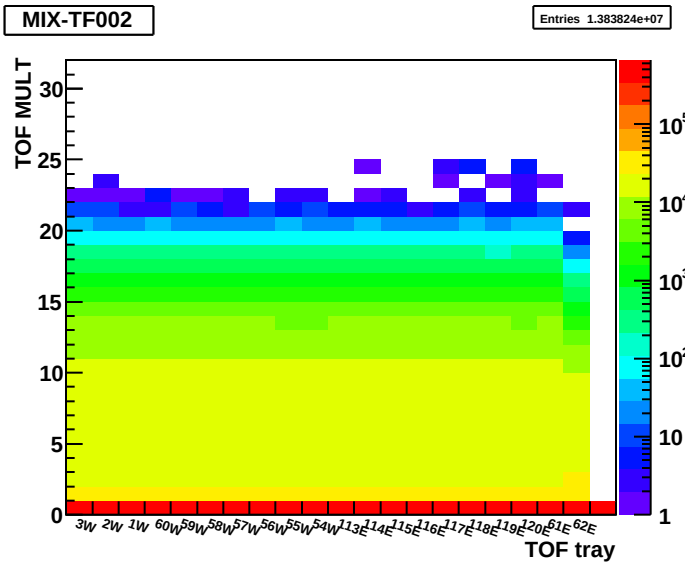

Endcap EMC L0 Input - High Tower
Entries 2.965338e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

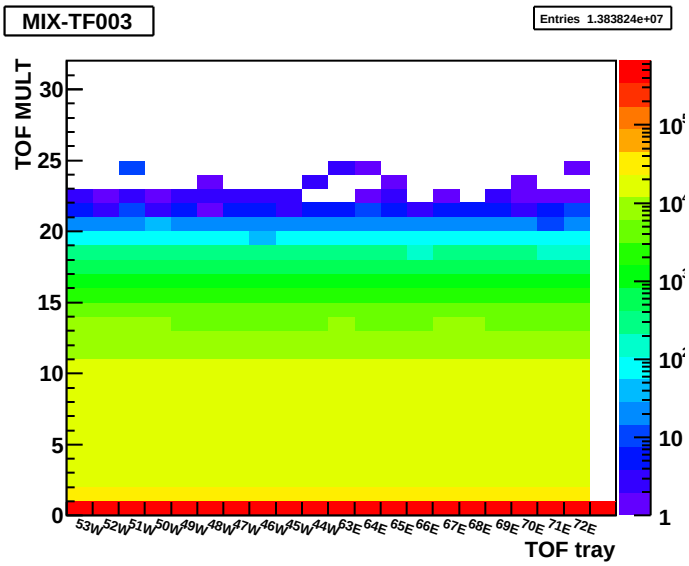

Endcap EMC L0 Input - Patch Sum
Entries 2.965338e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

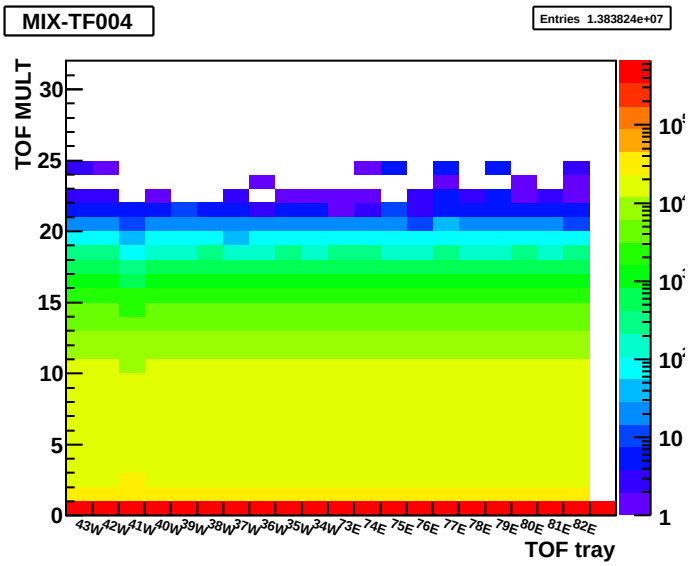

Barrel Jet Patches
Entries 5930676
JP ADC
JP ID

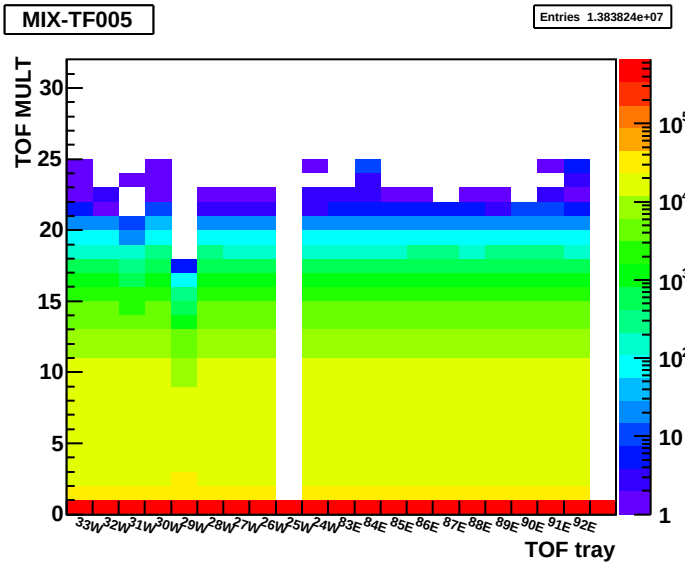

Hybrid Jet Patches
Entries 658964
JP ADC
JP ID

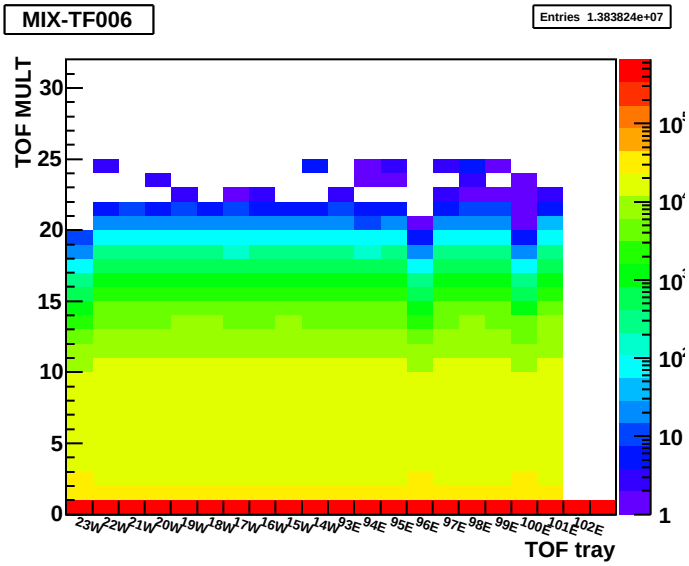

MIX-TF001
Entries 1.383824e+07
TOF MULT
TOF tray

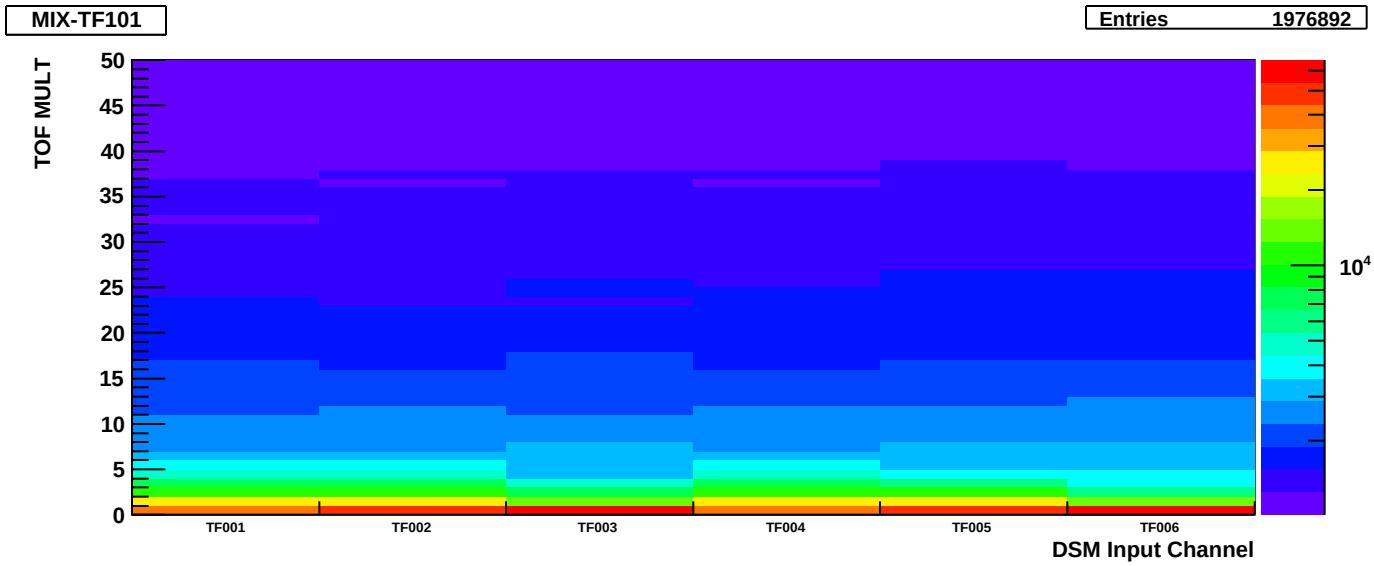

MIX-TF002
Entries 1.383824e+07
TOF MULT
TOF tray

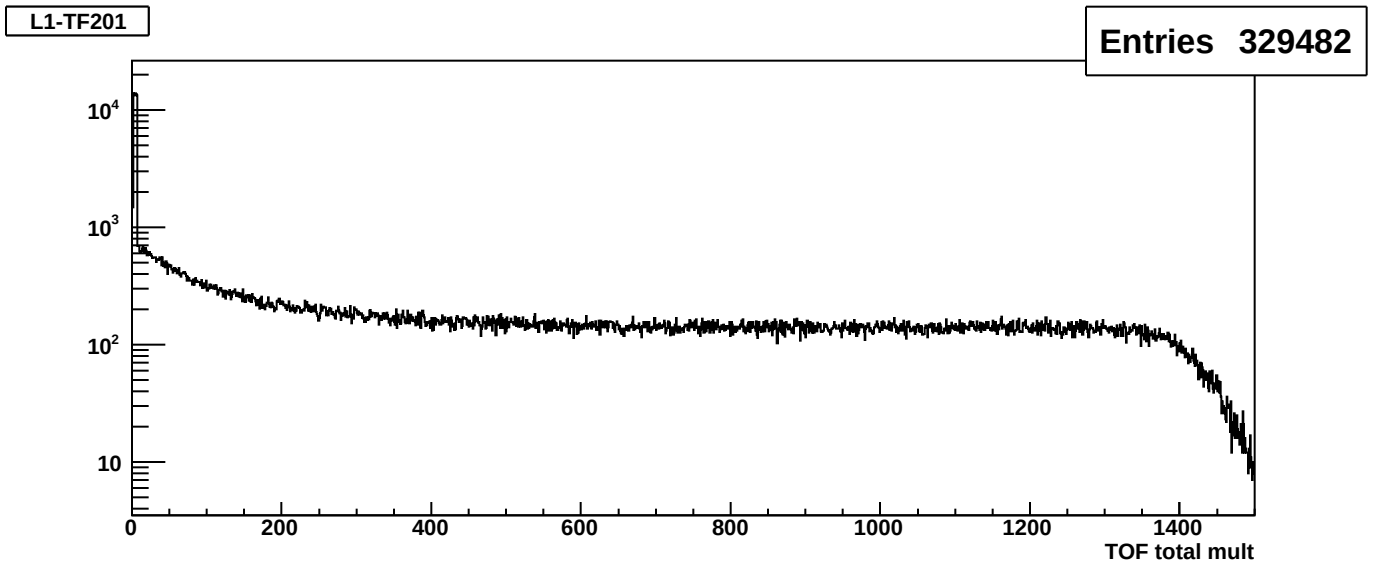

MIX-TF003
Entries 1.383824e+07
TOF MULT
TOF tray

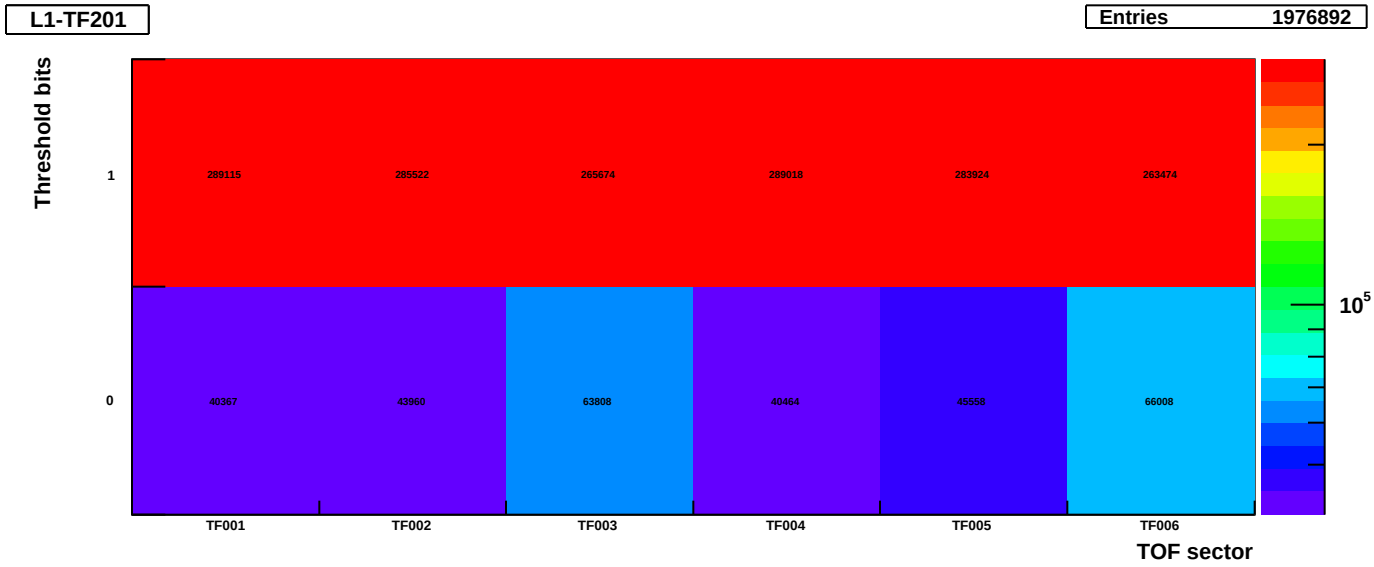

MIX-TF004
Entries 1.383824e+07
TOF MULT
TOF tray

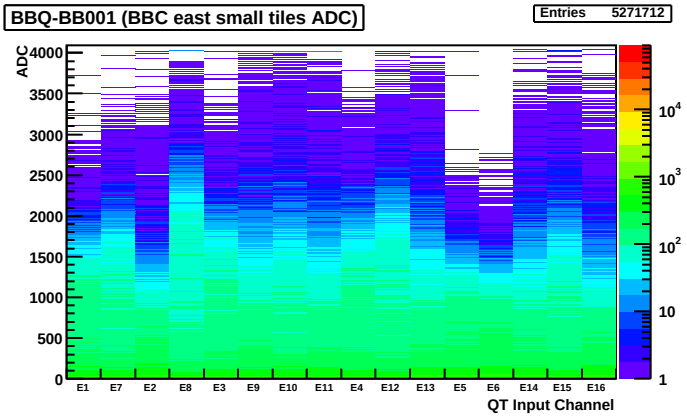

MIX-TF005
Entries 1.383824e+07
TOF MULT
TOF tray

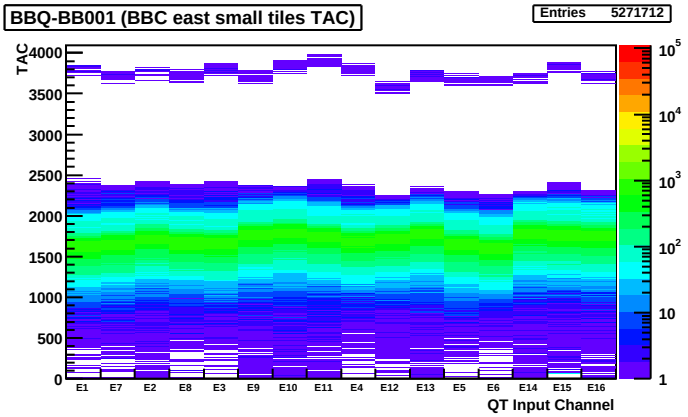

MIX-TF006
Entries 1.383824e+07
TOF MULT
TOF tray

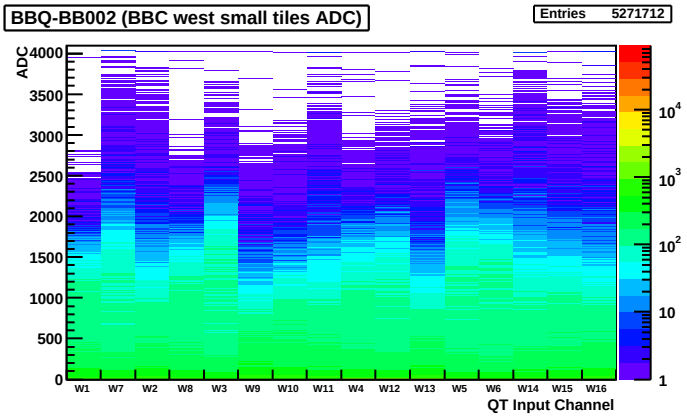

MIX-TF101
Entries 1976892
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

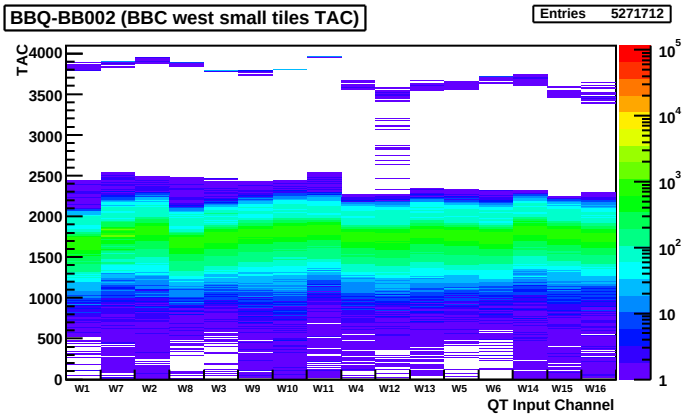

L1-TF201
Entries 329482
10^4
10^3
10^2
10
0
200
400
600
800
1000
1200
1400
TOF total mult

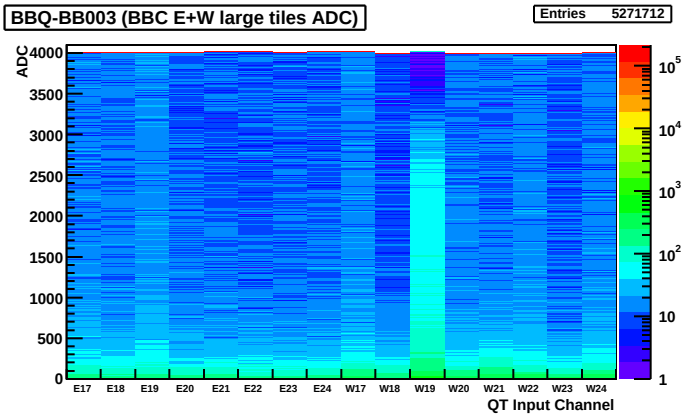

L1-TF201
Entries 1976892
Threshold bits
1
0
289115
285522
265674
289018
283924
263474
40367
43960
63808
40464
45558
66008
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

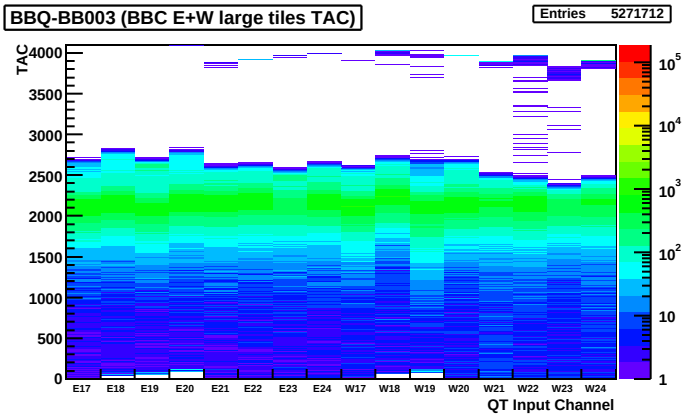

BBQ-BB001 (BBC east small tiles ADC)
Entries 5271712
ADC
QT Input Channel

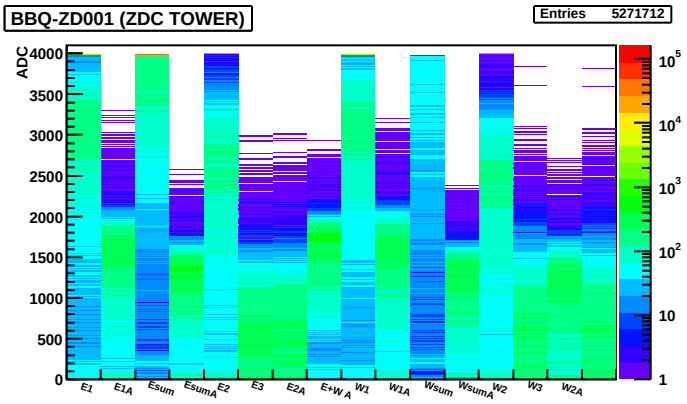

BBQ-BB001 (BBC east small tiles TAC)
Entries 5271712
TAC
QT Input Channel

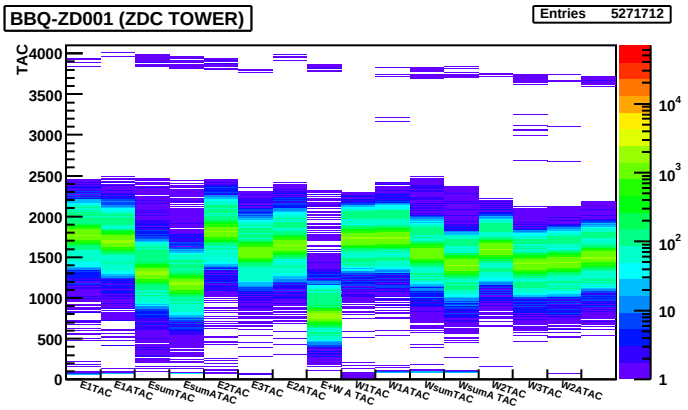

BBQ-BB002 (BBC west small tiles ADC)
Entries 5271712
ADC
QT Input Channel

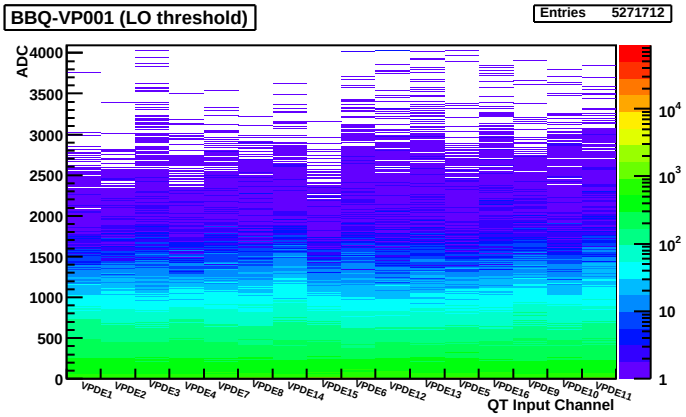

BBQ-BB002 (BBC west small tiles TAC)
Entries 5271712
TAC
QT Input Channel

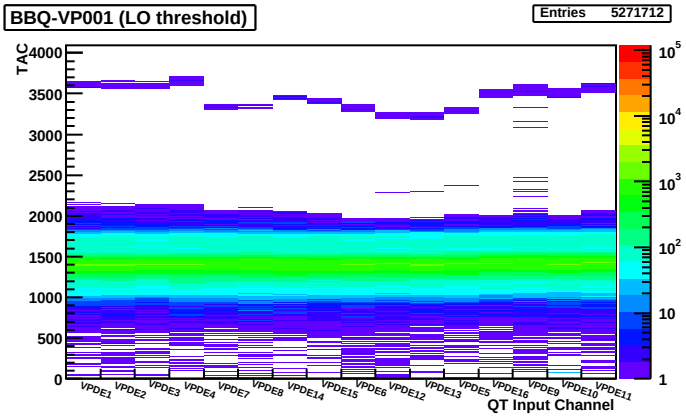

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 5271712
ADC
QT Input Channel

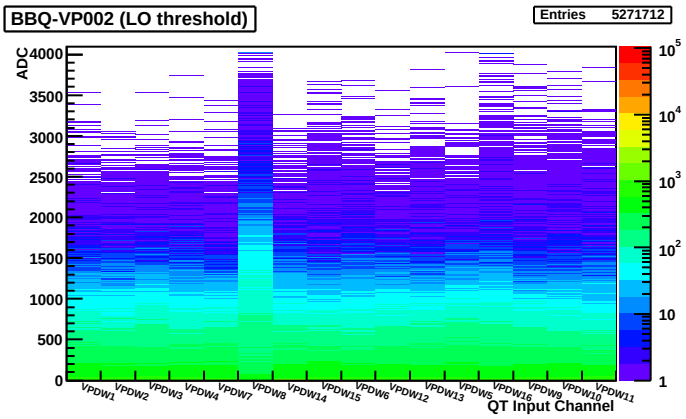

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 5271712
TAC
QT Input Channel

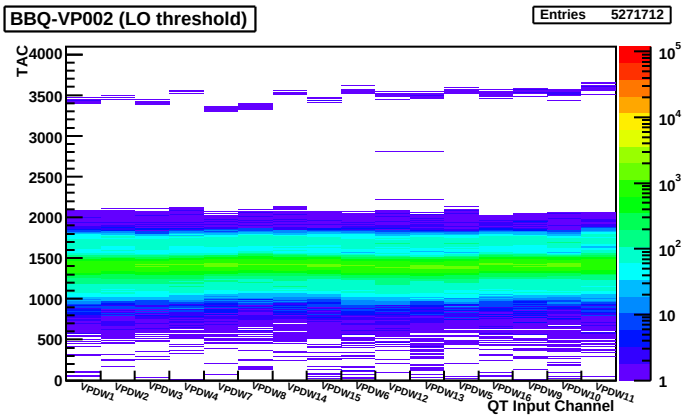

BBQ-ZD001 (ZDC TOWER)
Entries 5271712
ADC

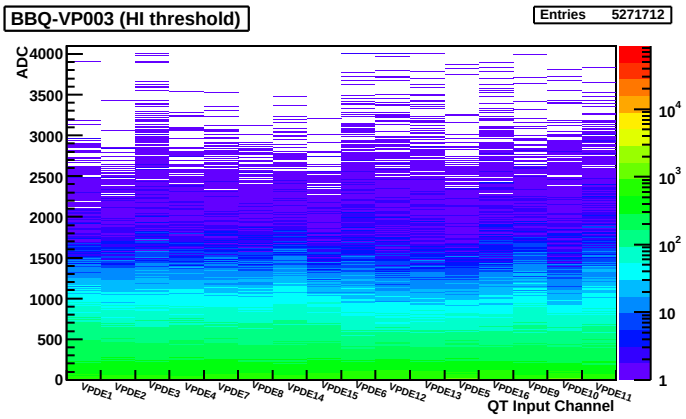

BBQ-ZD001 (ZDC TOWER)
Entries 5271712
TAC

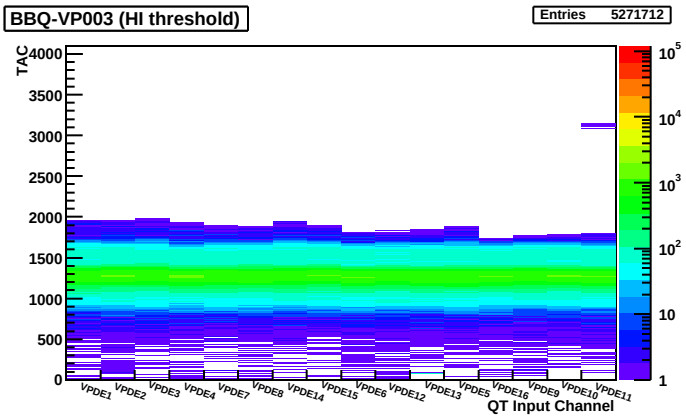

BBQ-VP001 (LO threshold)
Entries 5271712
ADC
QT Input Channel

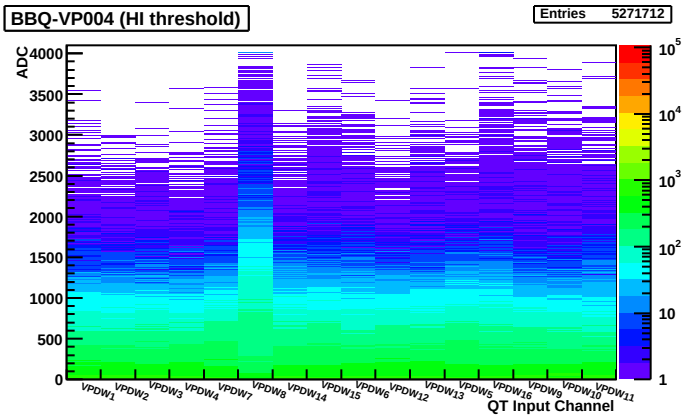

BBQ-VP001 (LO threshold)
Entries 5271712
TAC
QT Input Channel

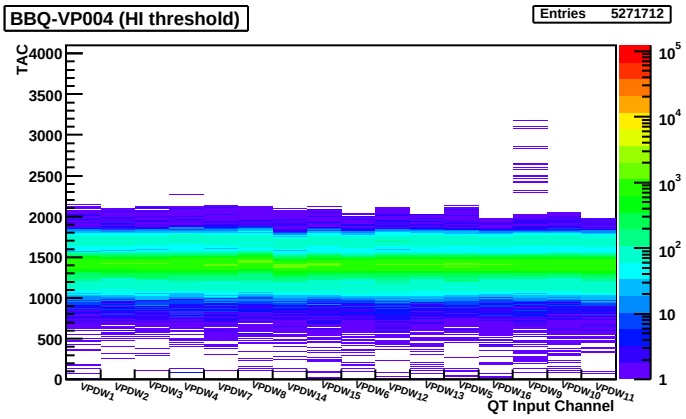

BBQ-VP002 (LO threshold)
Entries 5271712
ADC
QT Input Channel

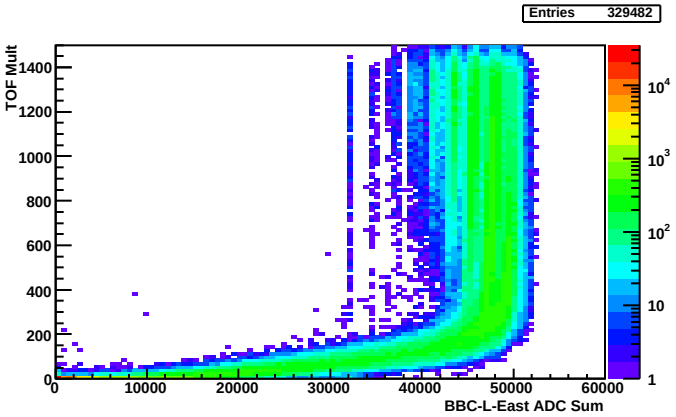

BBQ-VP002 (LO threshold)
Entries 5271712
TAC
QT Input Channel

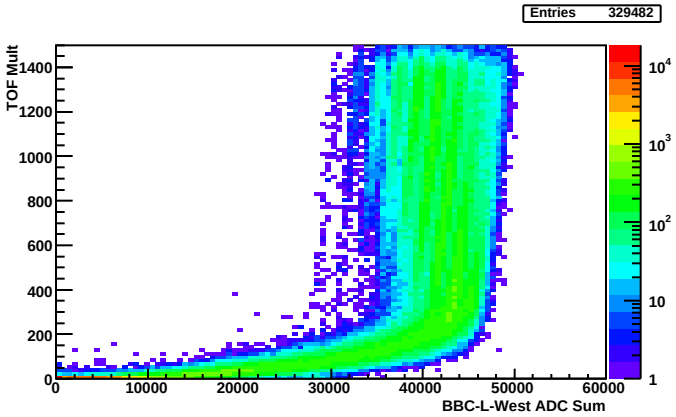

BBQ-VP003 (HI threshold)
Entries 5271712
ADC
QT Input Channel

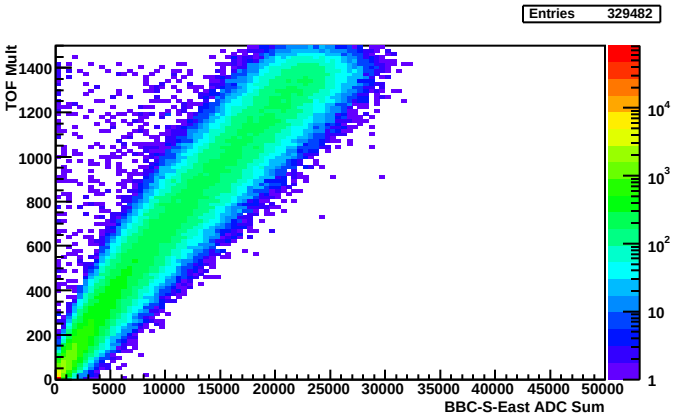

BBQ-VP003 (HI threshold)
Entries 5271712
TAC
QT Input Channel

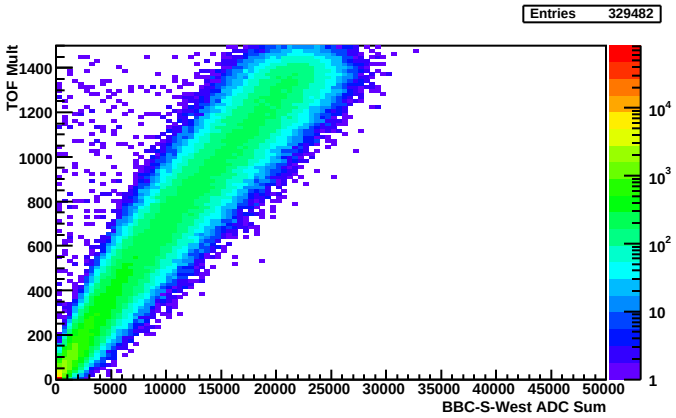

BBQ-VP004 (HI threshold)
Entries 5271712
ADC
QT Input Channel

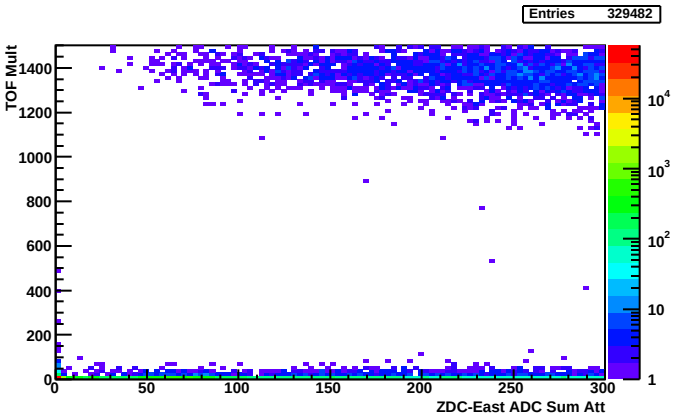

BBQ-VP004 (HI threshold)
Entries 5271712
TAC
QT Input Channel

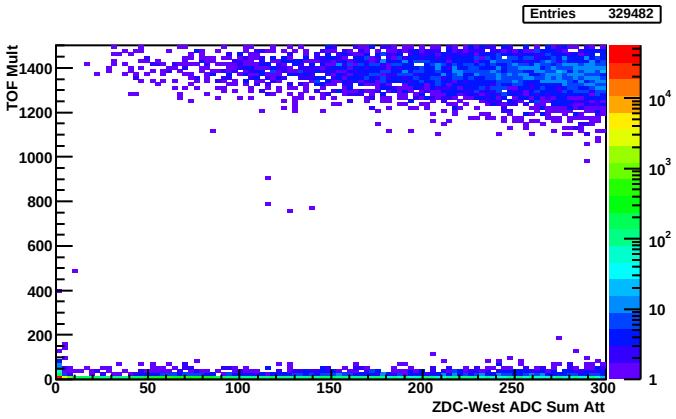

Entries 329482
TOF Mult
BBC-L-East ADC Sum

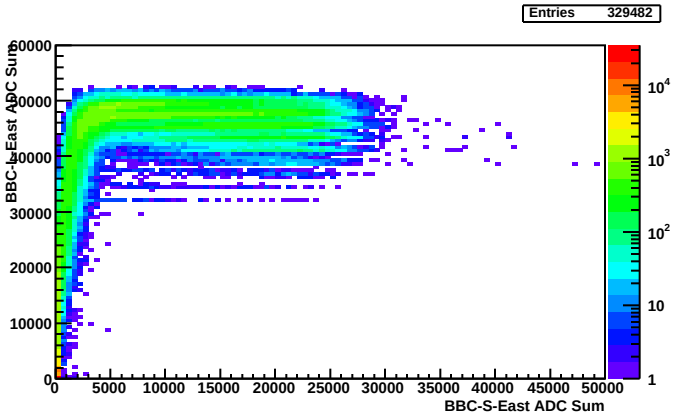

Entries 329482
TOF Mult
BBC-L-West ADC Sum

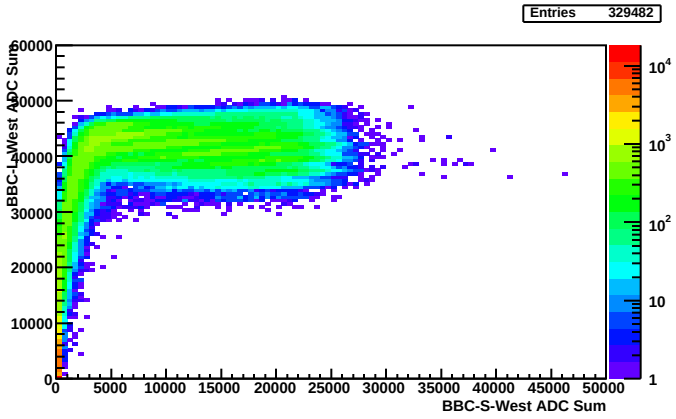

Entries 329482
TOF Mult
BBC-S-East ADC Sum

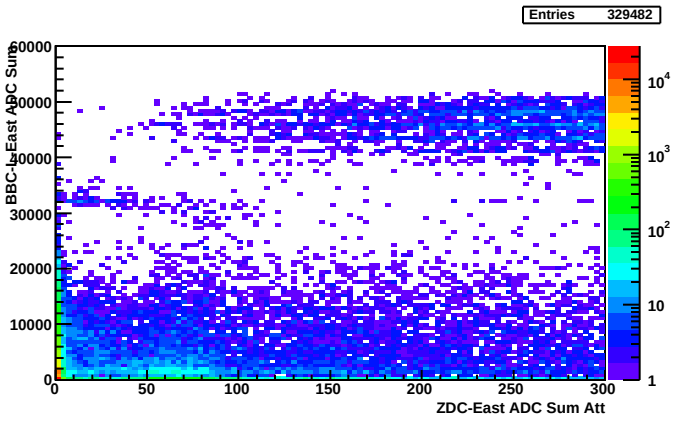

Entries 329482
TOF Mult
BBC-S-West ADC Sum

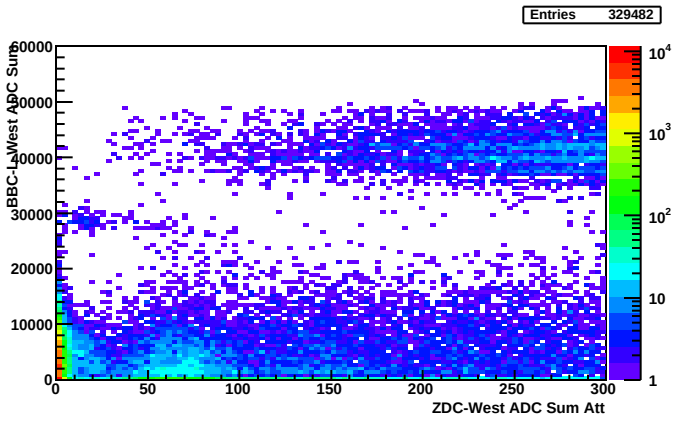

Entries 329482
TOF Mult
ZDC-East ADC Sum Att


Entries 329482
TOF Mult
ZDC-West ADC Sum Att


Entries 329482
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 329482
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 329482
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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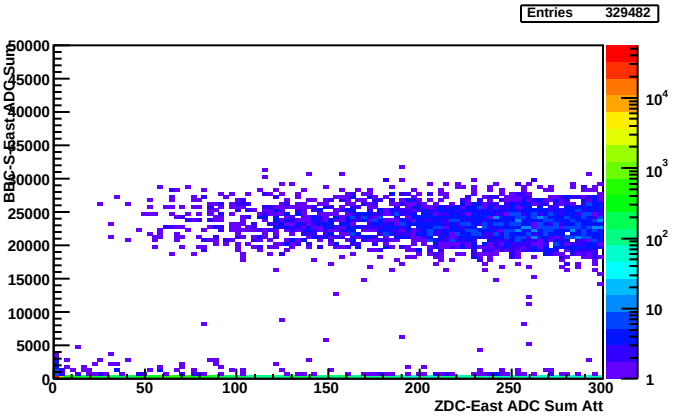
BBC-L-West ADC Sum
ZDC-West ADC Sum Att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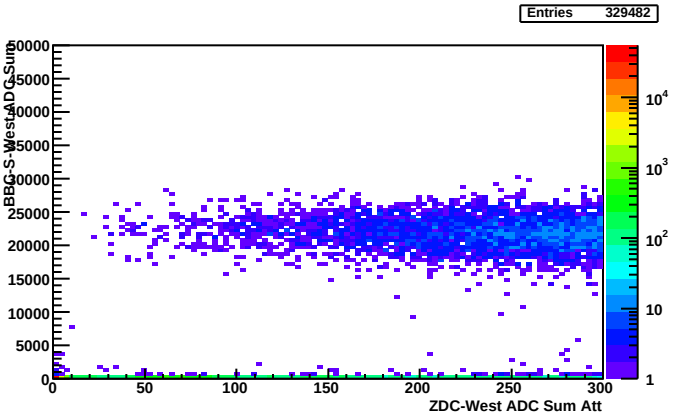
BBC-S-East ADC Sum
ZDC-East ADC Sum Att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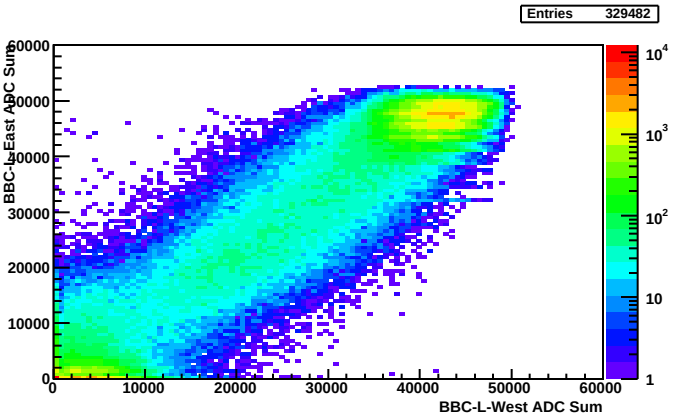
BBC-S-West ADC Sum
ZDC-West ADC Sum Att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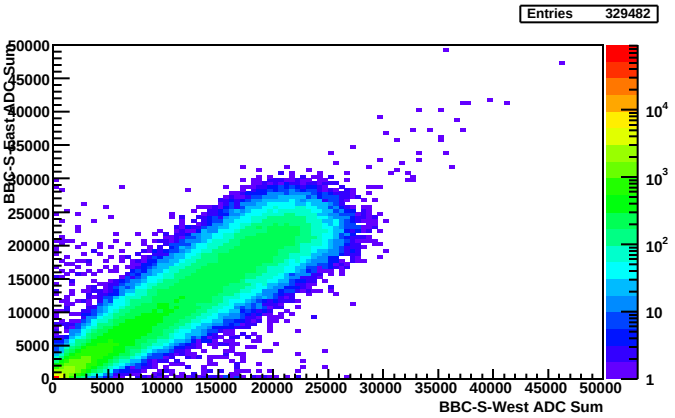
BBC-L-East ADC Sum
BBC-L-West ADC Sum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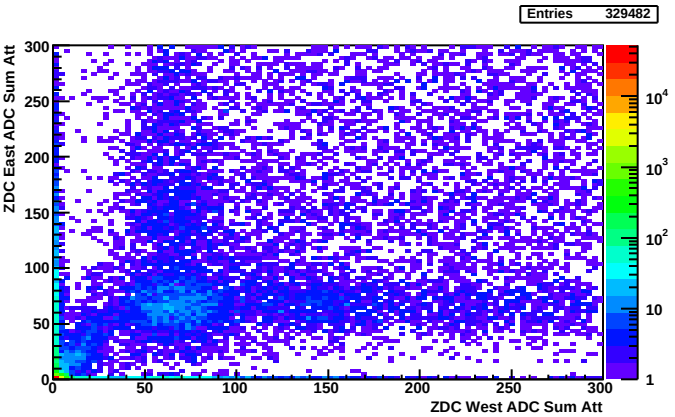
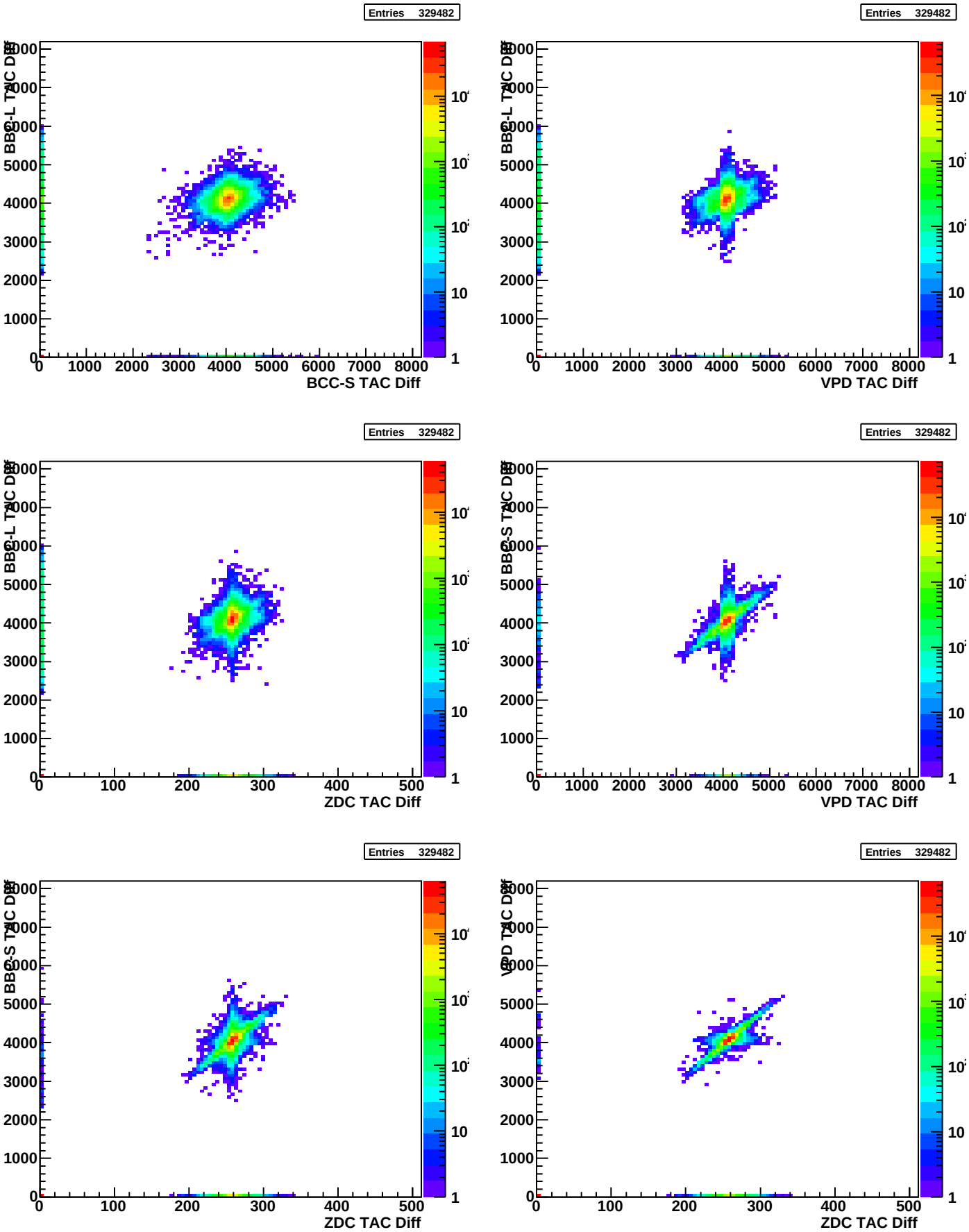
BBC-S-East ADC Sum
BBC-S-West ADC Sum


Entries 329482
ZDC East ADC Sum Att
ZDC West ADC Sum Att


Entries 329482
BBC-L TAC Diff
BCC-S TAC Diff
Entries 329482
BBC-L TAC Diff
VPD TAC Diff
Entries 329482
BBC-L TAC Diff
ZDC TAC Diff
Entries 329482
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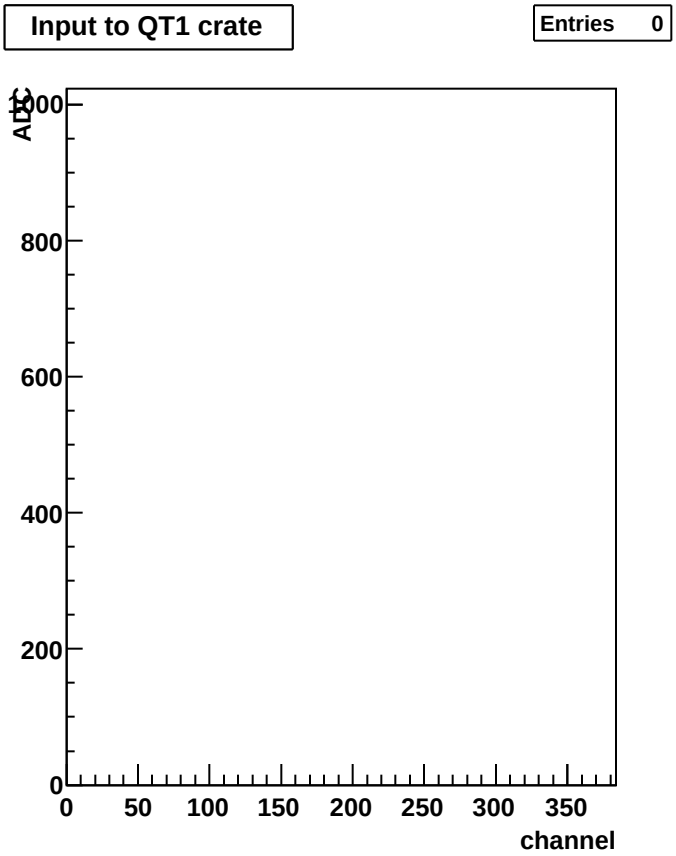
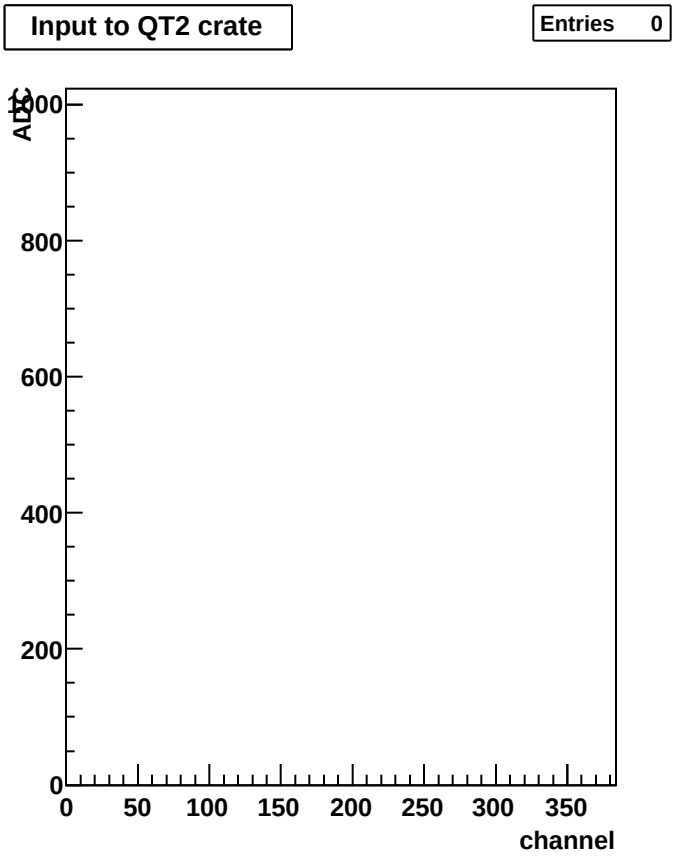
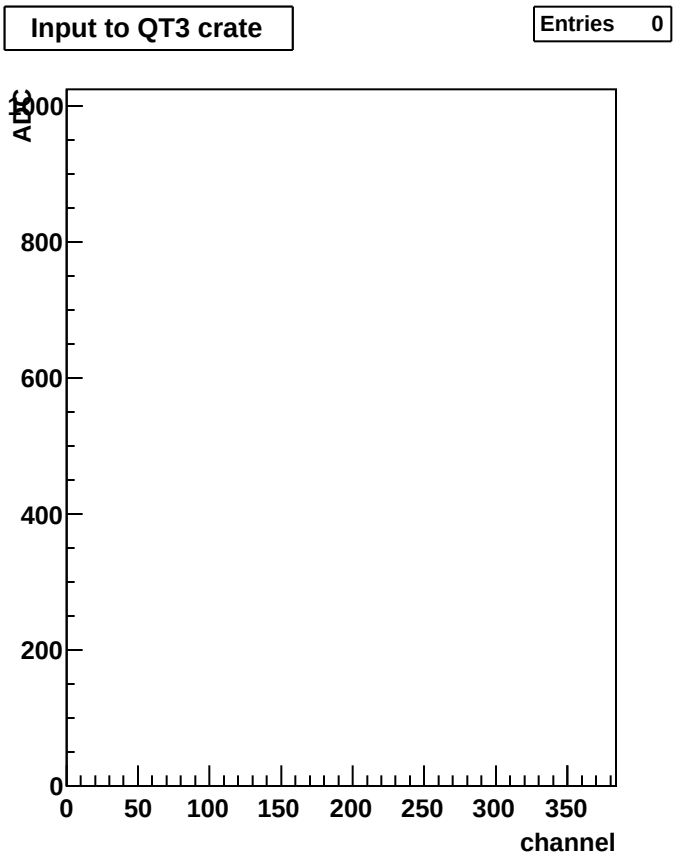
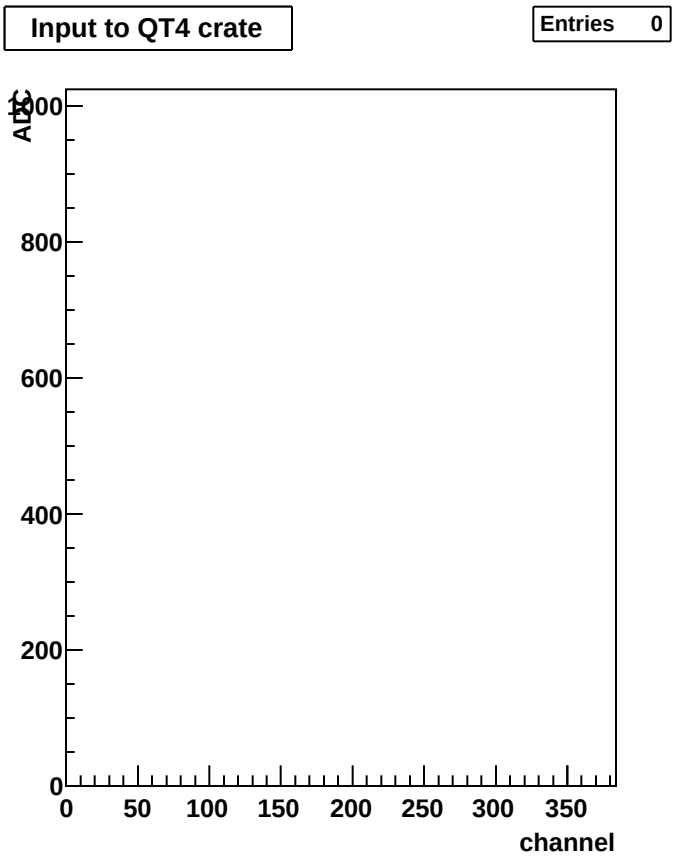
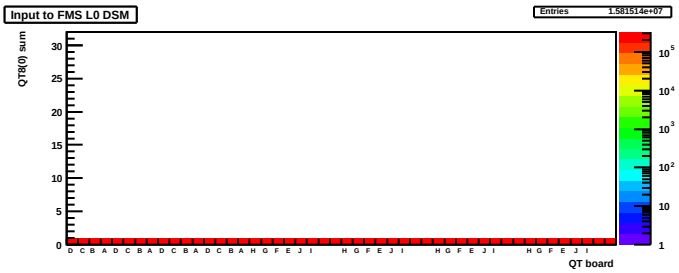
VPD TAC Diff
Entries 329482
BBC-S TAC Diff
ZDC TAC Diff
Entries 329482
VPD TAC Diff
ZDC TAC Diff

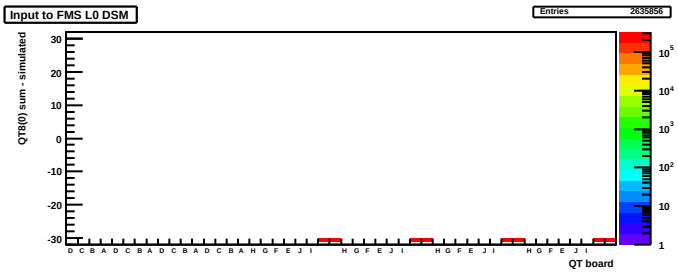

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

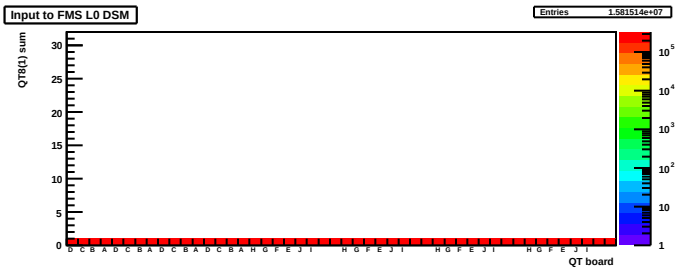

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

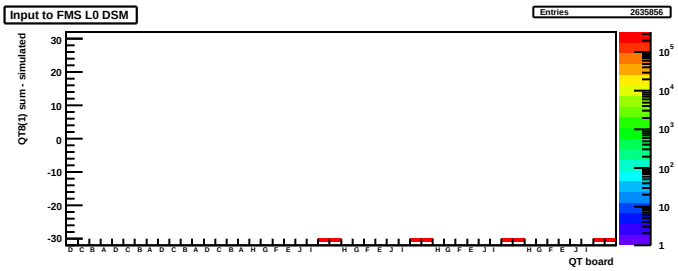

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

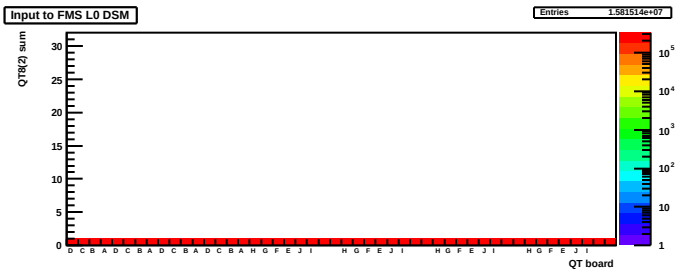

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

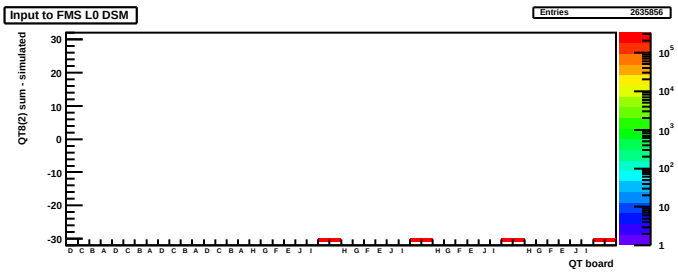

Input to FMS L0 DSM
Entries 1.581514e+07
QT8(0) sum
QT board

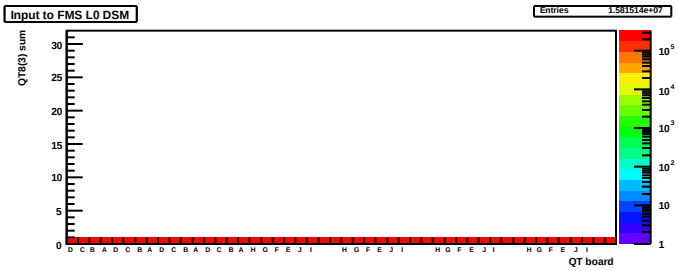

Input to FMS L0 DSM
Entries 2635856
QT8(0) sum - simulated
QT board

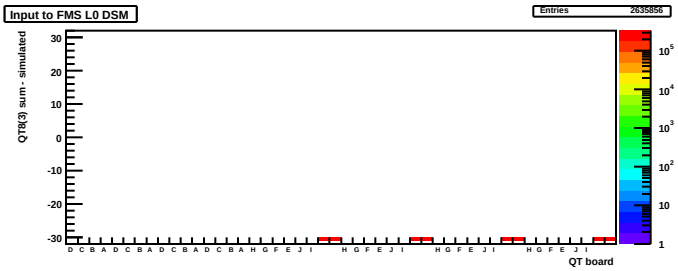

Input to FMS L0 DSM
Entries 1.581514e+07
QT8(1) sum
QT board

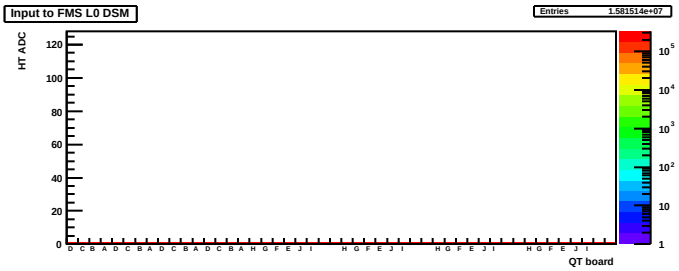

Input to FMS L0 DSM
Entries 2635856
QT8(1) sum - simulated
QT board

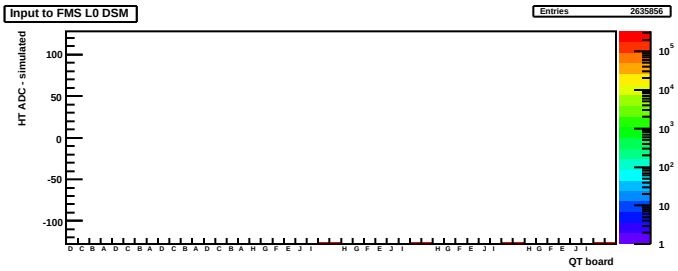

Input to FMS L0 DSM
Entries 1.581514e+07
QT8(2) sum
QT board

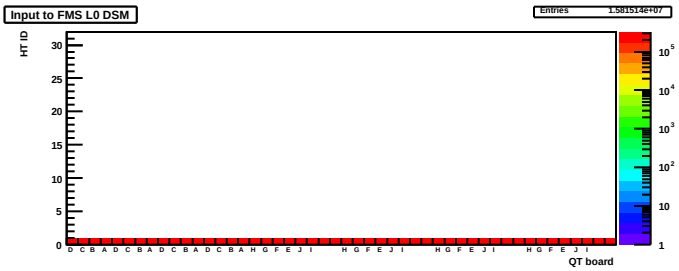

Input to FMS L0 DSM
Entries 2635856
QT8(2) sum - simulated
QT board

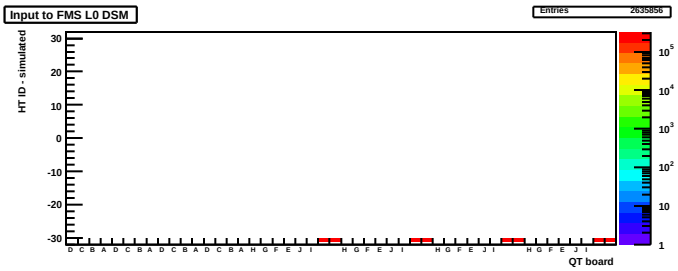

Input to FMS L0 DSM
Entries 1.581514e+07
QT8(3) sum
QT board

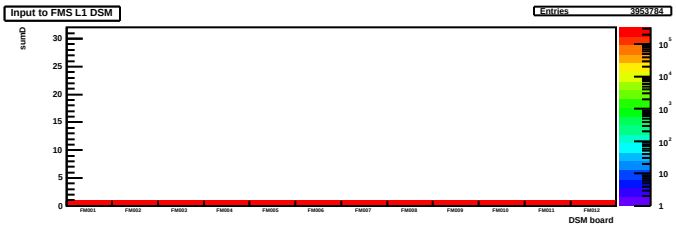

Input to FMS L0 DSM
Entries 2635856
QT8(3) sum - simulated
QT board

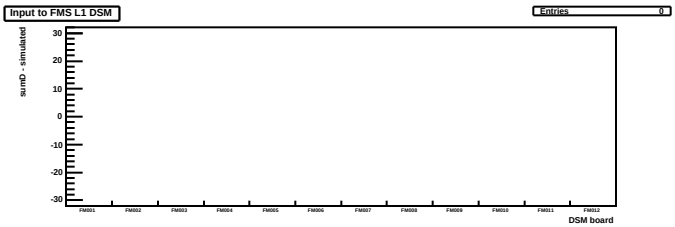

Input to FMS L0 DSM
Entries 1.581514e+07
HT ADC
QT board

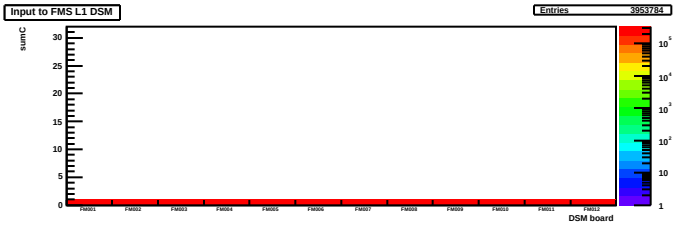

Input to FMS L0 DSM
Entries 2635856
HT ADC - simulated
QT board

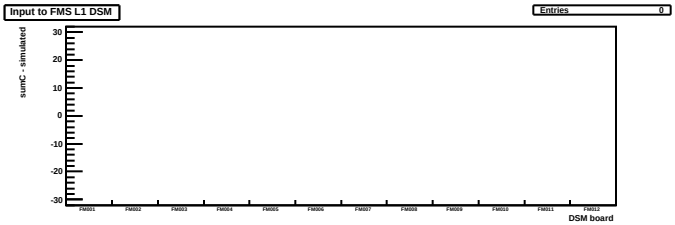

Input to FMS L0 DSM
Entries 1.581514e+07
HT ID
QT board

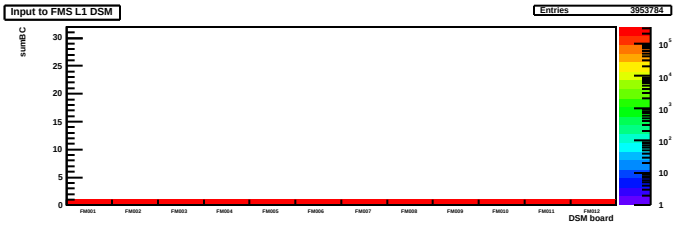

Input to FMS L0 DSM
Entries 2635856
HT ID - simulated
QT board

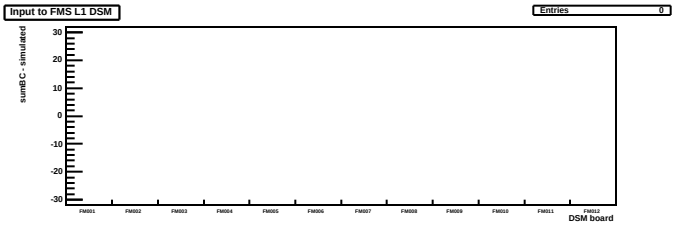

Input to FMS L1 DSM
Entries 3953784
sumD
DSM board

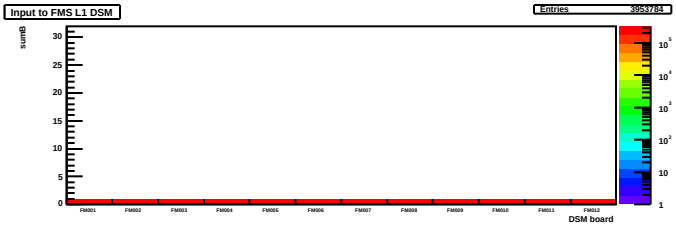

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

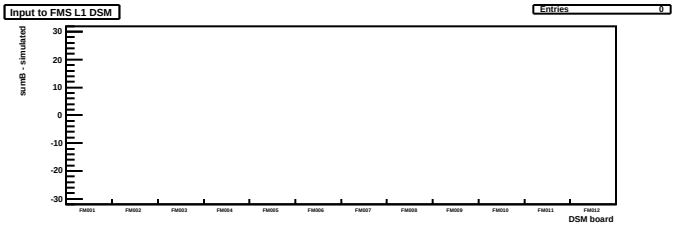

Input to FMS L1 DSM
Entries 3953784
sumC
DSM board

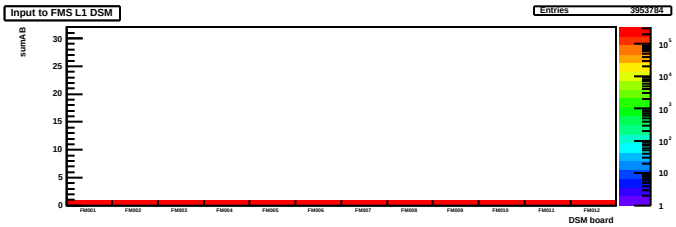

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

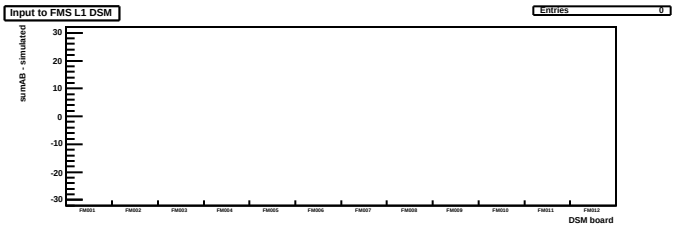

Input to FMS L1 DSM
Entries 3953784
sumBC
DSM board

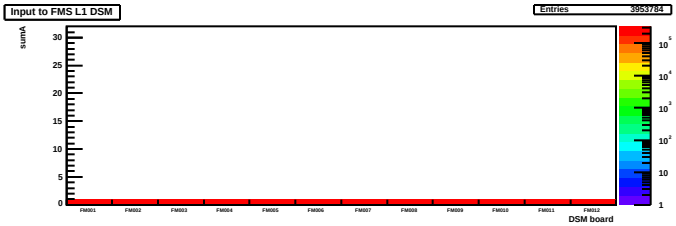

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

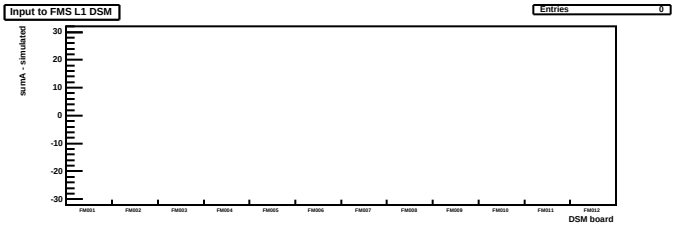

Input to FMS L1 DSM
Entries 3953784
sumB
DSM board

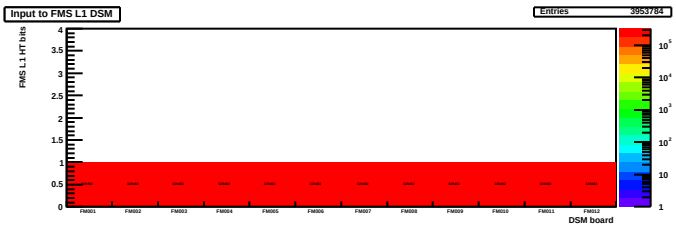

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

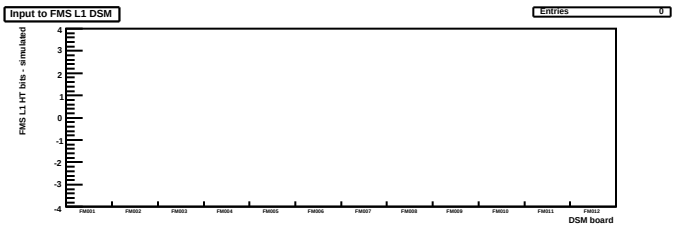

Input to FMS L1 DSM
Entries 3953784
sumAB
DSM board

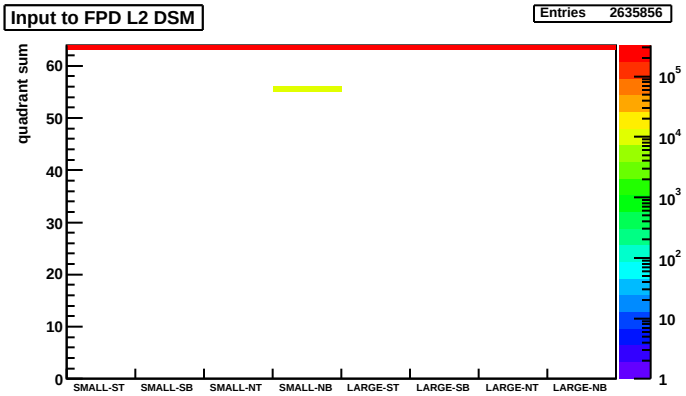

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

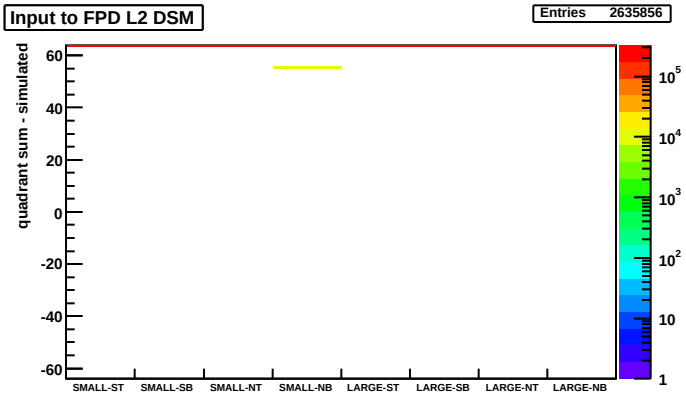

Input to FMS L1 DSM
Entries 3953784
sumA
DSM board

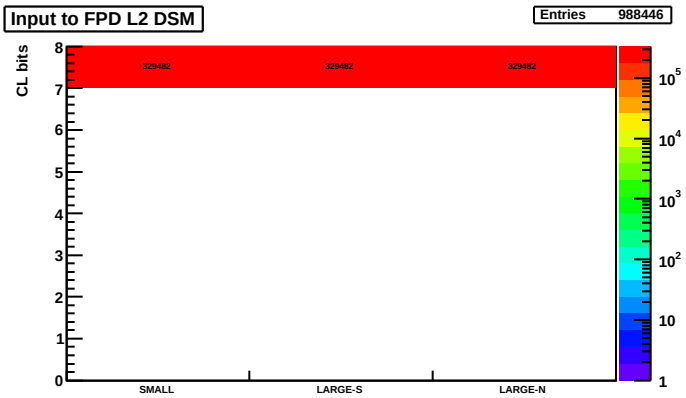

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

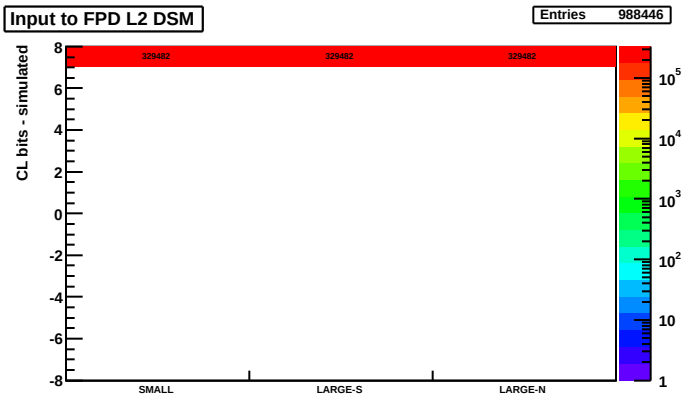

Input to FMS L1 DSM
Entries 3953784
FMS L1 HT bits
DSM board

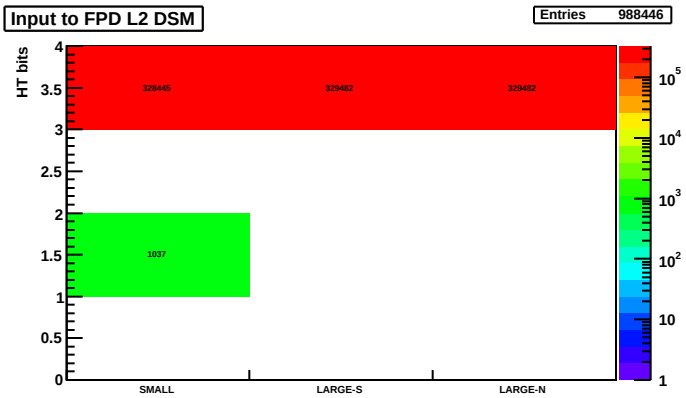

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

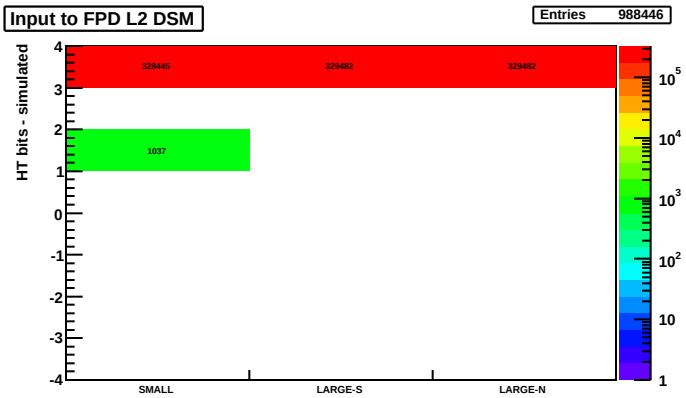

Input to FPD L2 DSM
Entries 2635856
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

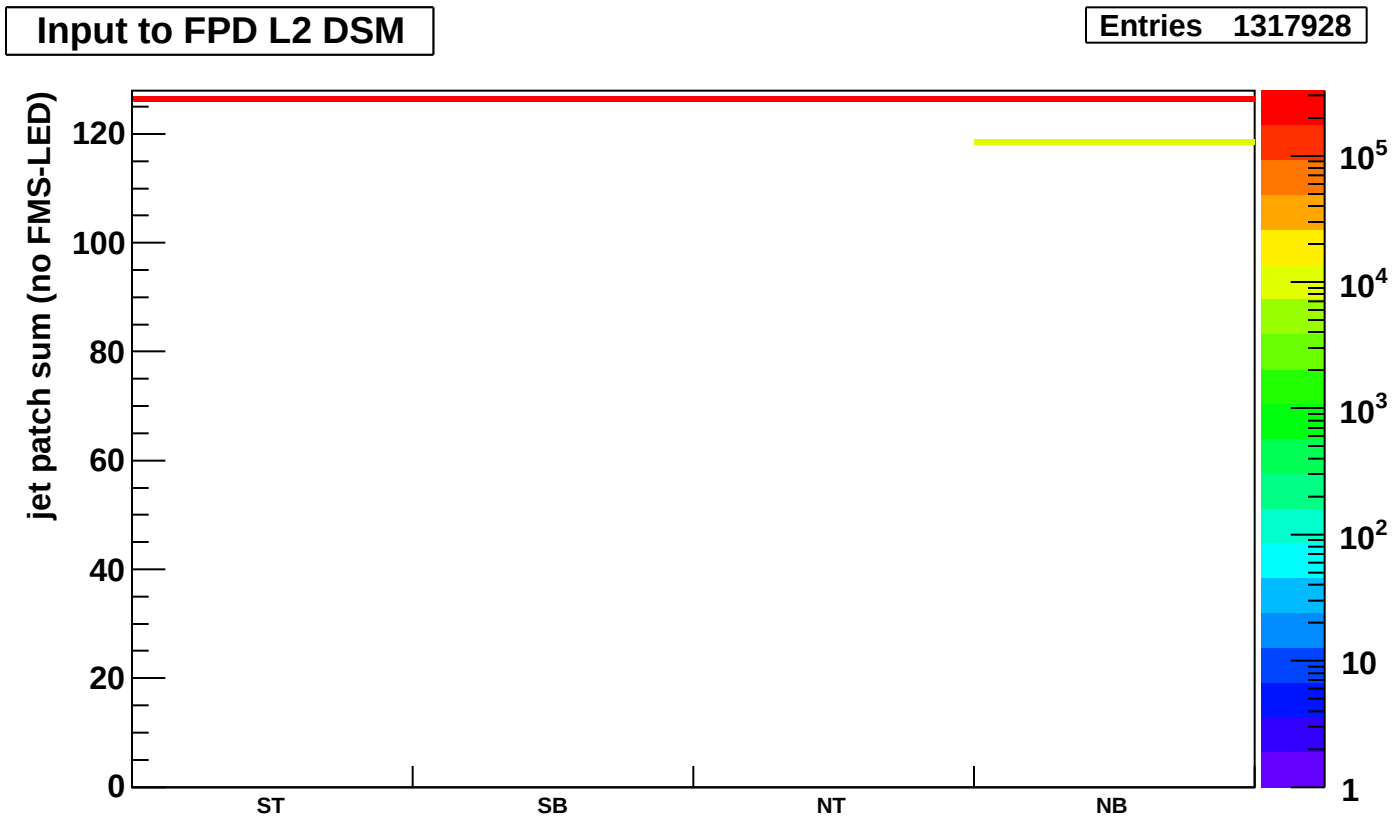

Input to FPD L2 DSM
Entries 2635856
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

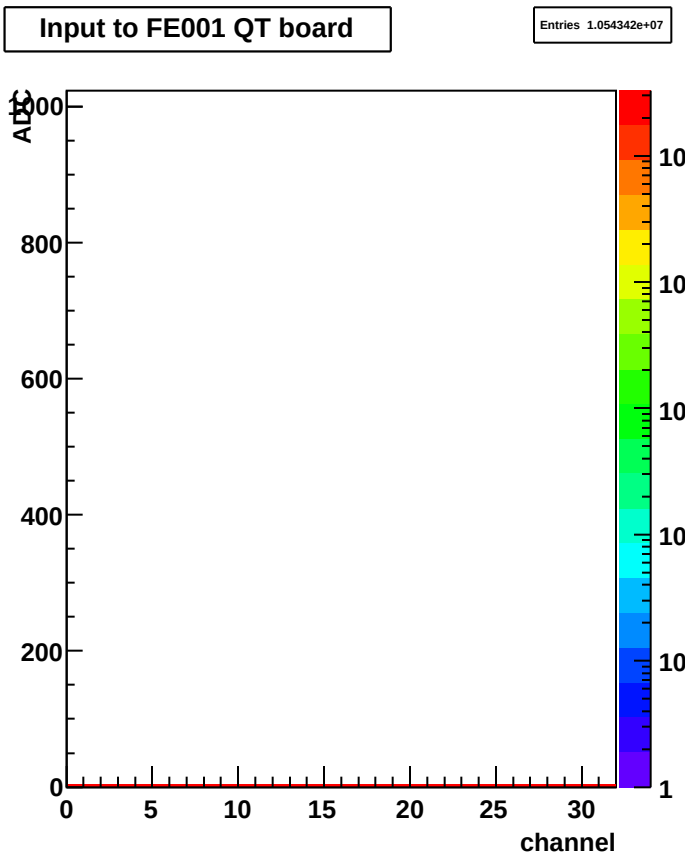

Input to FPD L2 DSM
Entries 988446
CL bits
329482
329482
329482
SMALL
LARGE-S
LARGE-N

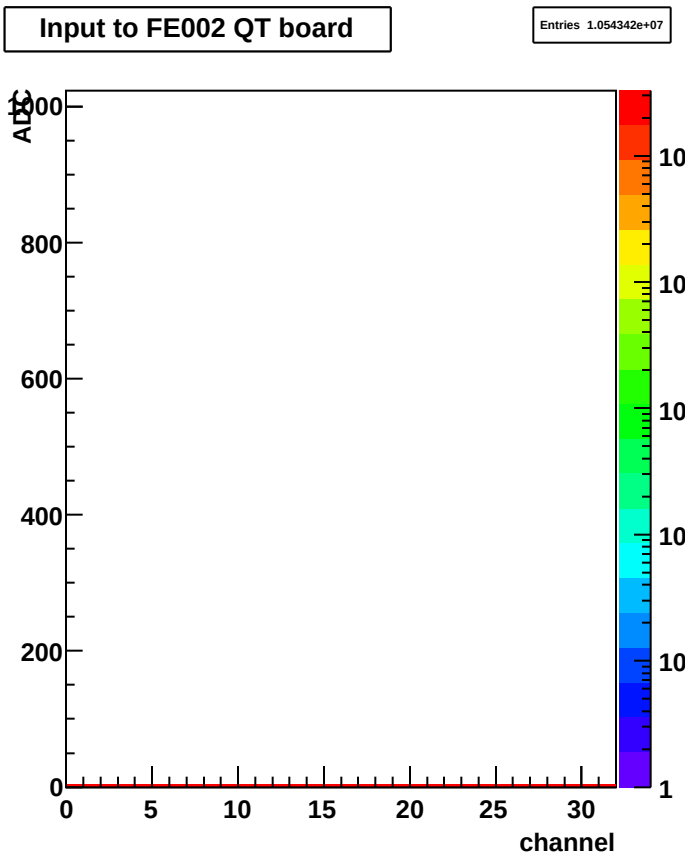

Input to FPD L2 DSM
Entries 988446
CL bits - simulated
329482
329482
329482
SMALL
LARGE-S
LARGE-N

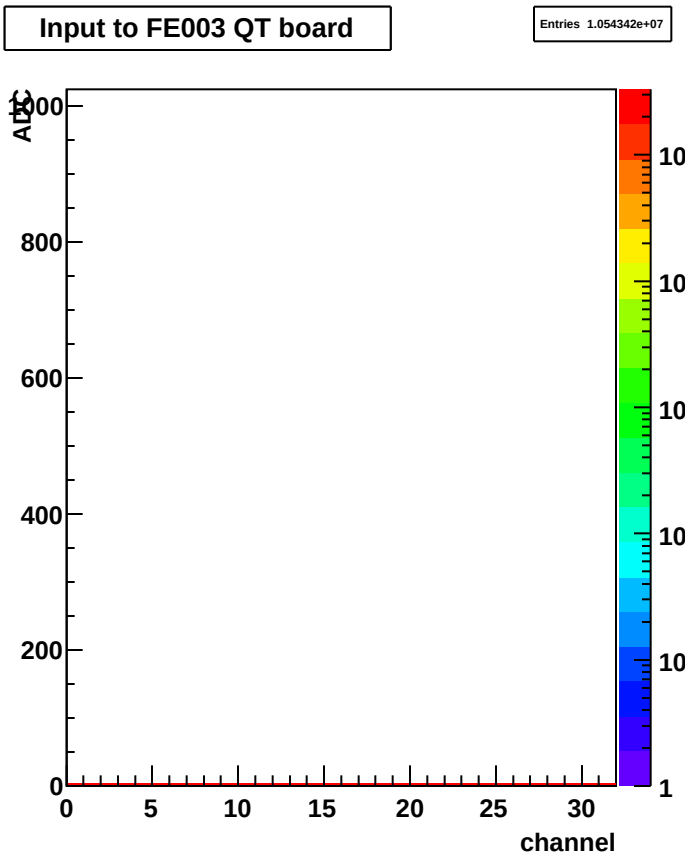

Input to FPD L2 DSM
Entries 988446
HT bits
328445
329482
329482
1037
SMALL
LARGE-S
LARGE-N

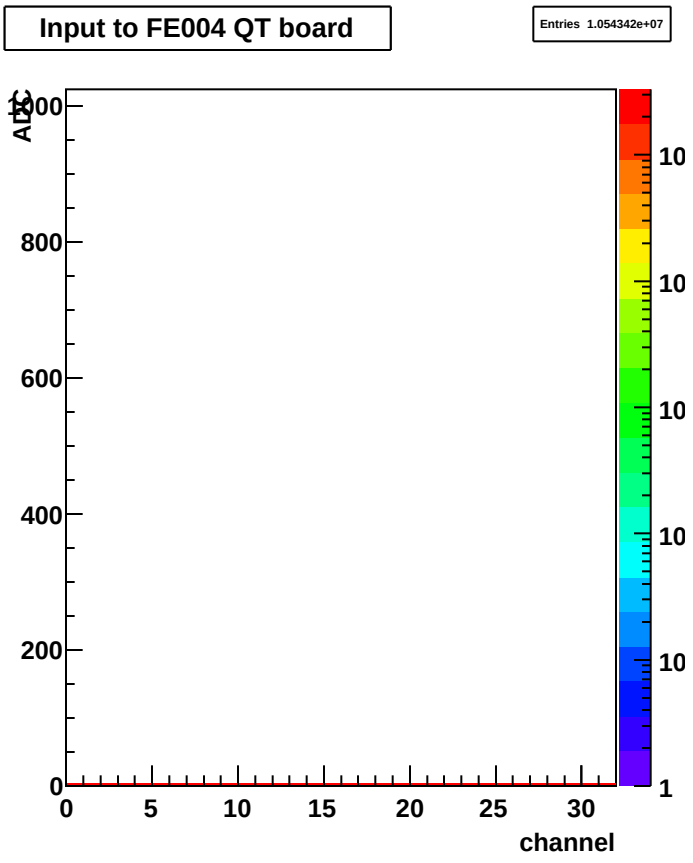

Input to FPD L2 DSM
Entries 988446
HT bits - simulated
328445
329482
329482
1037
SMALL
LARGE-S
LARGE-N

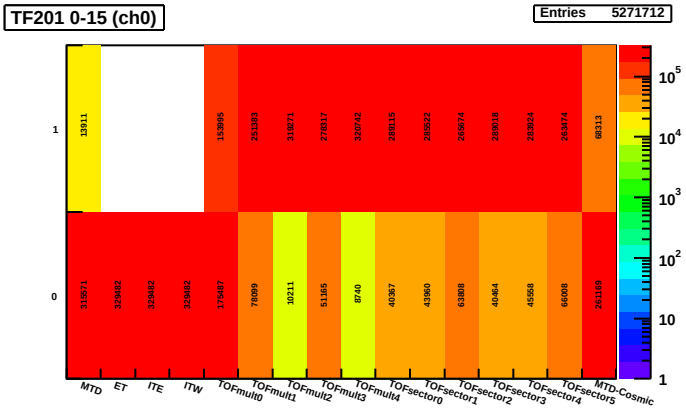

Input to FPD L2 DSM
Entries 1317928
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

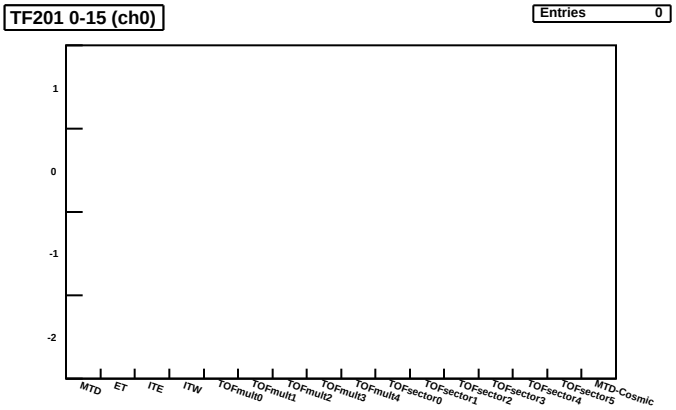

Input to FE001 QT board
Entries 1.054342e+07
ADC
channel

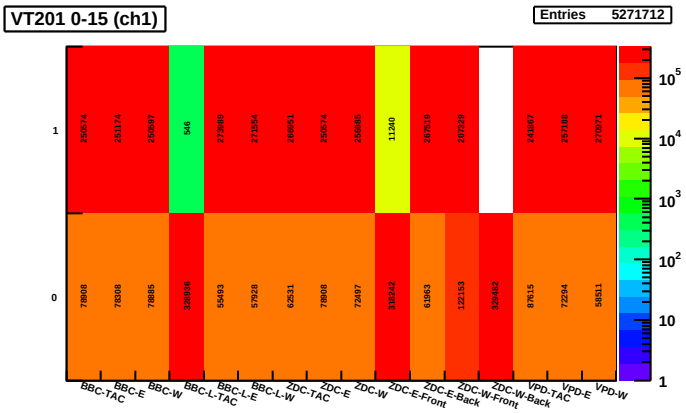

Input to FE002 QT board
Entries 1.054342e+07
ADC
channel

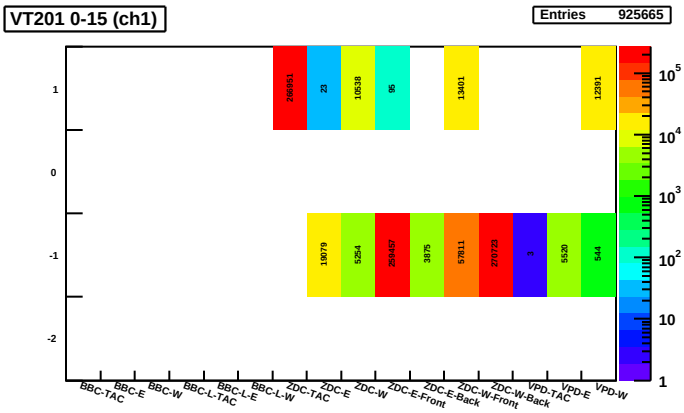

Input to FE003 QT board
Entries 1.054342e+07
ADC
channel

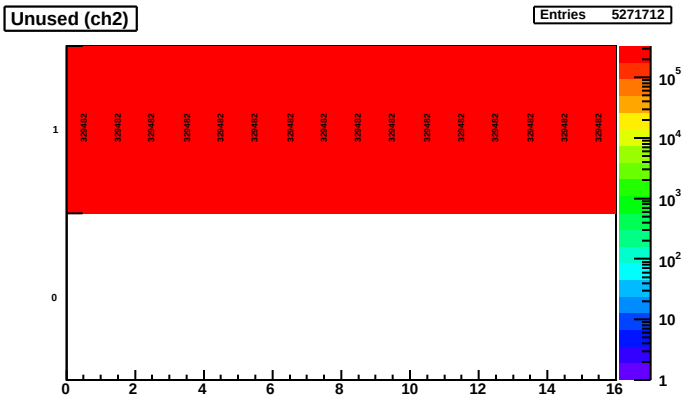

Input to FE004 QT board
Entries 1.054342e+07
ADC
channel

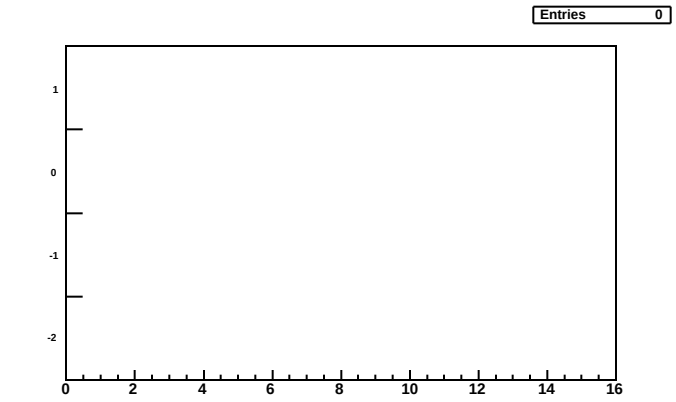

TF201 0-15 (ch0)
Entries 5271712
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

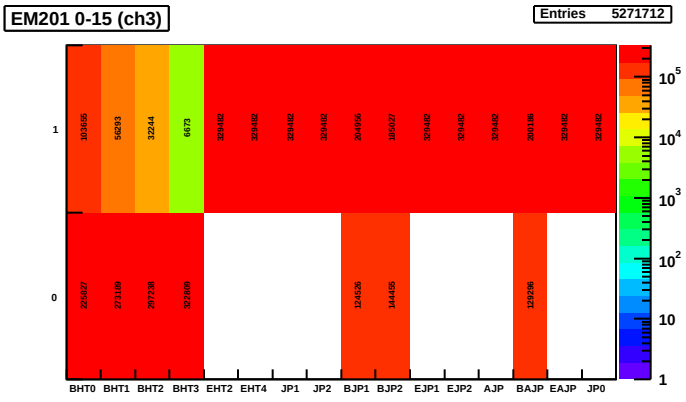

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

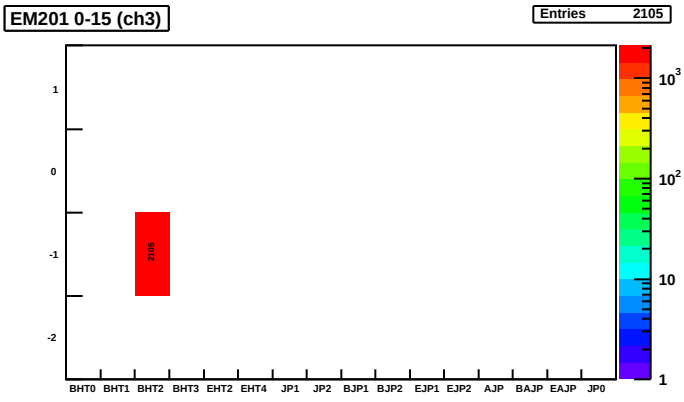

VT201 0-15 (ch1)
Entries 5271712
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 925665
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

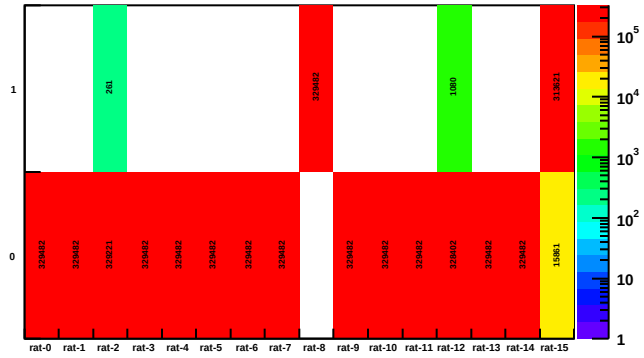

Unused (ch2)
Entries 5271712

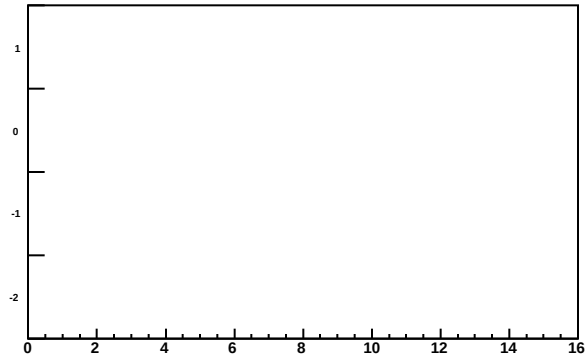

Entries 0


EM201 0-15 (ch3)
Entries 5271712
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

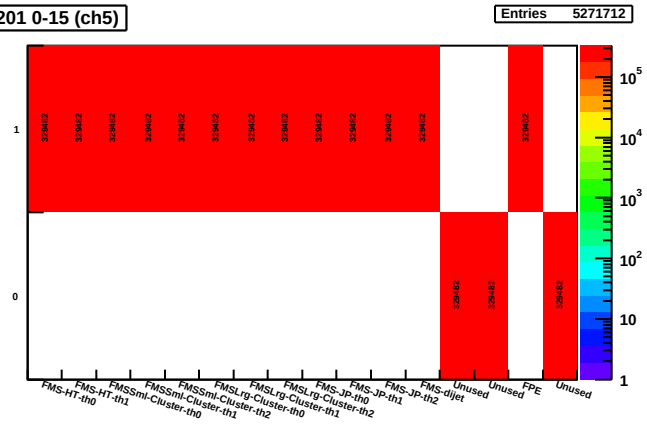

EM201 0-15 (ch3)
Entries 2105
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

RAT board (ch4)

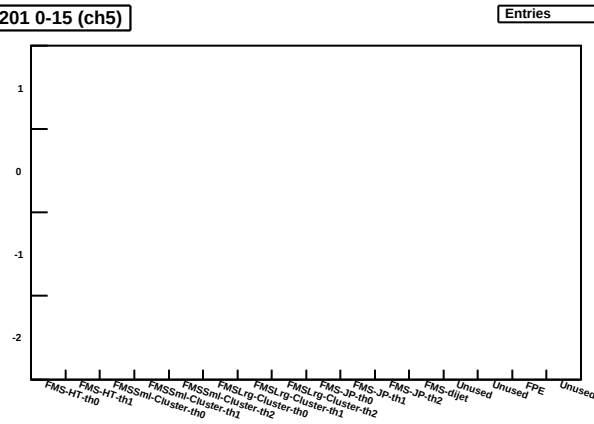




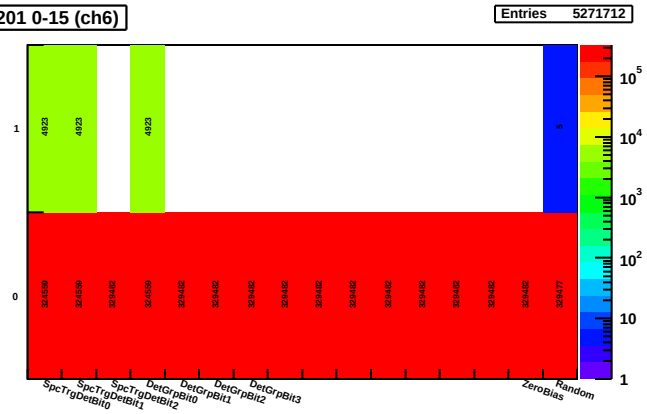
FP201 0-15 (ch5)

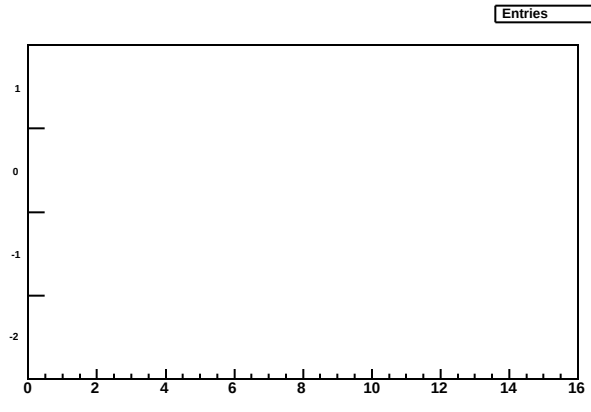
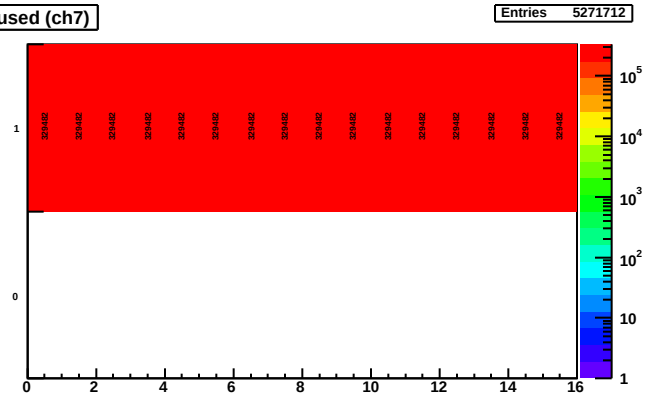
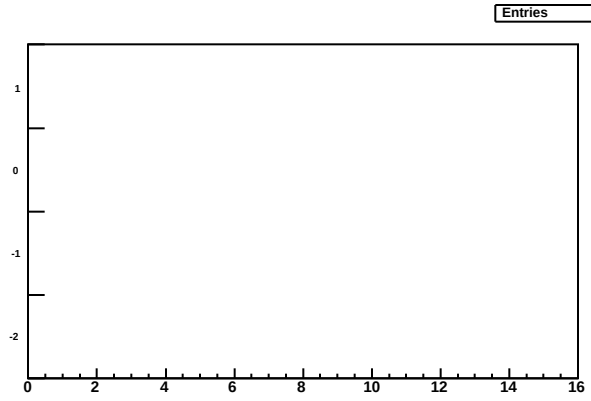


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)