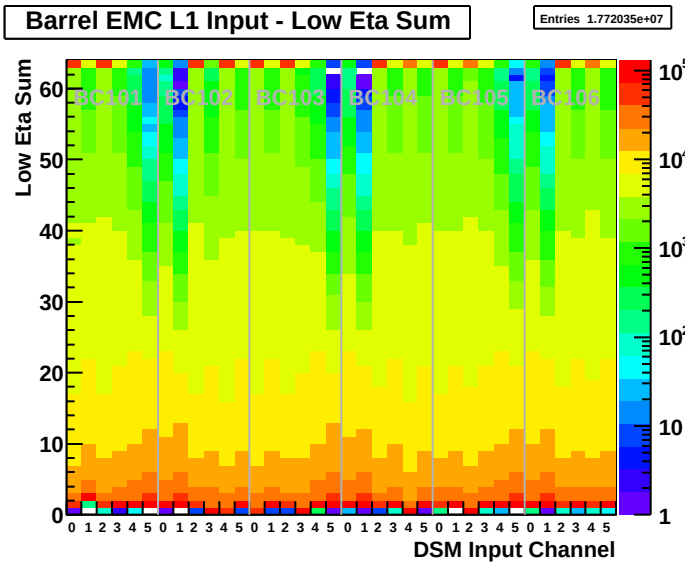
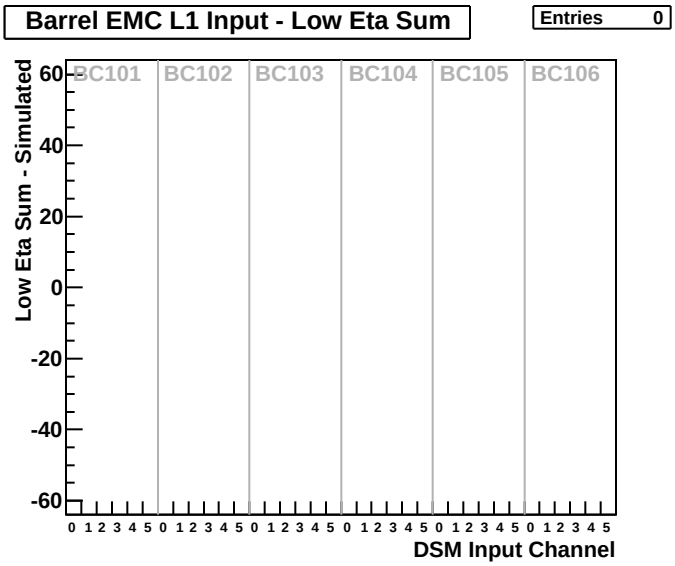
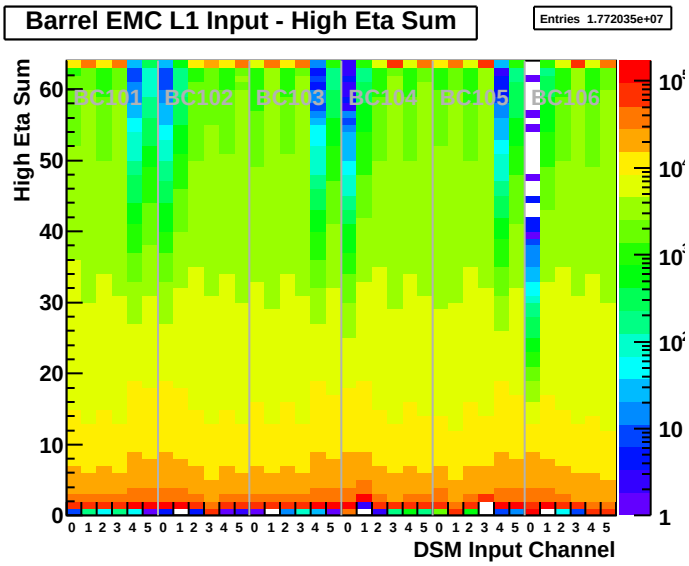
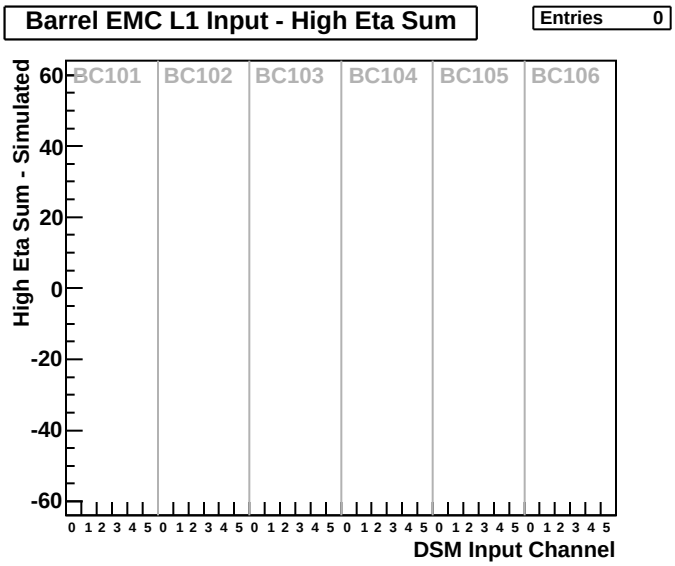
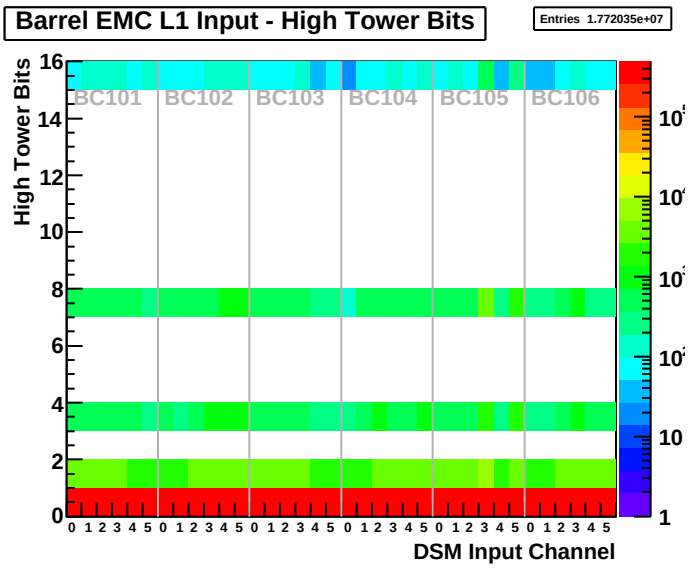
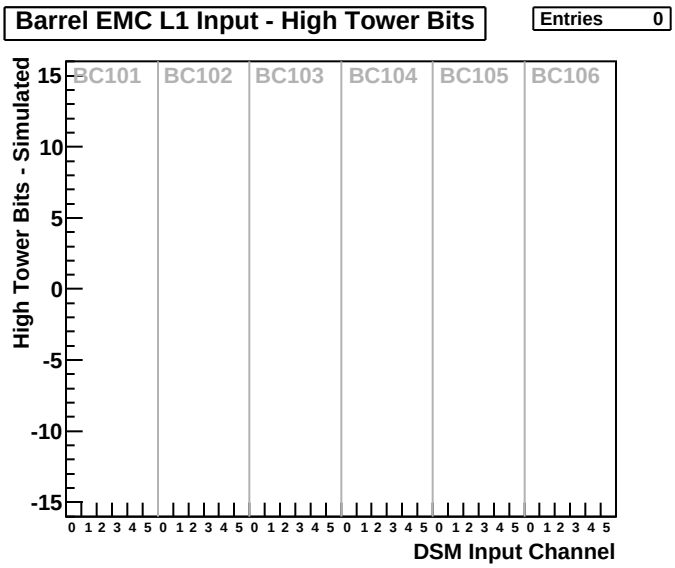

Barrel EMC L1 Input - Low Eta Sum
Entries 1.772035e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

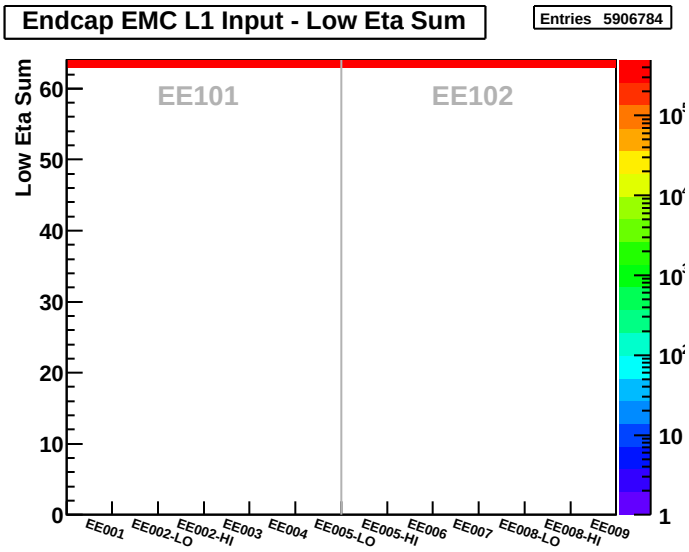

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

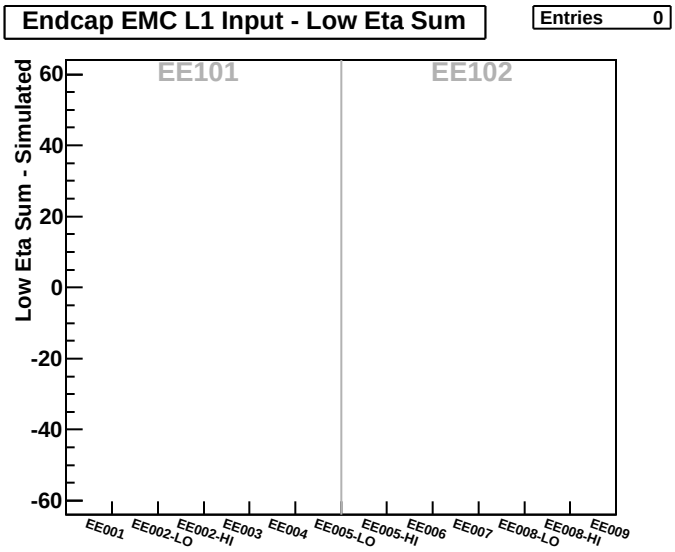

Barrel EMC L1 Input - High Eta Sum
Entries 1.772035e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

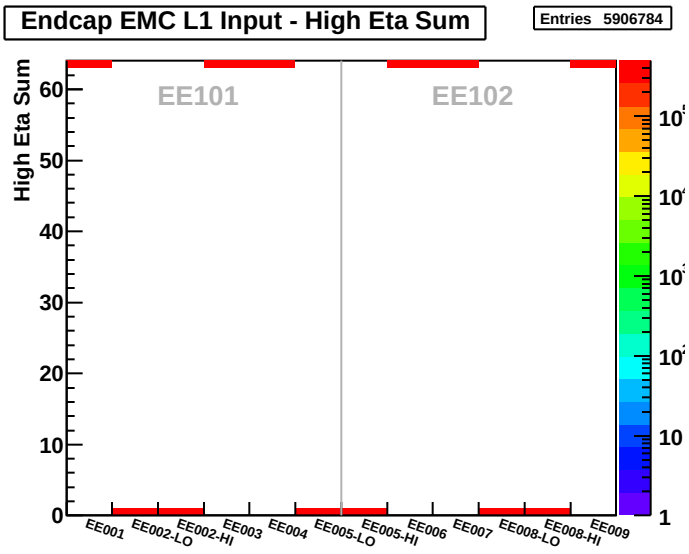

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

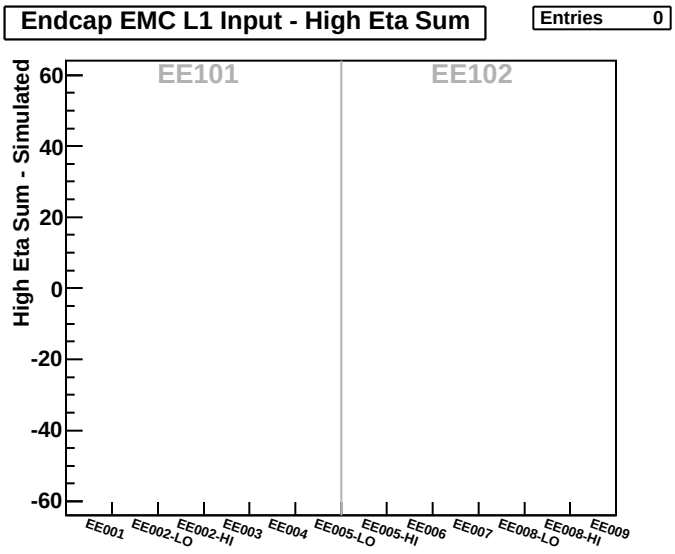

Barrel EMC L1 Input - High Tower Bits
Entries 1.772035e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

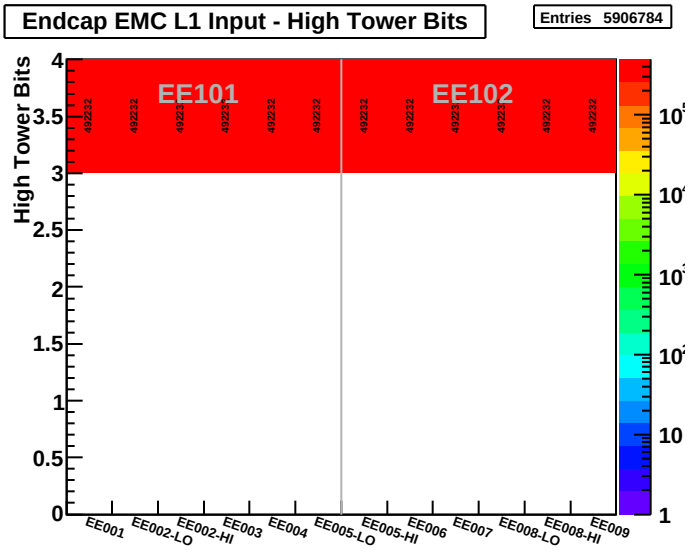

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

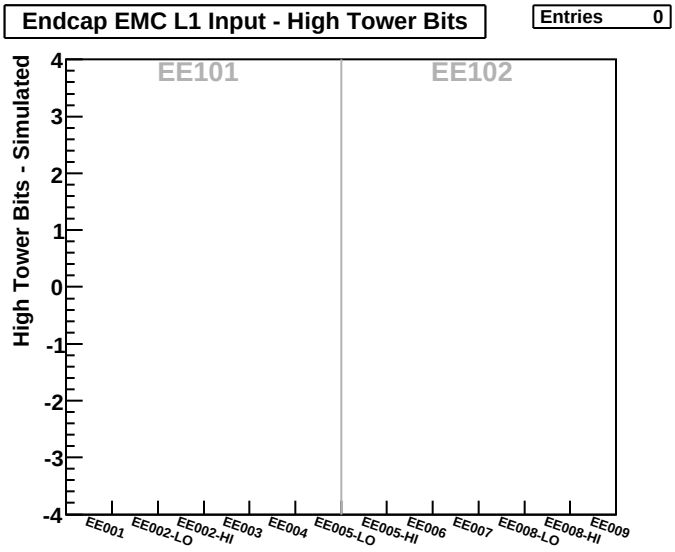

Endcap EMC L1 Input - Low Eta Sum
Entries 5906784
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

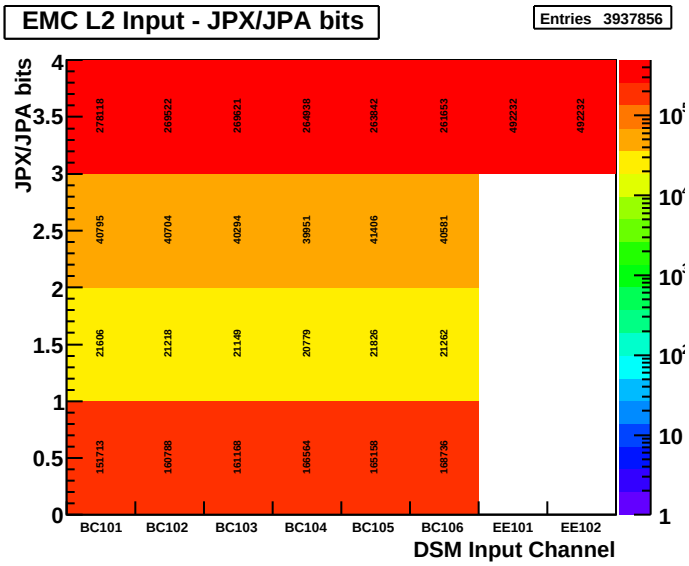

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

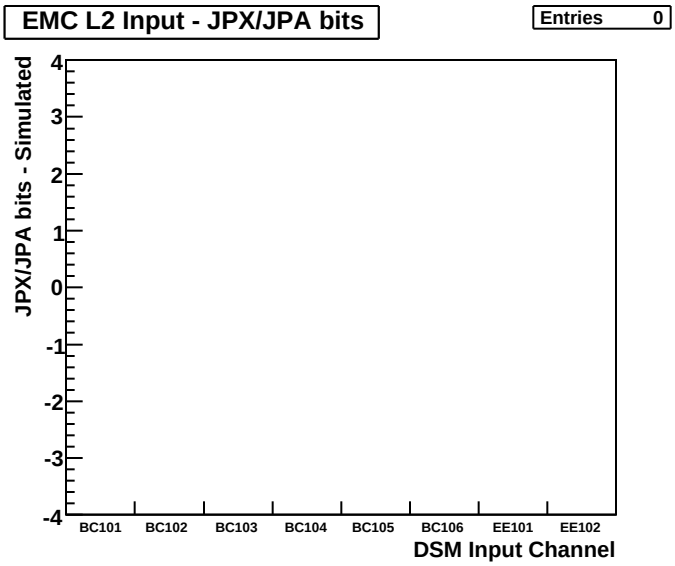

Endcap EMC L1 Input - High Eta Sum
Entries 5906784
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

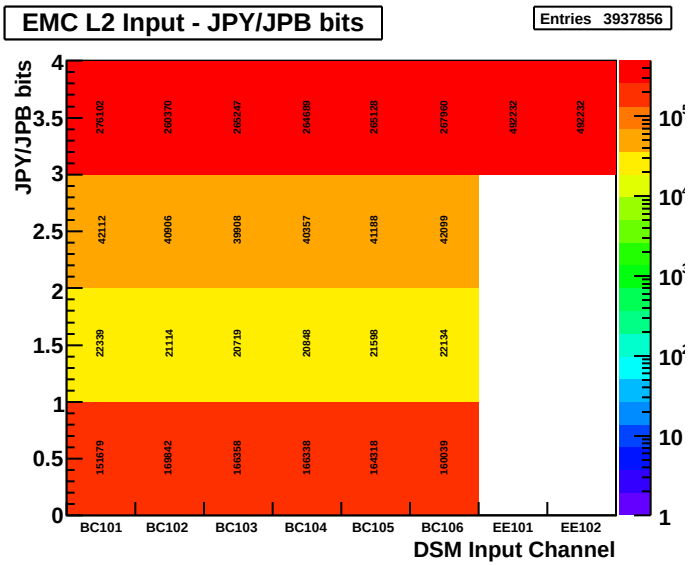

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

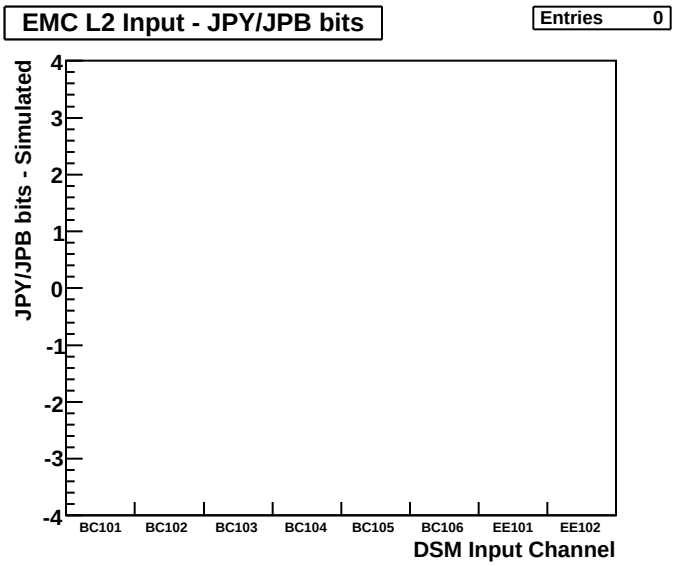

Endcap EMC L1 Input - High Tower Bits
Entries 5906784
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

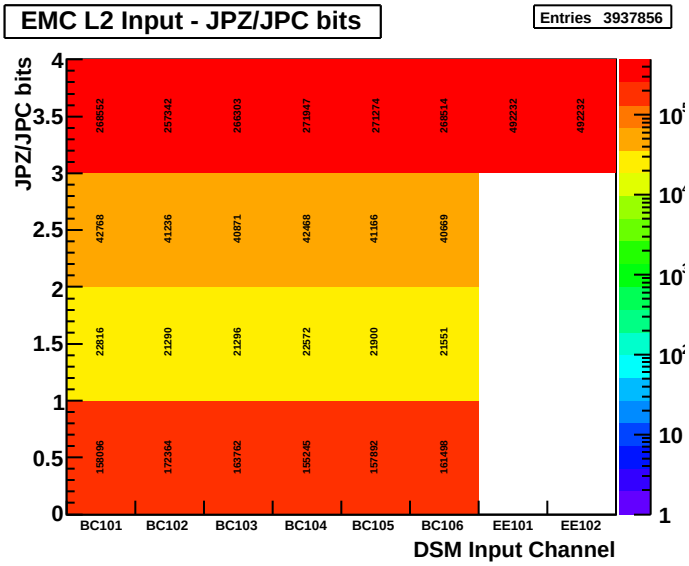

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

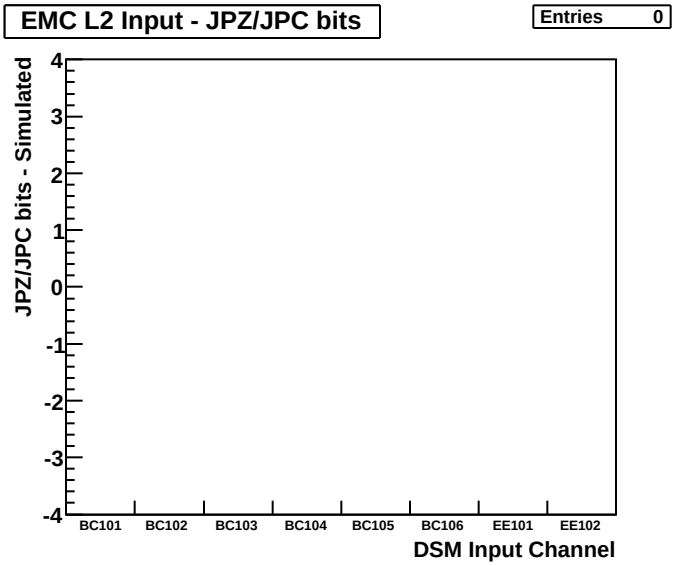

EMC L2 Input - JPX/JPA bits
Entries 3937856
JPX/JPA bits
276118 258522 258621 264038 263842 261653 492232 492232
40795 40704 40294 39951 41406 40581
21606 21218 21149 20779 21826 21262
151713 160788 161168 166564 165158 168736
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
DSM Input Channel

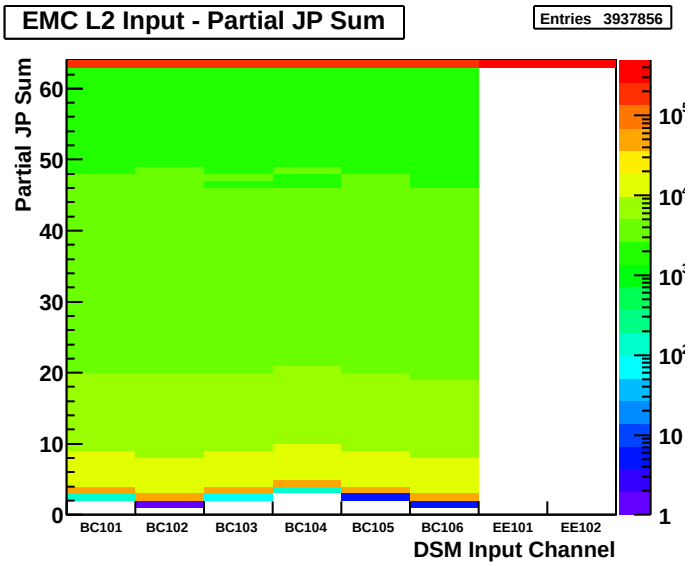

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
DSM Input Channel

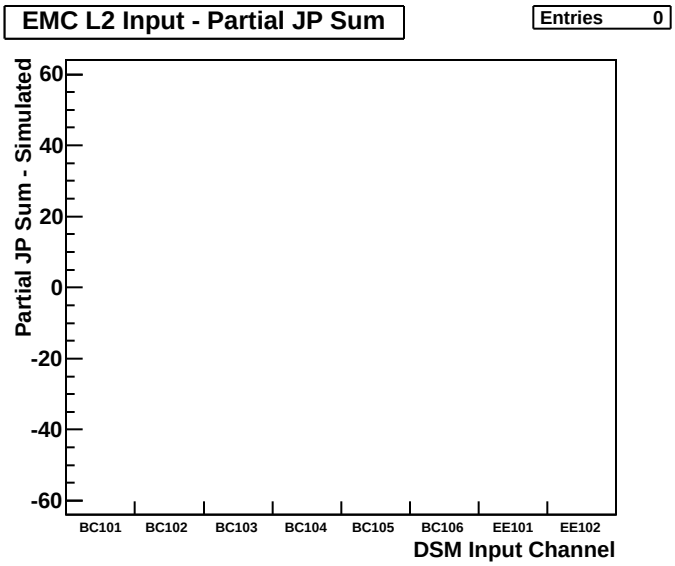

EMC L2 Input - JPY/JPB bits
Entries 3937856
JPY/JPB bits
276102 260370 265247 264689 265128 267950 492232 492232
42112 40906 39908 40357 41188 42099
22339 21114 20719 20848 21598 22134
151679 168842 166358 166338 164318 160039
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
DSM Input Channel

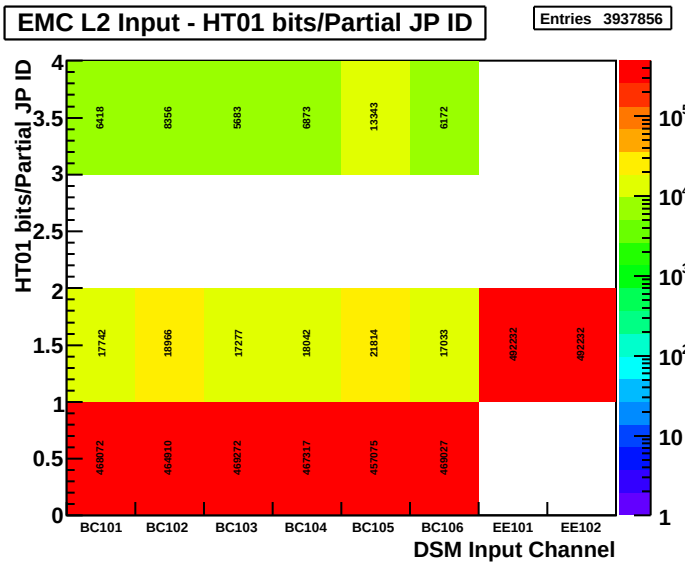

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
DSM Input Channel

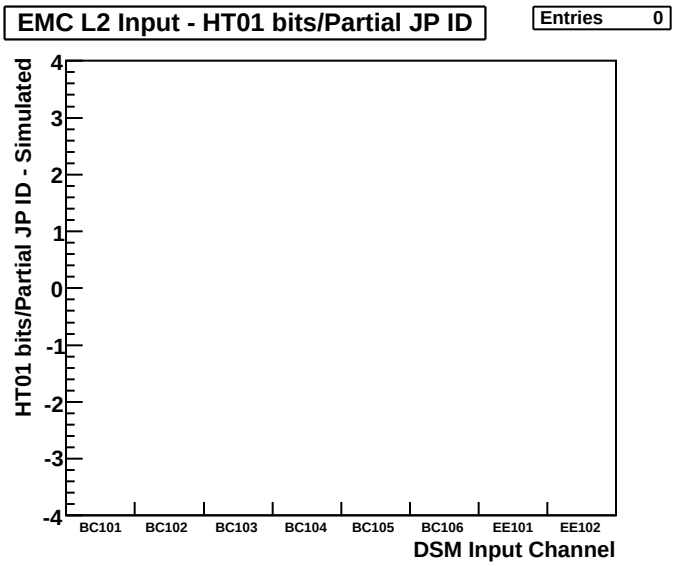

EMC L2 Input - JPZ/JPC bits
Entries 3937856
JPZ/JPC bits
268552 257342 263203 271047 272274 265514 492232 492232
42768 41236 40871 42468 41166 40669
22816 21290 21296 22572 21900 21551
158096 172364 163762 165245 157892 161498
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
DSM Input Channel

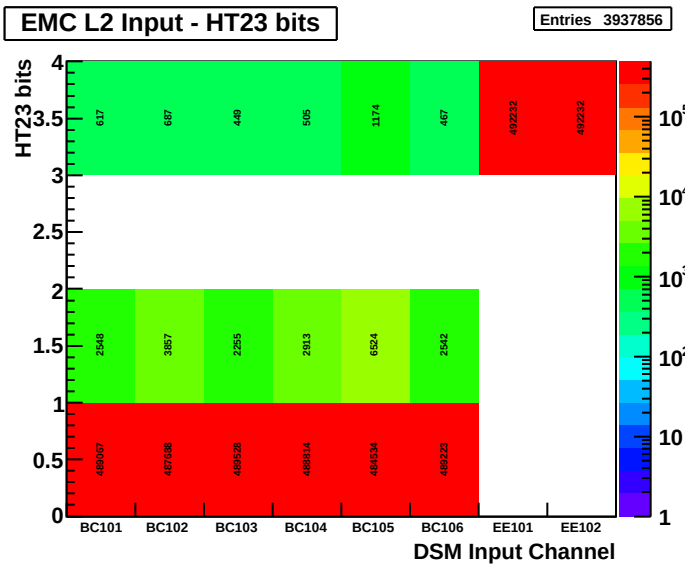

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
DSM Input Channel

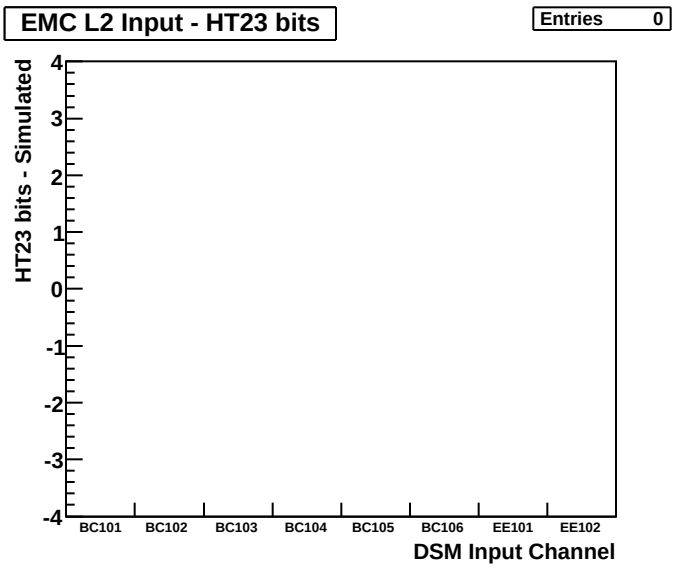

EMC L2 Input - Partial JP Sum
Entries 3937856
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

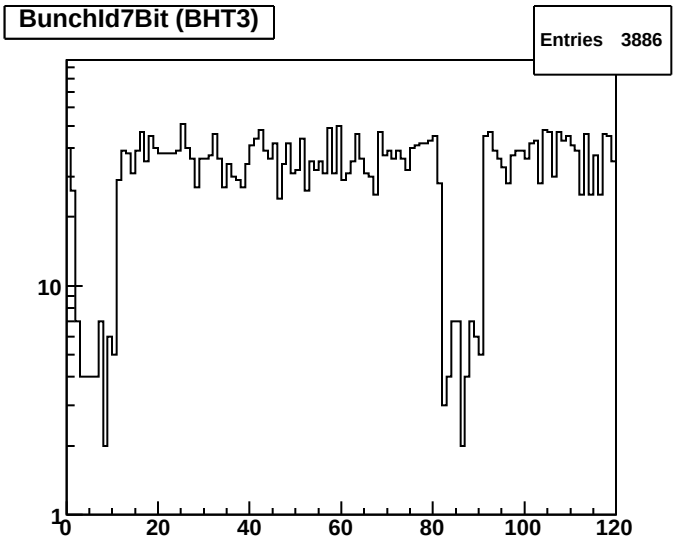

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

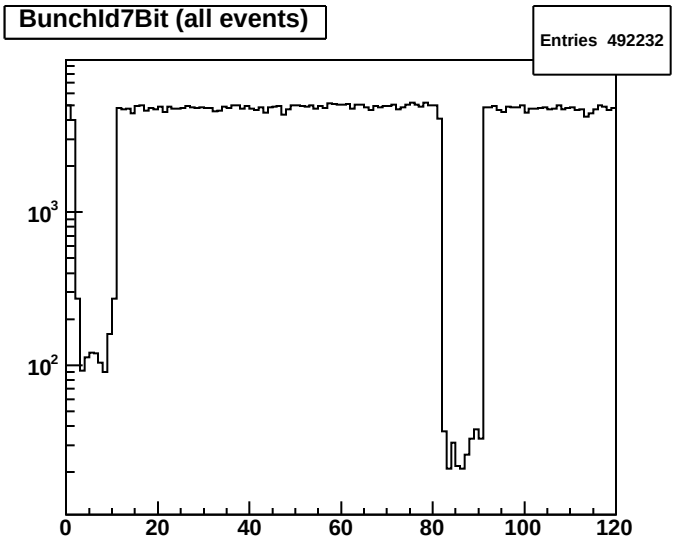

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3937856
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

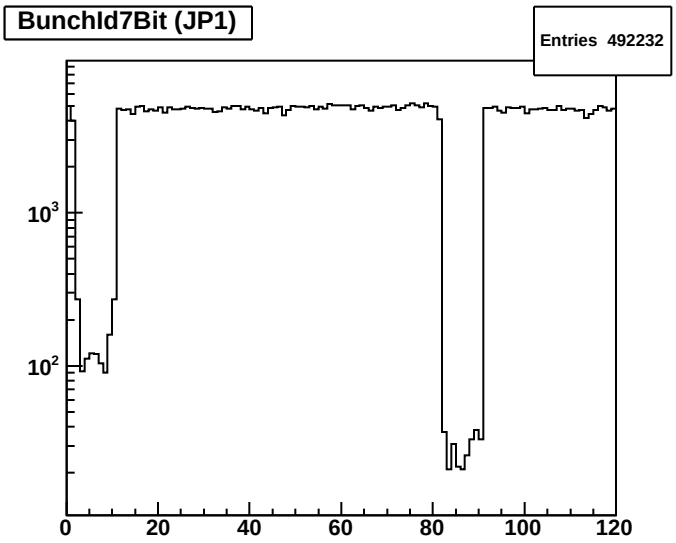

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

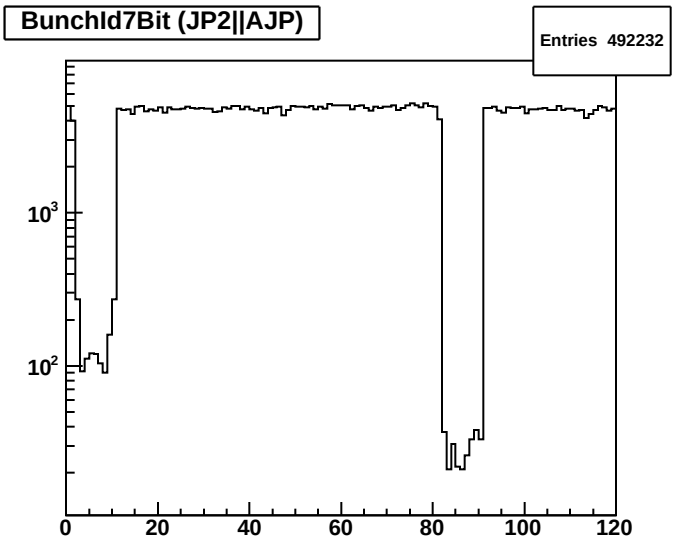

EMC L2 Input - HT23 bits
Entries 3937856
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

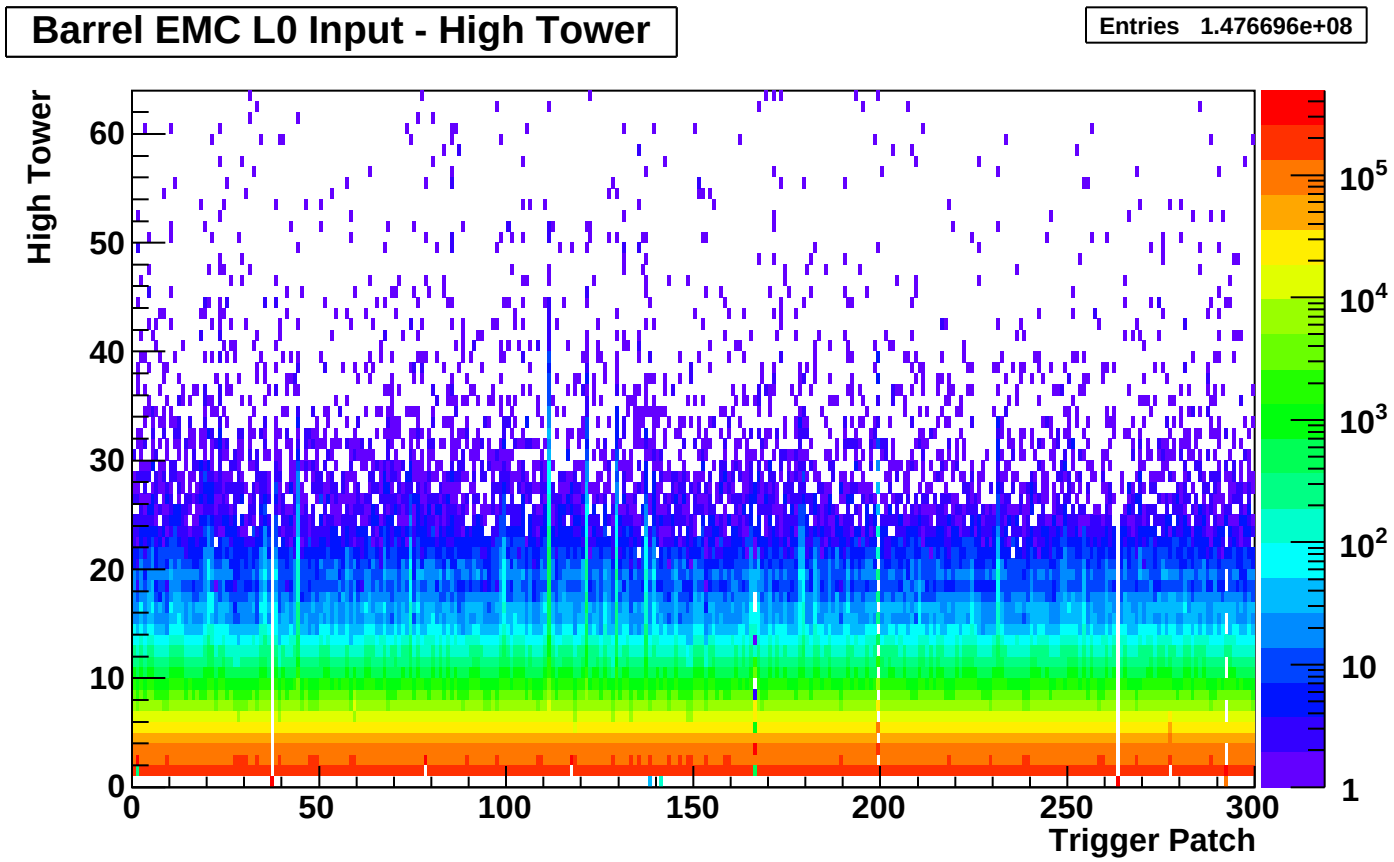

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

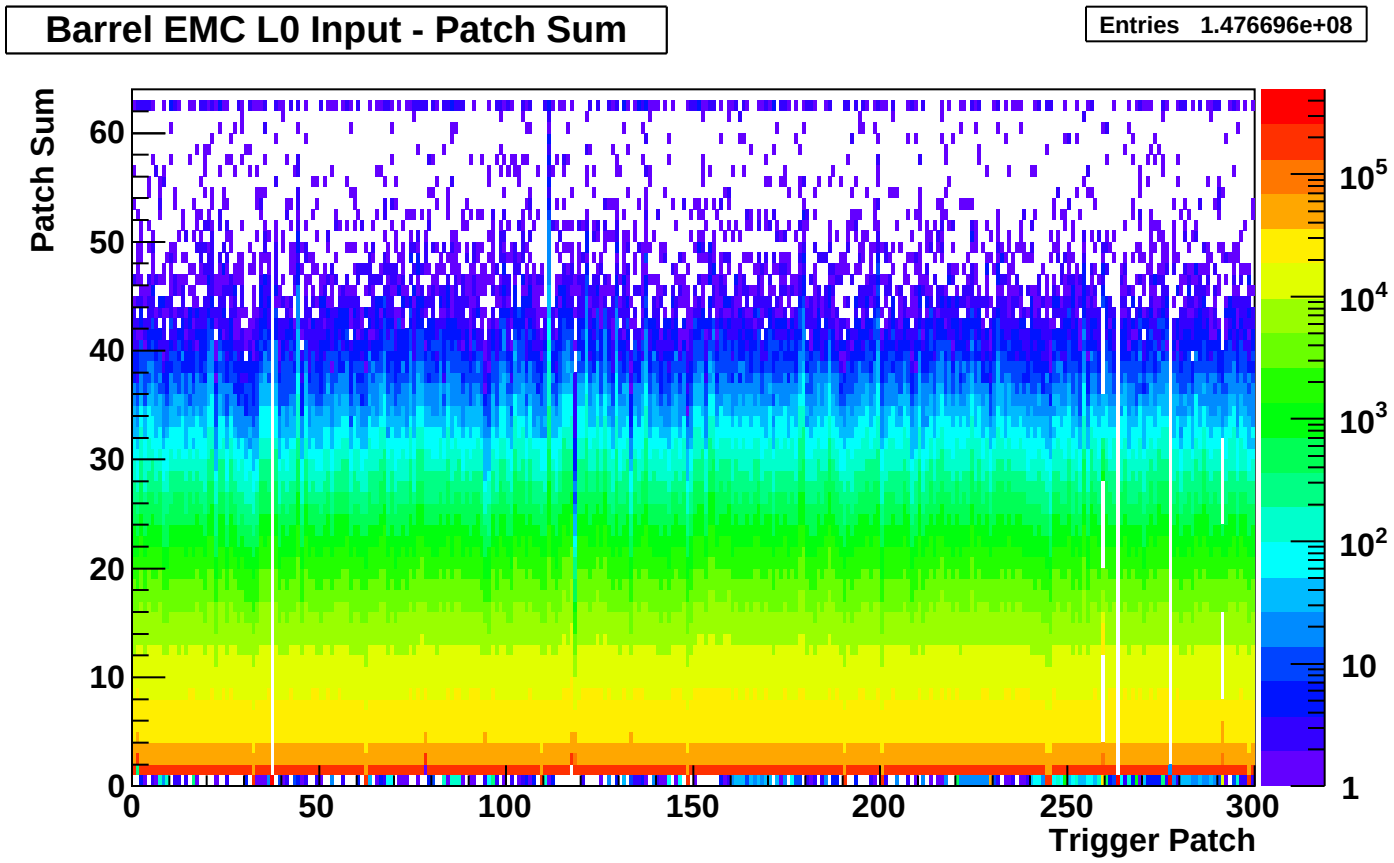

BunchId7Bit (BHT3)
Entries 3886
10
1
0
20
40
60
80
100
120

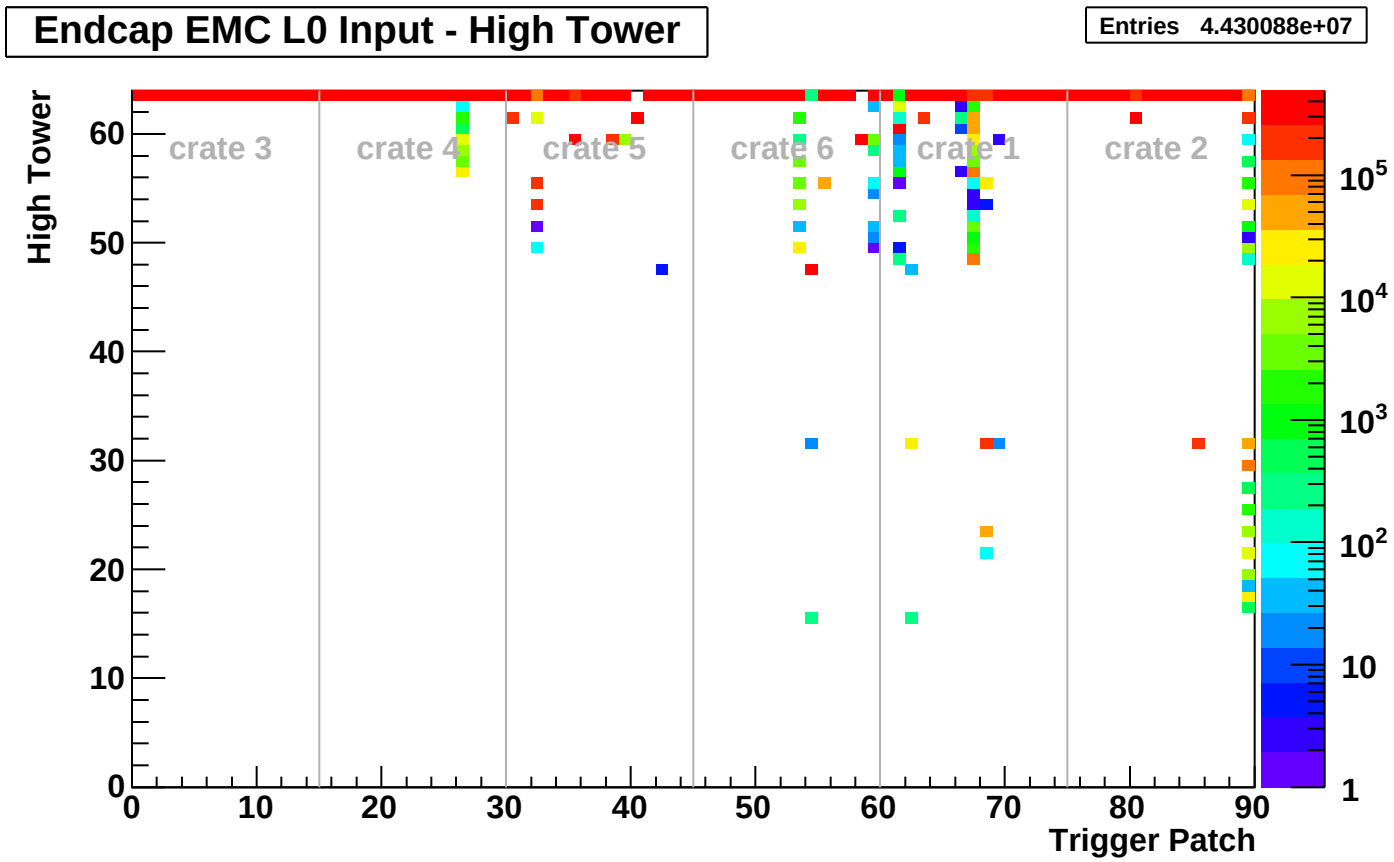

BunchId7Bit (all events)
Entries 492232
10^3
10^2
0
20
40
60
80
100
120

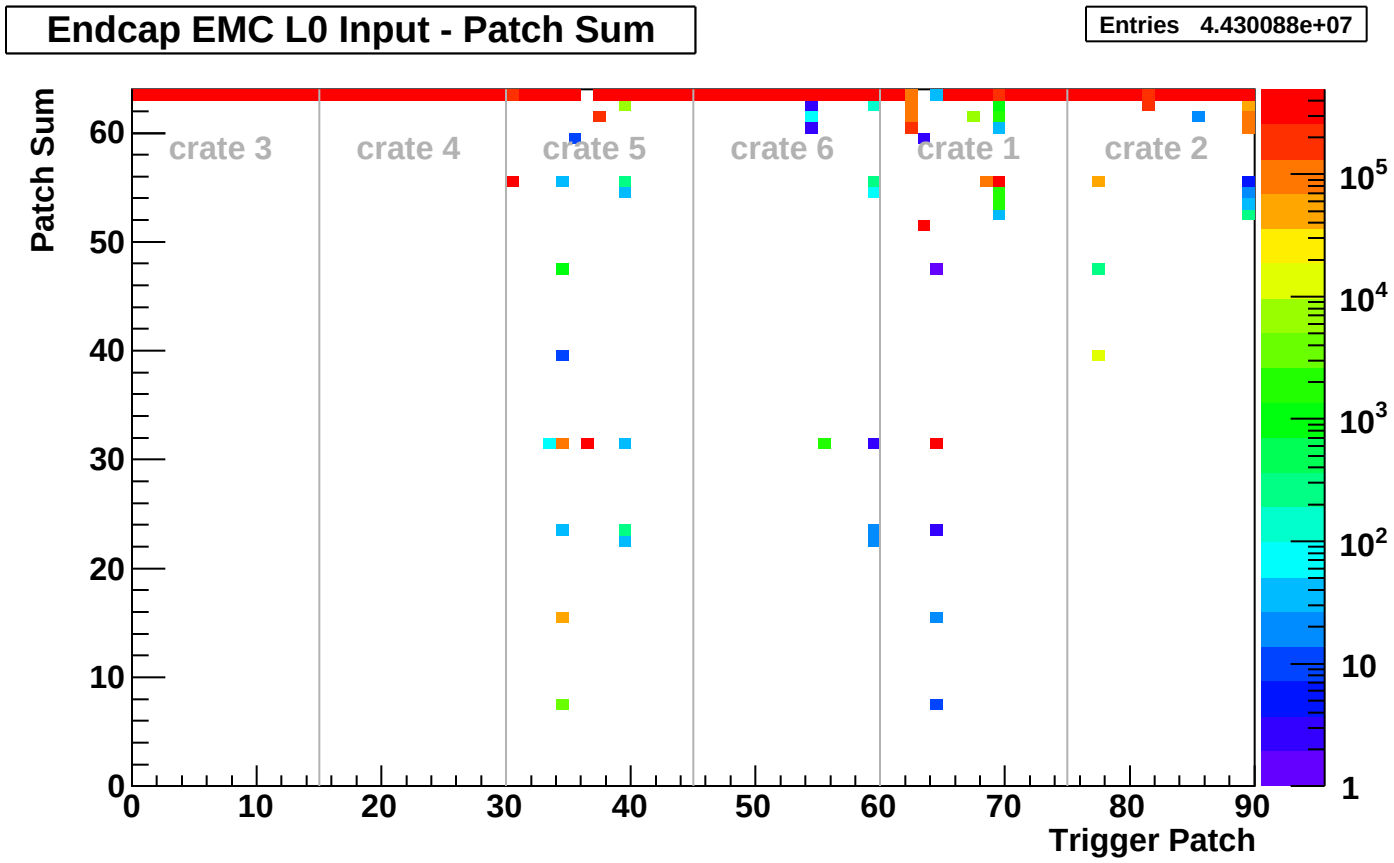

BunchId7Bit (JP1)
Entries 492232
10^3
10^2
0
20
40
60
80
100
120

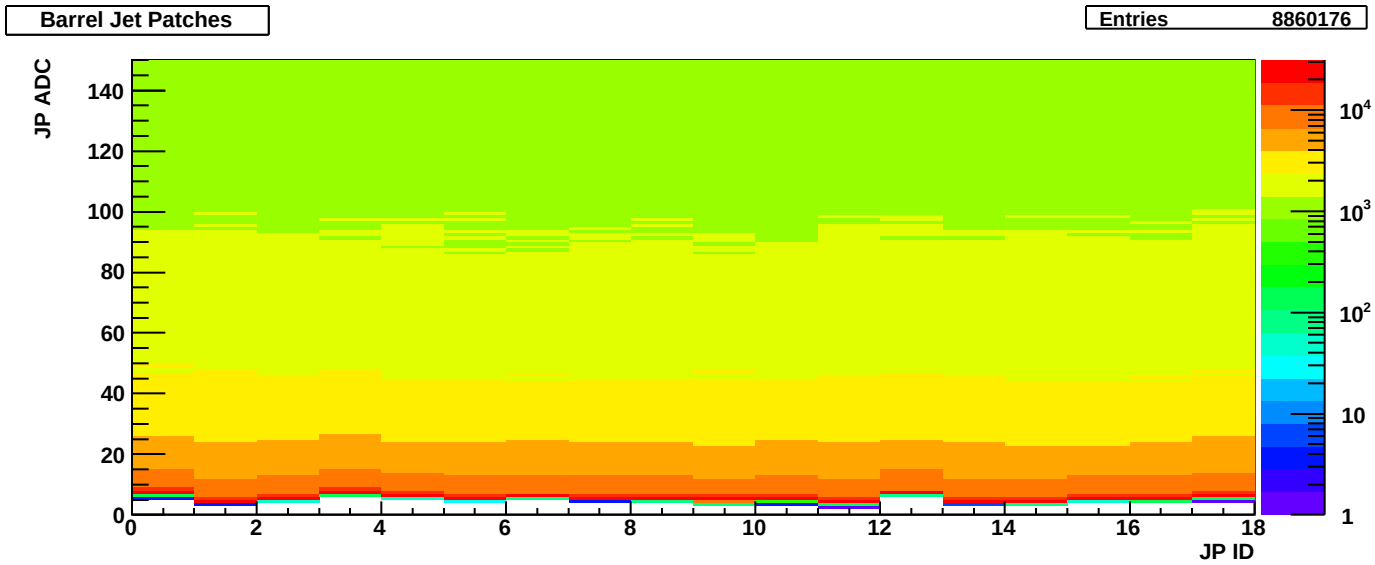

BunchId7Bit (JP2||AJP)
Entries 492232
10^3
10^2
0
20
40
60
80
100
120

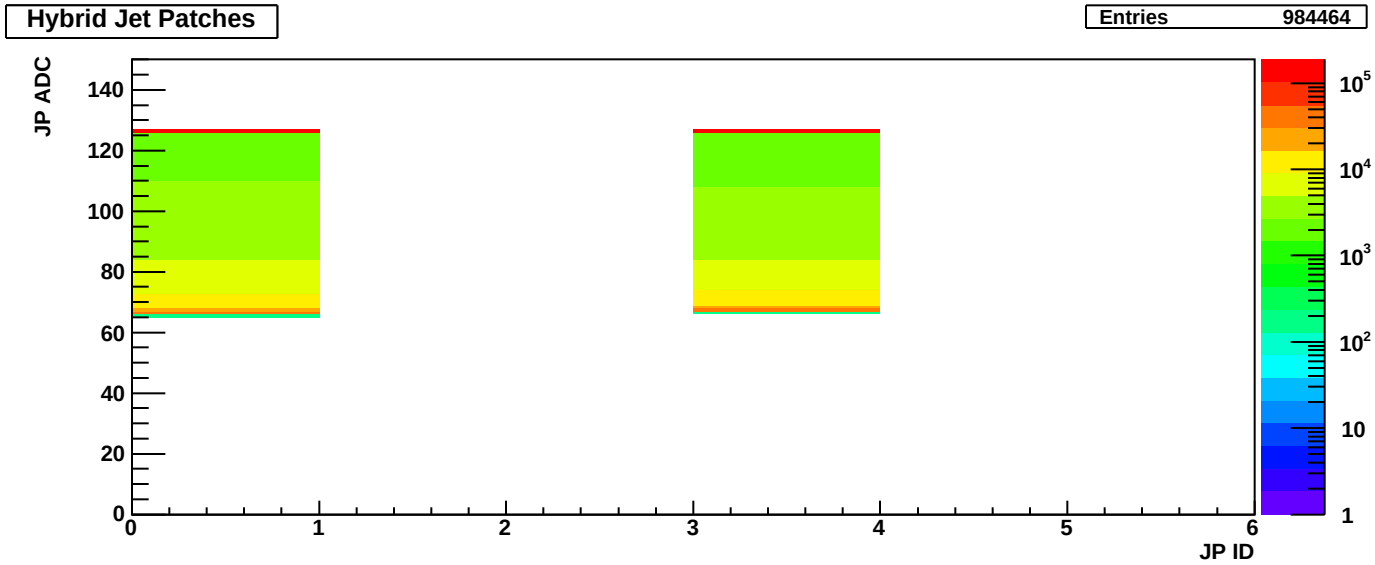

Barrel EMC L0 Input - High Tower
Entries 1.476696e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

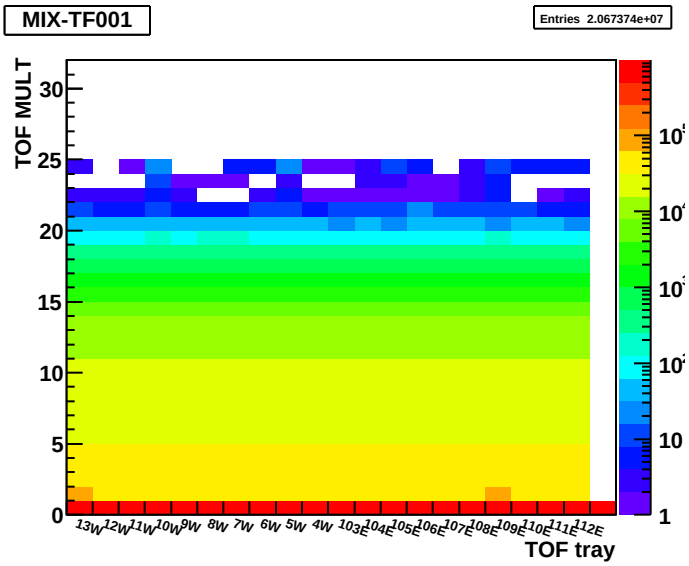

Barrel EMC L0 Input - Patch Sum
Entries 1.476696e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

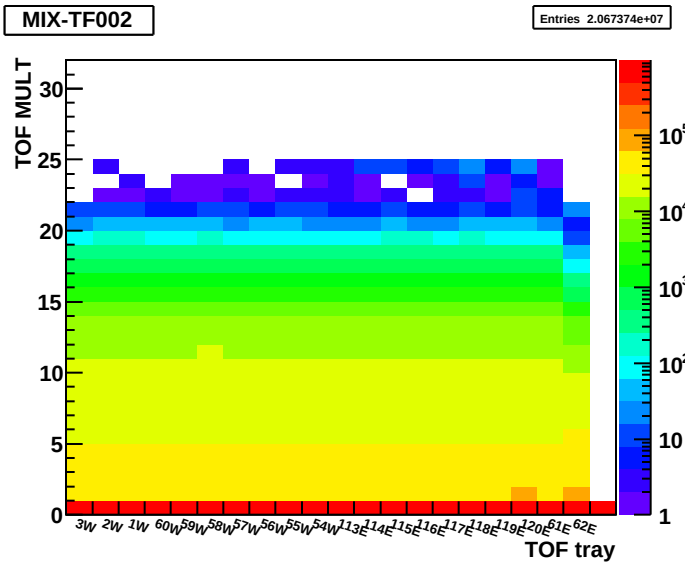

Endcap EMC L0 Input - High Tower
Entries 4.430088e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

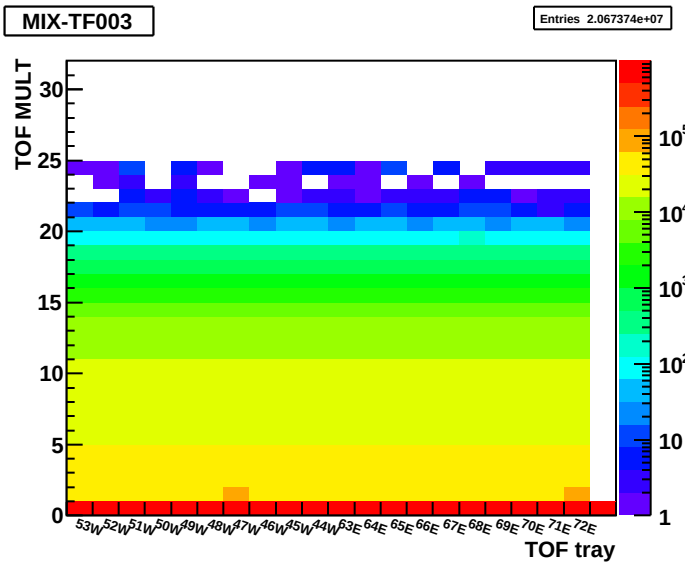

Endcap EMC L0 Input - Patch Sum
Entries 4.430088e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

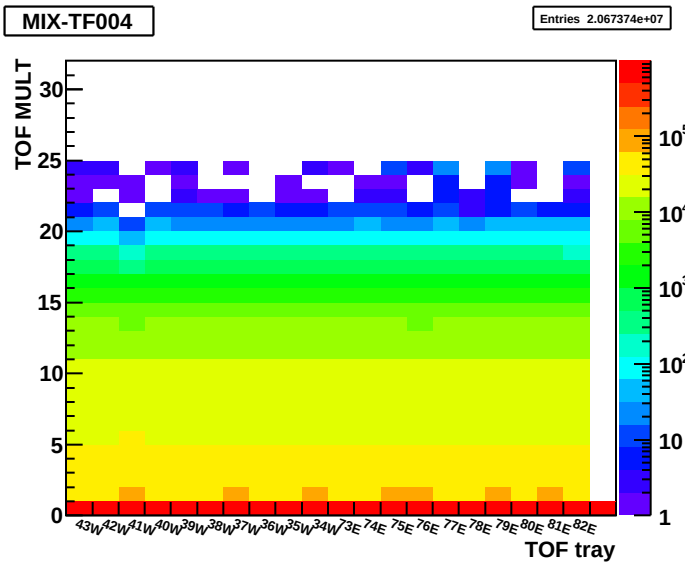

Barrel Jet Patches
Entries 8860176
JP ADC
JP ID

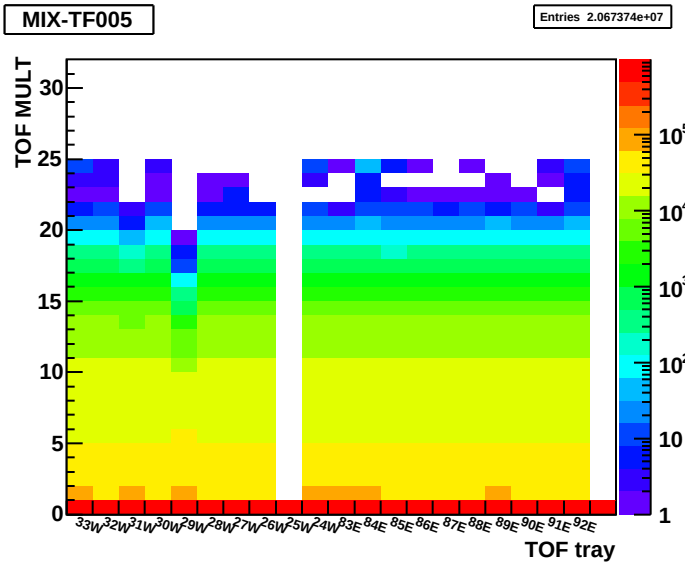

Hybrid Jet Patches
Entries 984464
JP ADC
JP ID

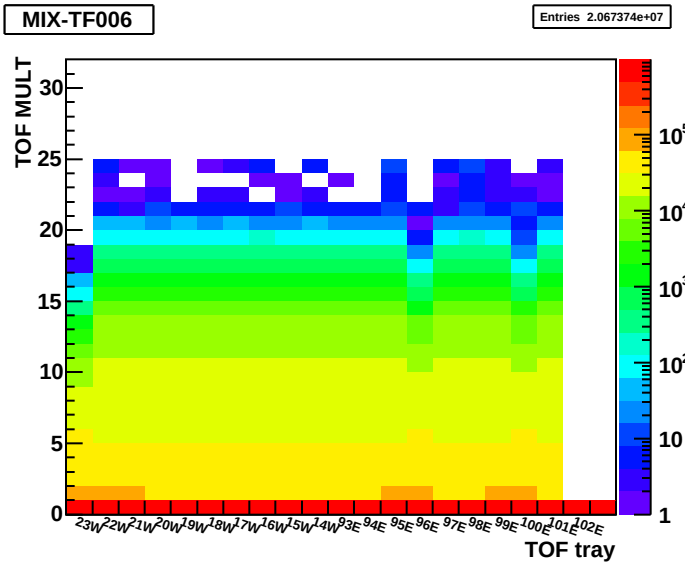

MIX-TF001
Entries 2.067374e+07
TOF MULT
TOF tray

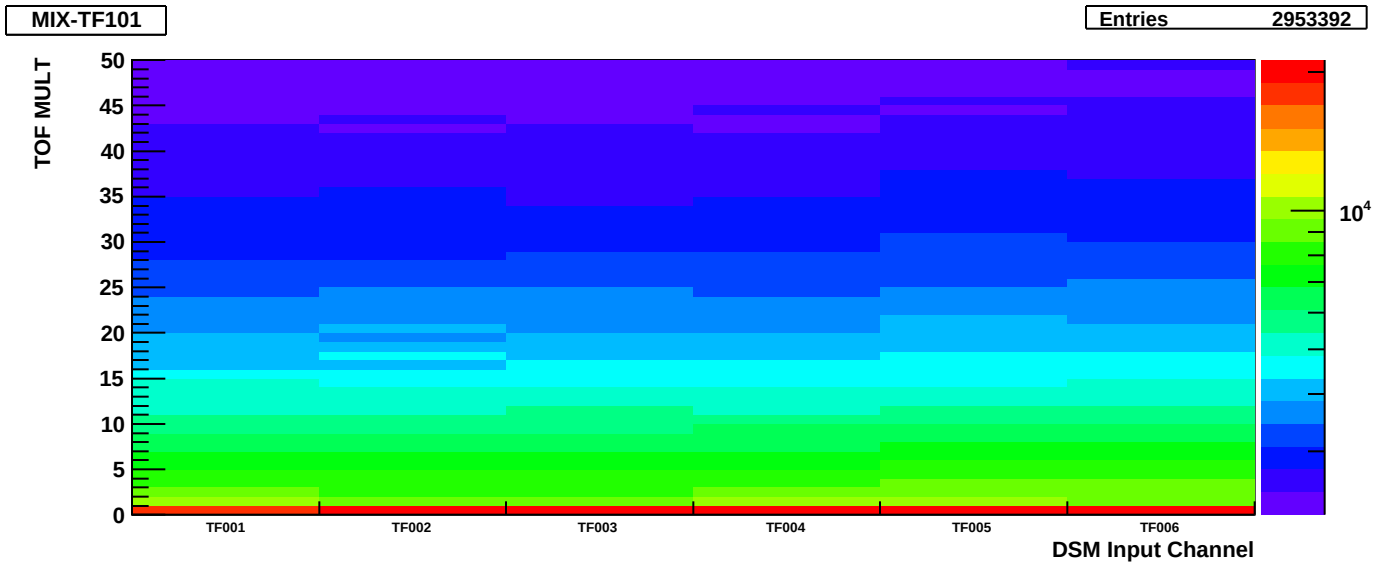

MIX-TF002
Entries 2.067374e+07
TOF MULT
TOF tray

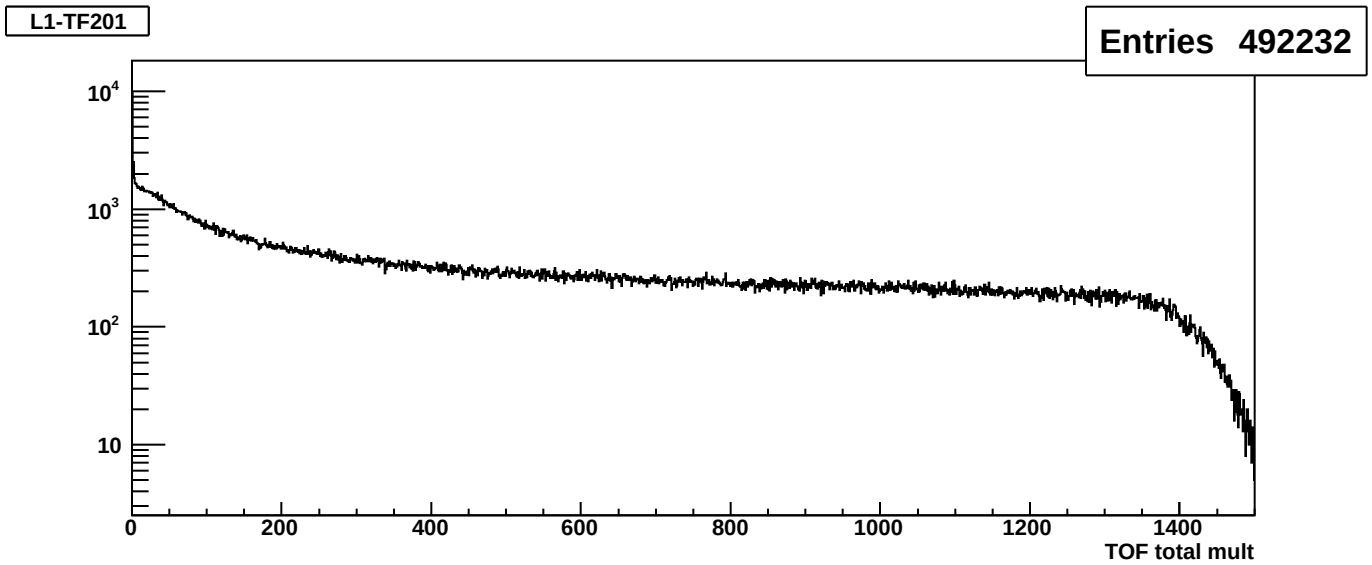

MIX-TF003
Entries 2.067374e+07
TOF MULT
TOF tray

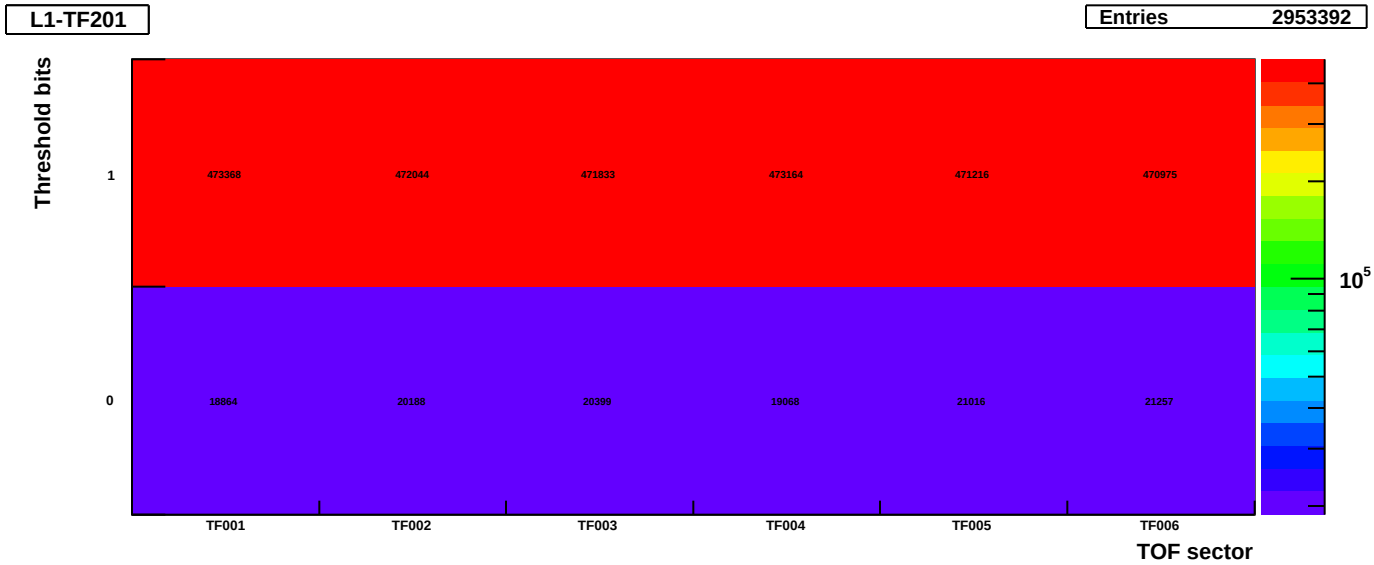

MIX-TF004
Entries 2.067374e+07
TOF MULT
TOF tray

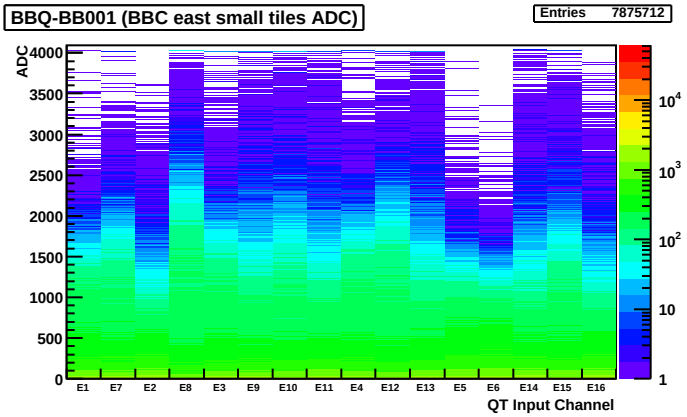

MIX-TF005
Entries 2.067374e+07
TOF MULT
TOF tray

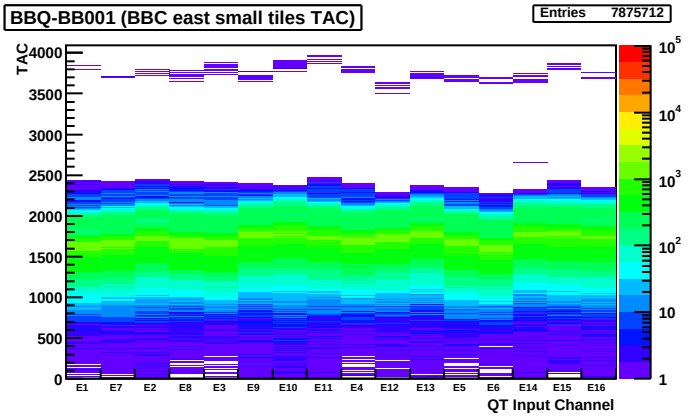

MIX-TF006
Entries 2.067374e+07
TOF MULT
TOF tray

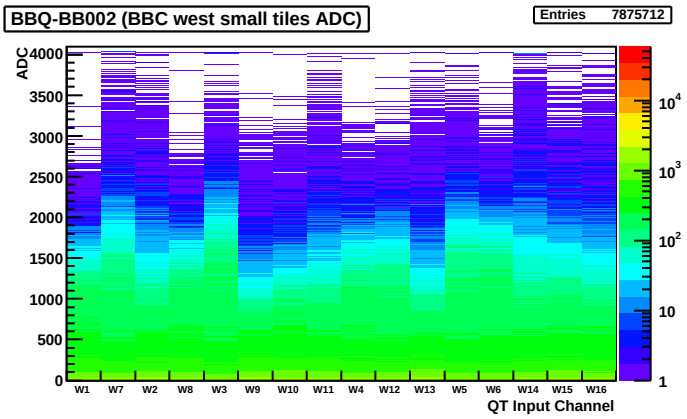

MIX-TF101
Entries 2953392
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

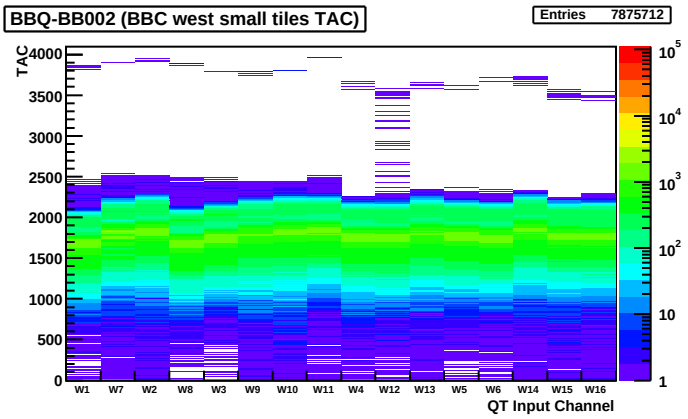

L1-TF201
Entries 492232
10^4
10^3
10^2
10
0
200
400
600
800
1000
1200
1400
TOF total mult

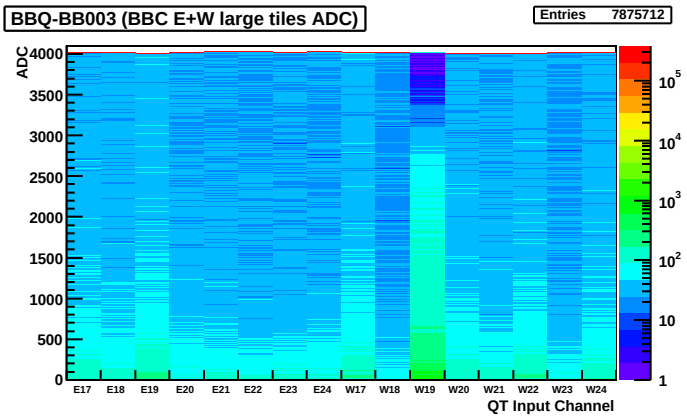

L1-TF201
Entries 2953392
Threshold bits
1
0
473368
472044
471833
473164
471216
470975
18864
20188
20399
19068
21016
21257
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

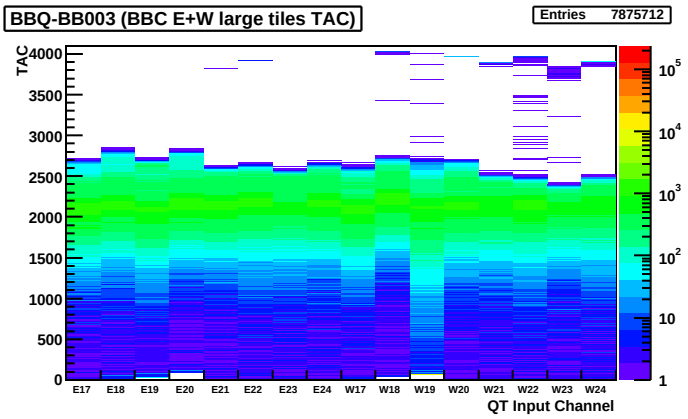

BBQ-BB001 (BBC east small tiles ADC)
Entries 7875712
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

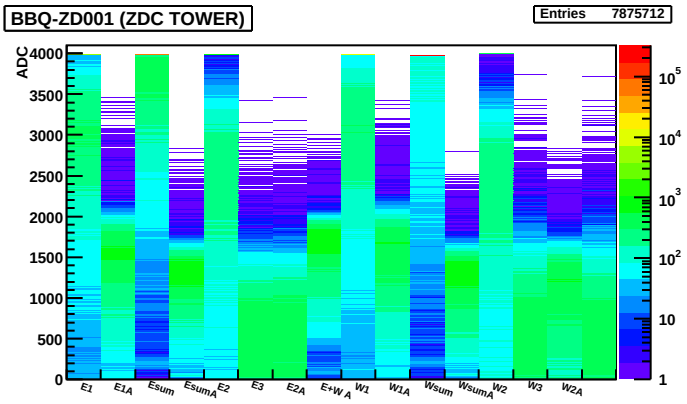

BBQ-BB001 (BBC east small tiles TAC)
Entries 7875712
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

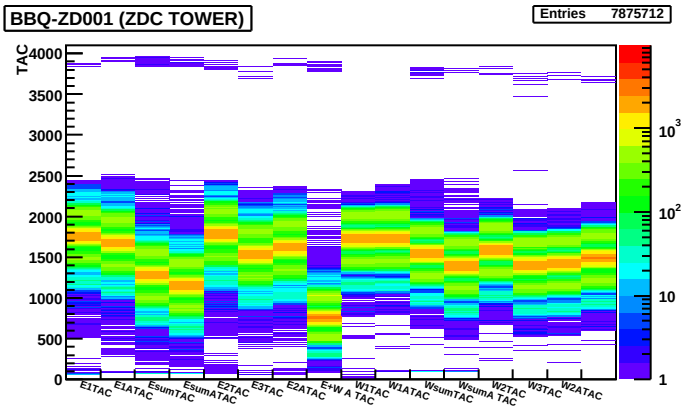

BBQ-BB002 (BBC west small tiles ADC)
Entries 7875712
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

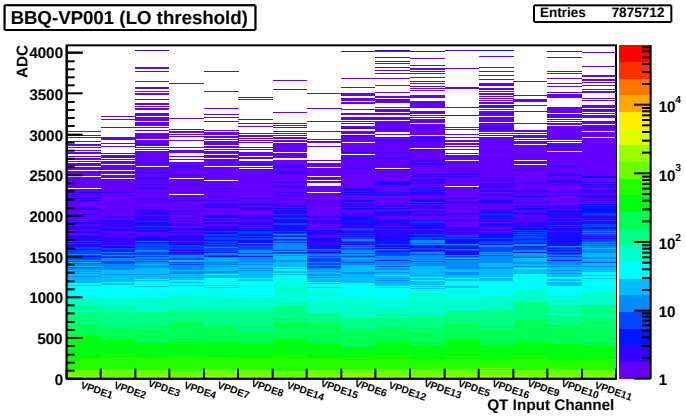

BBQ-BB002 (BBC west small tiles TAC)
Entries 7875712
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

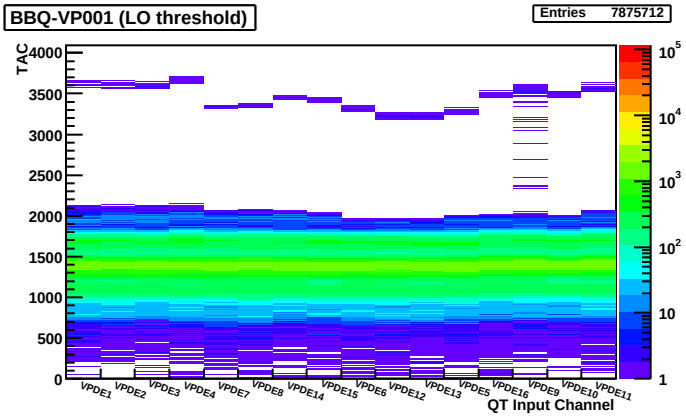

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 7875712
ADC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

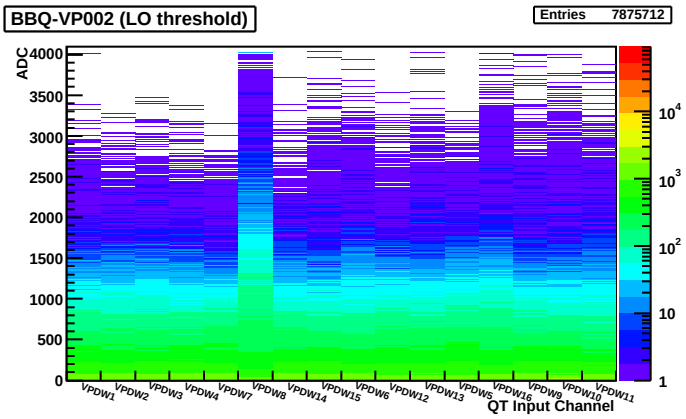

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 7875712
TAC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

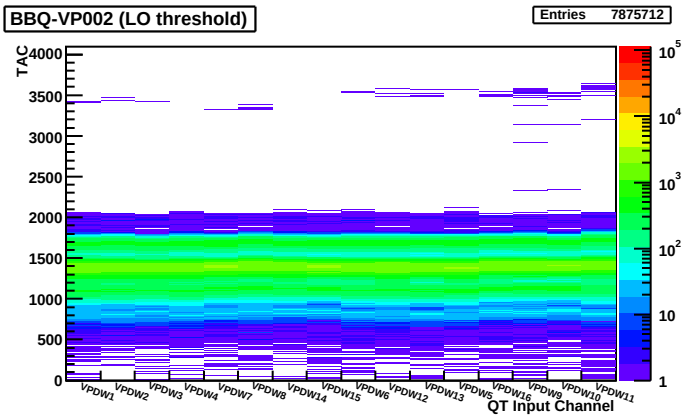

BBQ-ZD001 (ZDC TOWER)
Entries 7875712
ADC
E1 E1A Esum EsumA E2 E3 E2A E+W A W1 W1A Wsum WsumA W2 W3 W2A

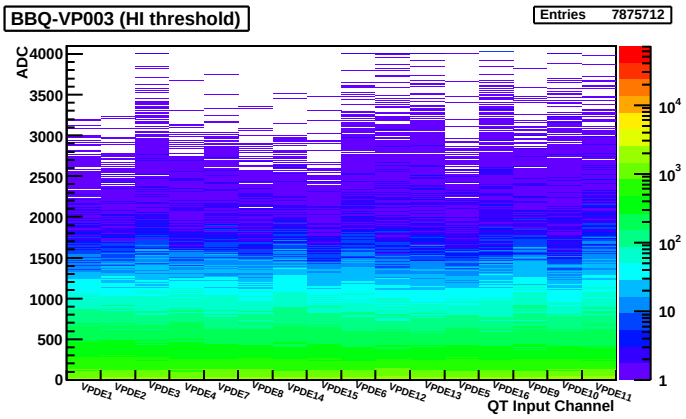

BBQ-ZD001 (ZDC TOWER)
Entries 7875712
TAC
E1TAC E1ATAC EsumTAC EsumATAC E2TAC E3TAC E2ATAC E+W A TAC W1TAC W1ATAC WsumTAC WsumA TAC W2TAC W3TAC W2ATAC

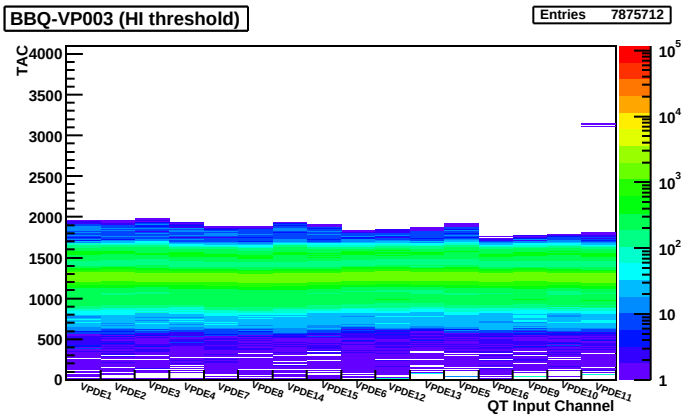

BBQ-VP001 (LO threshold)
Entries 7875712
ADC
QT Input Channel

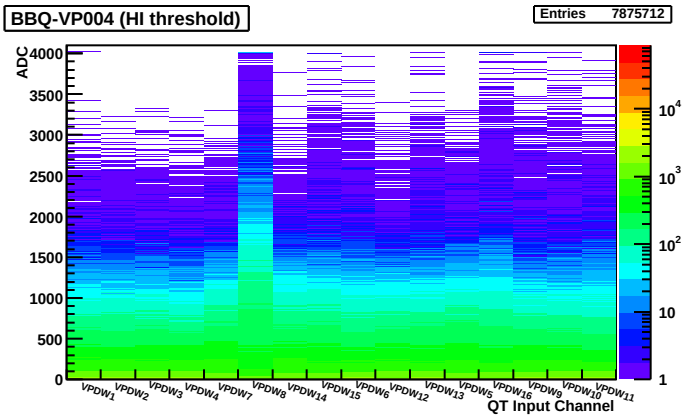

BBQ-VP001 (LO threshold)
Entries 7875712
TAC
QT Input Channel

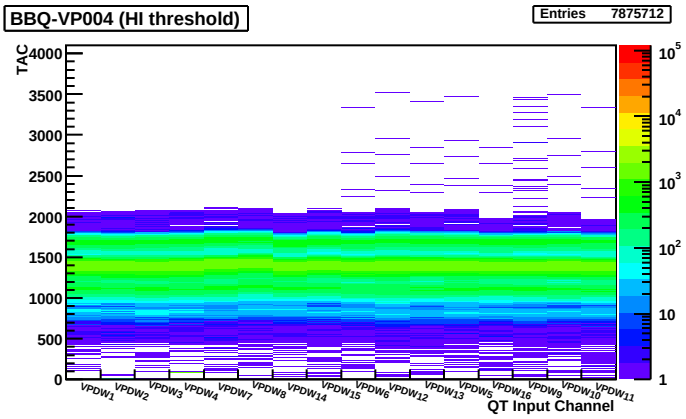

BBQ-VP002 (LO threshold)
Entries 7875712
ADC
QT Input Channel

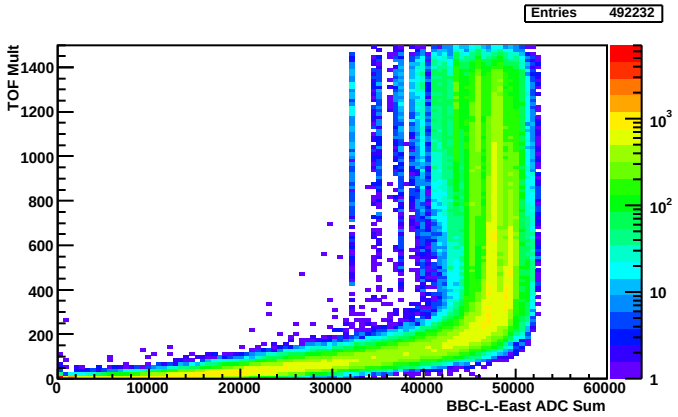

BBQ-VP002 (LO threshold)
Entries 7875712
TAC
QT Input Channel

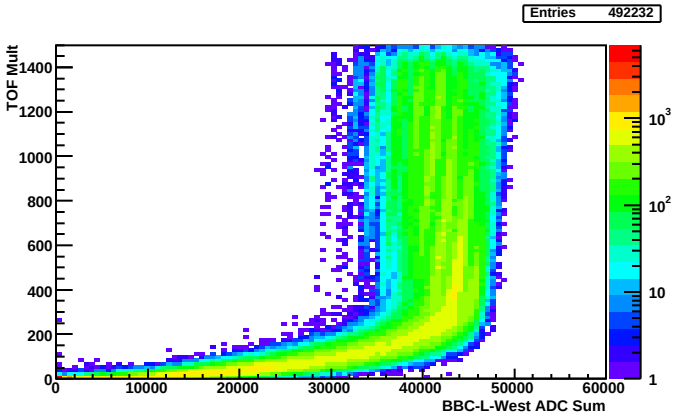

BBQ-VP003 (HI threshold)
Entries 7875712
ADC
QT Input Channel

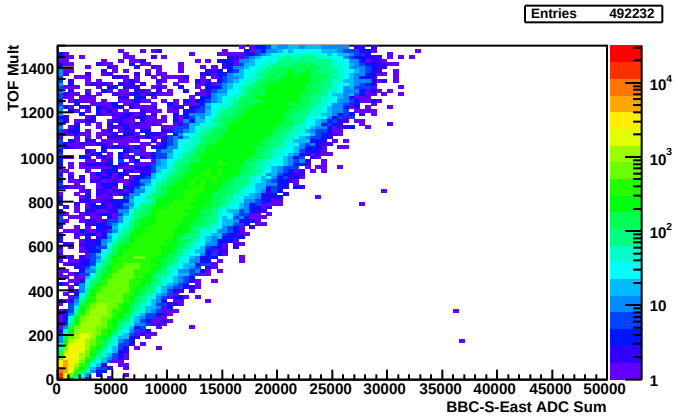

BBQ-VP003 (HI threshold)
Entries 7875712
TAC
QT Input Channel

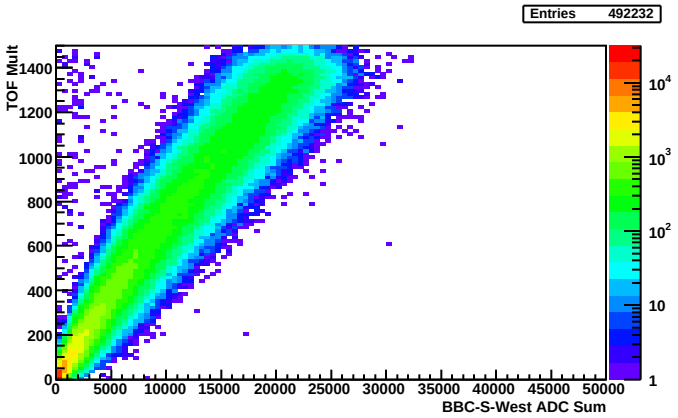

BBQ-VP004 (HI threshold)
Entries 7875712
ADC
QT Input Channel

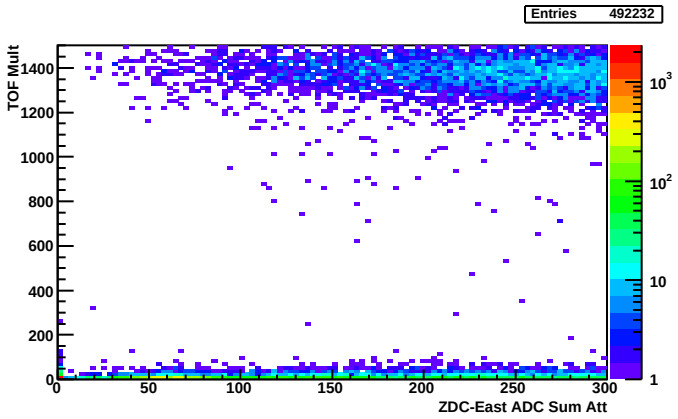

BBQ-VP004 (HI threshold)
Entries 7875712
TAC
QT Input Channel

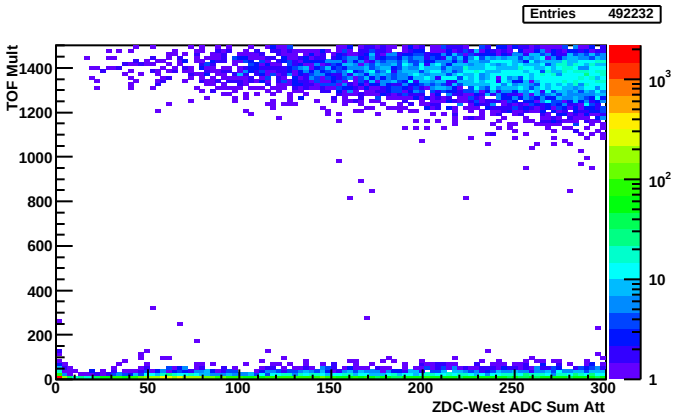

Entries 492232
TOF Mult
BBC-L-East ADC Sum

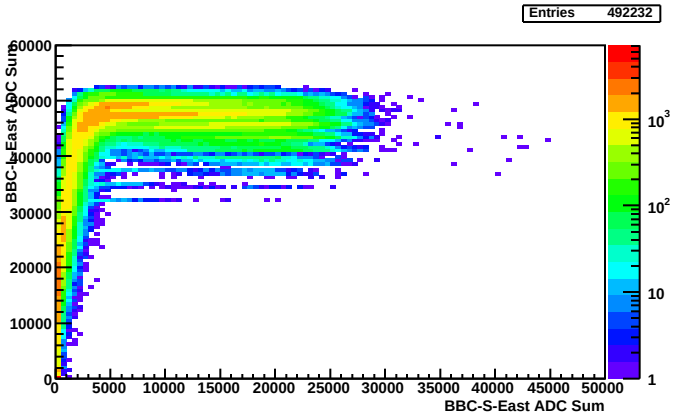

Entries 492232
TOF Mult
BBC-L-West ADC Sum

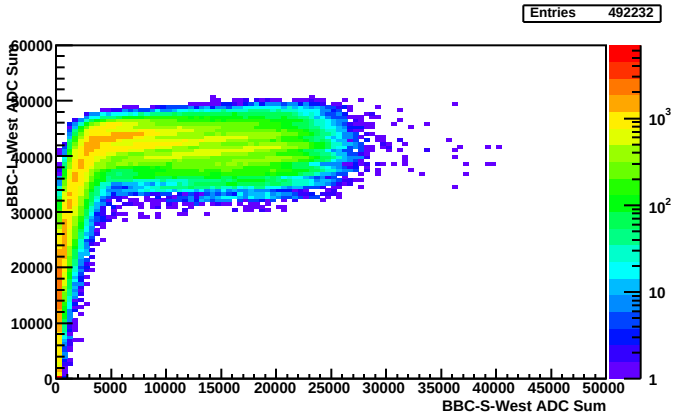

Entries 492232
TOF Mult
BBC-S-East ADC Sum

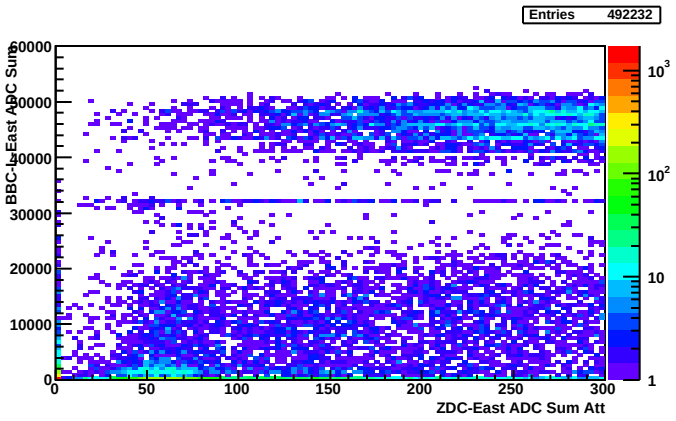

Entries 492232
TOF Mult
BBC-S-West ADC Sum

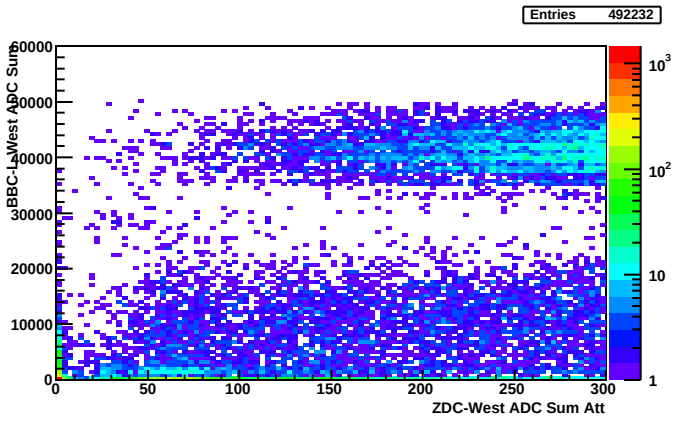

Entries 492232
TOF Mult
ZDC-East ADC Sum Att

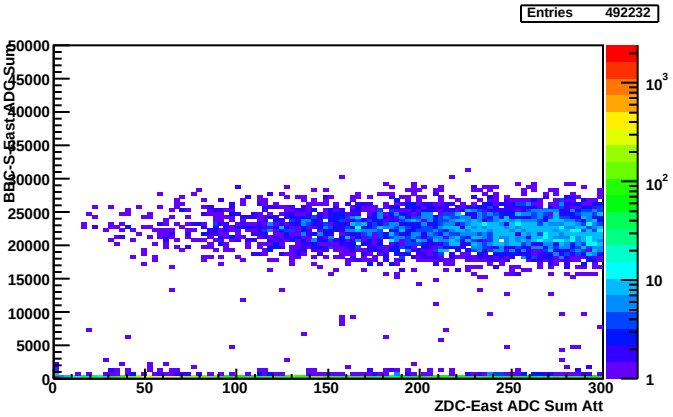

Entries 492232
TOF Mult
ZDC-West ADC Sum Att

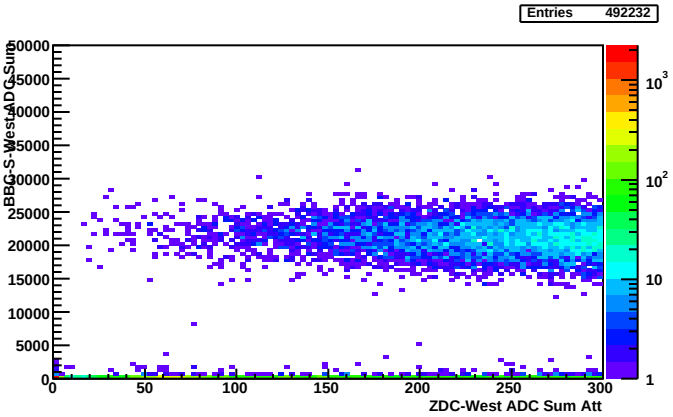

Entries 492232
BBC-L-East ADC Sum
BBC-S-East ADC Sum

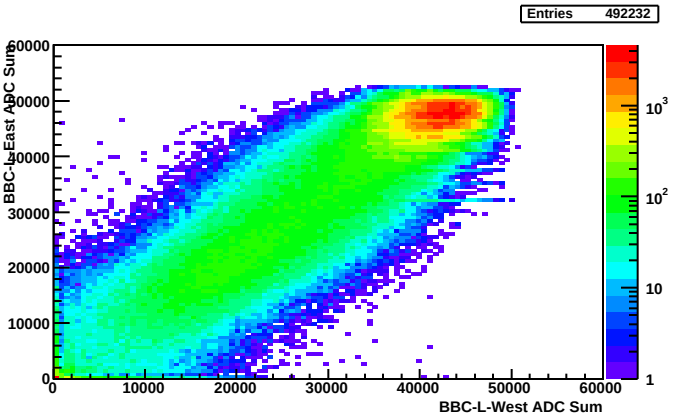

Entries 492232
BBC-L-West ADC Sum
BBC-S-West ADC Sum

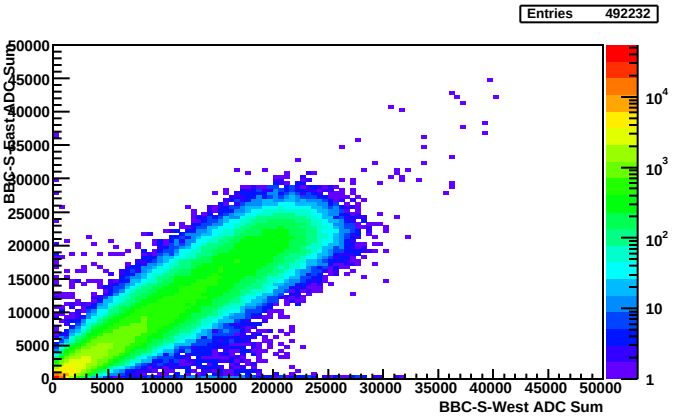

Entries 492232
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

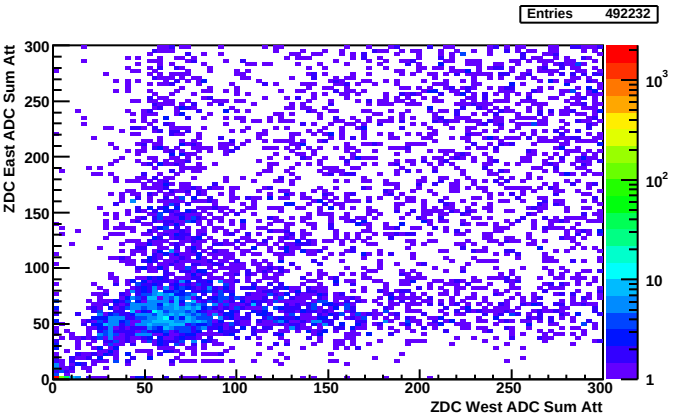

Entries 492232
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

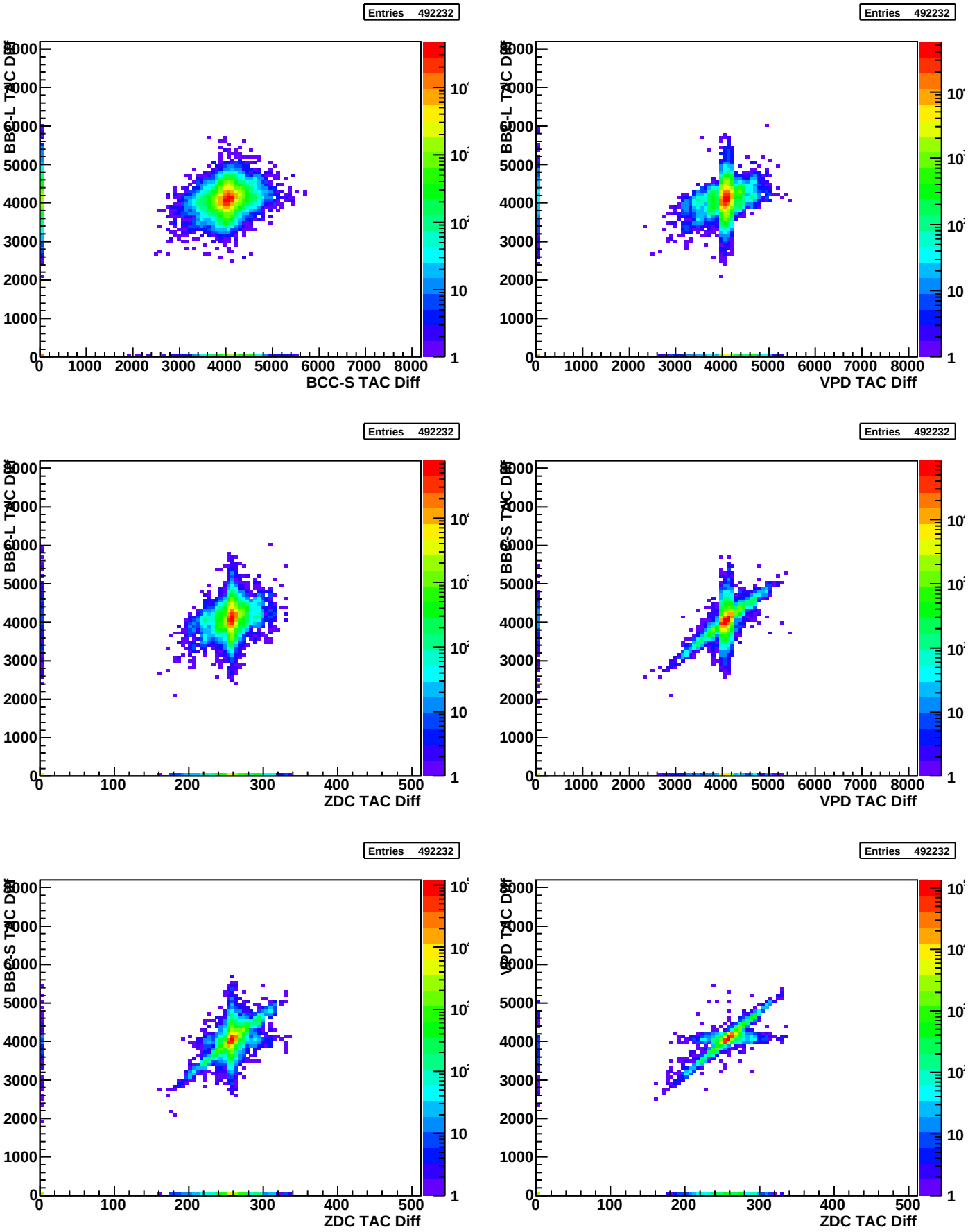

Entries 492232
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

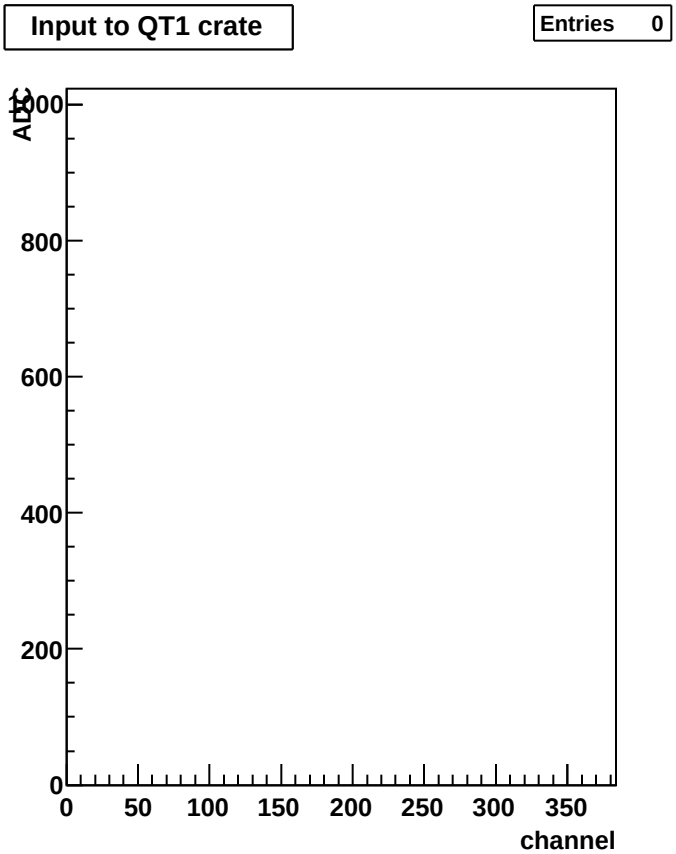

Entries 492232
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

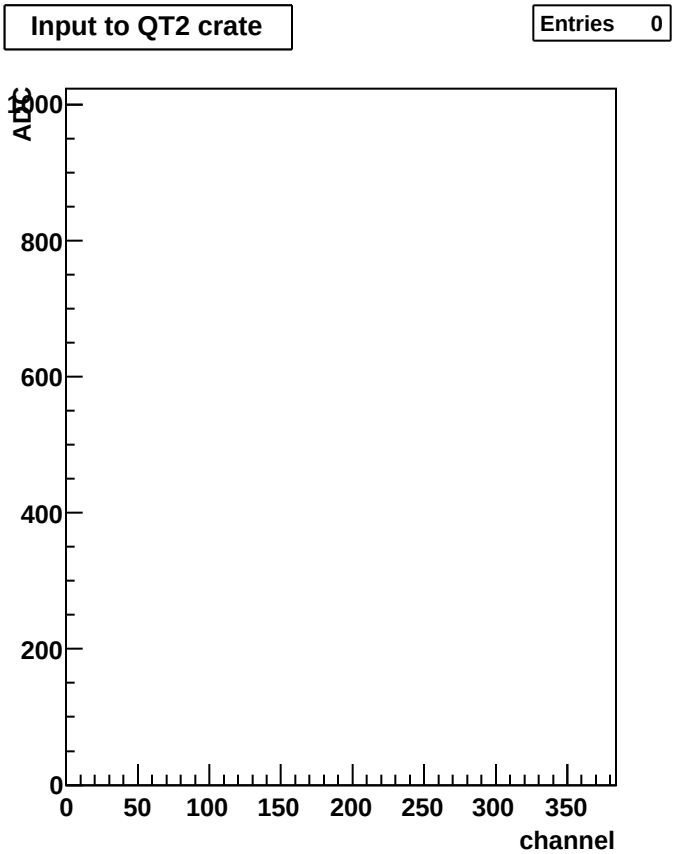

Entries 492232
BBC-L-East ADC Sum
BBC-L-West ADC Sum

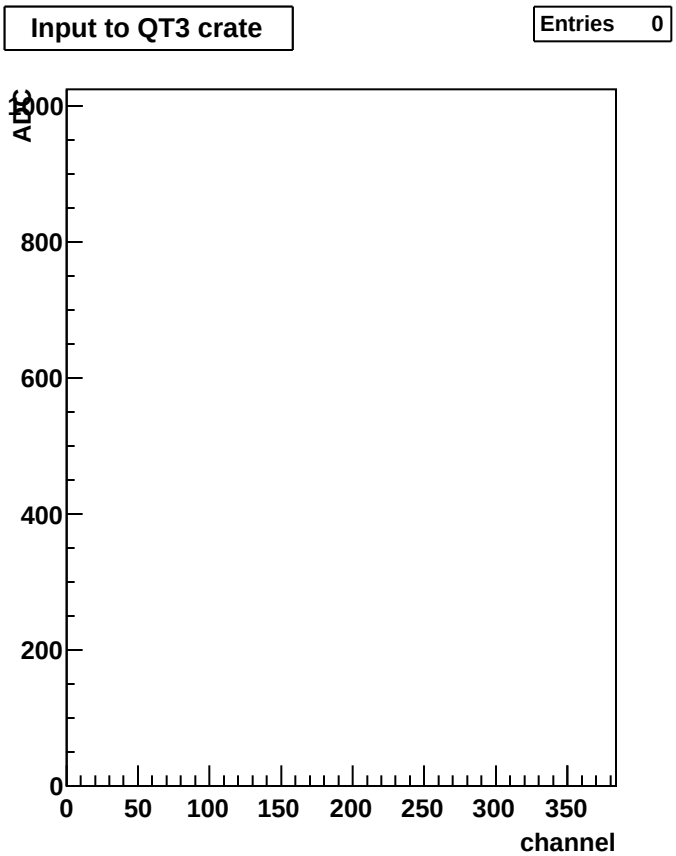

Entries 492232
BBC-S-East ADC Sum
BBC-S-West ADC Sum

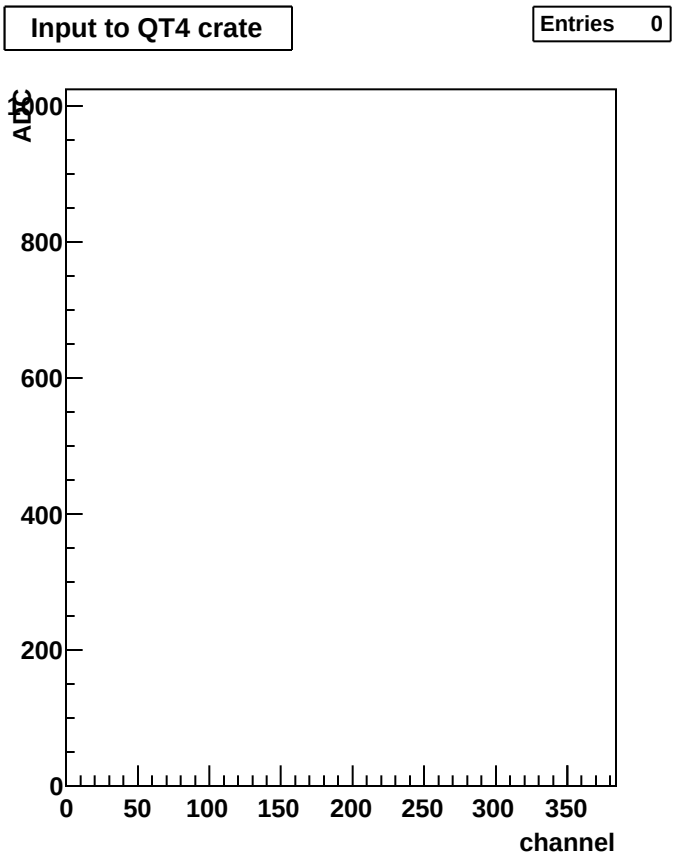

Entries 492232
ZDC East ADC Sum Att
ZDC West ADC Sum Att

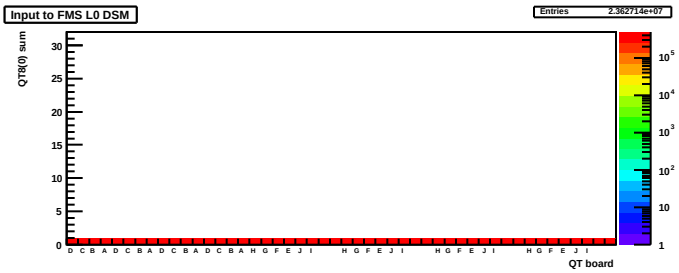

Entries 492232
BBC-L TAC Diff
BCC-S TAC Diff
Entries 492232
BBC-L TAC Diff
VPD TAC Diff
Entries 492232
BBC-L TAC Diff
ZDC TAC Diff
Entries 492232
BBC-S TAC Diff
VPD TAC Diff
Entries 492232
BBC-S TAC Diff
ZDC TAC Diff
Entries 492232
VPD TAC Diff
ZDC TAC Diff

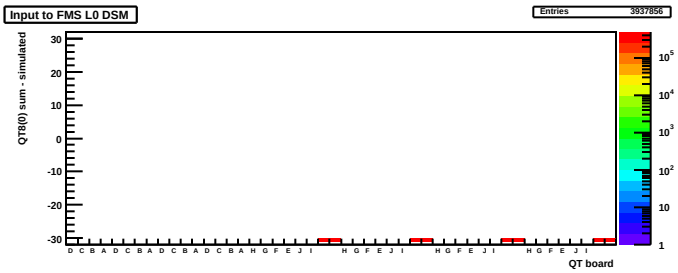

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

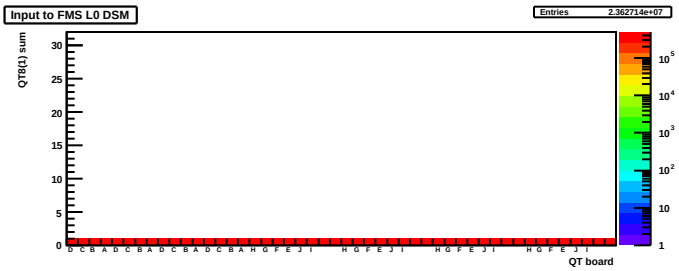

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

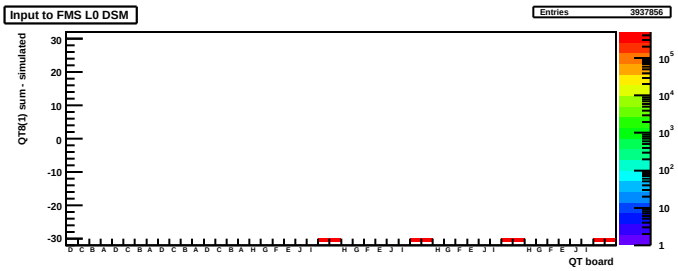

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

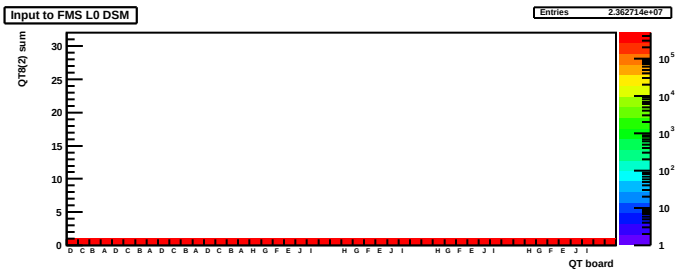

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

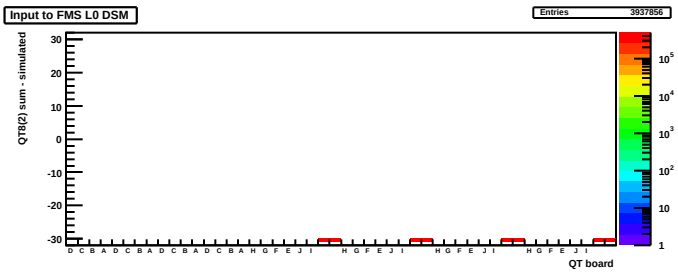

Input to FMS L0 DSM
Entries 2.362714e+07
QT8(0) sum
QT board

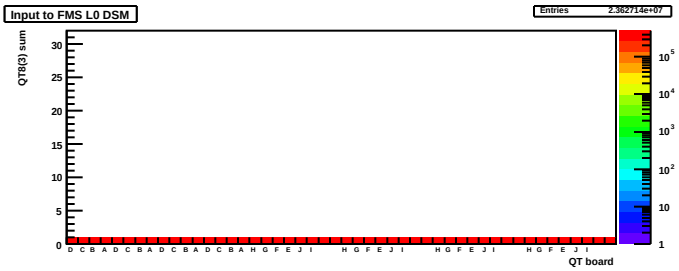

Input to FMS L0 DSM
Entries 3937856
QT8(0) sum - simulated
QT board

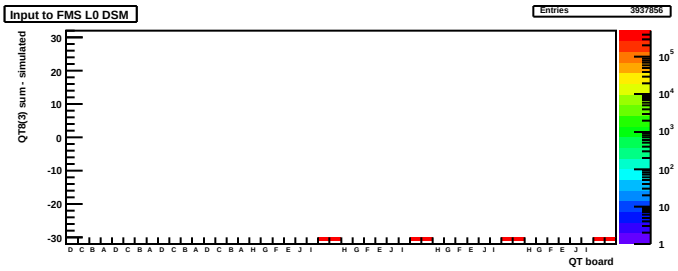

Input to FMS L0 DSM
Entries 2.362714e+07
QT8(1) sum
QT board

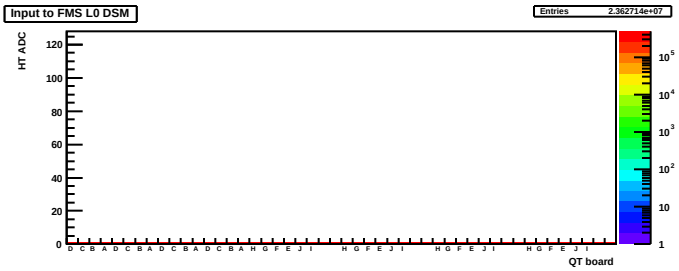

Input to FMS L0 DSM
Entries 3937856
QT8(1) sum - simulated
QT board

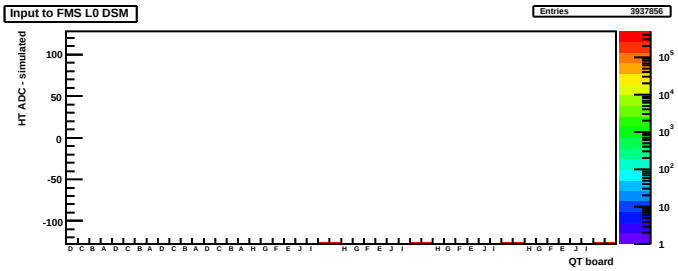

Input to FMS L0 DSM
Entries 2.362714e+07
QT8(2) sum
QT board

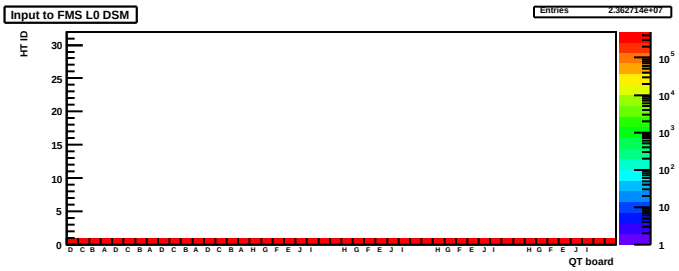

Input to FMS L0 DSM
Entries 3937856
QT8(2) sum - simulated
QT board

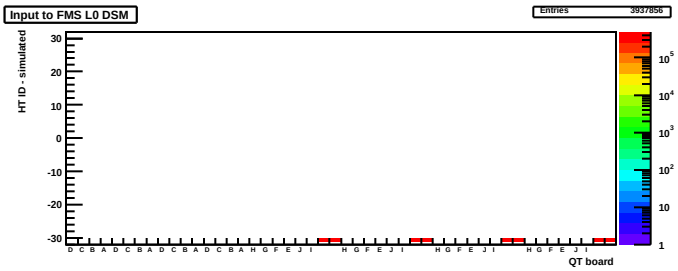

Input to FMS L0 DSM
Entries 2.362714e+07
QT8(3) sum
QT board

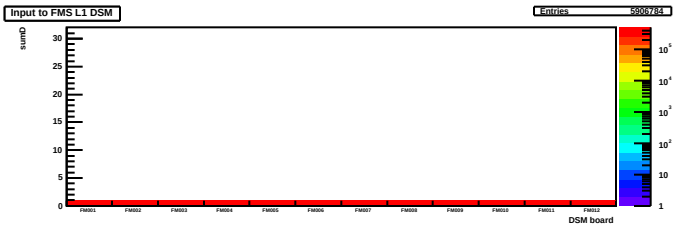

Input to FMS L0 DSM
Entries 3937856
QT8(3) sum - simulated
QT board

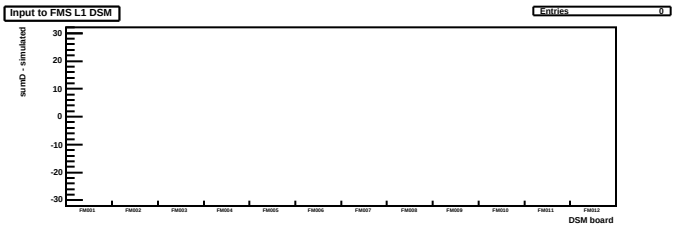

Input to FMS L0 DSM
Entries 2.362714e+07
HT ADC
QT board

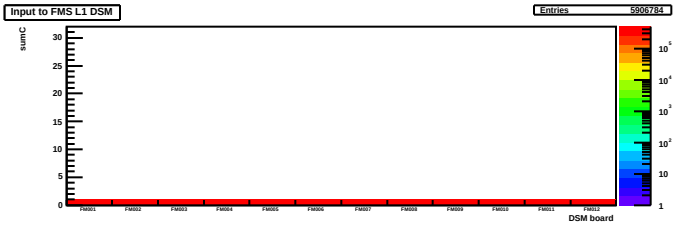

Input to FMS L0 DSM
Entries 3937856
HT ADC - simulated
QT board

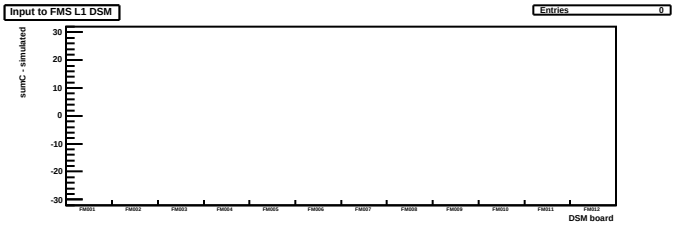

Input to FMS L0 DSM
Entries 2.362714e+07
HT ID
QT board

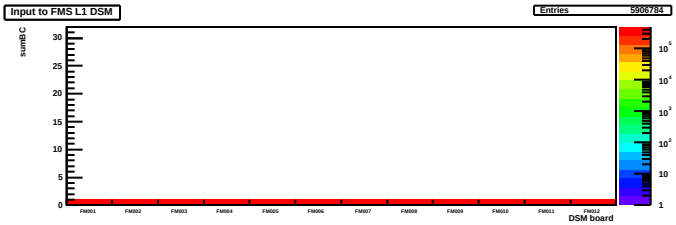

Input to FMS L0 DSM
Entries 3937856
HT ID - simulated
QT board

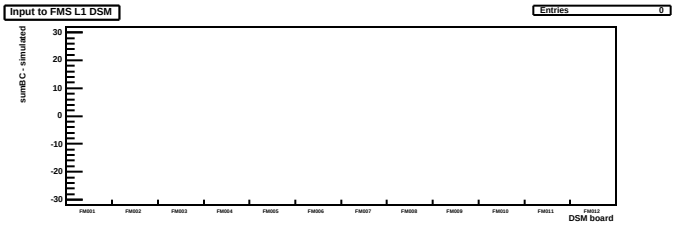

Input to FMS L1 DSM
Entries 5906784
sumD
DSM board

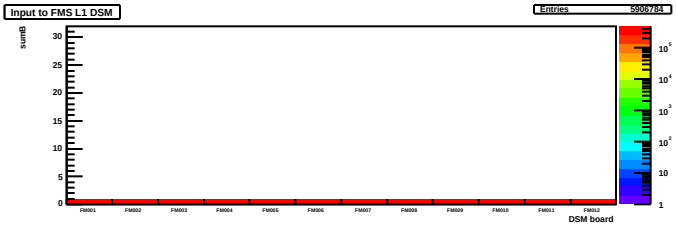

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

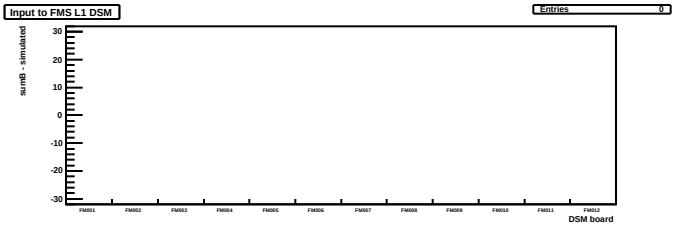

Input to FMS L1 DSM
Entries 5906784
sumC
DSM board

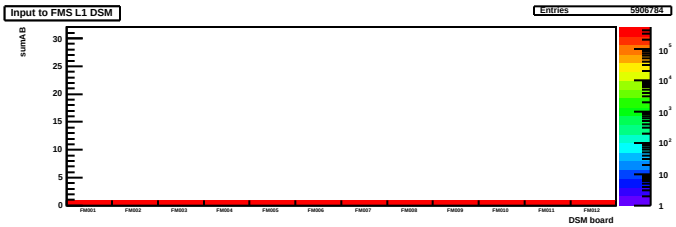

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

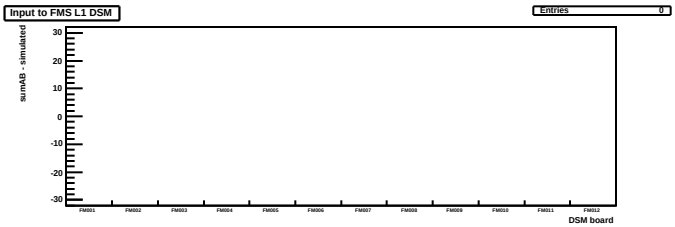

Input to FMS L1 DSM
Entries 5906784
sumBC
DSM board

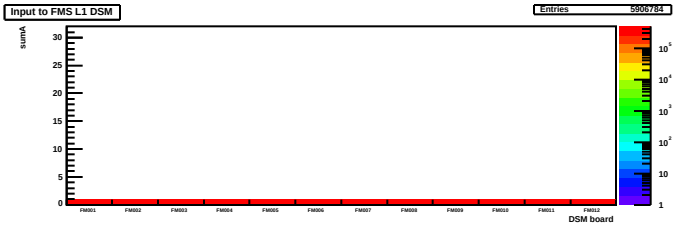

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

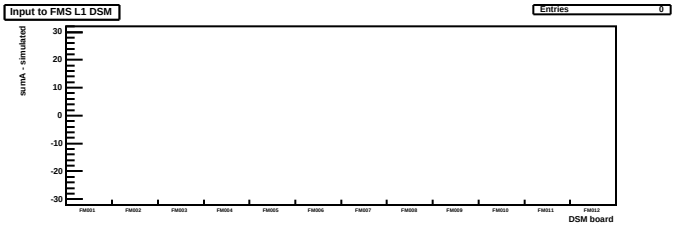

Input to FMS L1 DSM
Entries 5906784
sumB
DSM board

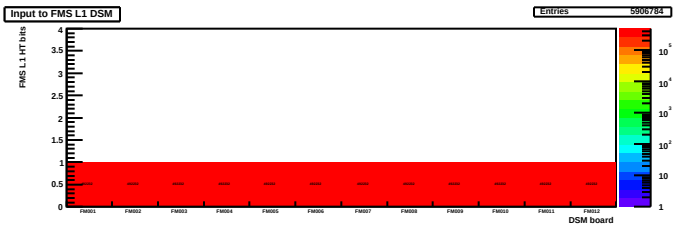

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

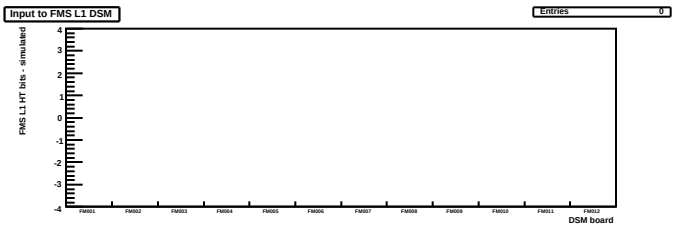

Input to FMS L1 DSM
Entries 5906784
sumAB
DSM board

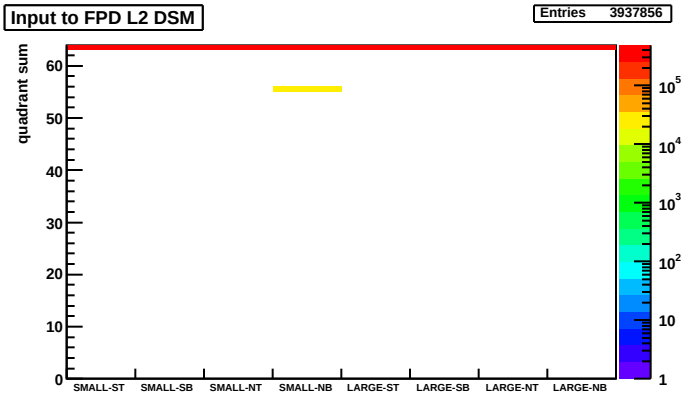

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

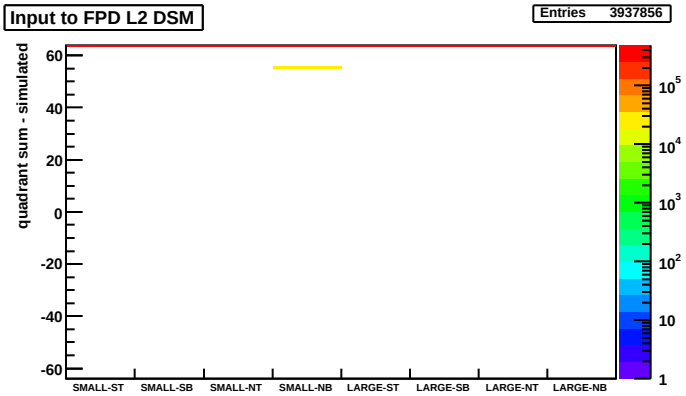

Input to FMS L1 DSM
Entries 5906784
sumA
DSM board

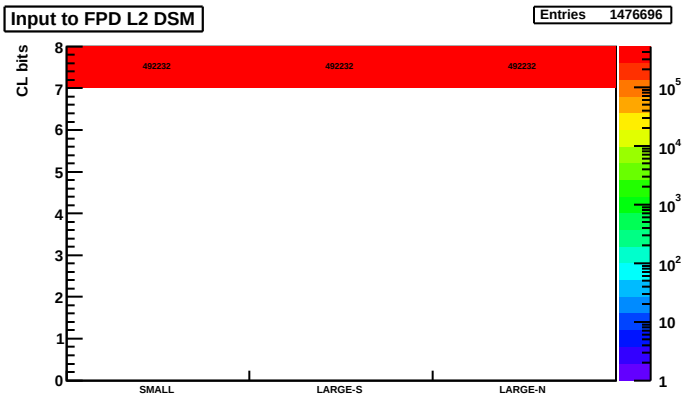

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

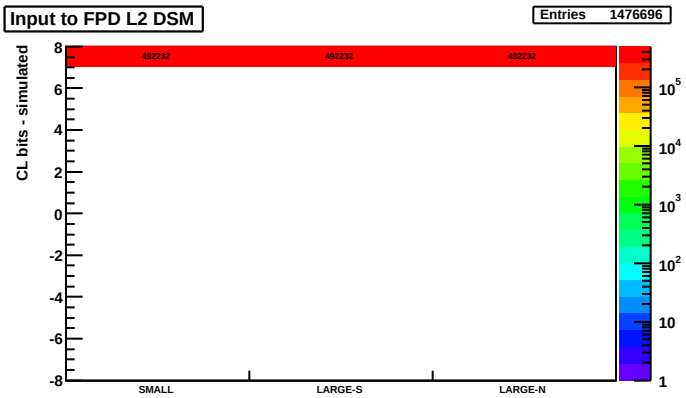

Input to FMS L1 DSM
Entries 5906784
FMS L1 HT bits
DSM board

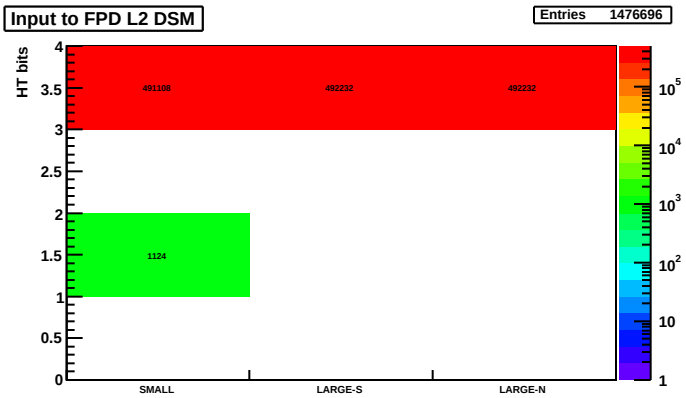

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

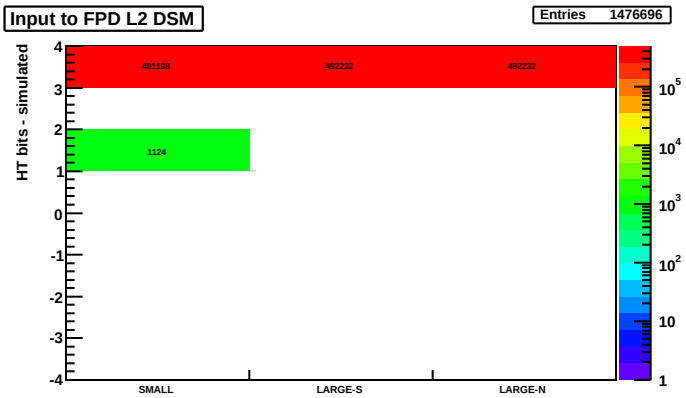

Input to FPD L2 DSM
Entries 3937856
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

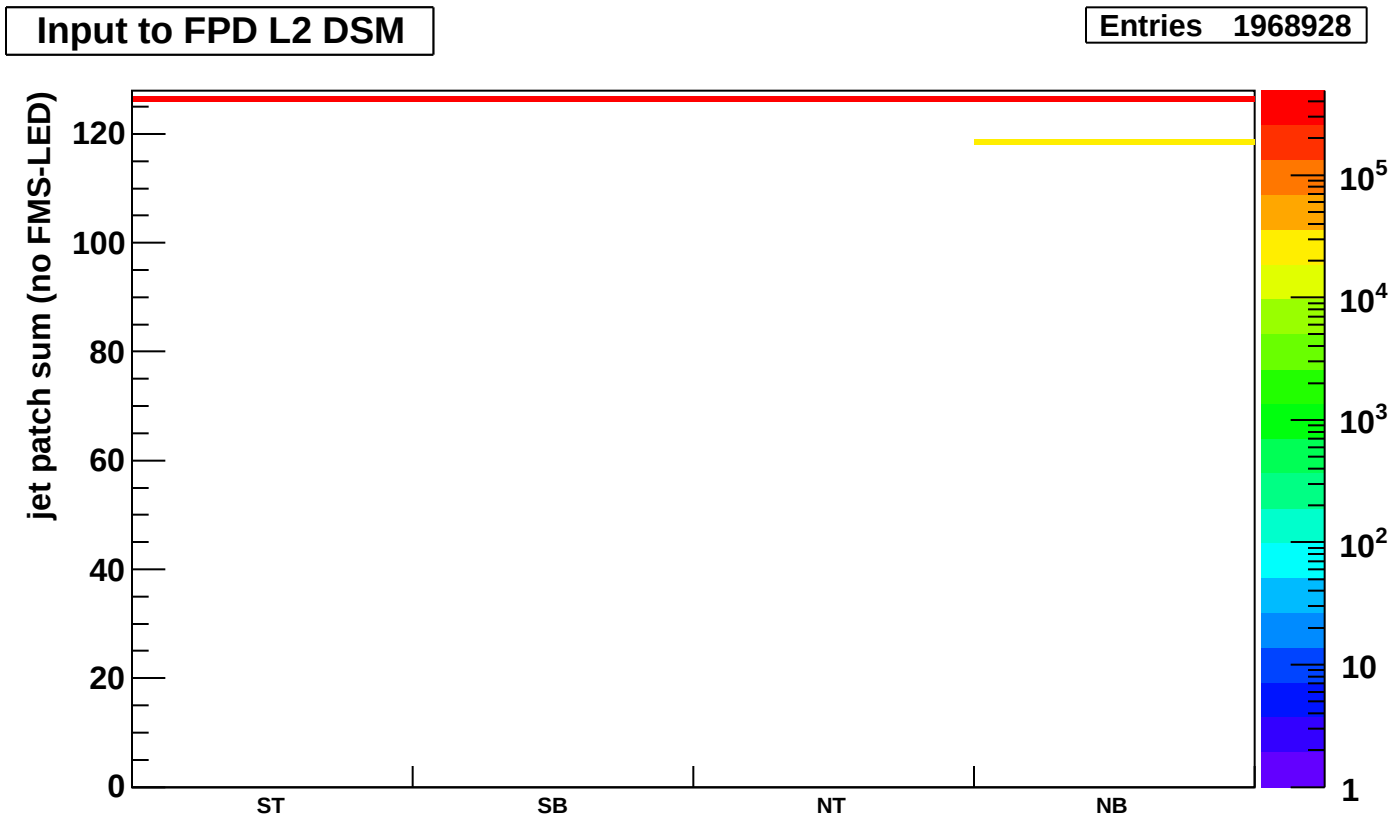

Input to FPD L2 DSM
Entries 3937856
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

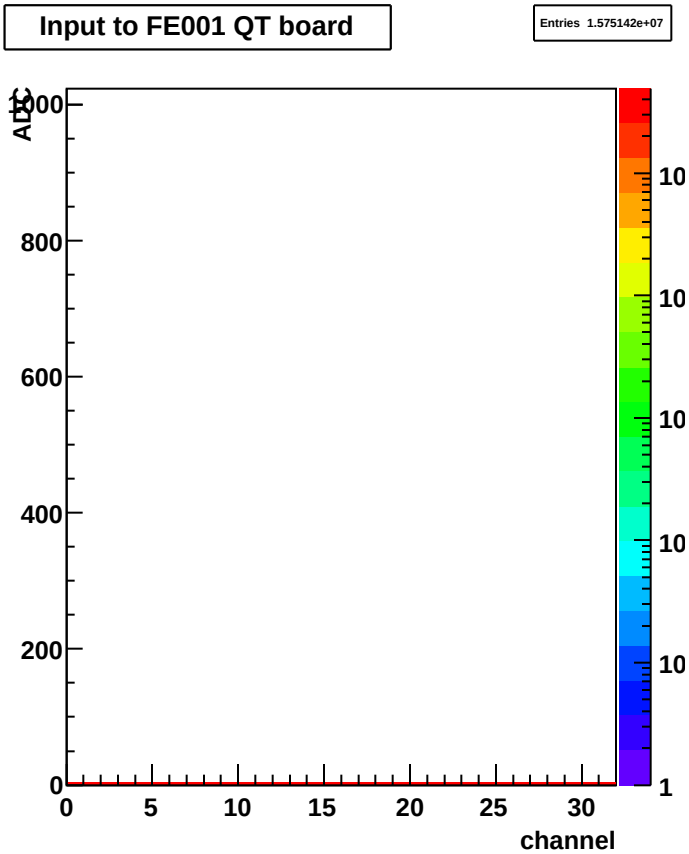

Input to FPD L2 DSM
Entries 1476696
CL bits
492232
492232
492232
SMALL
LARGE-S
LARGE-N

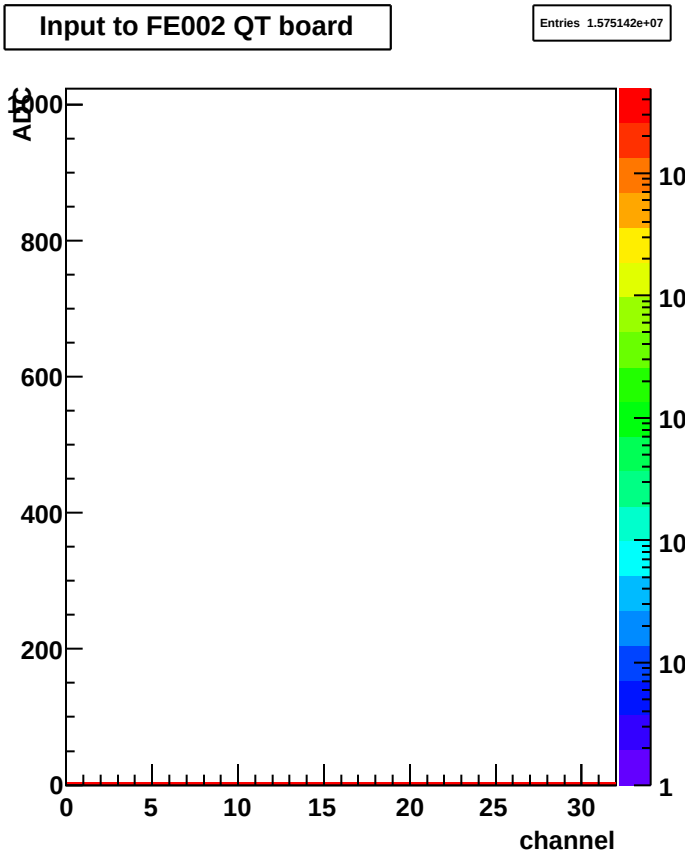

Input to FPD L2 DSM
Entries 1476696
CL bits - simulated
492232
492232
492232
SMALL
LARGE-S
LARGE-N

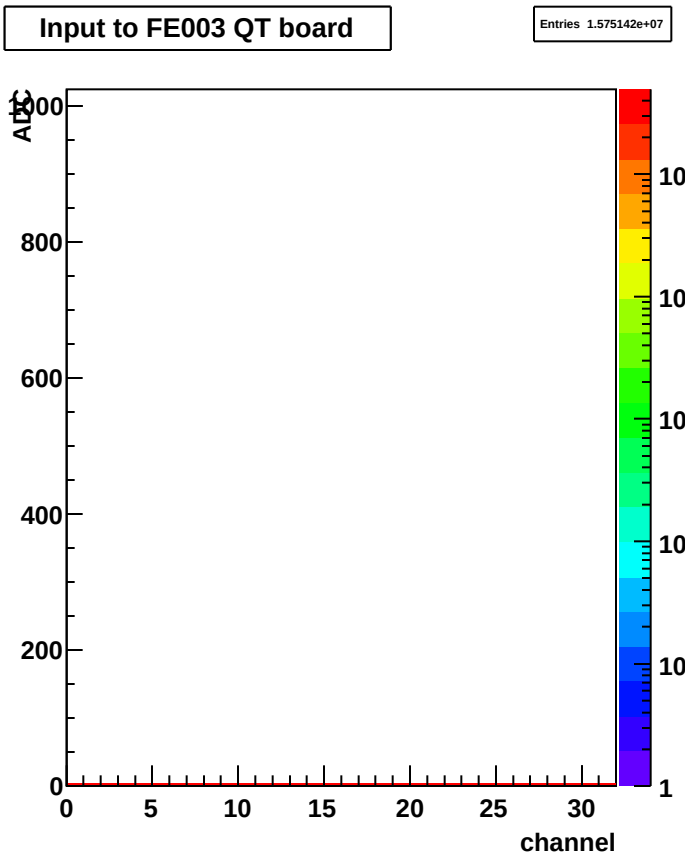

Input to FPD L2 DSM
Entries 1476696
HT bits
491108
492232
492232
1124
SMALL
LARGE-S
LARGE-N

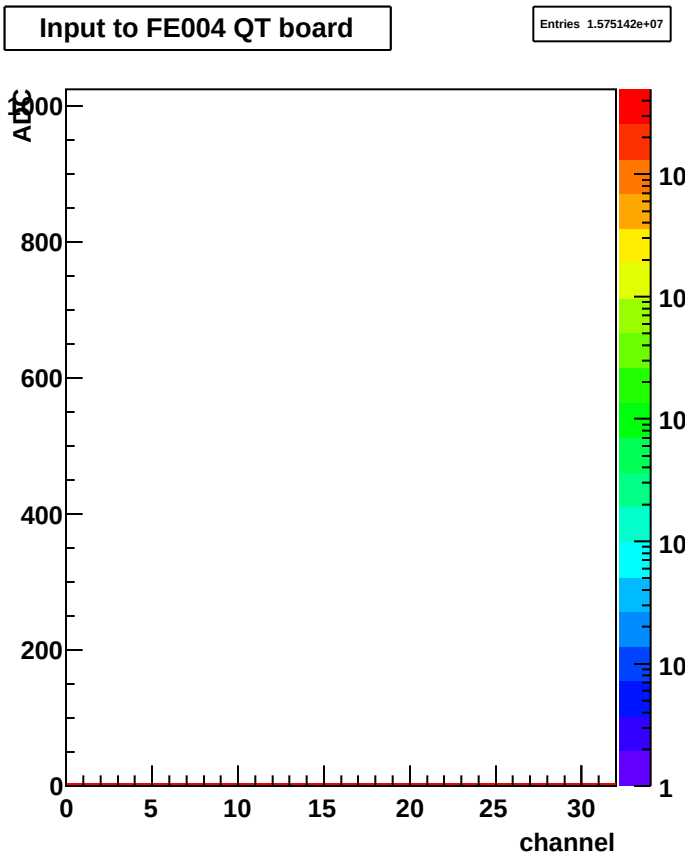

Input to FPD L2 DSM
Entries 1476696
HT bits - simulated
491108
492232
492232
1124
SMALL
LARGE-S
LARGE-N


Input to FPD L2 DSM
Entries 1968928
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

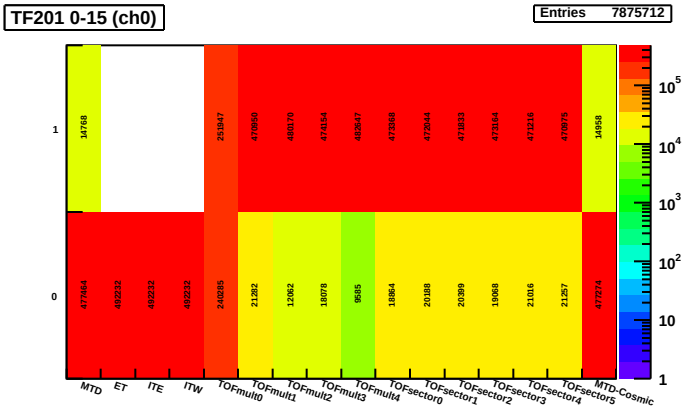

Input to FE001 QT board
Entries 1.575142e+07
ADC
channel


Input to FE002 QT board
Entries 1.575142e+07
ADC
channel

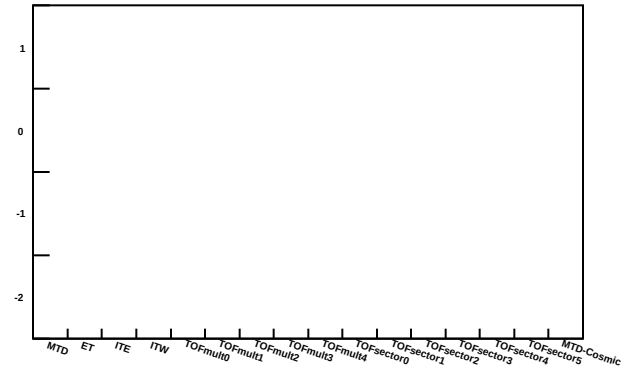

Input to FE003 QT board
Entries 1.575142e+07
ADC
channel


Input to FE004 QT board
Entries 1.575142e+07
ADC
channel

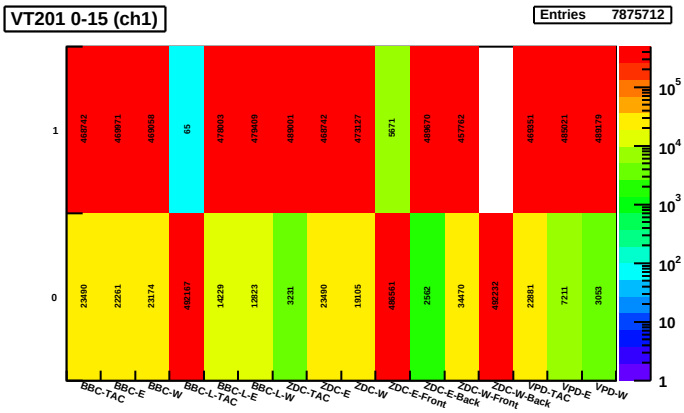
TF201 0-15 (ch0)



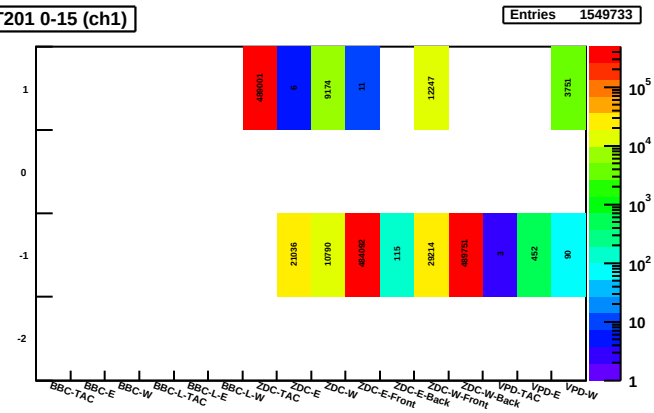
TF201 0-15 (ch0)



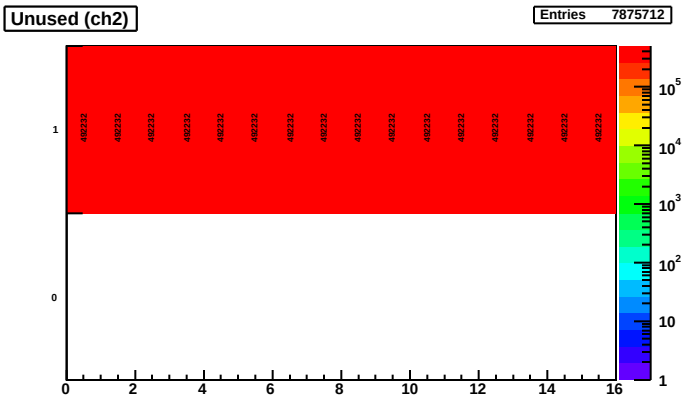
VT201 0-15 (ch1)

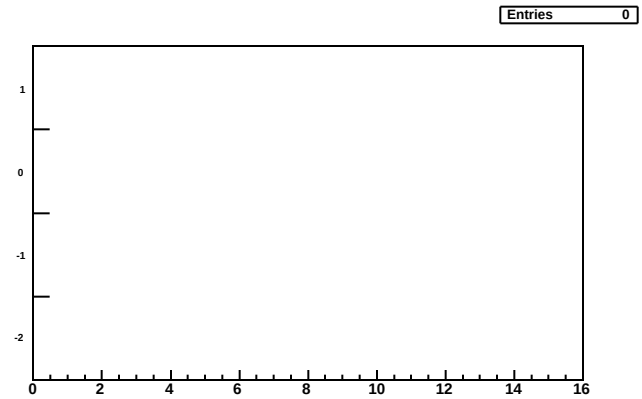


VT201 0-15 (ch1)

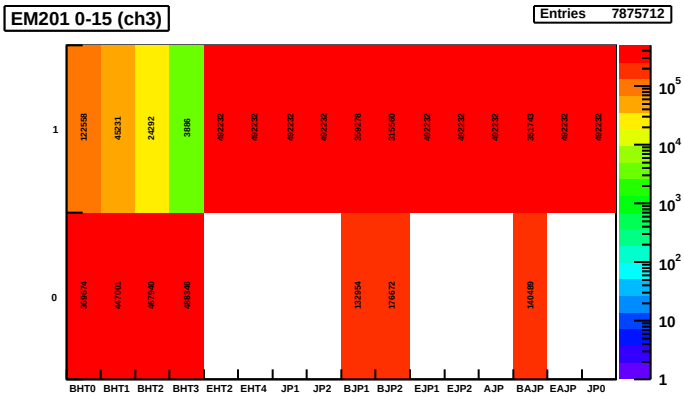


Unused (ch2)

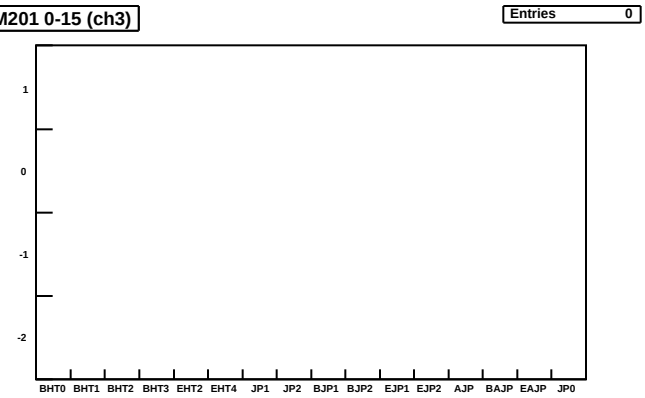




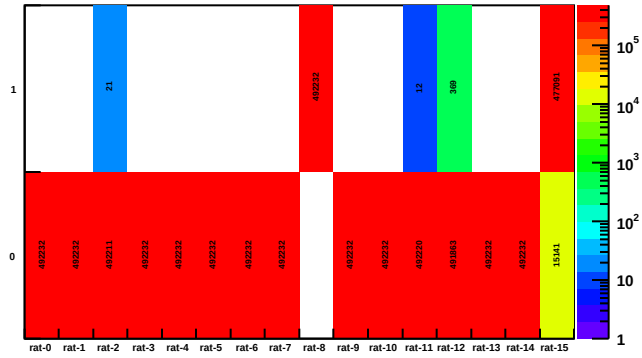
EM201 0-15 (ch3)

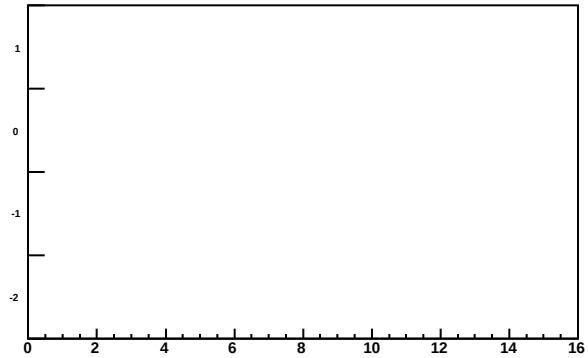


EM201 0-15 (ch3)

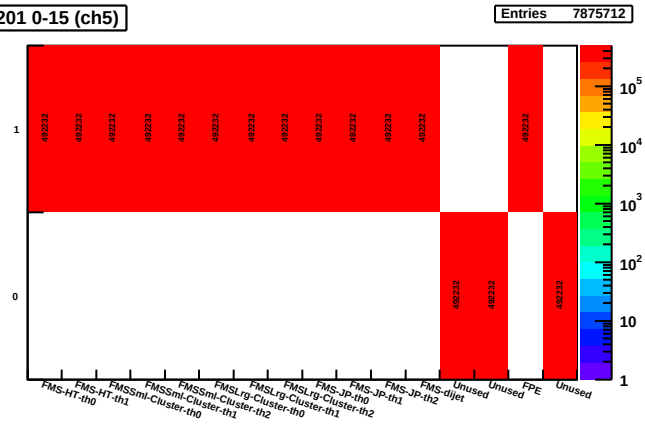


RAT board (ch4)

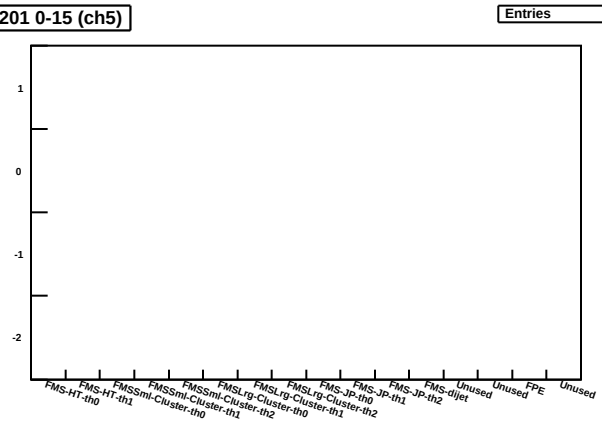




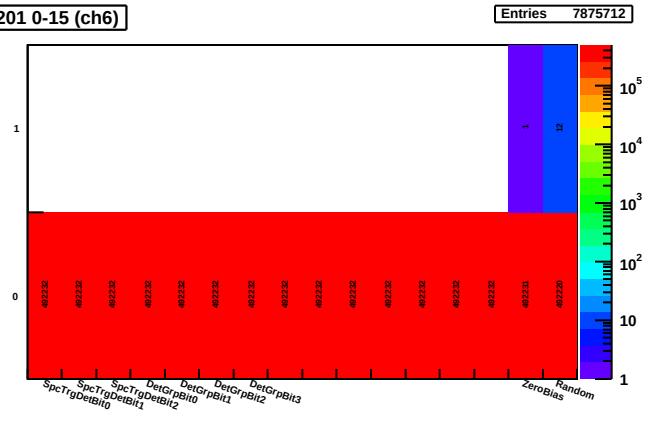
FP201 0-15 (ch5)

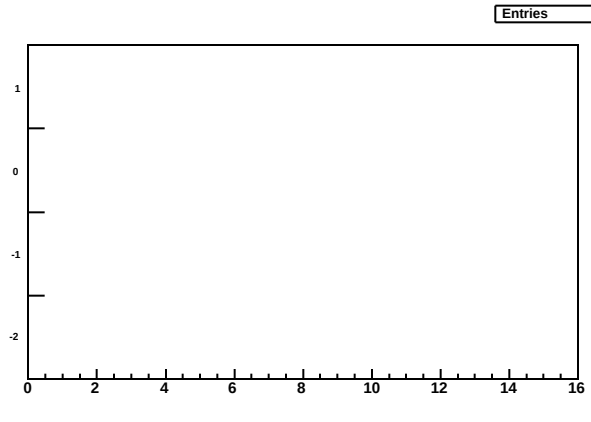
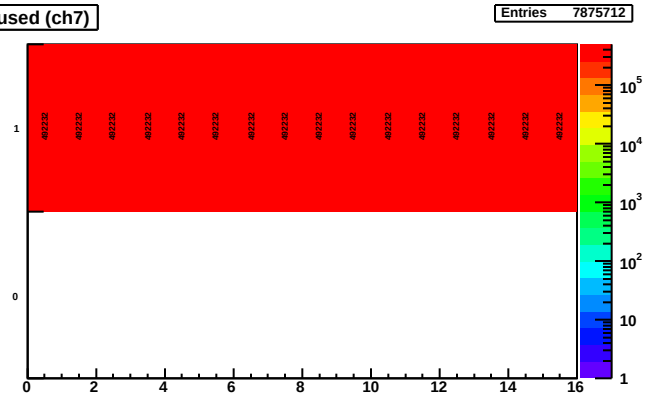
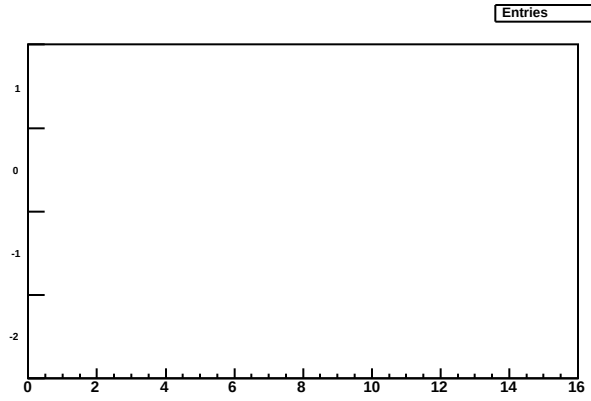


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)