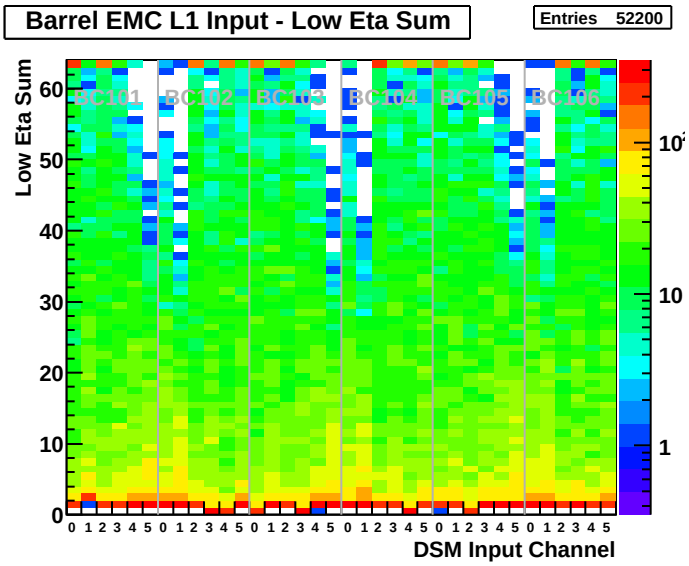
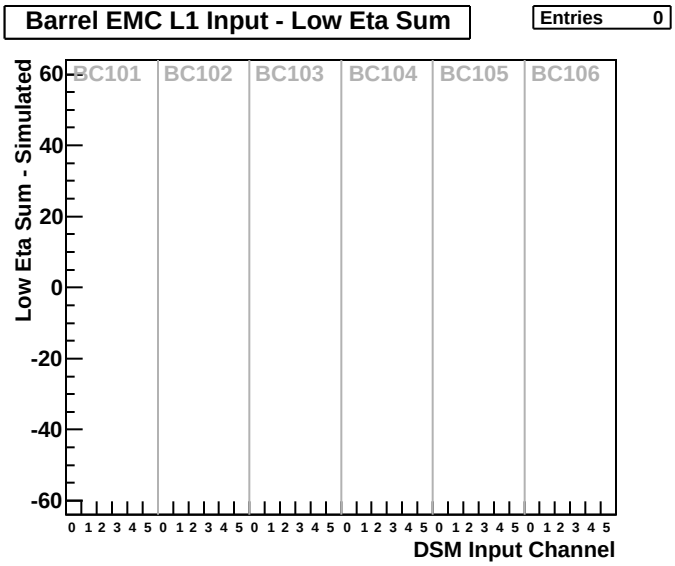
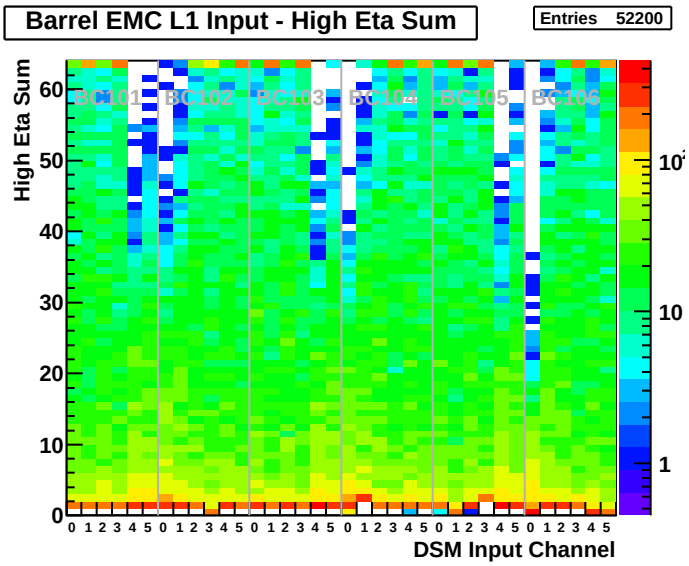
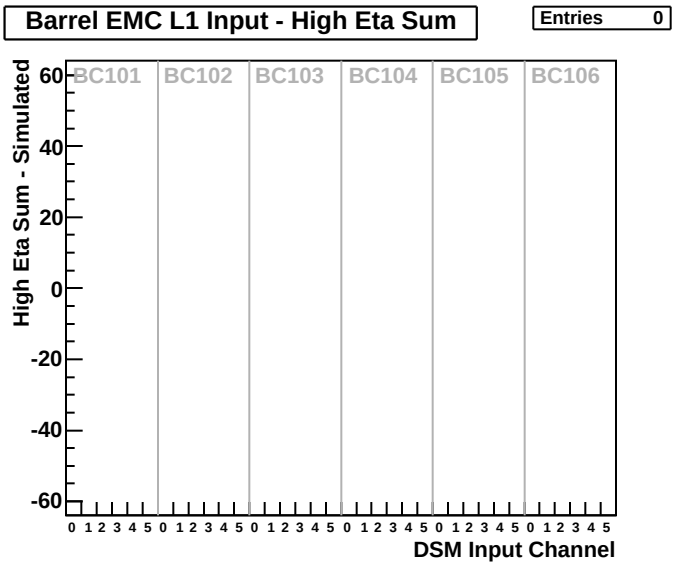
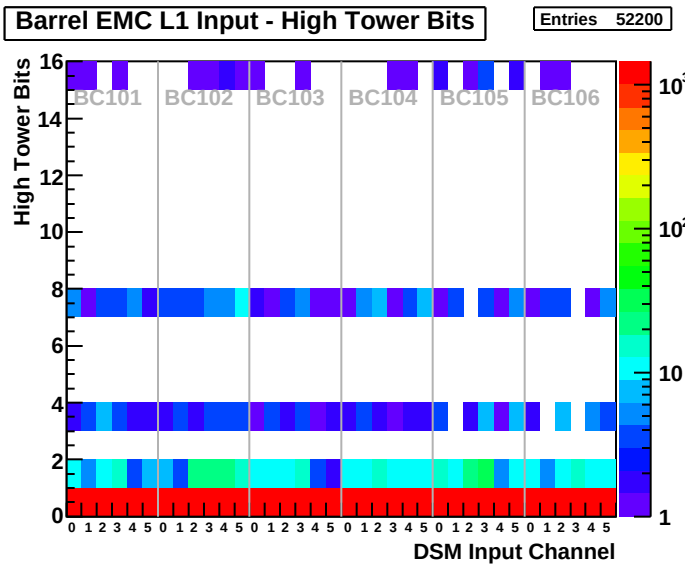
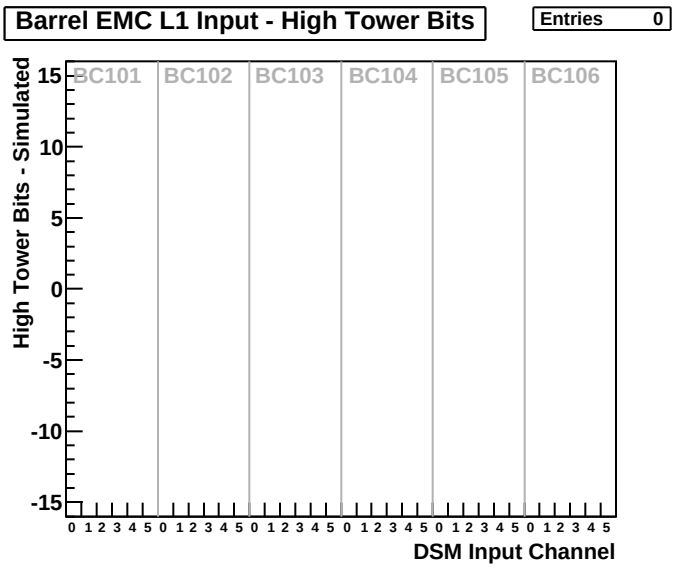

Barrel EMC L1 Input - Low Eta Sum
Entries 52200
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

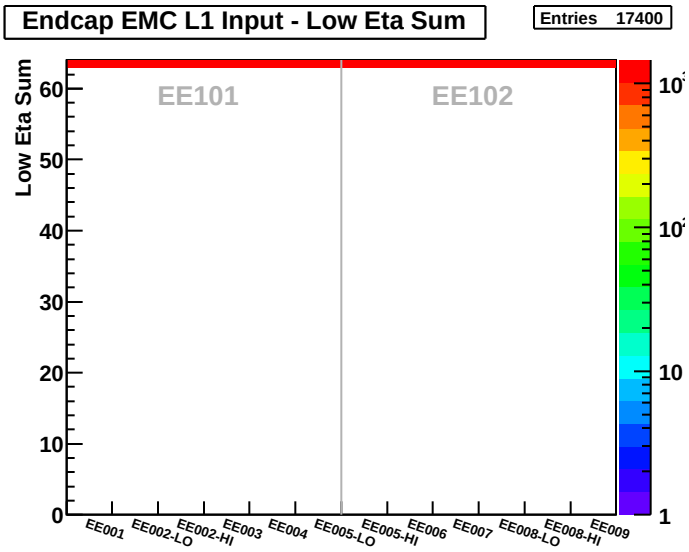

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

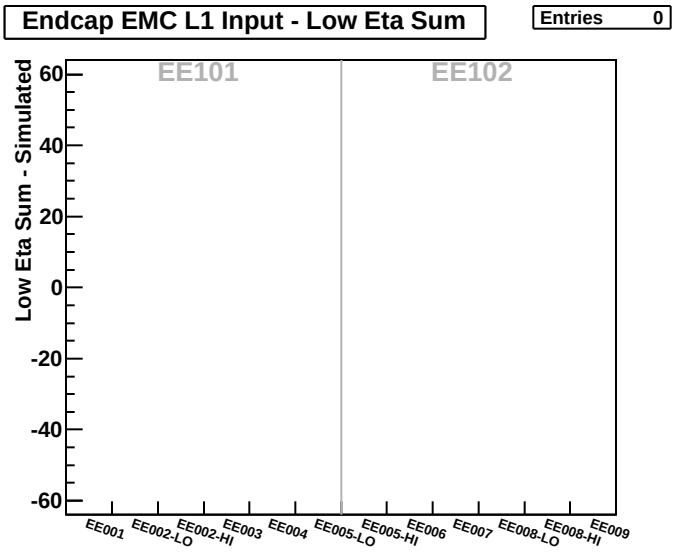

Barrel EMC L1 Input - High Eta Sum
Entries 52200
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

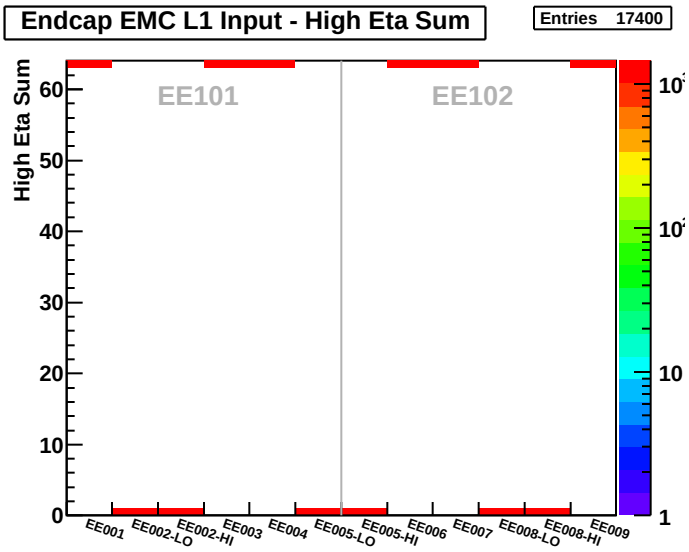

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

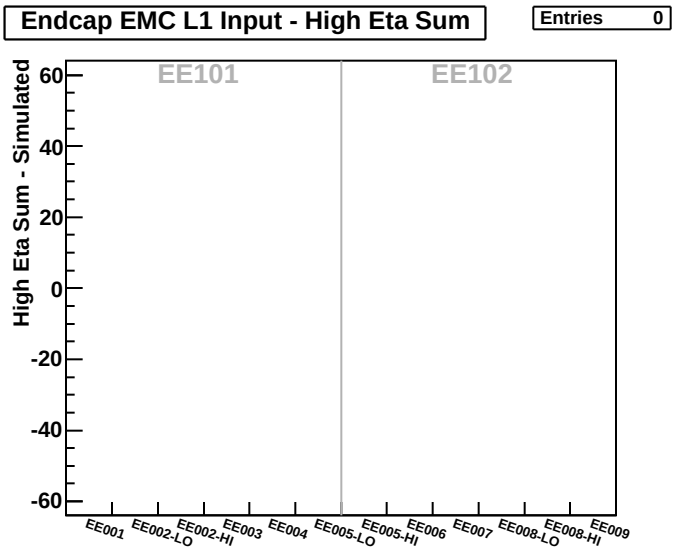

Barrel EMC L1 Input - High Tower Bits
Entries 52200
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

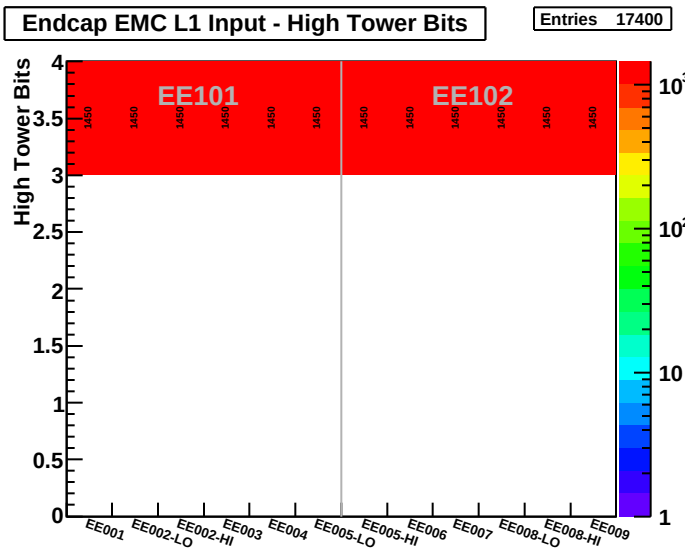

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

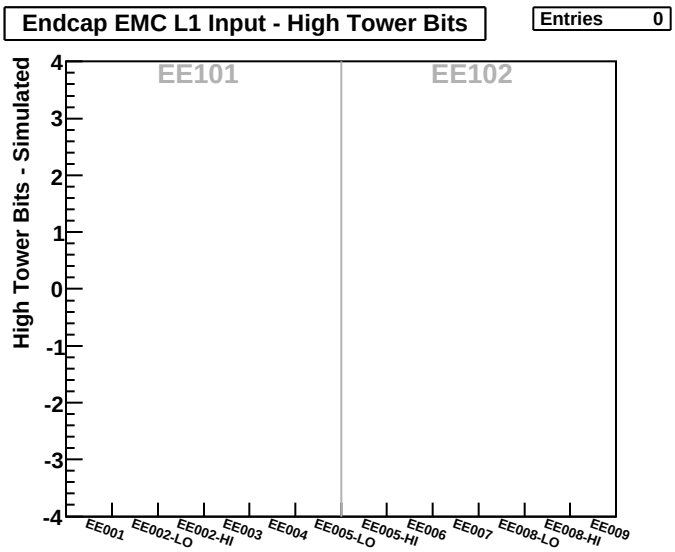

Endcap EMC L1 Input - Low Eta Sum
Entries 17400
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

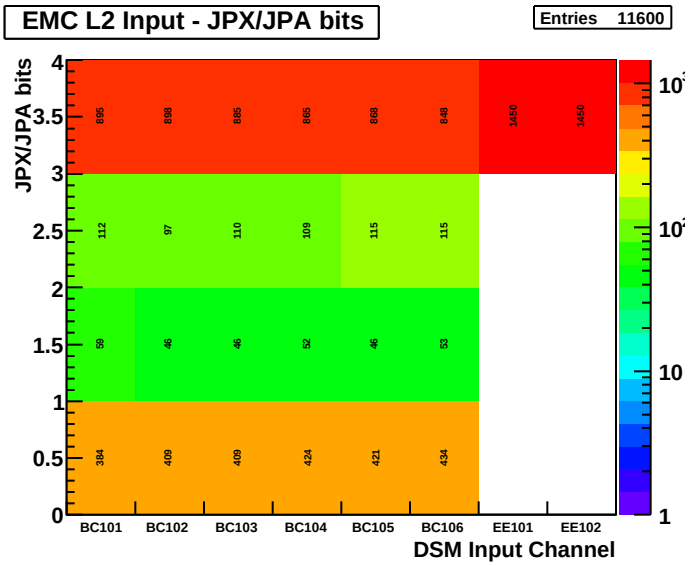

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

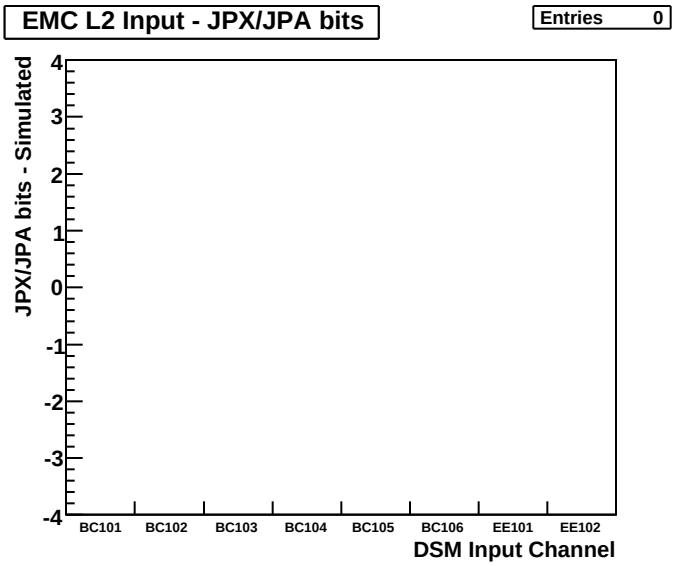

Endcap EMC L1 Input - High Eta Sum
Entries 17400
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

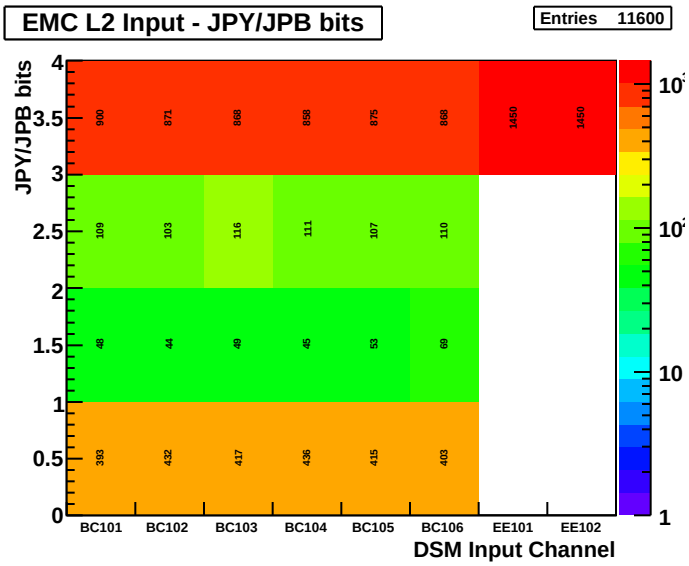

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

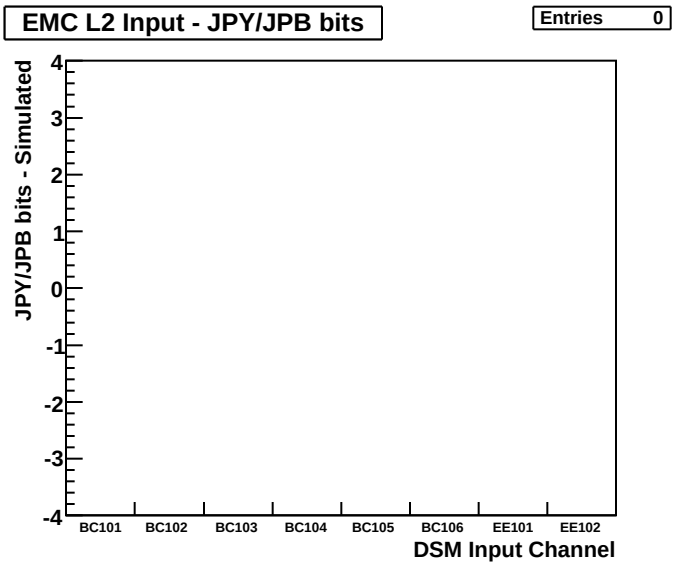

Endcap EMC L1 Input - High Tower Bits
Entries 17400
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

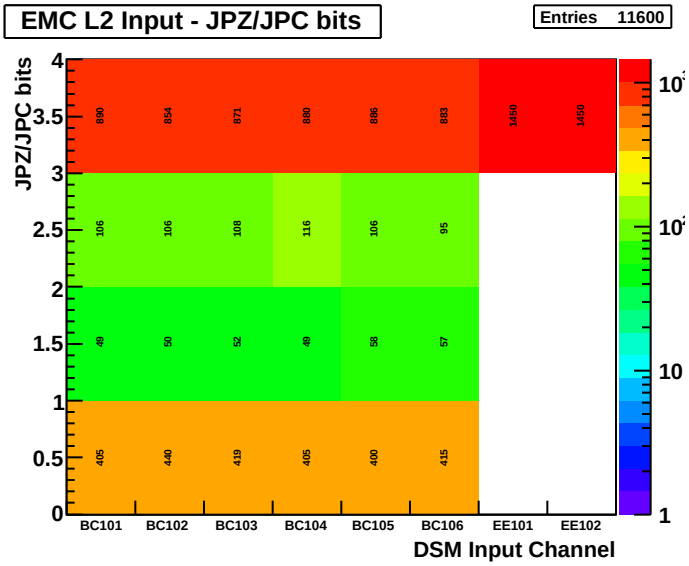

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

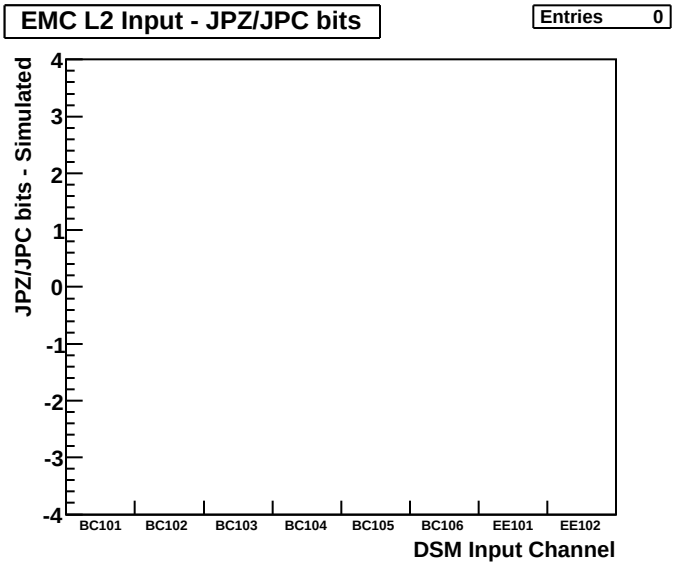

EMC L2 Input - JPX/JPA bits
Entries 11600
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
895 898 885 865 868 848 1450 1450
112 97 110 109 115 115
59 46 46 52 46 53
384 409 409 424 421 434

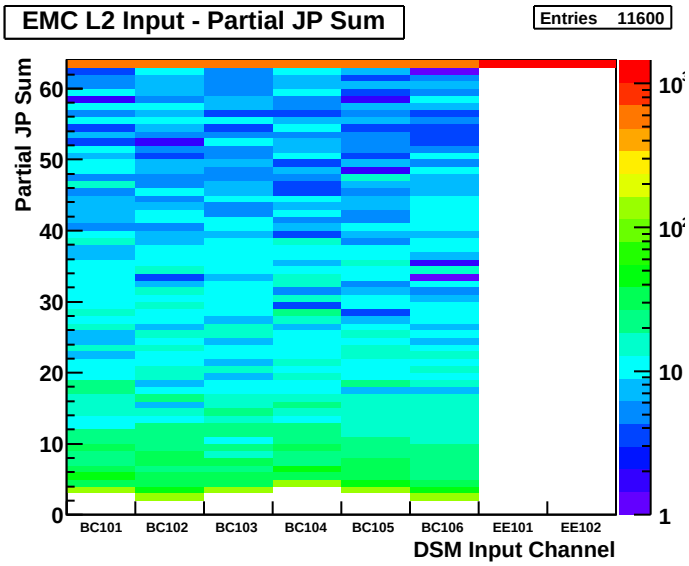

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

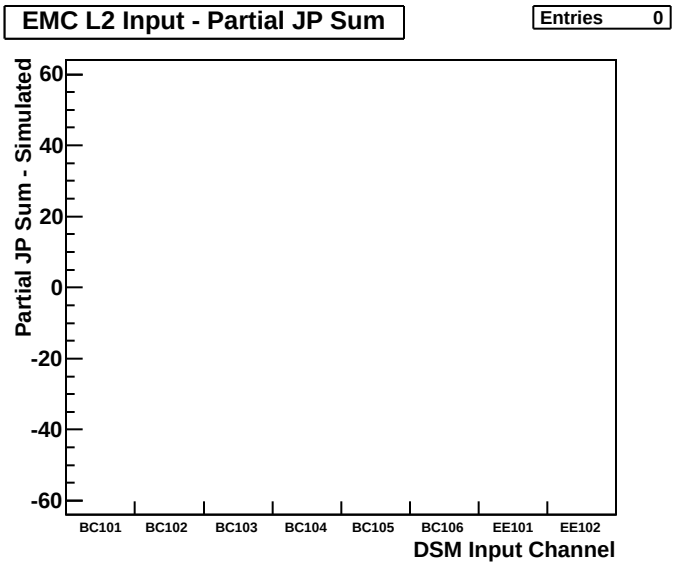

EMC L2 Input - JPY/JPB bits
Entries 11600
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
900 871 868 858 875 868 1450 1450
109 103 116 111 107 110
48 44 49 45 53 69
393 432 417 436 415 403

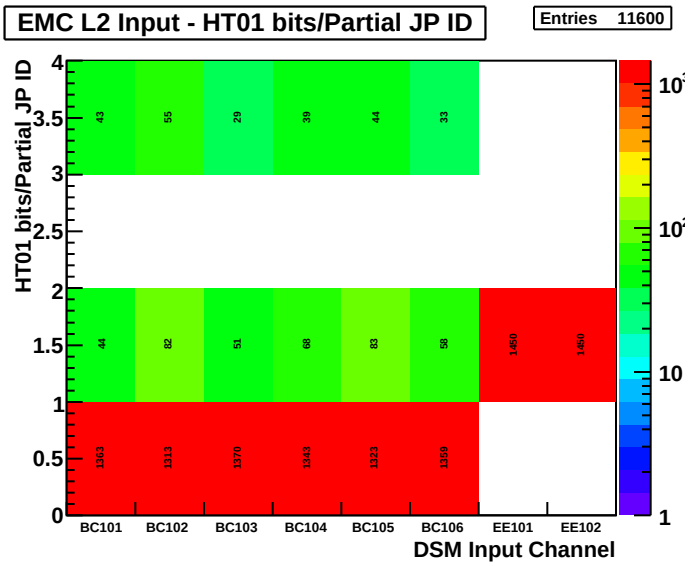

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

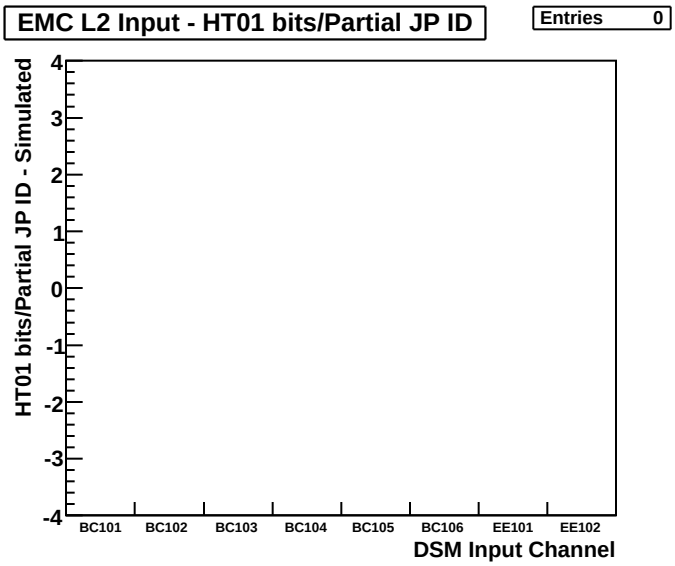

EMC L2 Input - JPZ/JPC bits
Entries 11600
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
890 854 871 880 886 883 1450 1450
106 106 108 116 106 95
49 50 52 49 58 57
405 440 419 405 400 415

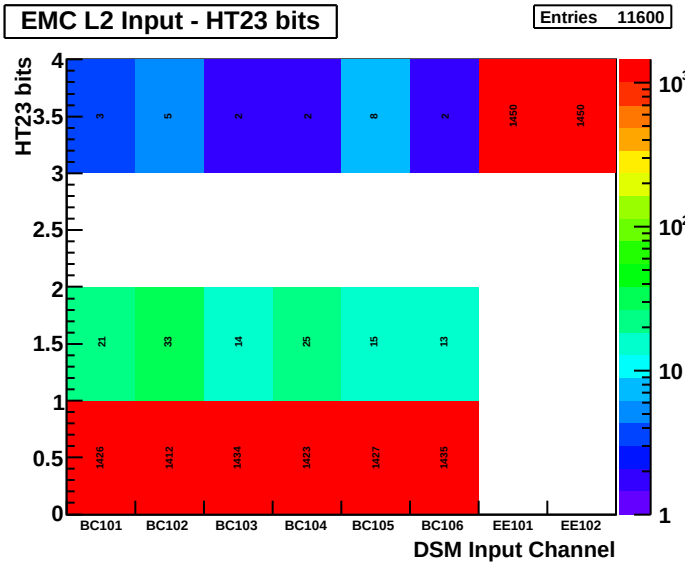

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

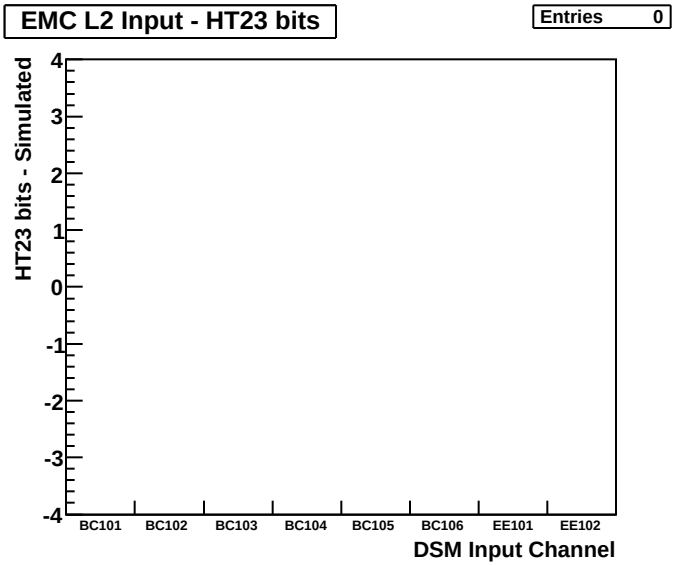

EMC L2 Input - Partial JP Sum
Entries 11600
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

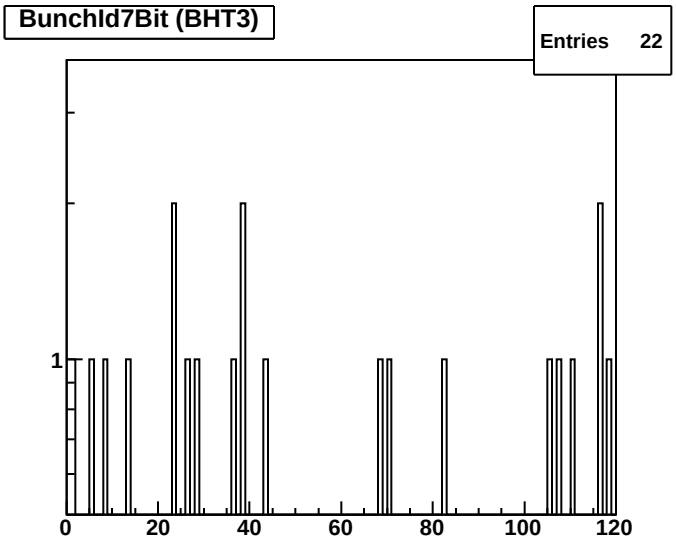

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

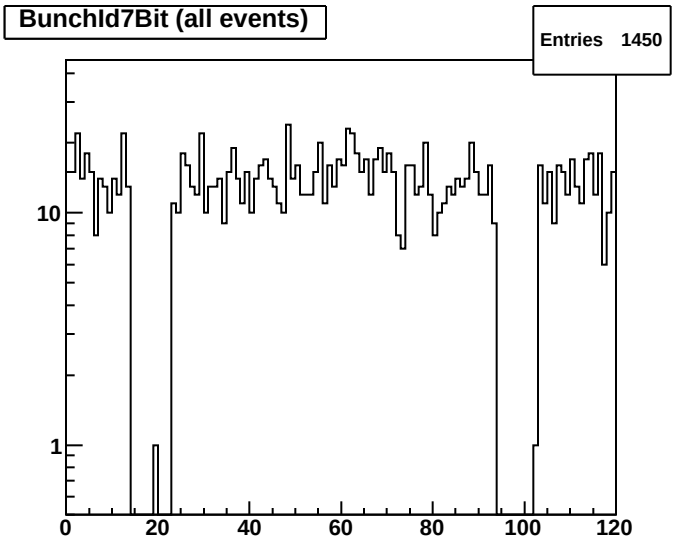

EMC L2 Input - HT01 bits/Partial JP ID
Entries 11600
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

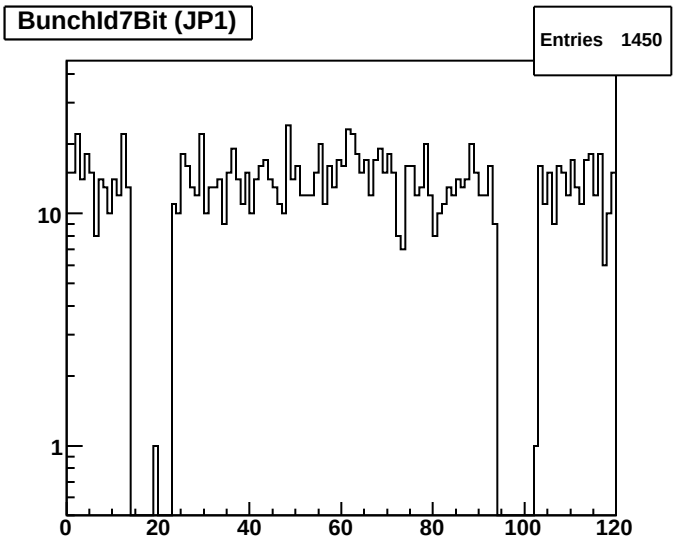

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

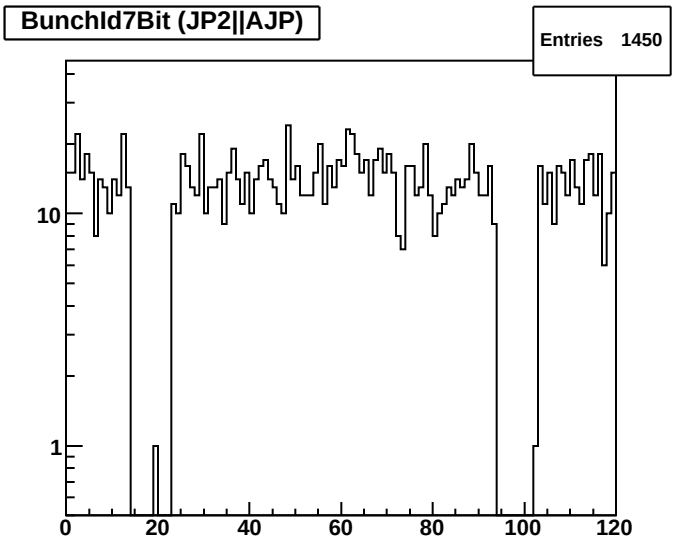

EMC L2 Input - HT23 bits
Entries 11600
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

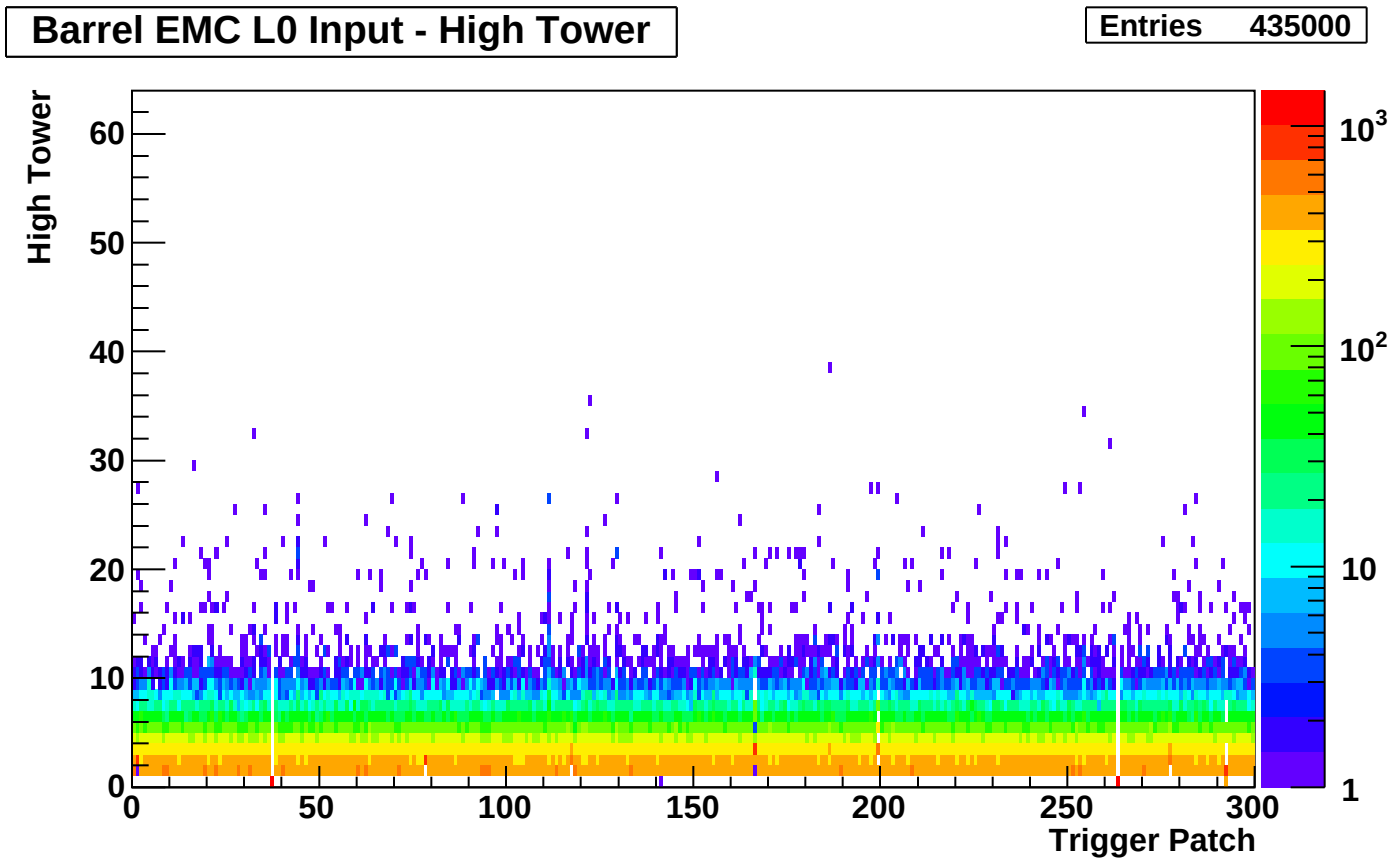

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

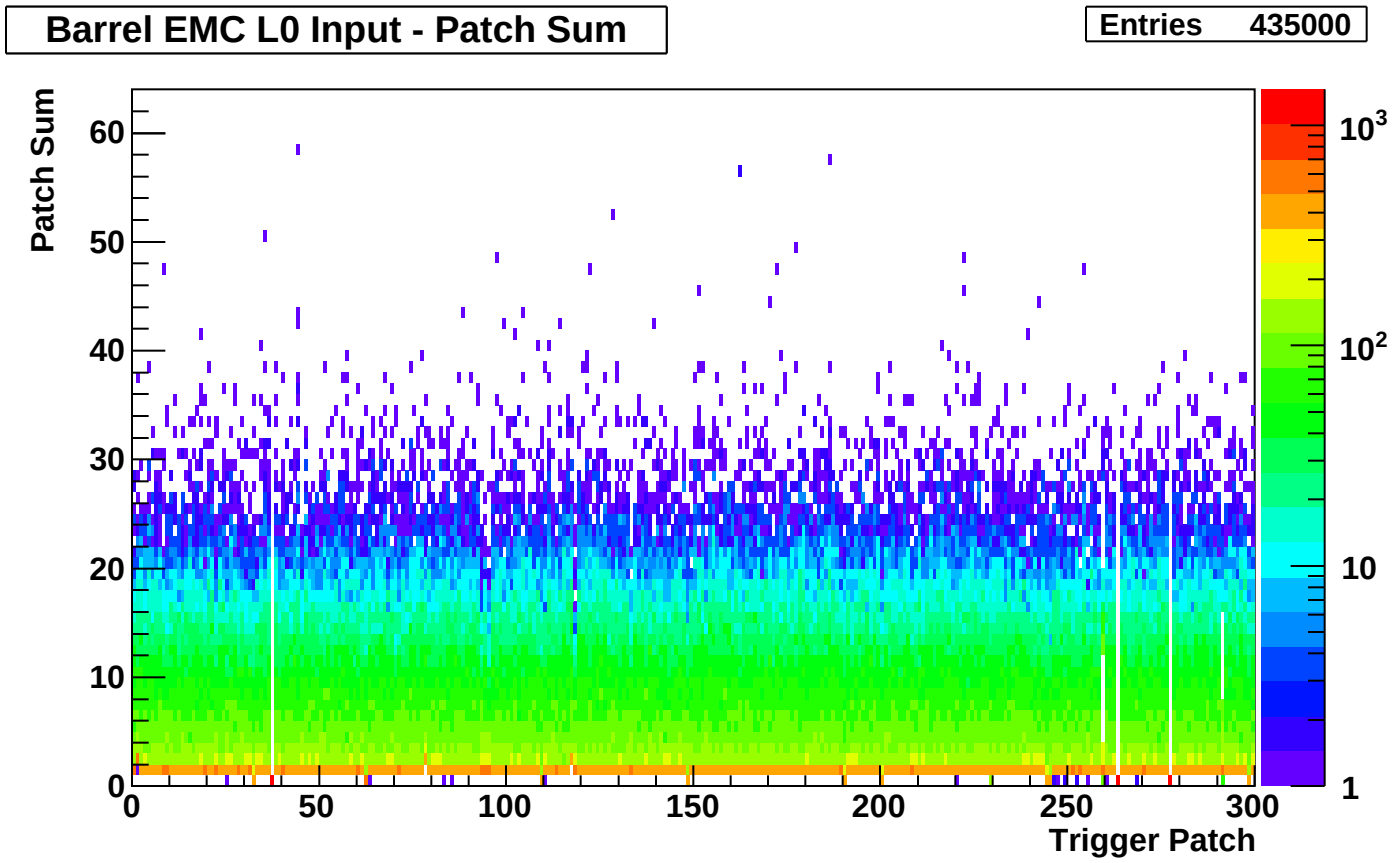

BunchId7Bit (BHT3)
Entries 22
1
0
20
40
60
80
100
120

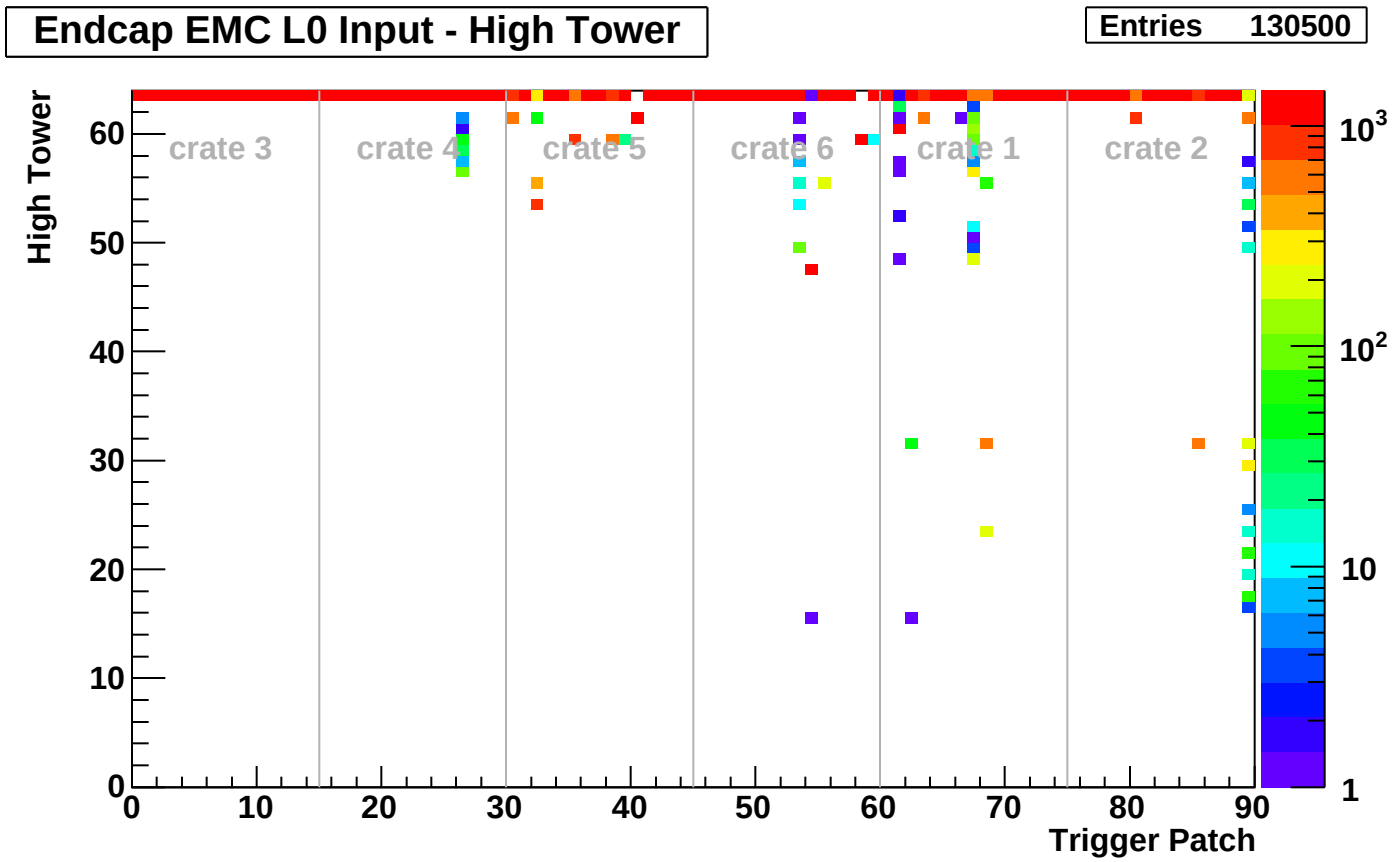

BunchId7Bit (all events)
Entries 1450
10
1
0
20
40
60
80
100
120

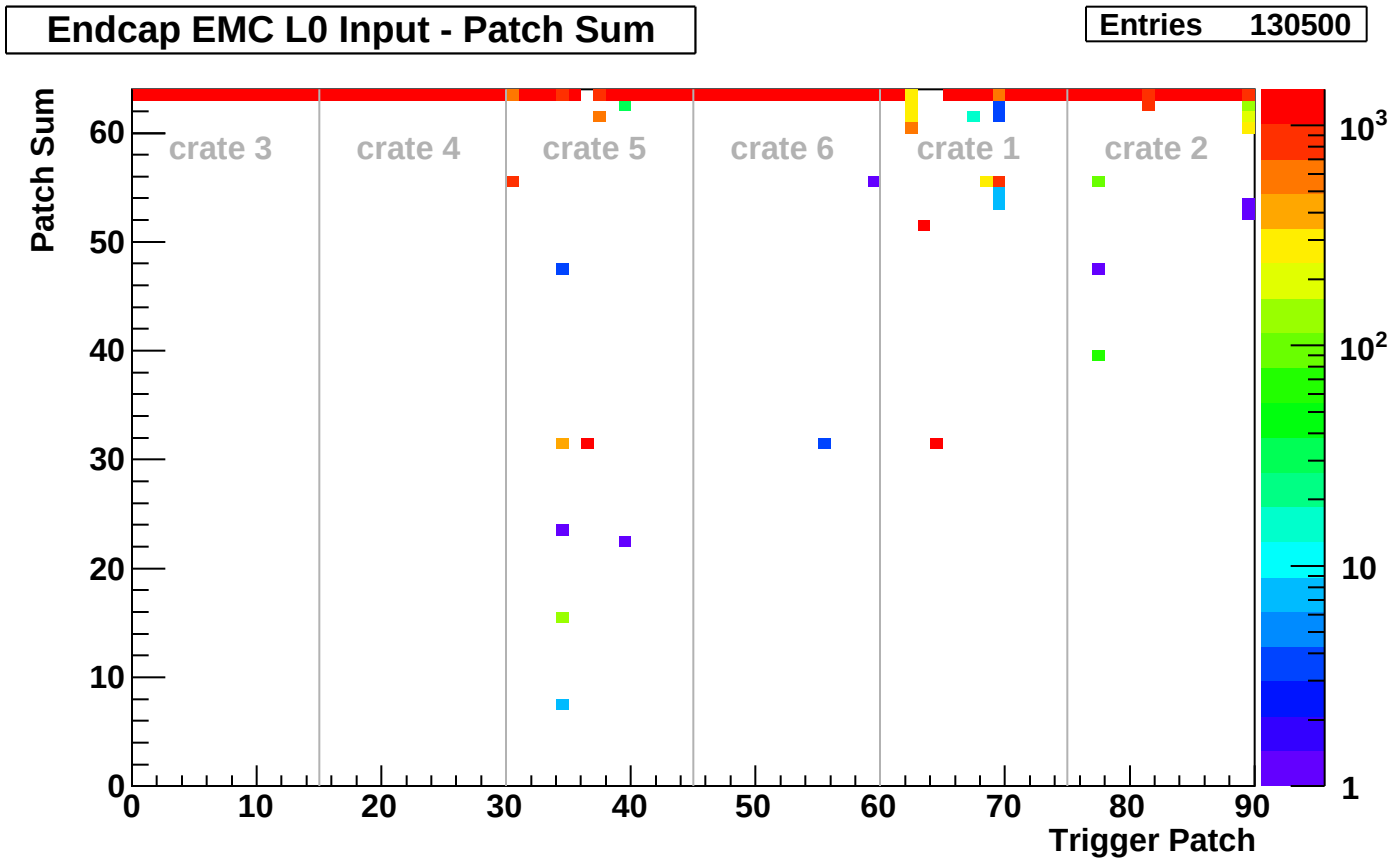

BunchId7Bit (JP1)
Entries 1450
10
1
0
20
40
60
80
100
120

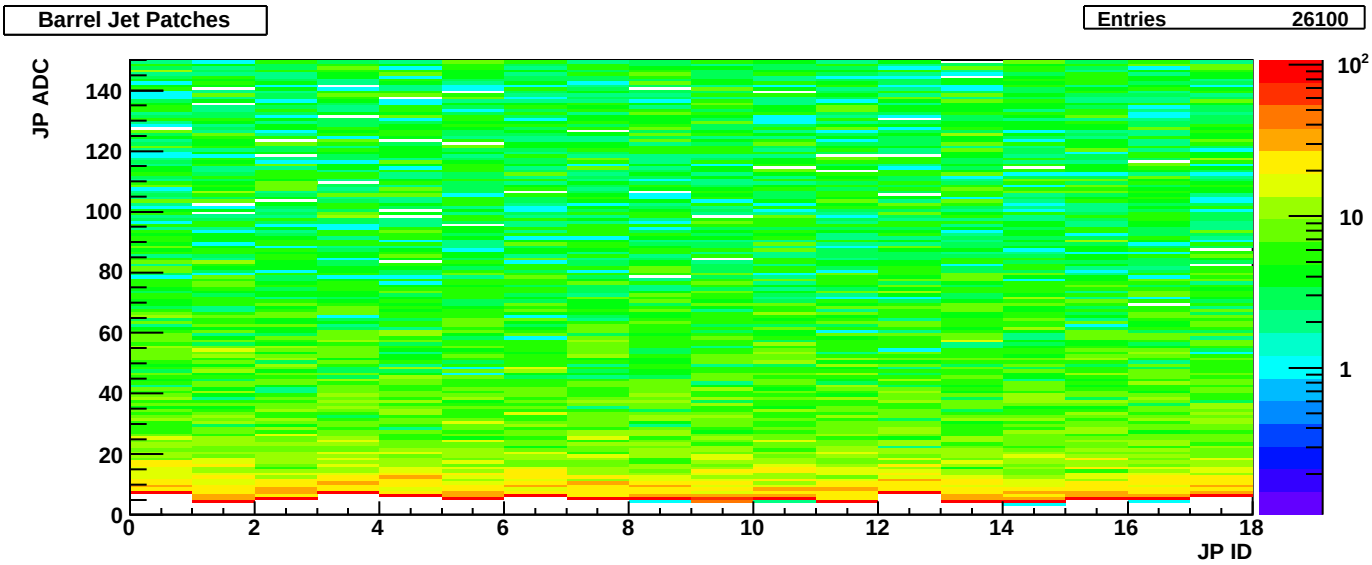

BunchId7Bit (JP2||AJP)
Entries 1450
10
1
0
20
40
60
80
100
120

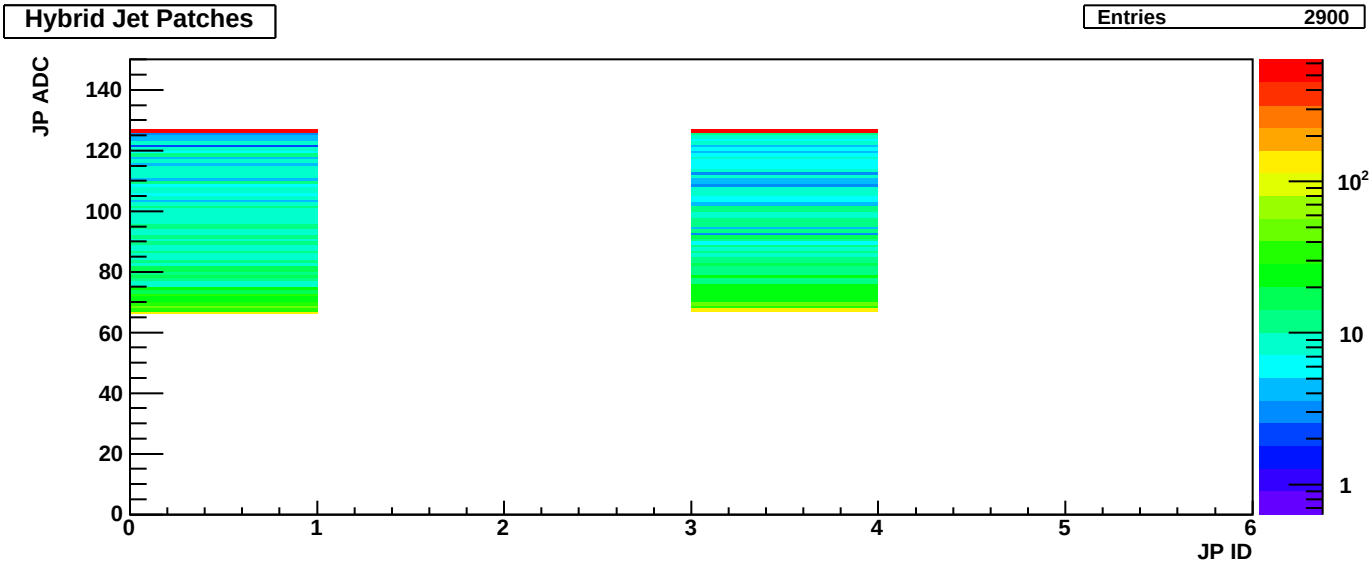

Barrel EMC L0 Input - High Tower
Entries 435000
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3

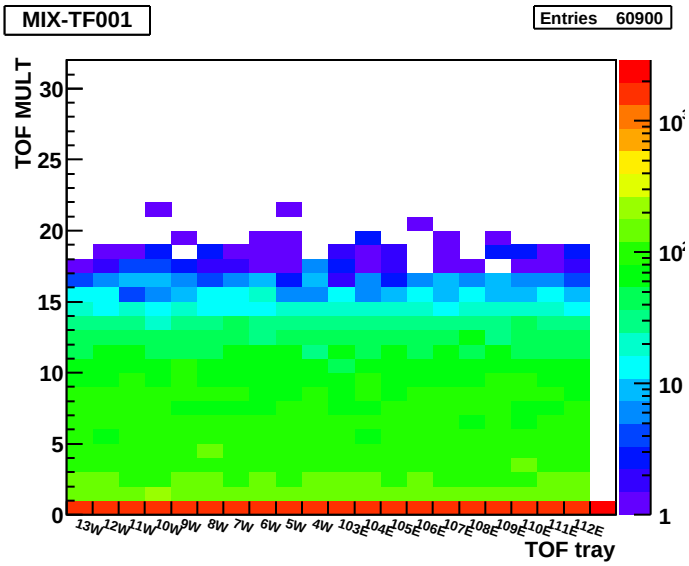

Barrel EMC L0 Input - Patch Sum
Entries 435000
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3

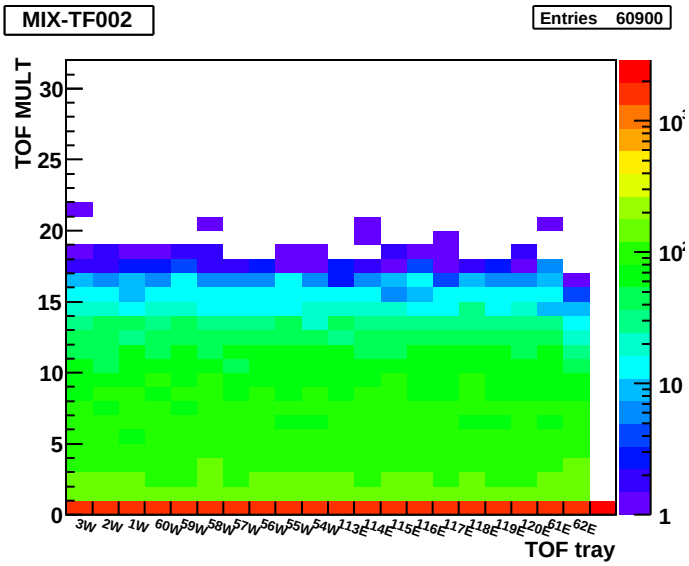

Endcap EMC L0 Input - High Tower
Entries 130500
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

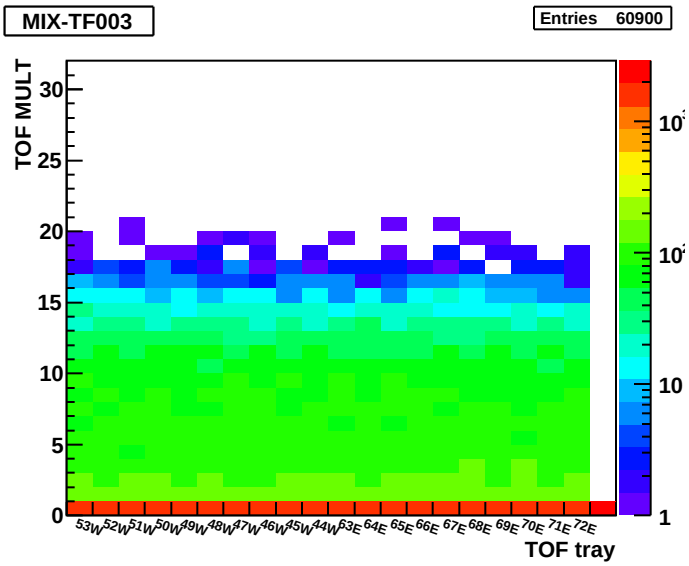

Endcap EMC L0 Input - Patch Sum
Entries 130500
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

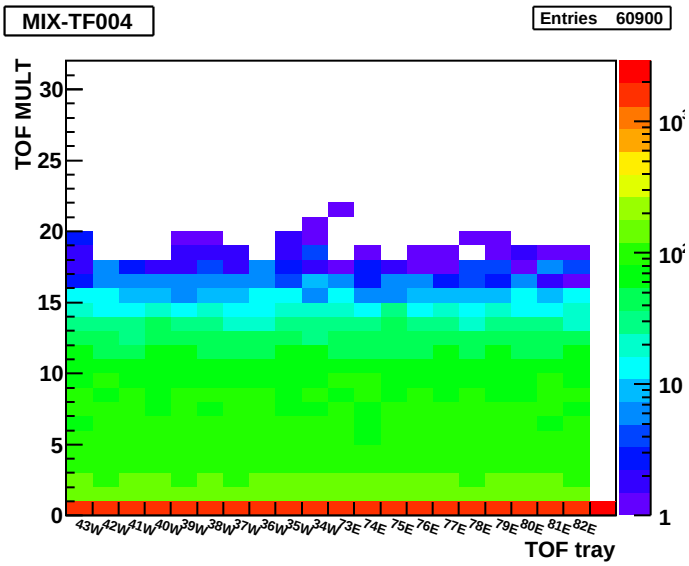

Barrel Jet Patches
Entries 26100
JP ADC
JP ID

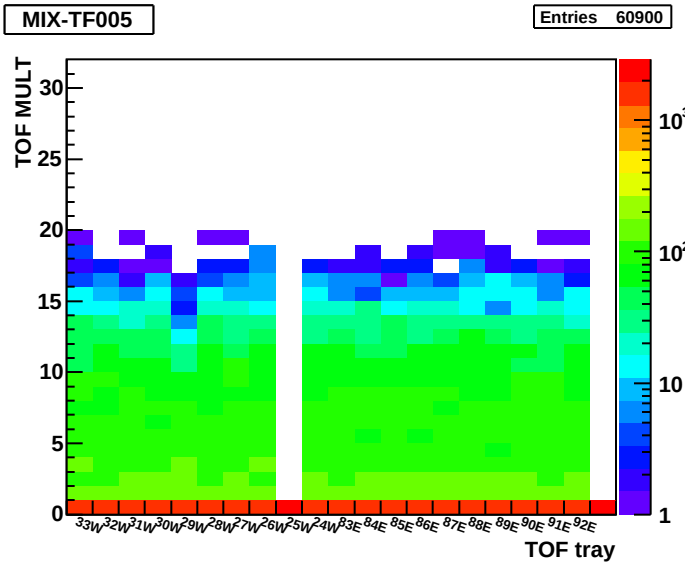

Hybrid Jet Patches
Entries 2900
JP ADC
JP ID

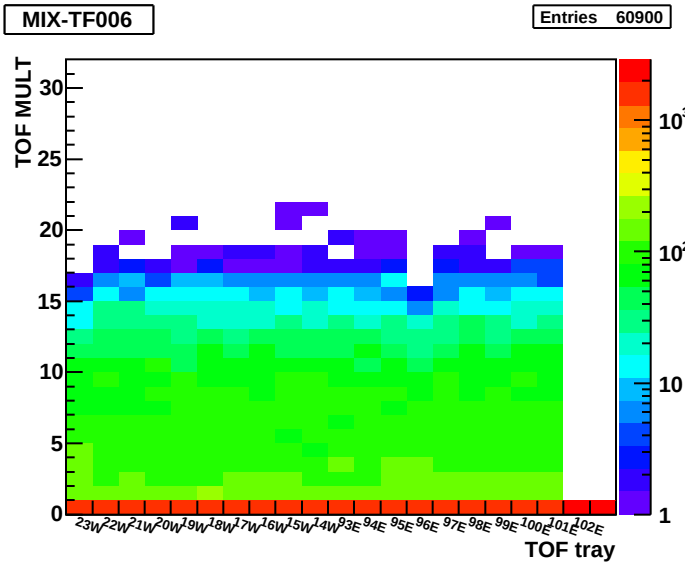

MIX-TF001
Entries 60900
TOF MULT
TOF tray

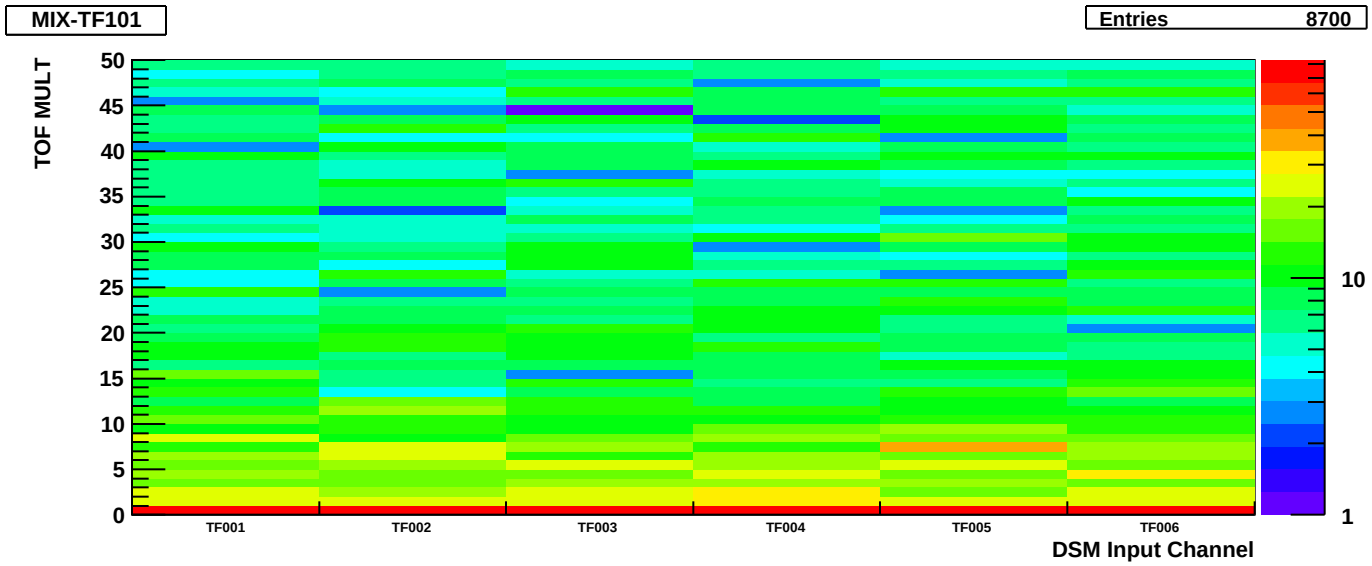

MIX-TF002
Entries 60900
TOF MULT
TOF tray

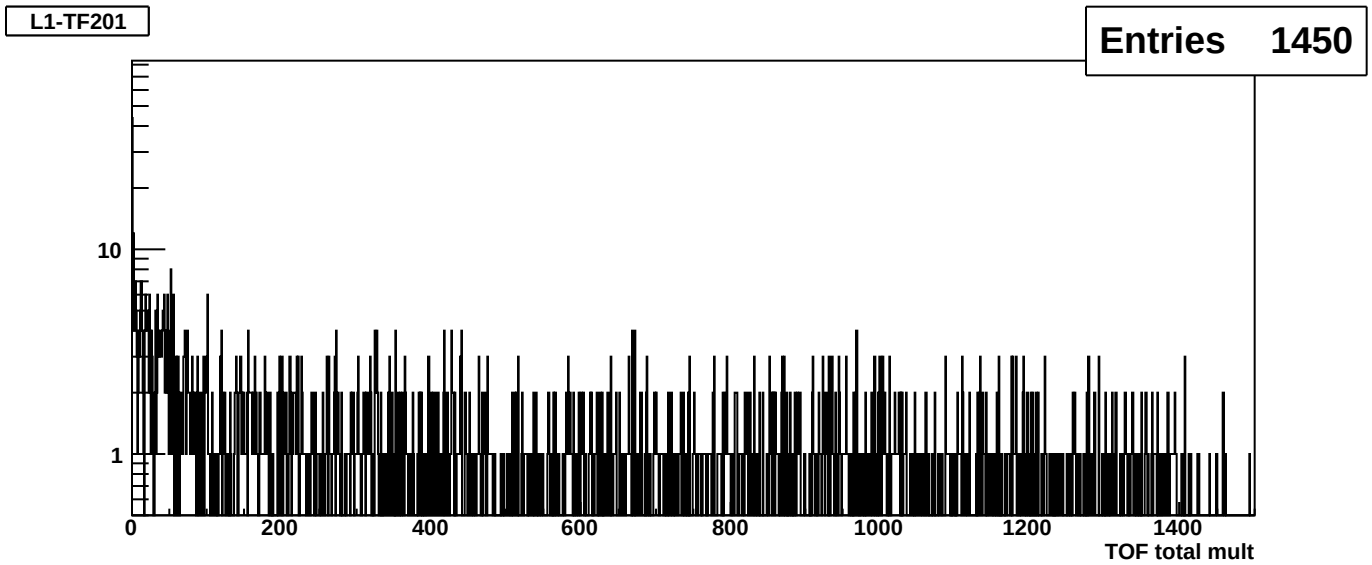

MIX-TF003
Entries 60900
TOF MULT
TOF tray

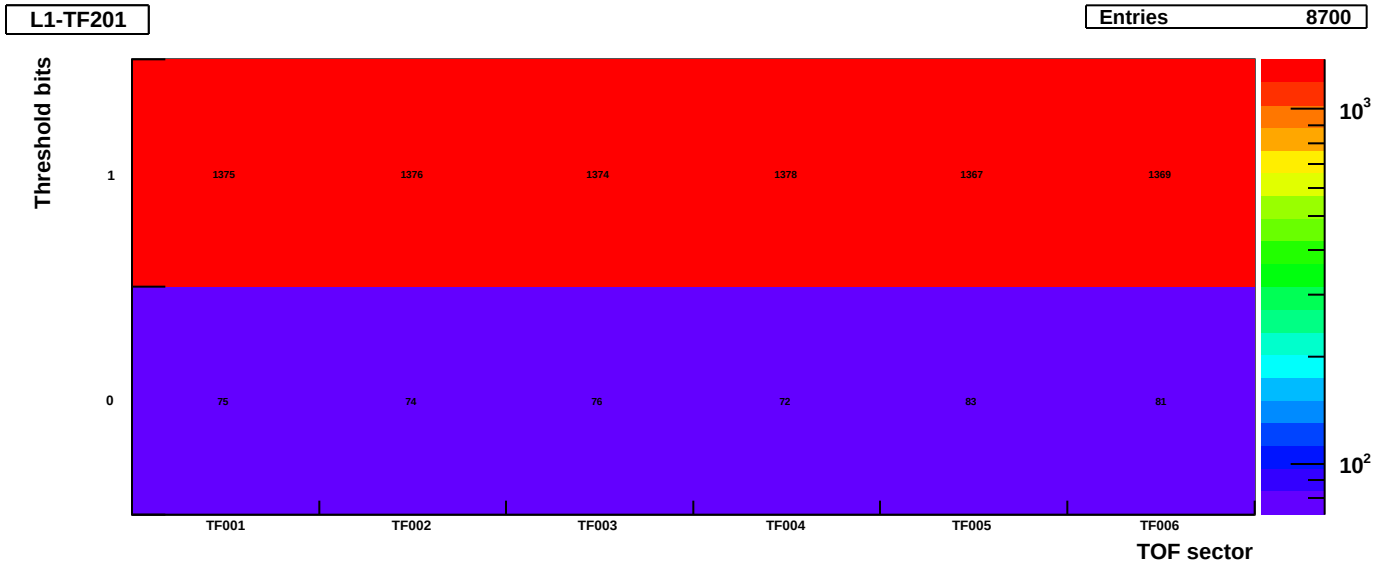

MIX-TF004
Entries 60900
TOF MULT
TOF tray

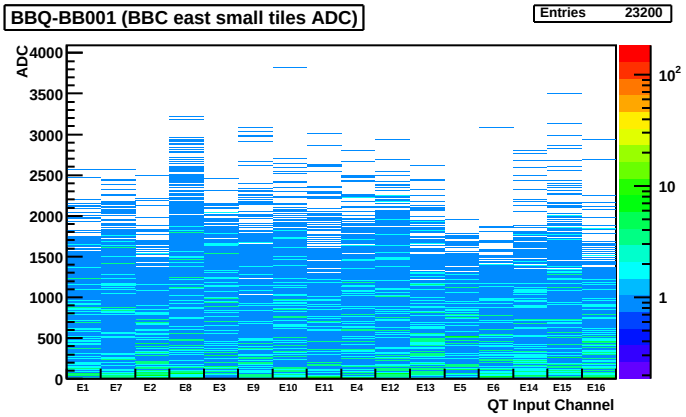

MIX-TF005
Entries 60900
TOF MULT
TOF tray

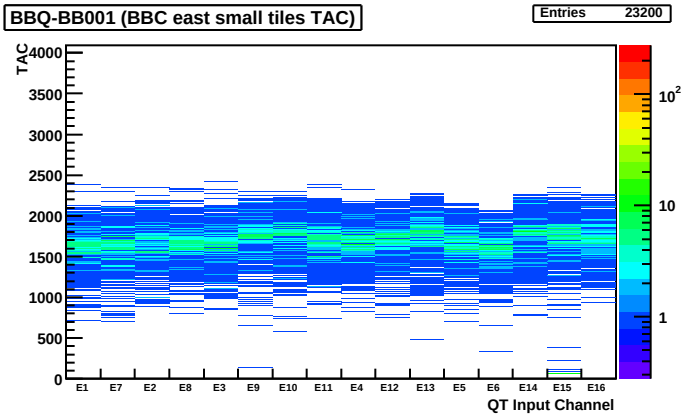

MIX-TF006
Entries 60900
TOF MULT
TOF tray

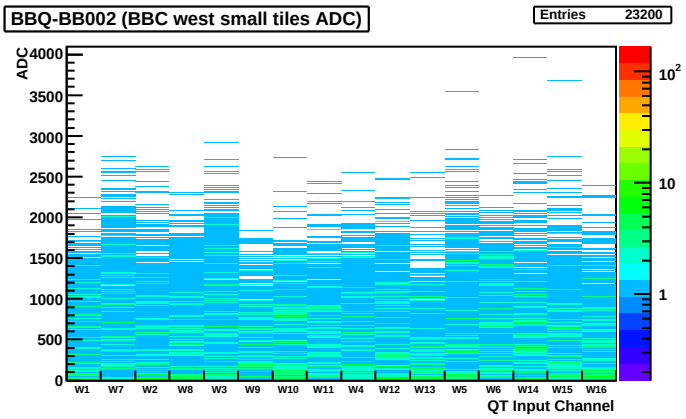

MIX-TF101
Entries 8700
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10
1

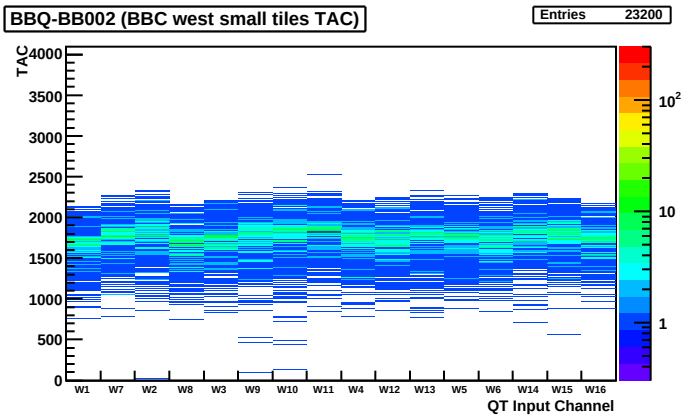

L1-TF201
Entries 1450
10
1
0
200
400
600
800
1000
1200
1400
TOF total mult

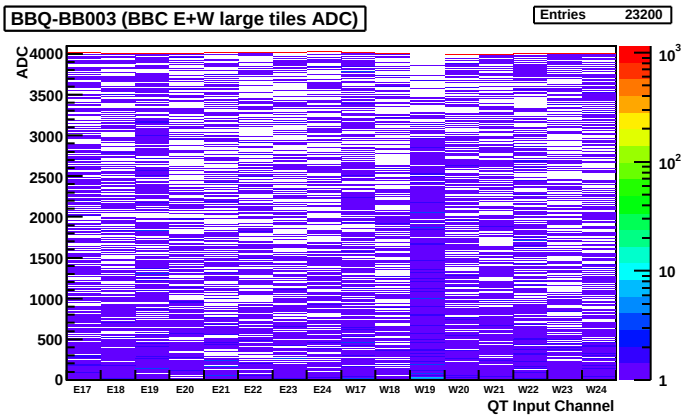

L1-TF201
Entries 8700
Threshold bits
1
0
1375
1376
1374
1378
1367
1369
75
74
76
72
83
81
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^3
10^2

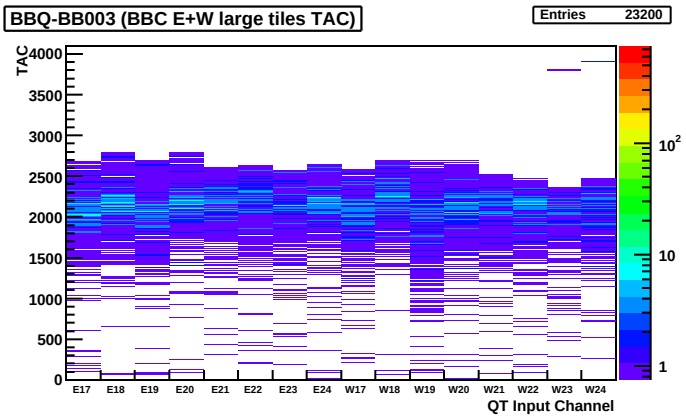

BBQ-BB001 (BBC east small tiles ADC)
Entries 23200
ADC
QT Input Channel

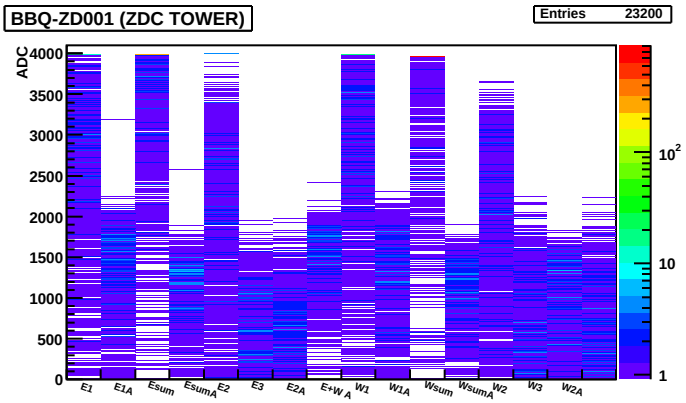

BBQ-BB001 (BBC east small tiles TAC)
Entries 23200
TAC
QT Input Channel

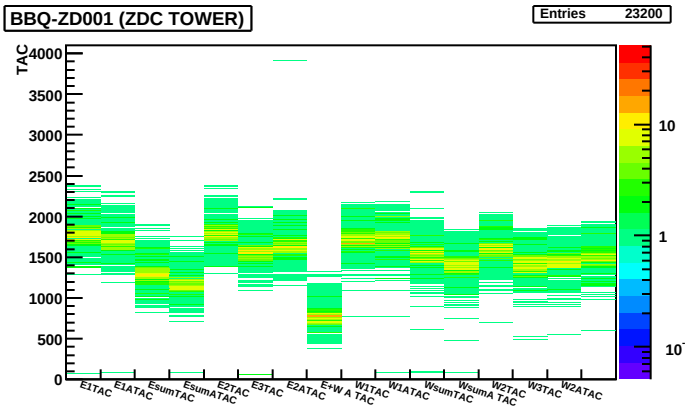

BBQ-BB002 (BBC west small tiles ADC)
Entries 23200
ADC
QT Input Channel

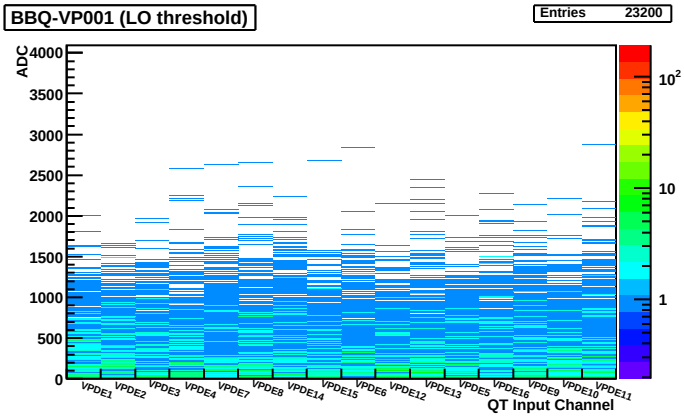

BBQ-BB002 (BBC west small tiles TAC)
Entries 23200
TAC
QT Input Channel

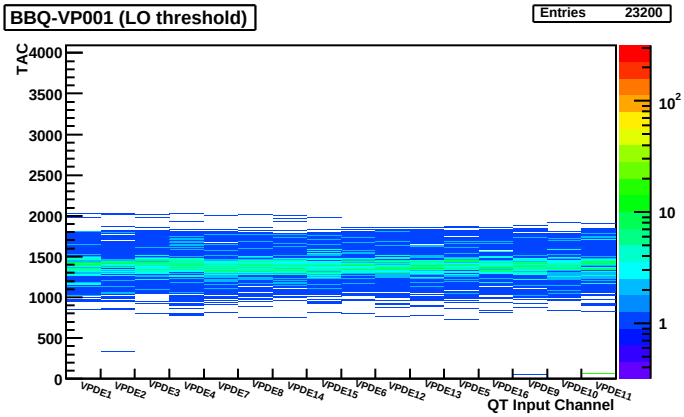

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 23200
ADC
QT Input Channel

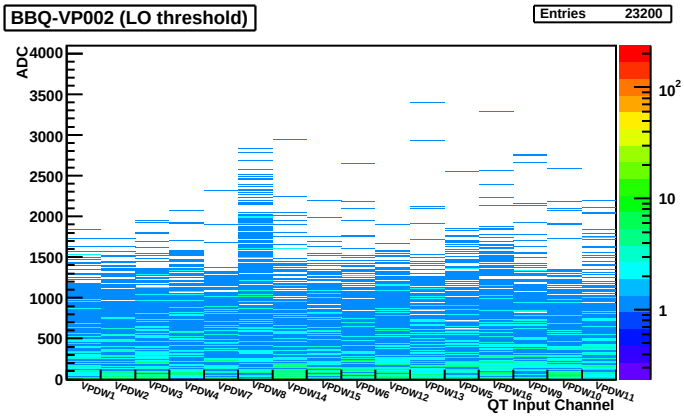

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 23200
TAC
QT Input Channel

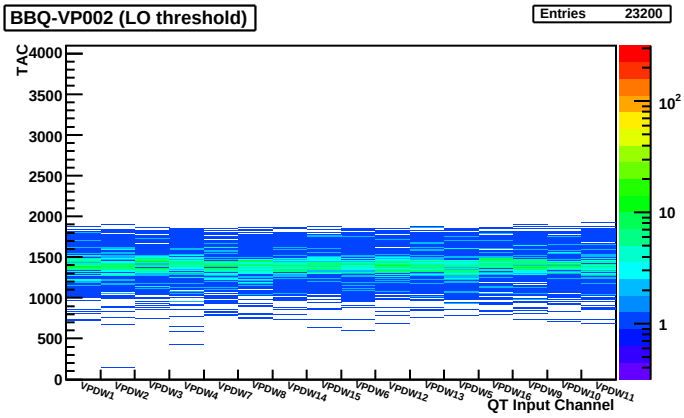

BBQ-ZD001 (ZDC TOWER)
Entries 23200
ADC

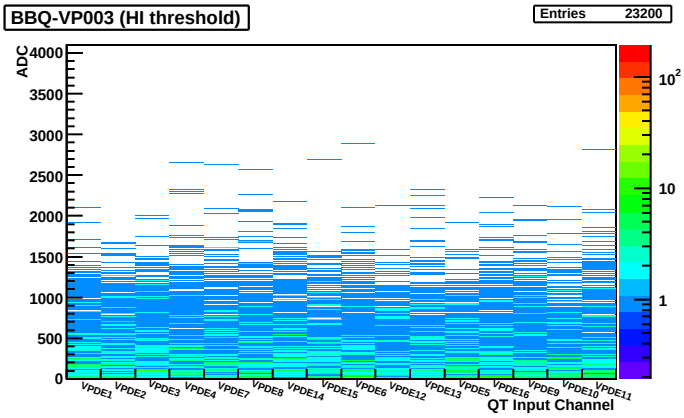

BBQ-ZD001 (ZDC TOWER)
Entries 23200
TAC

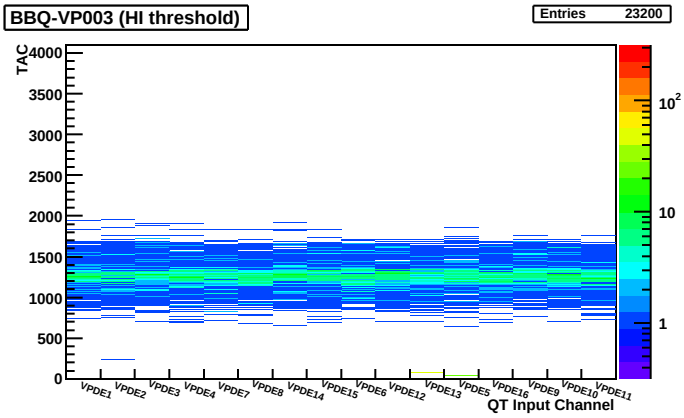

BBQ-VP001 (LO threshold)
Entries 23200
ADC
QT Input Channel

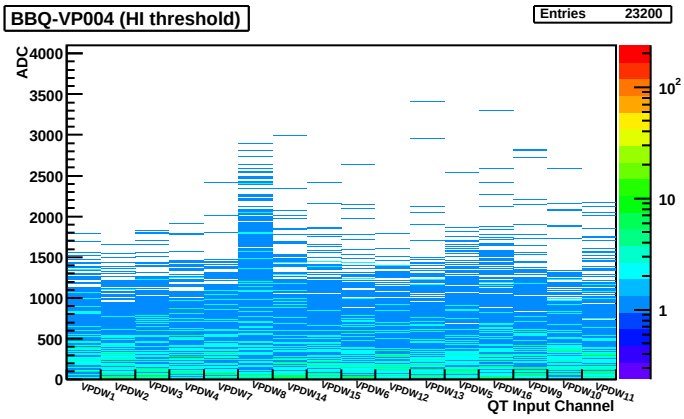

BBQ-VP001 (LO threshold)
Entries 23200
TAC
QT Input Channel

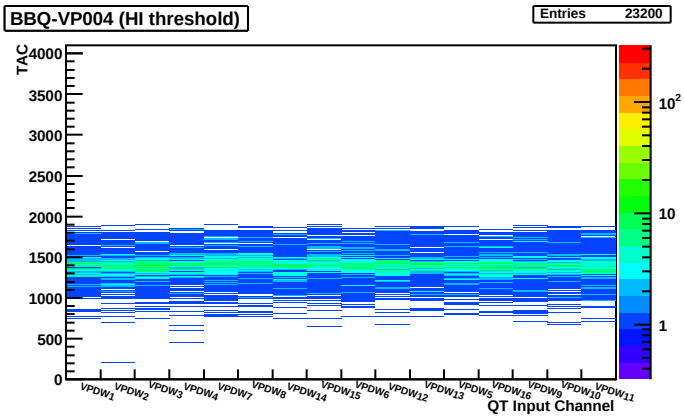

BBQ-VP002 (LO threshold)
Entries 23200
ADC
QT Input Channel

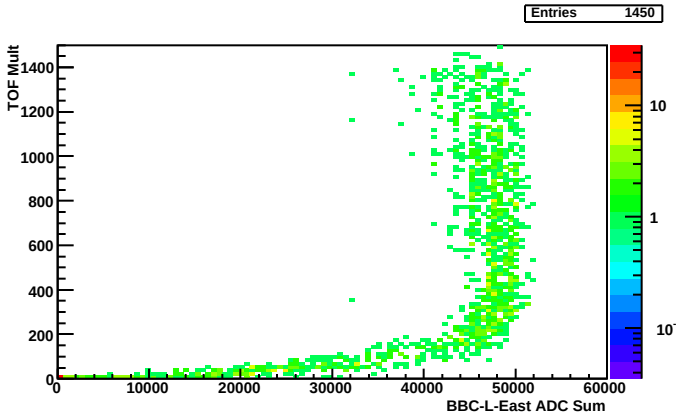

BBQ-VP002 (LO threshold)
Entries 23200
TAC
QT Input Channel

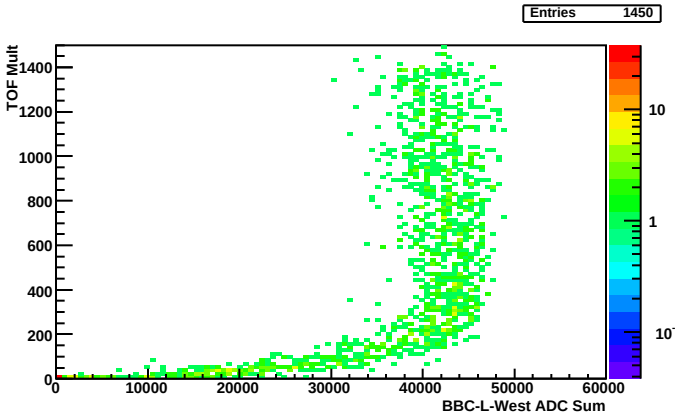

BBQ-VP003 (HI threshold)
Entries 23200
ADC
QT Input Channel

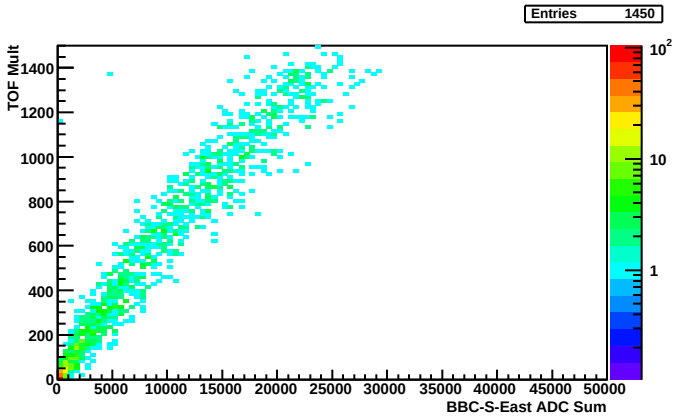

BBQ-VP003 (HI threshold)
Entries 23200
TAC
QT Input Channel

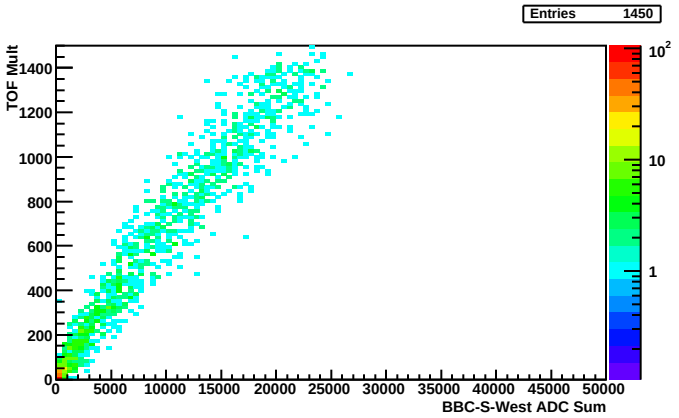

BBQ-VP004 (HI threshold)
Entries 23200
ADC
QT Input Channel

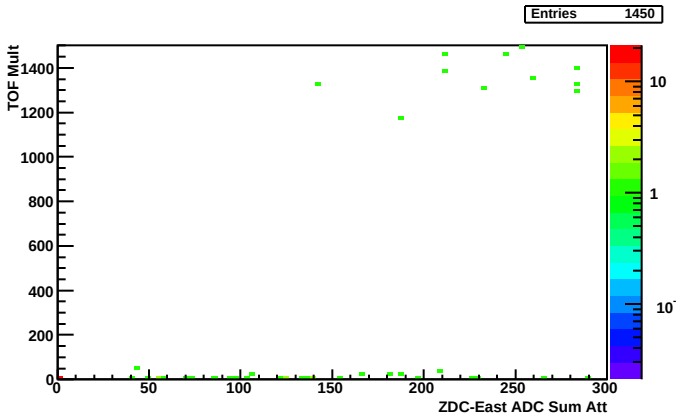

BBQ-VP004 (HI threshold)
Entries 23200
TAC
QT Input Channel

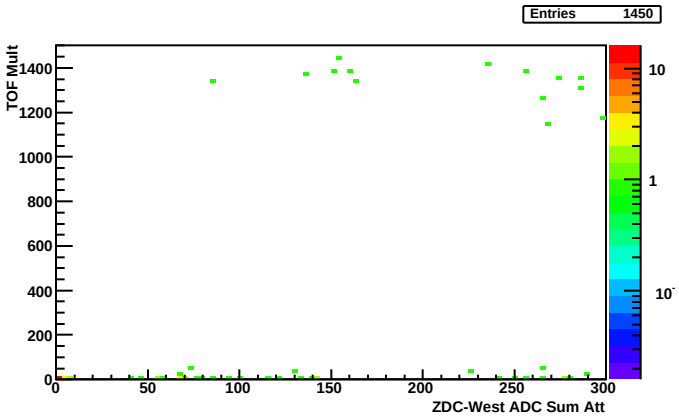

Entries 1450
TOF Mult
BBC-L-East ADC Sum

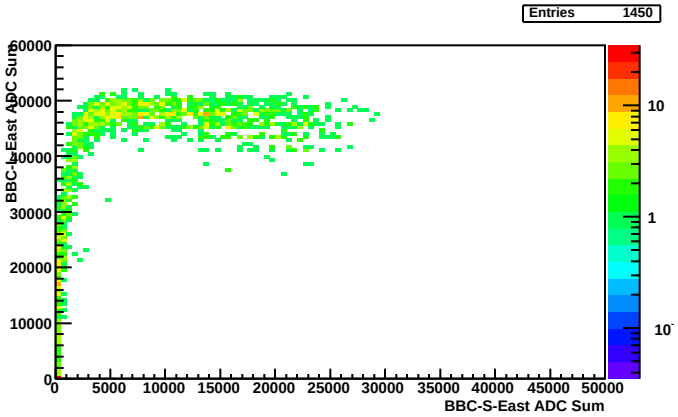

Entries 1450
TOF Mult
BBC-L-West ADC Sum

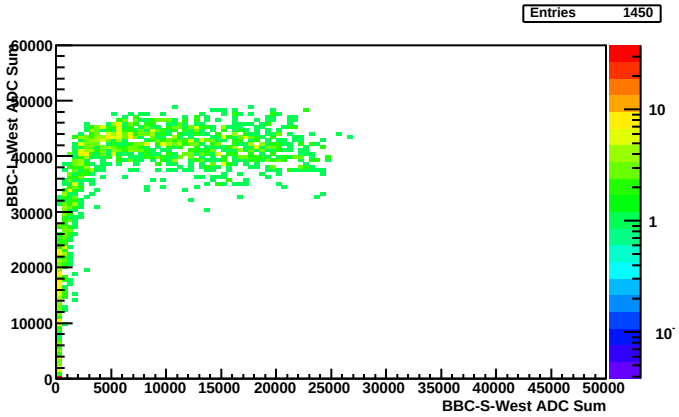

Entries 1450
TOF Mult
BBC-S-East ADC Sum

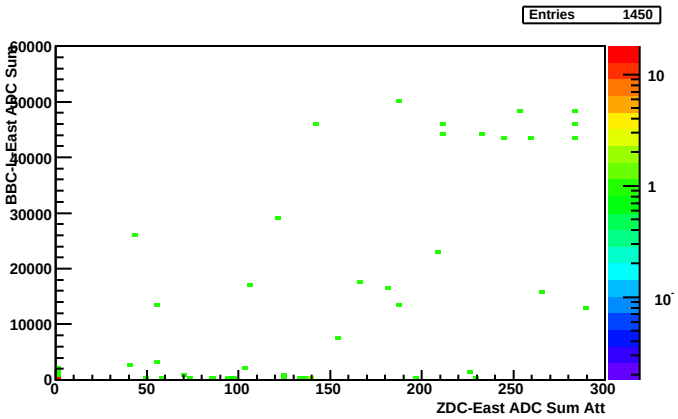

Entries 1450
TOF Mult
BBC-S-West ADC Sum

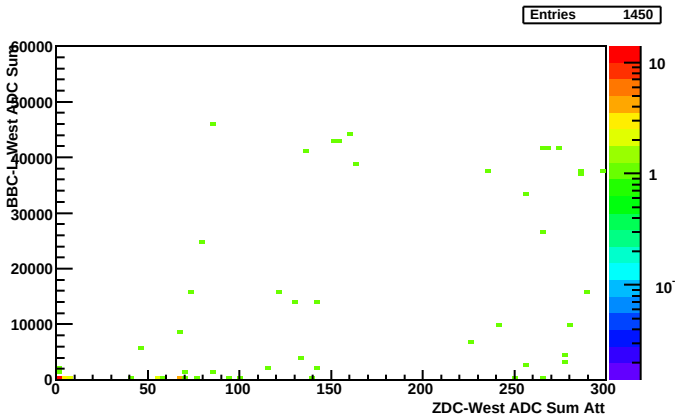

Entries 1450
TOF Mult
ZDC-East ADC Sum Att

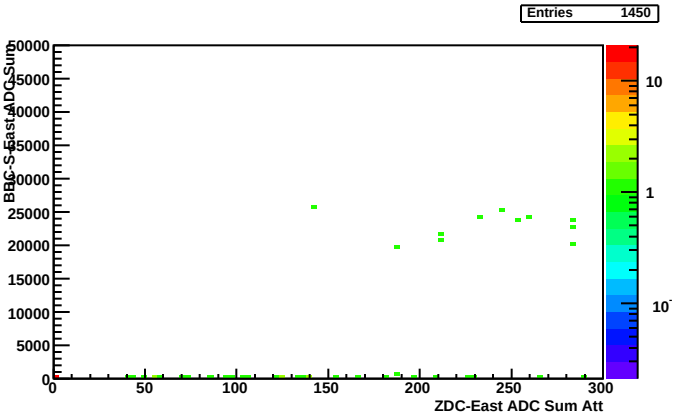

Entries 1450
TOF Mult
ZDC-West ADC Sum Att

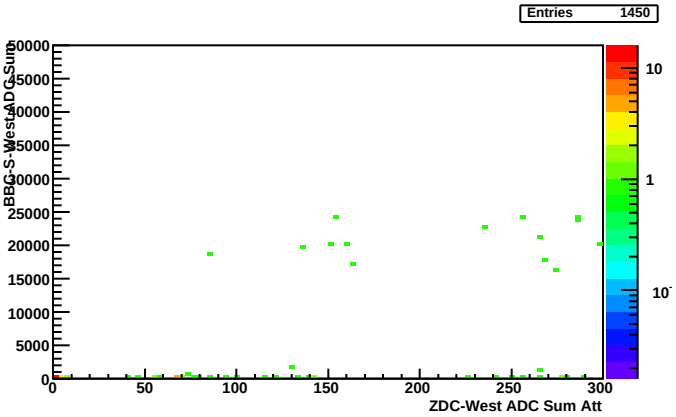

Entries 1450
BBC-L-East ADC Sum
BBC-S-East ADC Sum

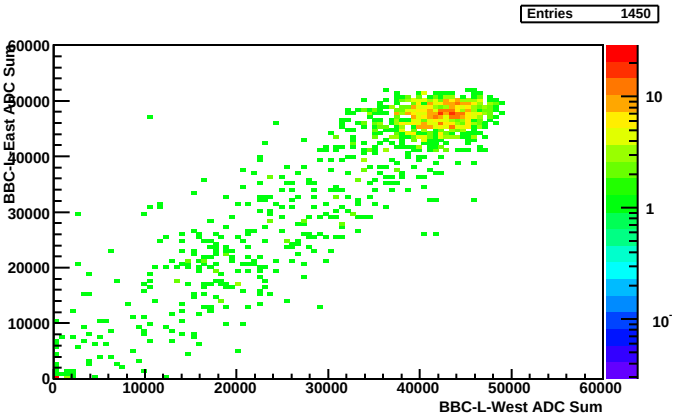

Entries 1450
BBC-L-West ADC Sum
BBC-S-West ADC Sum

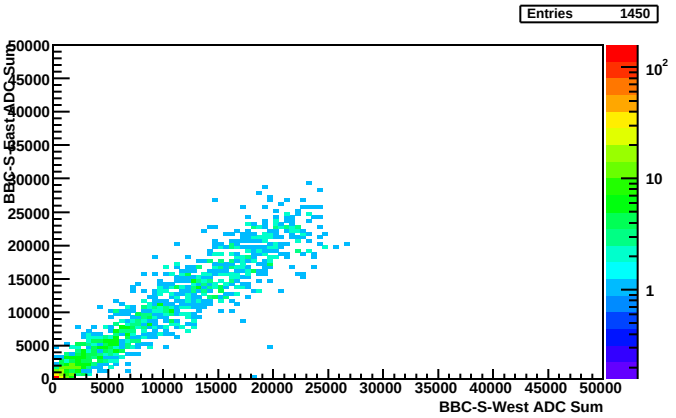

Entries 1450
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

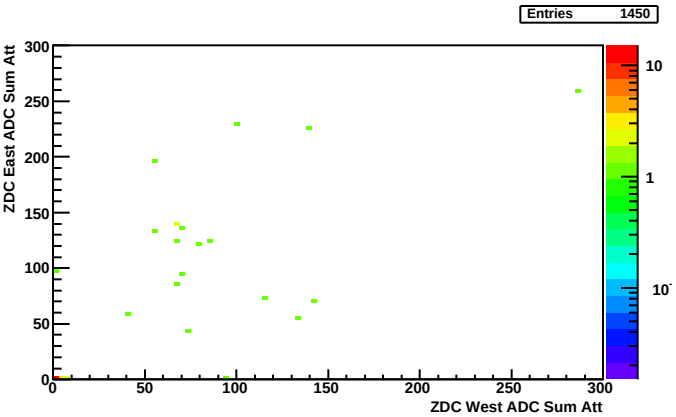

Entries 1450
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

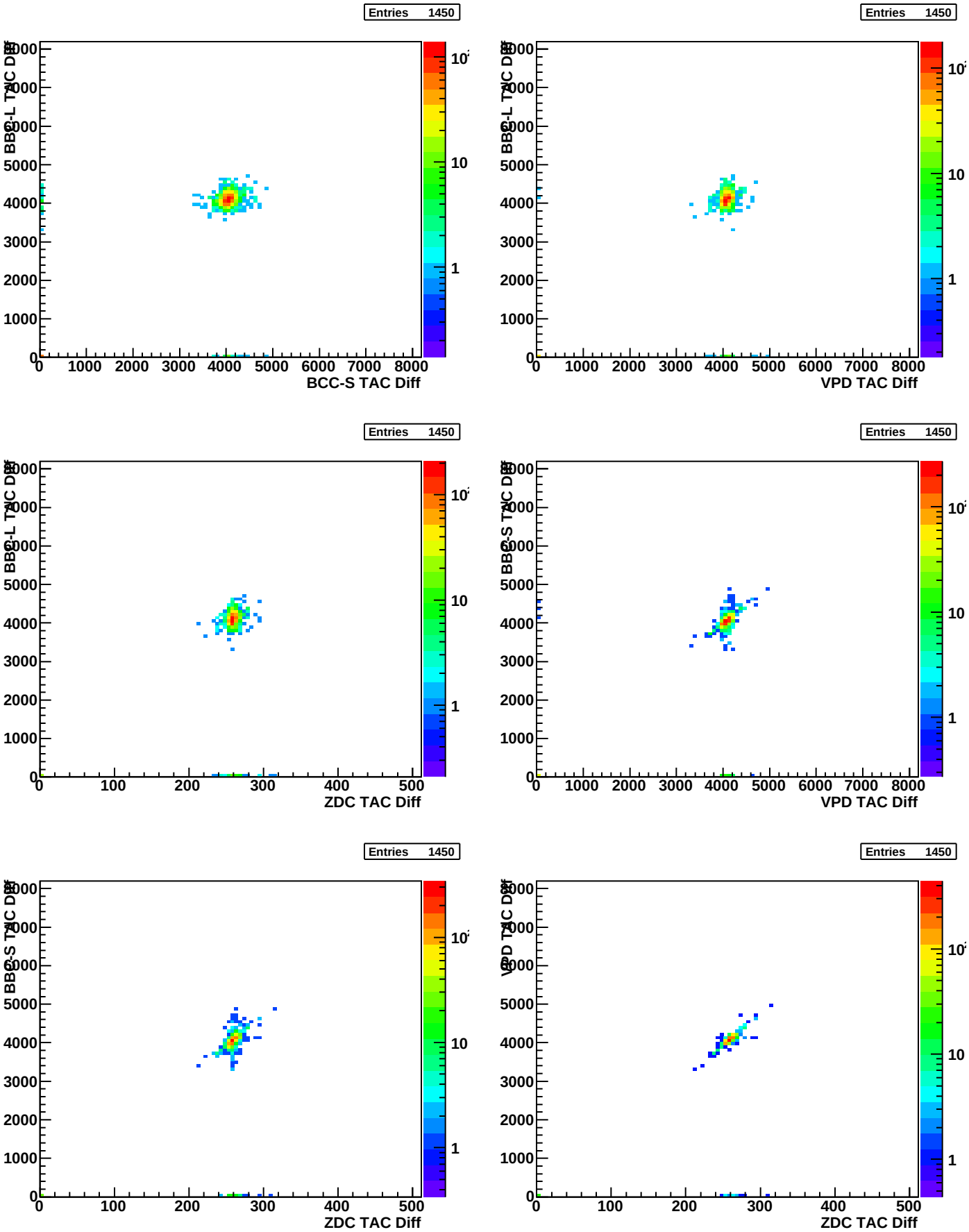

Entries 1450
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

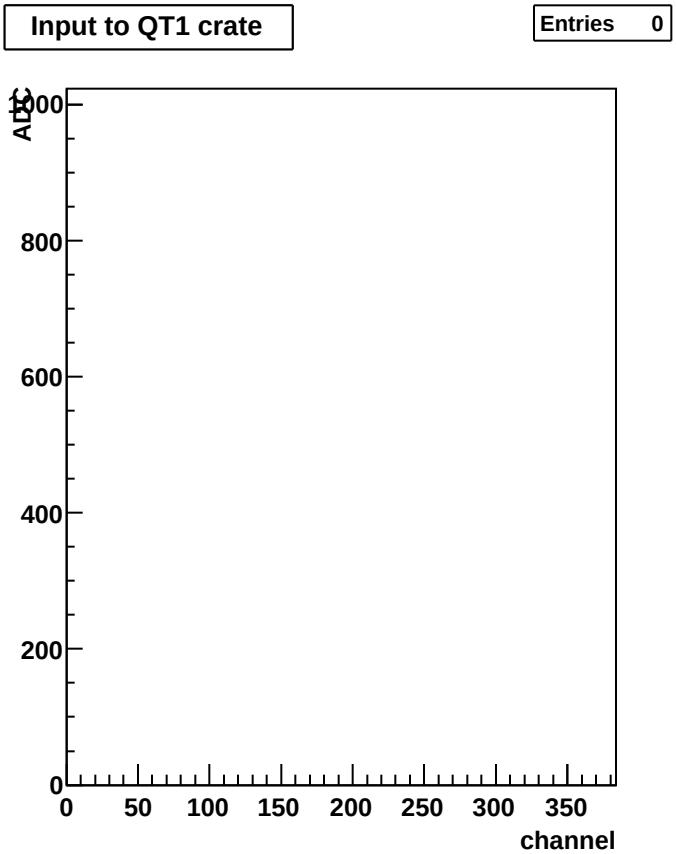

Entries 1450
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

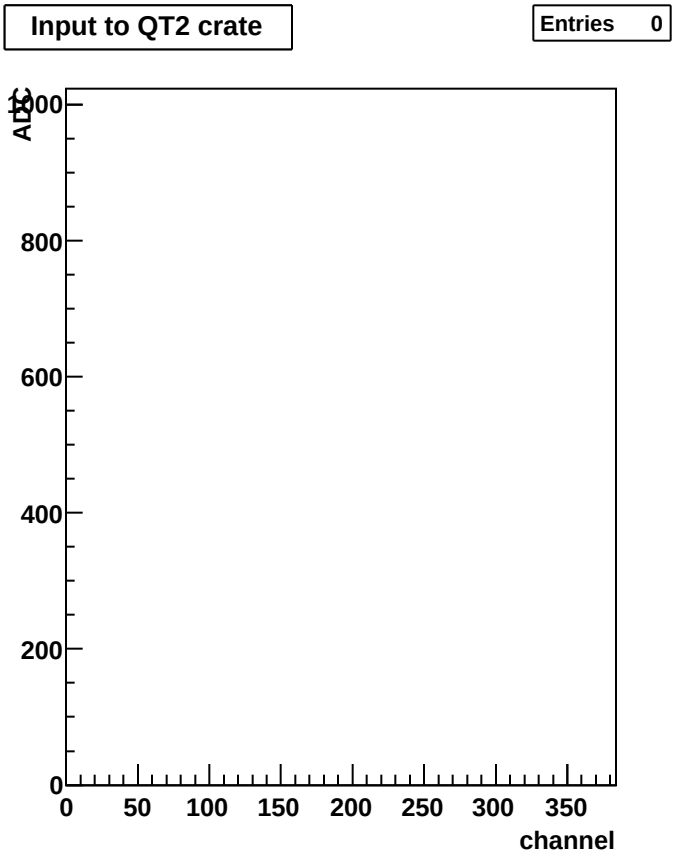

Entries 1450
BBC-L-East ADC Sum
BBC-L-West ADC Sum

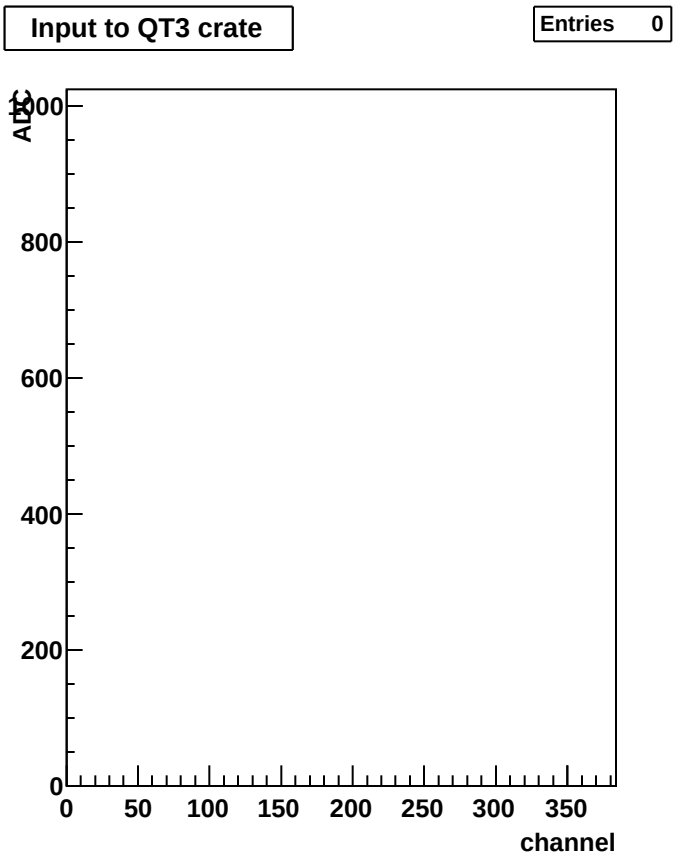

Entries 1450
BBC-S-East ADC Sum
BBC-S-West ADC Sum

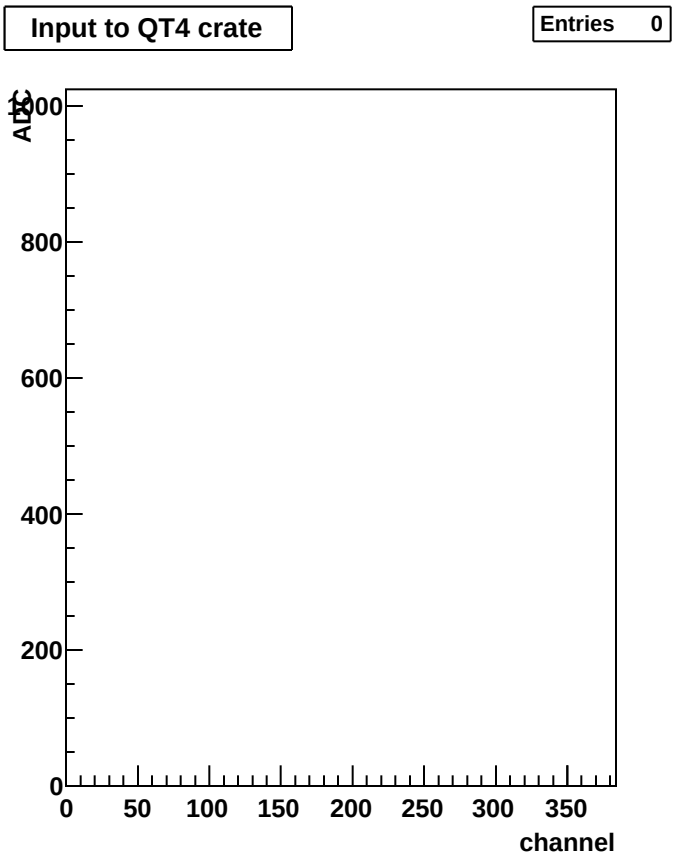

Entries 1450
ZDC East ADC Sum Att
ZDC West ADC Sum Att

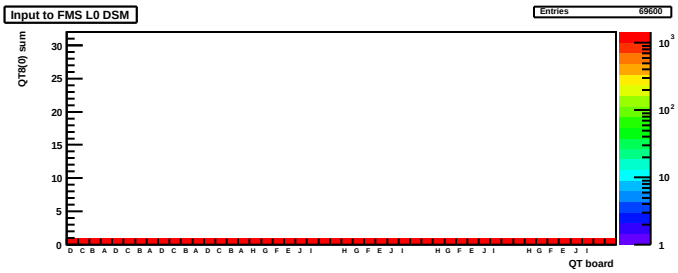

Entries 1450
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1450
BBC-L TAC Diff
VPD TAC Diff
Entries 1450
BBC-L TAC Diff
ZDC TAC Diff
Entries 1450
BBC-S TAC Diff
VPD TAC Diff
Entries 1450
BBC-S TAC Diff
ZDC TAC Diff
Entries 1450
VPD TAC Diff
ZDC TAC Diff

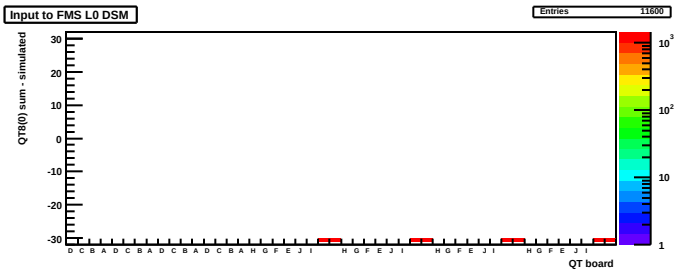

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

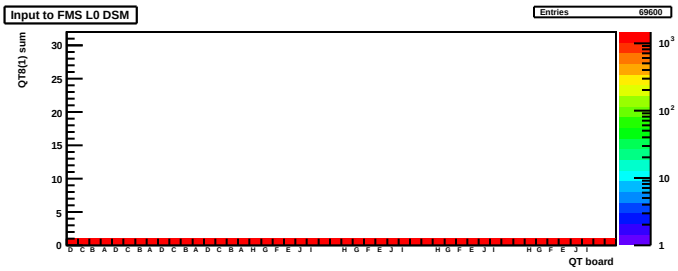

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

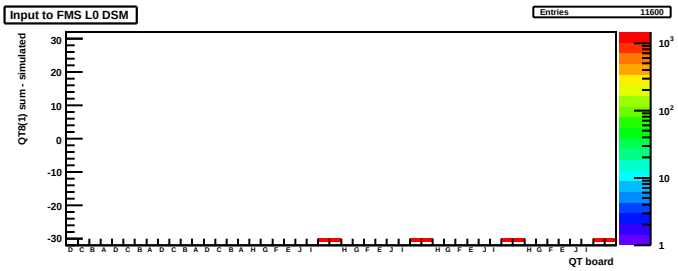

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

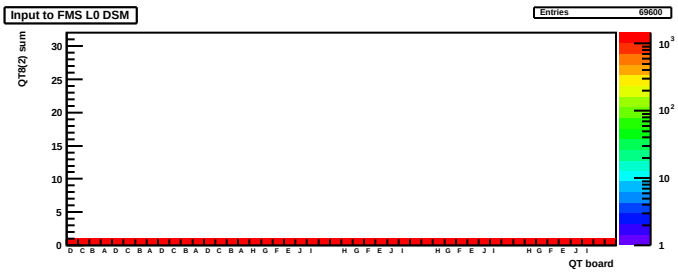

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

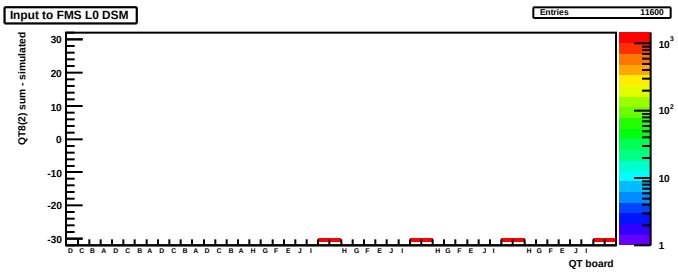

Input to FMS L0 DSM
Entries 69600
QT8(0) sum
QT board

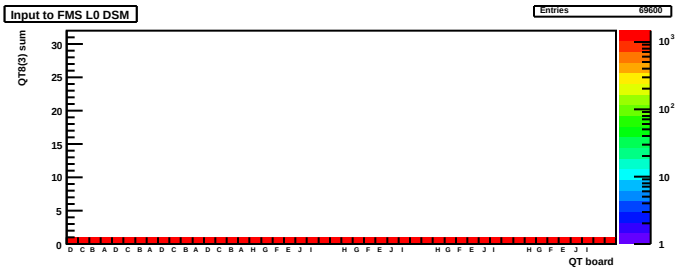

Input to FMS L0 DSM
Entries 11600
QT8(0) sum - simulated
QT board

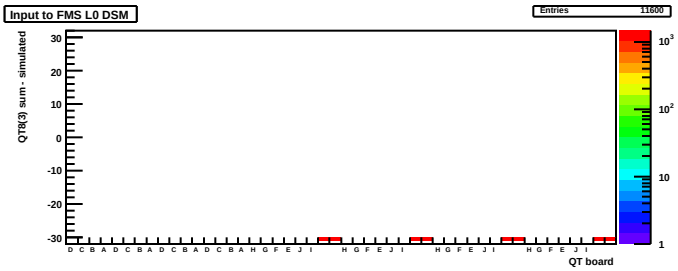

Input to FMS L0 DSM
Entries 69600
QT8(1) sum
QT board

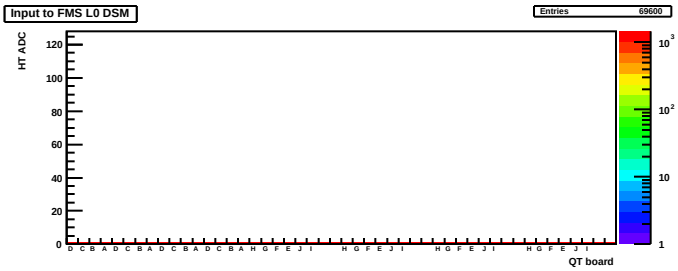

Input to FMS L0 DSM
Entries 11600
QT8(1) sum - simulated
QT board

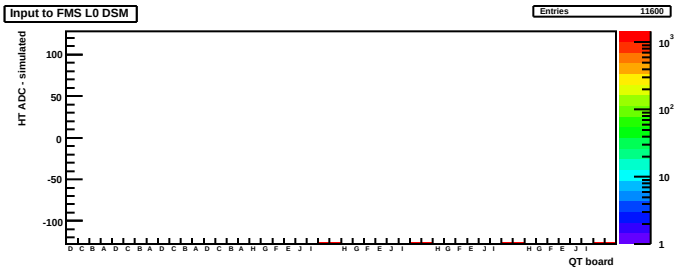

Input to FMS L0 DSM
Entries 69600
QT8(2) sum
QT board

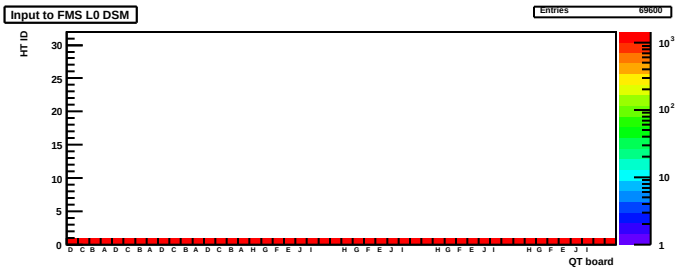

Input to FMS L0 DSM
Entries 11600
QT8(2) sum - simulated
QT board

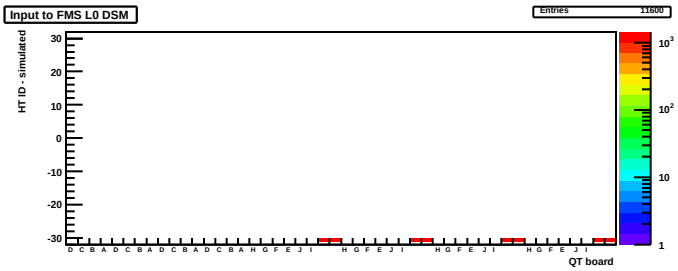

Input to FMS L0 DSM
Entries 69600
QT8(3) sum
QT board

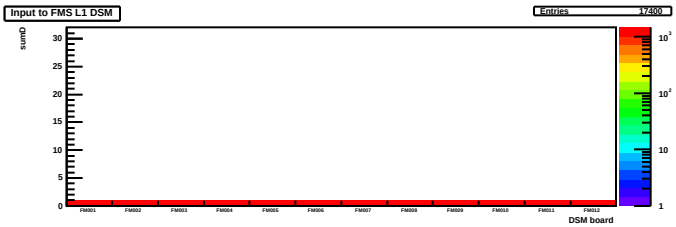

Input to FMS L0 DSM
Entries 11600
QT8(3) sum - simulated
QT board

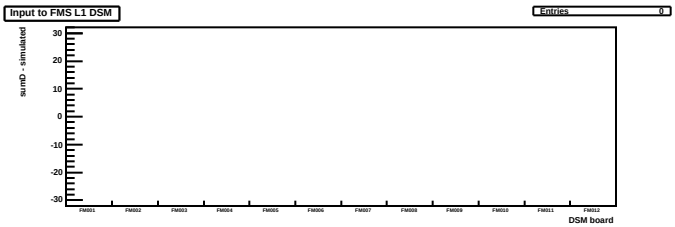

Input to FMS L0 DSM
Entries 69600
HT ADC
QT board

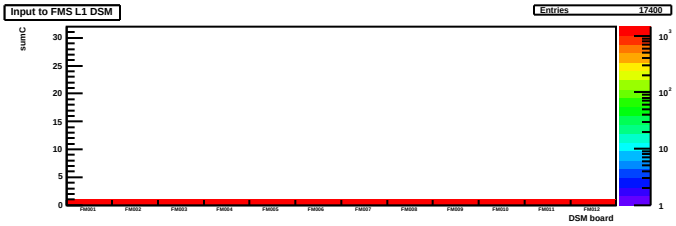

Input to FMS L0 DSM
Entries 11600
HT ADC - simulated
QT board

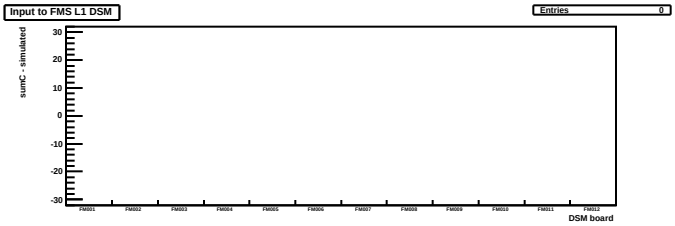

Input to FMS L0 DSM
Entries 69600
HT ID
QT board

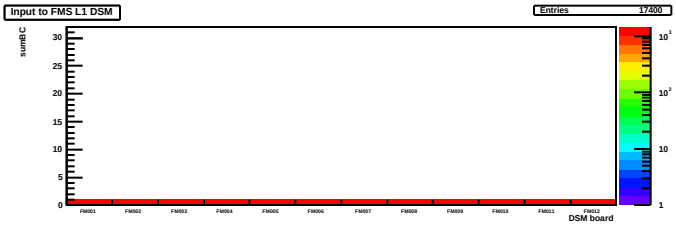

Input to FMS L0 DSM
Entries 11600
HT ID - simulated
QT board

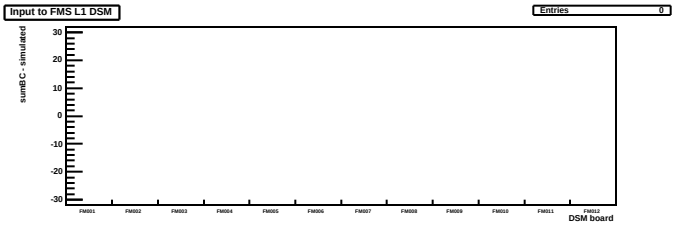

Input to FMS L1 DSM
Entries 17400
sumD
DSM board

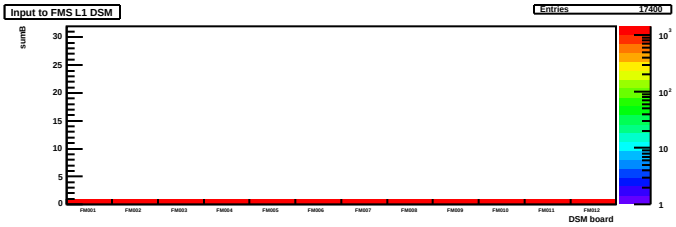

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

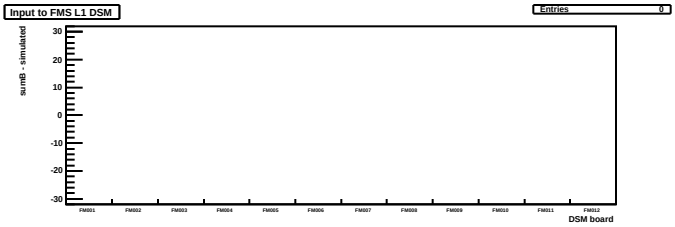

Input to FMS L1 DSM
Entries 17400
sumC
DSM board

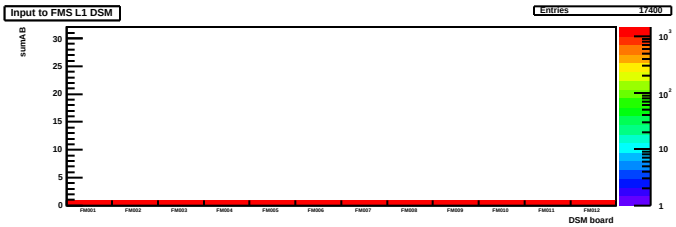

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

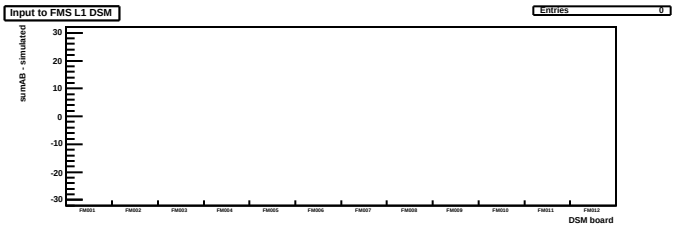

Input to FMS L1 DSM
Entries 17400
sumBC
DSM board

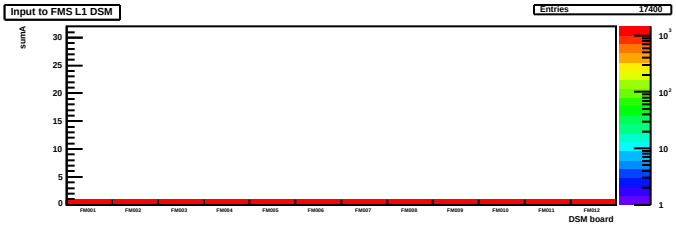

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

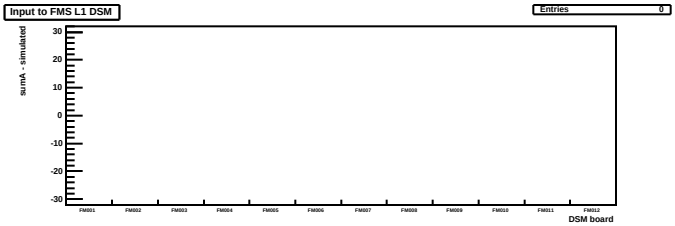

Input to FMS L1 DSM
Entries 17400
sumB
DSM board

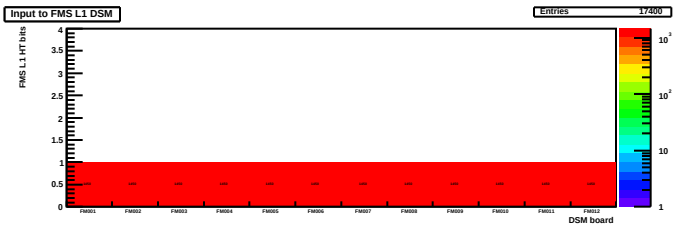

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

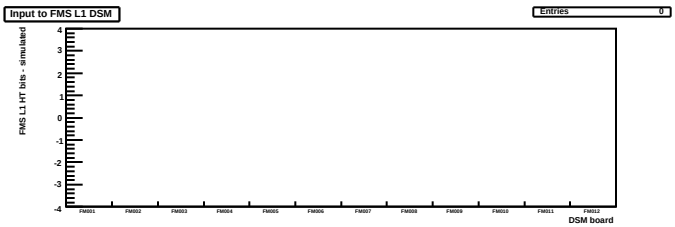

Input to FMS L1 DSM
Entries 17400
sumAB
DSM board

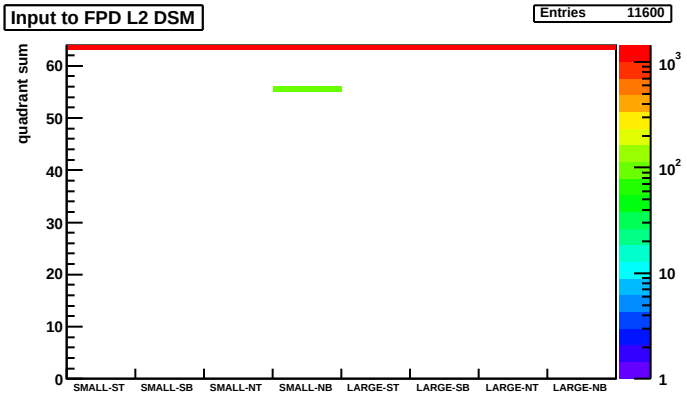

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

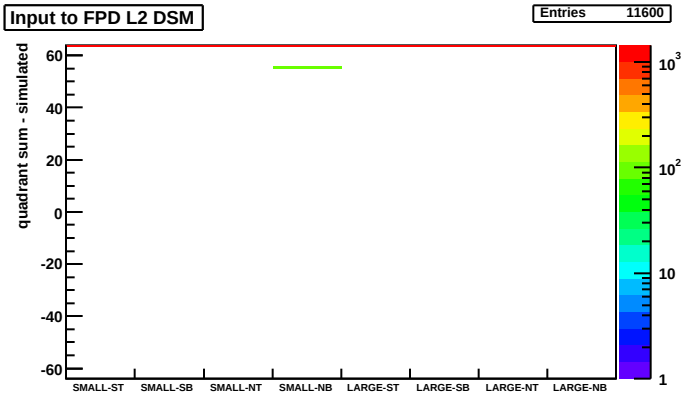

Input to FMS L1 DSM
Entries 17400
sumA
DSM board

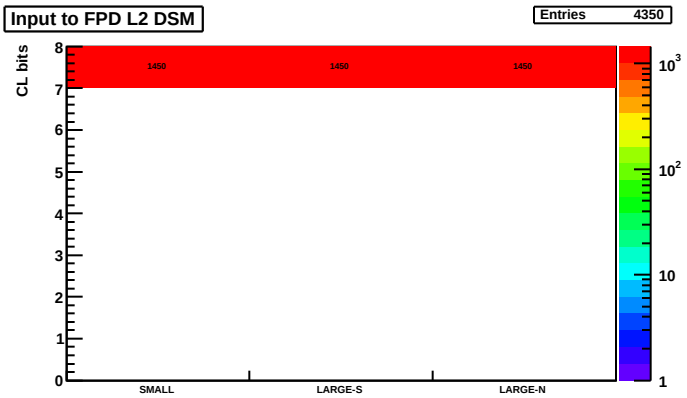

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

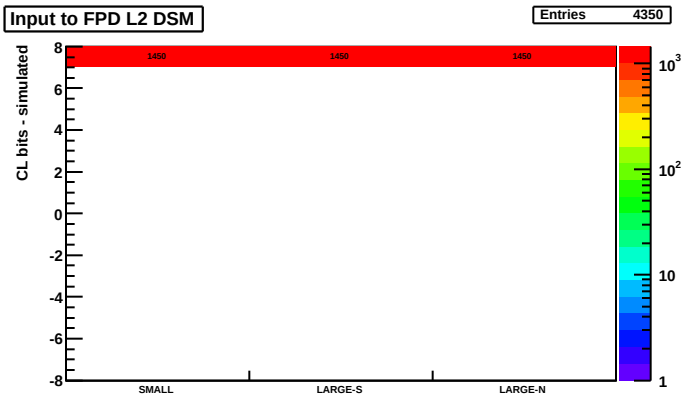

Input to FMS L1 DSM
Entries 17400
FMS L1 HT bits
DSM board

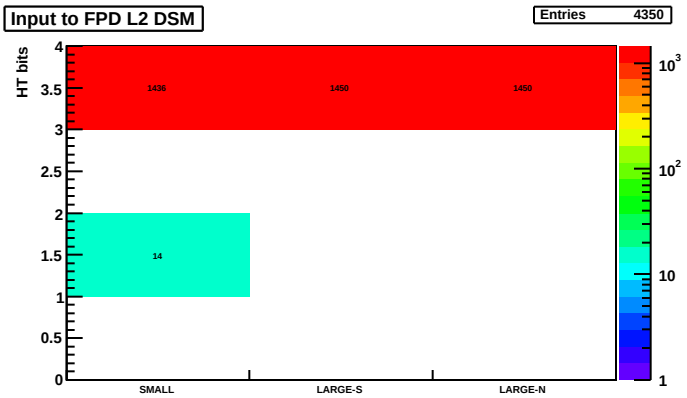

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

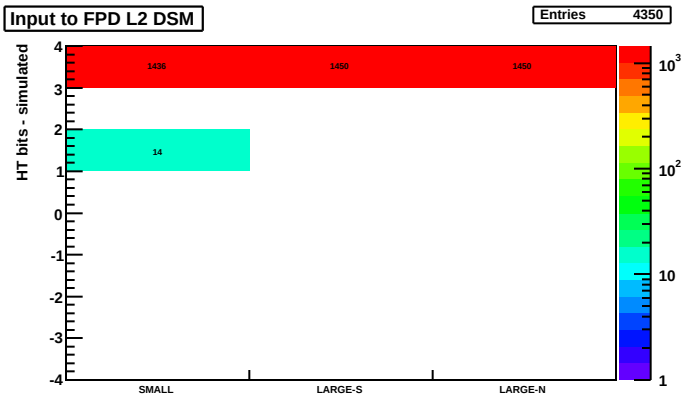

Input to FPD L2 DSM
Entries 11600
quadrant sum
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

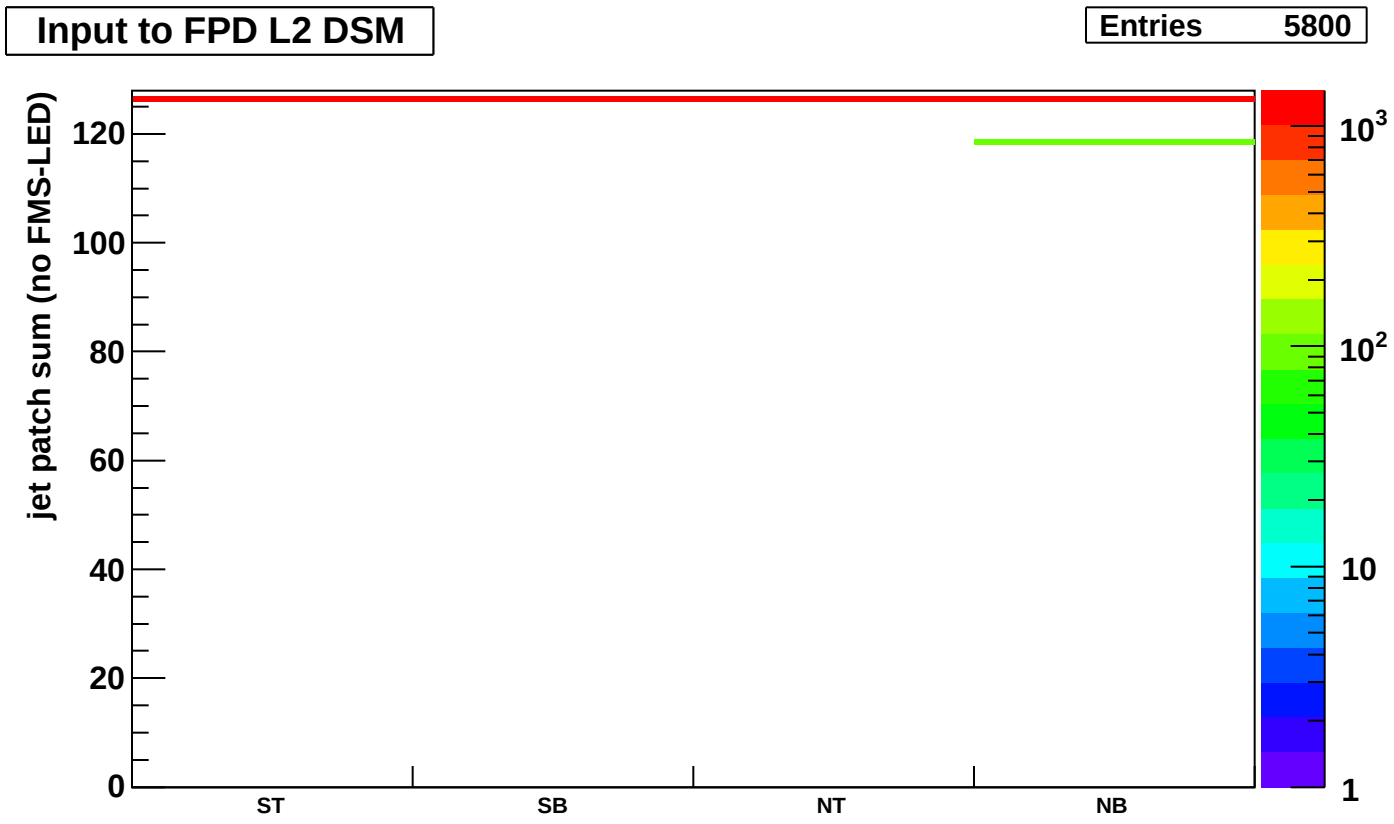

Input to FPD L2 DSM
Entries 11600
quadrant sum - simulated
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

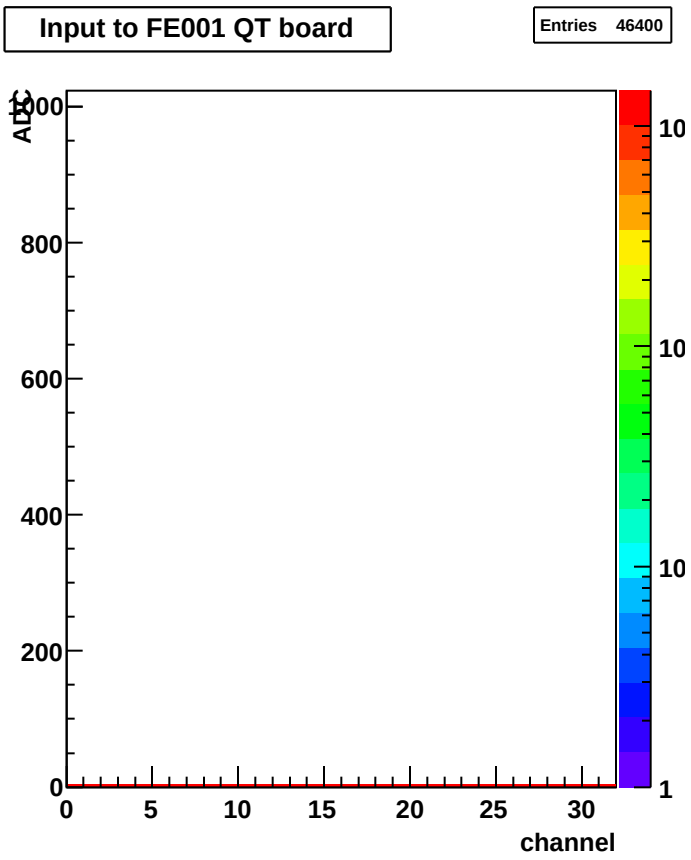

Input to FPD L2 DSM
Entries 4350
CL bits
1450 1450 1450
SMALL LARGE-S LARGE-N

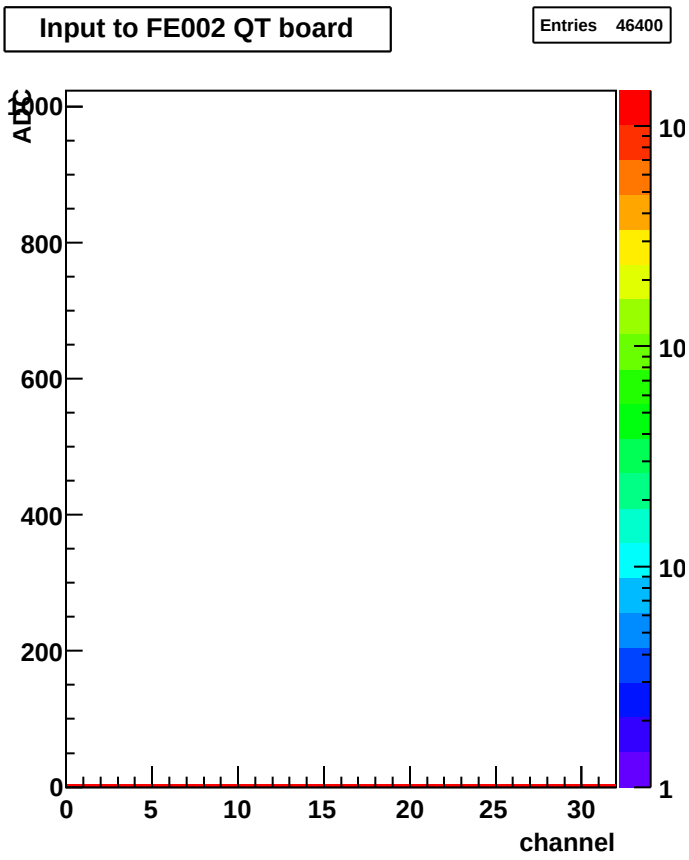

Input to FPD L2 DSM
Entries 4350
CL bits - simulated
1450 1450 1450
SMALL LARGE-S LARGE-N

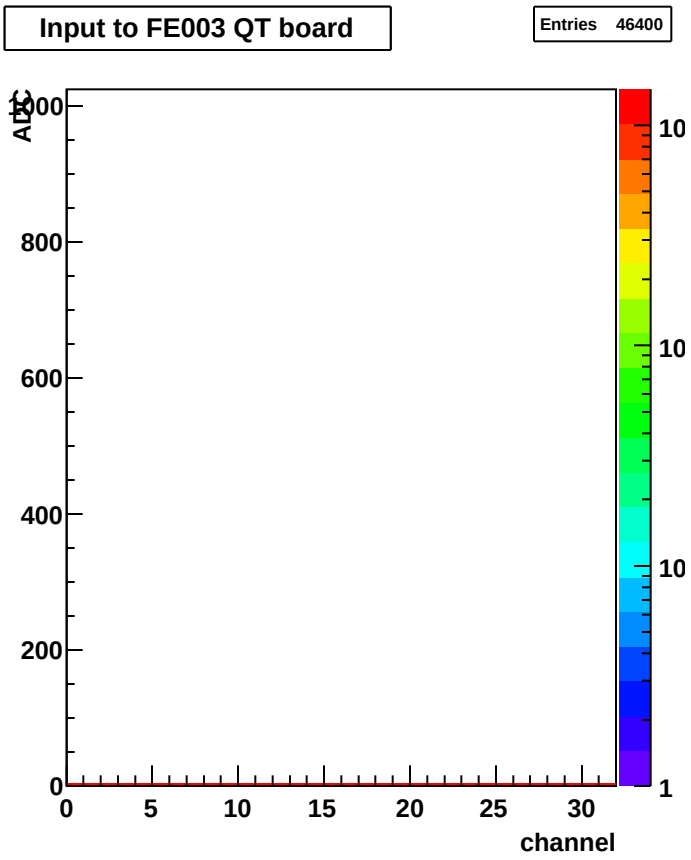

Input to FPD L2 DSM
Entries 4350
HT bits
1436 1450 1450
14
SMALL LARGE-S LARGE-N

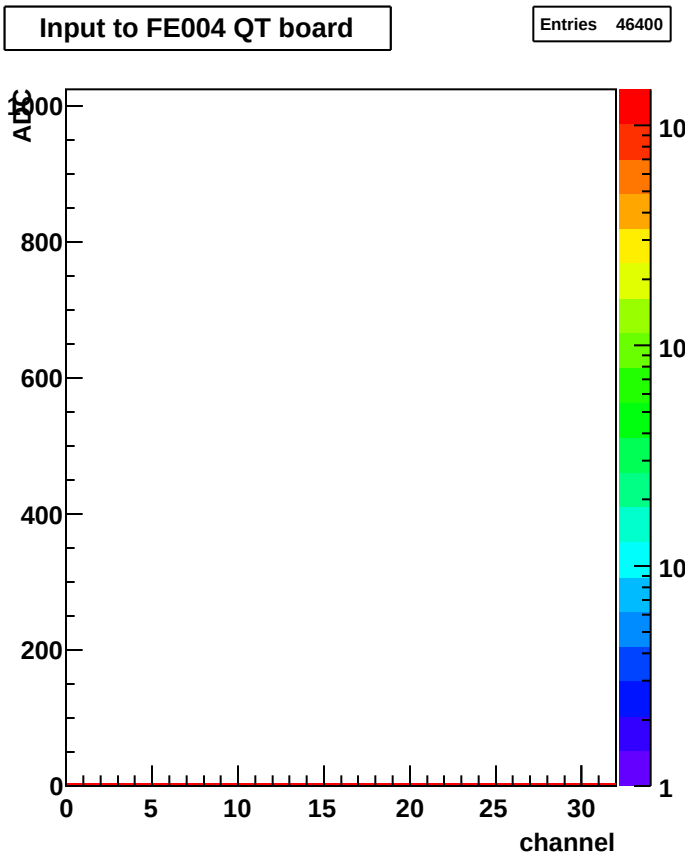

Input to FPD L2 DSM
Entries 4350
HT bits - simulated
1436 1450 1450
14
SMALL LARGE-S LARGE-N

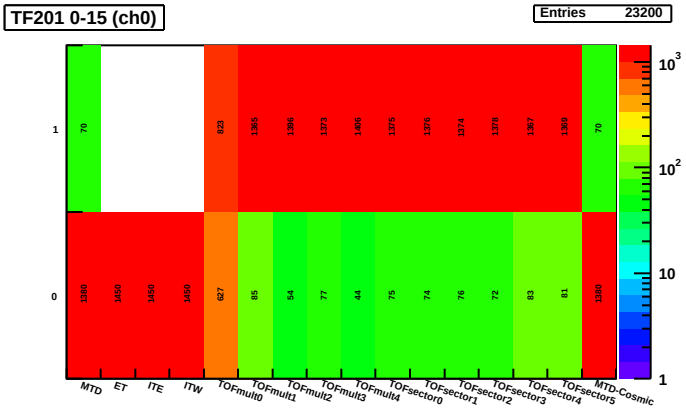

Input to FPD L2 DSM
Entries 5800
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3

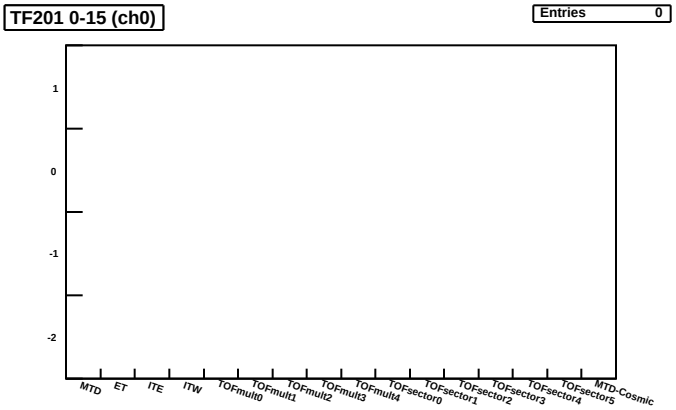

Input to FE001 QT board
Entries 46400
ADC
channel

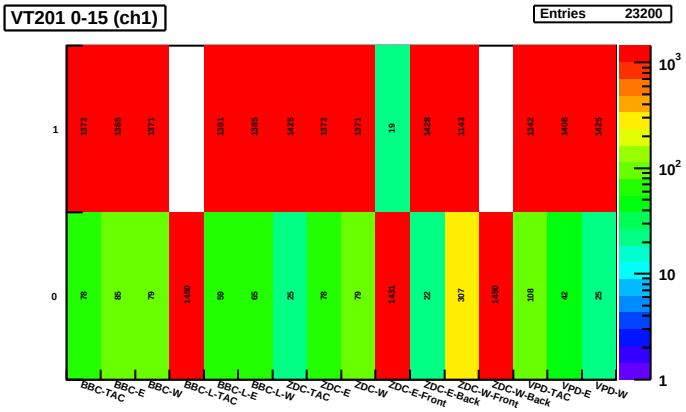

Input to FE002 QT board
Entries 46400
ADC
channel

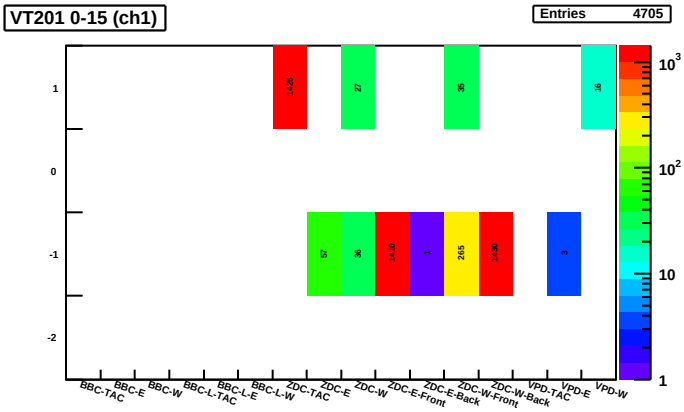

Input to FE003 QT board
Entries 46400
ADC
channel

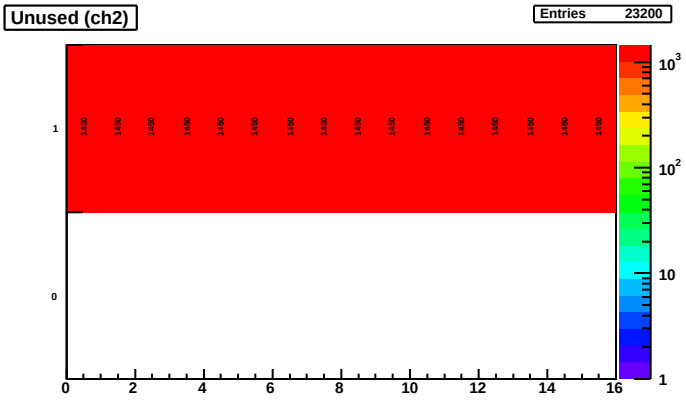

Input to FE004 QT board
Entries 46400
ADC
channel

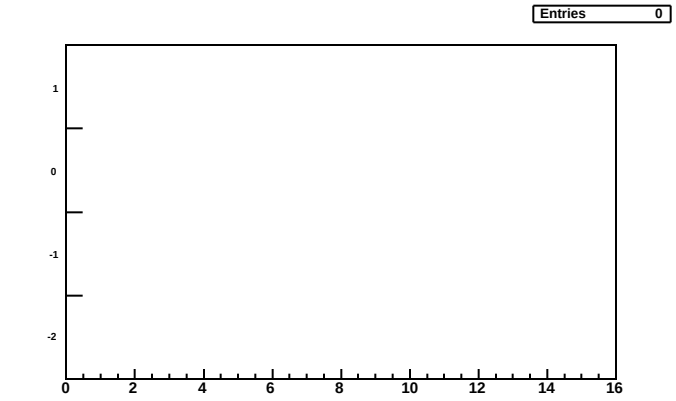

TF201 0-15 (ch0)
Entries 23200
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

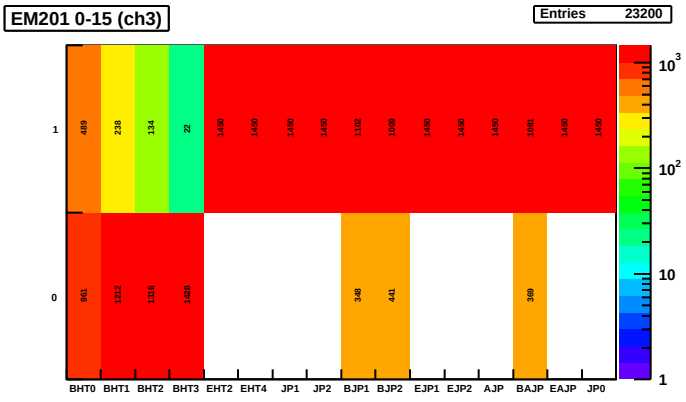

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

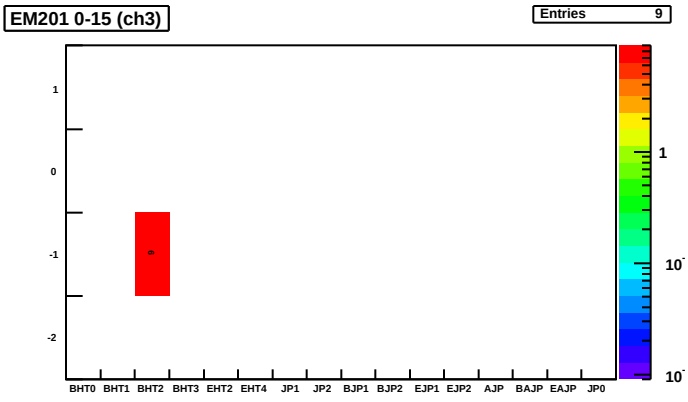

VT201 0-15 (ch1)
Entries 23200
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 4705
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

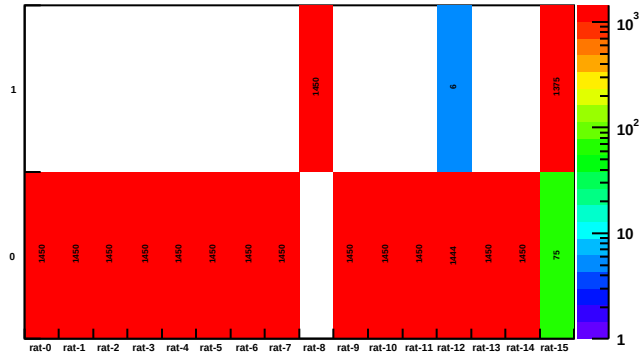

Unused (ch2)
Entries 23200

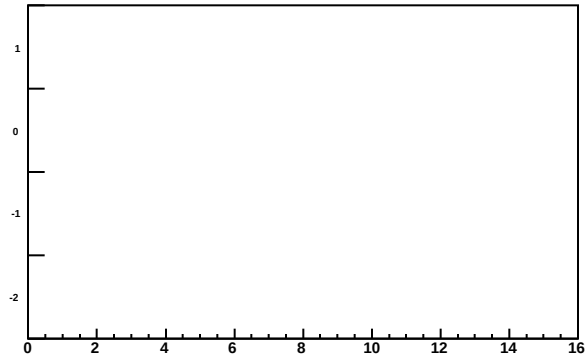

Entries 0


EM201 0-15 (ch3)
Entries 23200
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

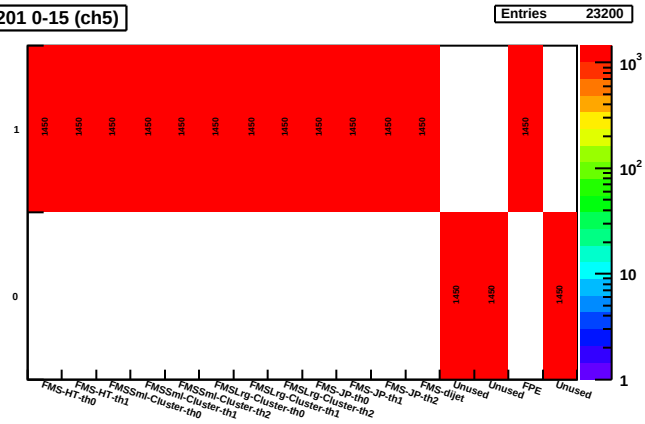

EM201 0-15 (ch3)
Entries 9
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

RAT board (ch4)

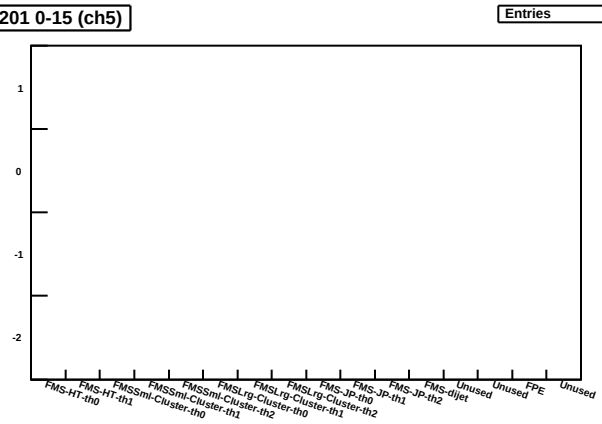




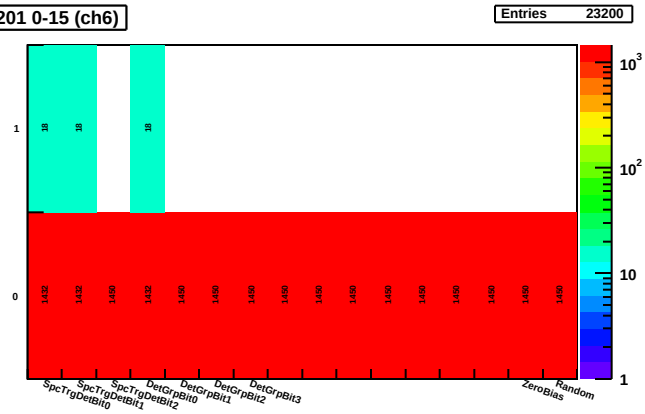
FP201 0-15 (ch5)

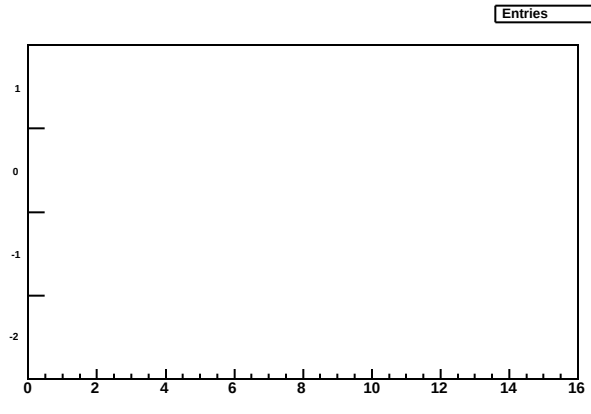
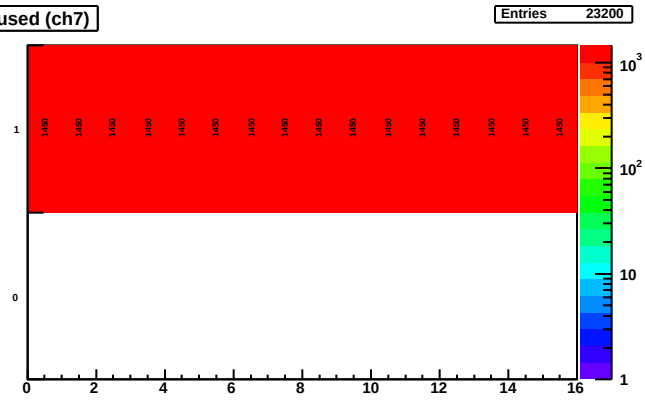
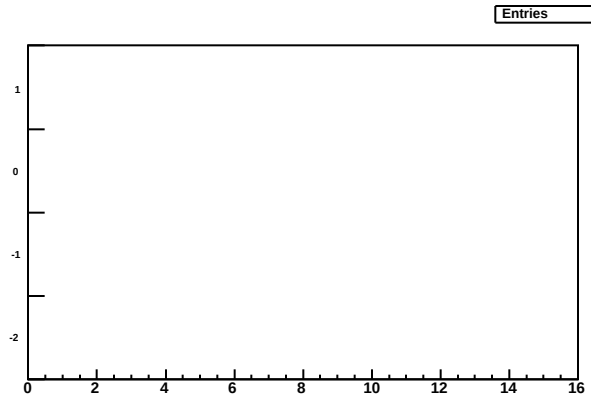


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)