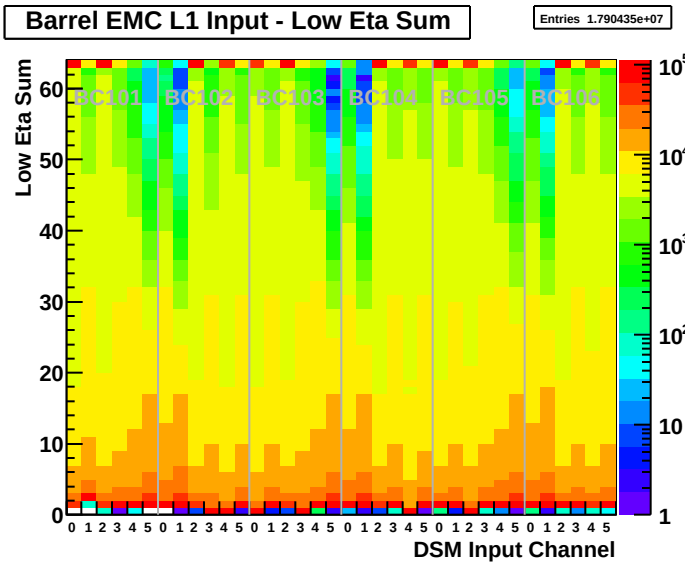
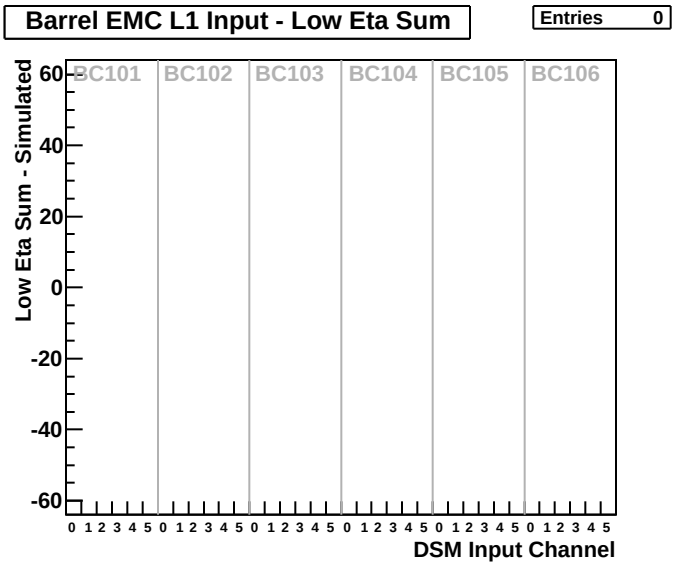
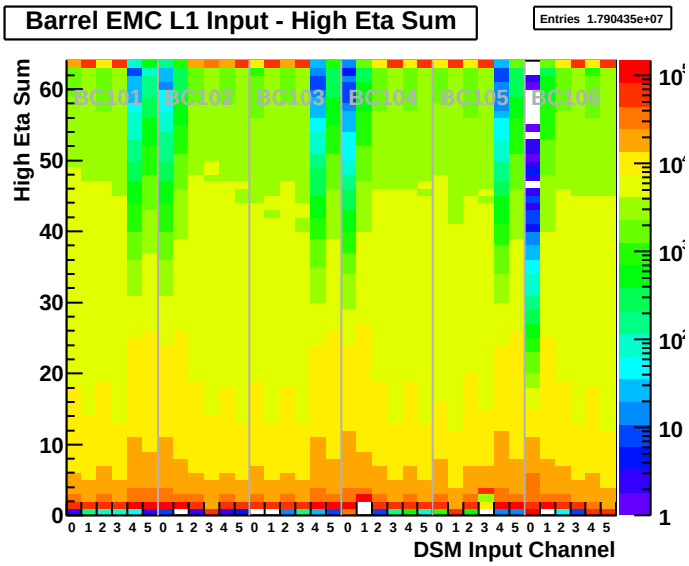
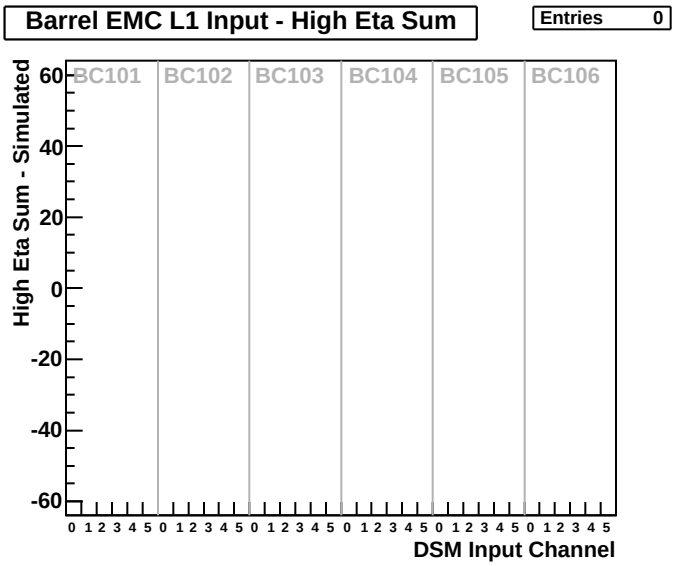
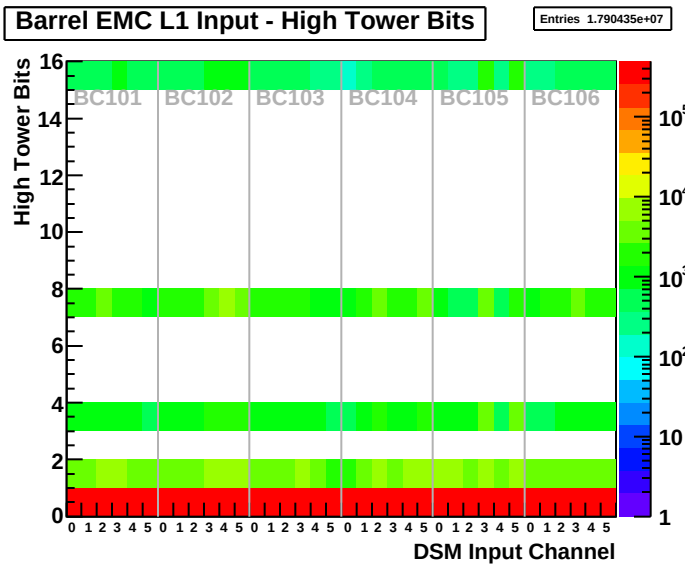
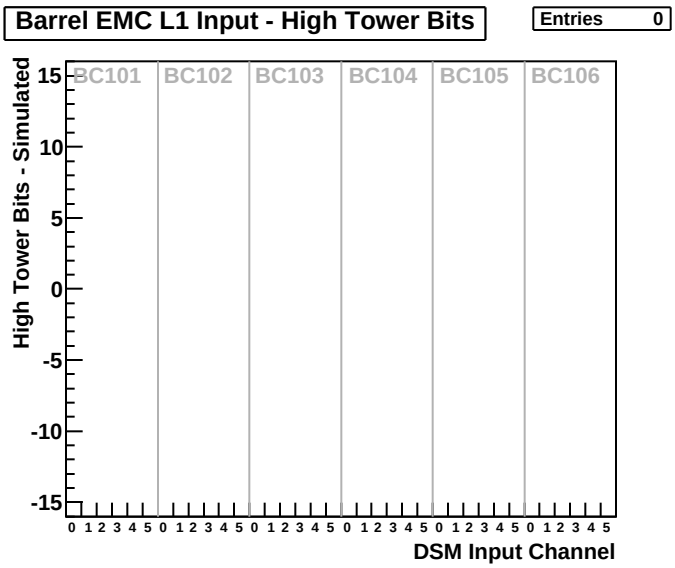

Barrel EMC L1 Input - Low Eta Sum
Entries 1.790435e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

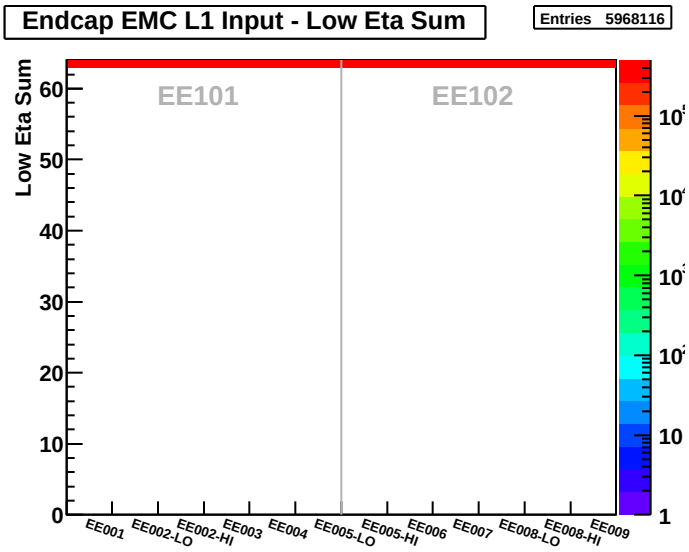

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

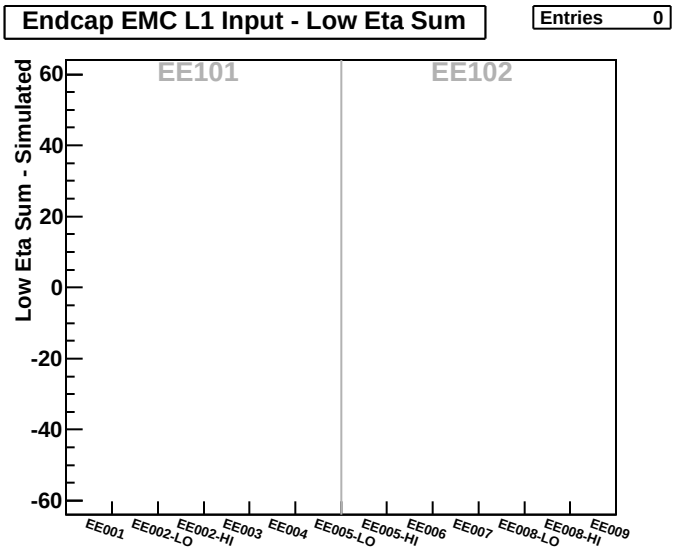

Barrel EMC L1 Input - High Eta Sum
Entries 1.790435e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

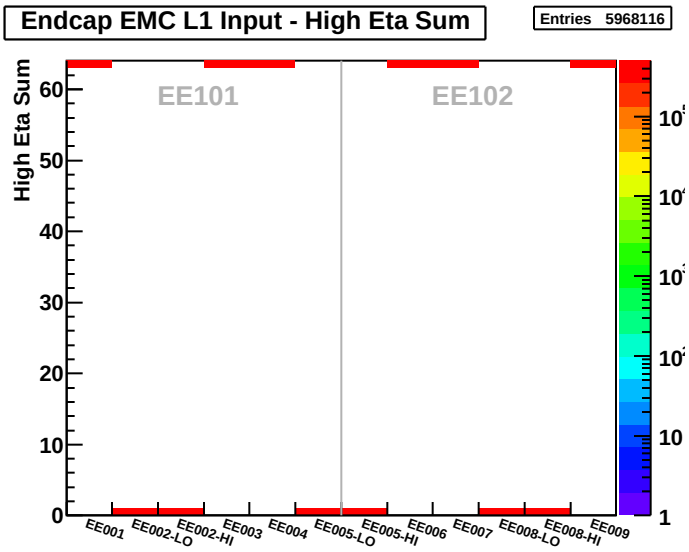

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

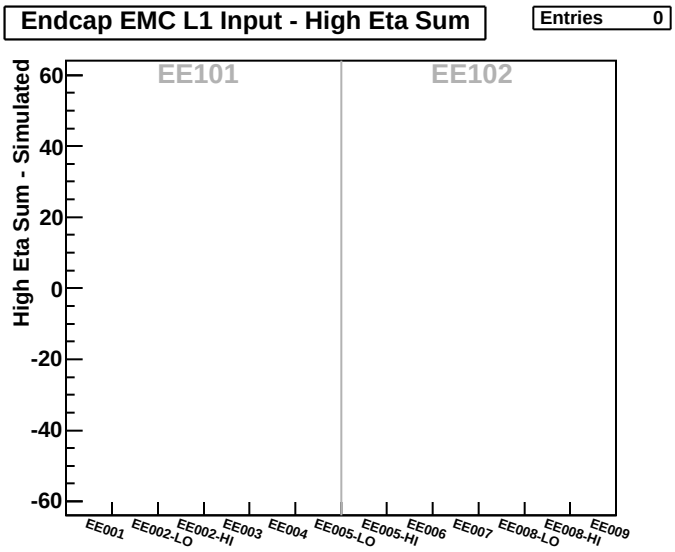

Barrel EMC L1 Input - High Tower Bits
Entries 1.790435e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

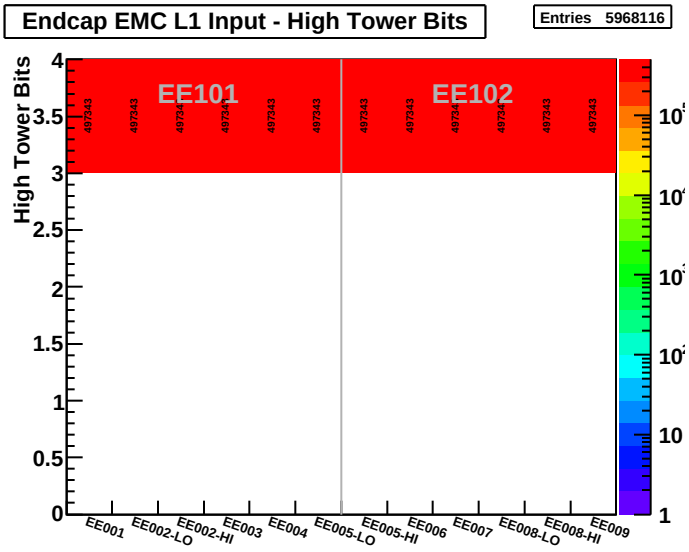

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

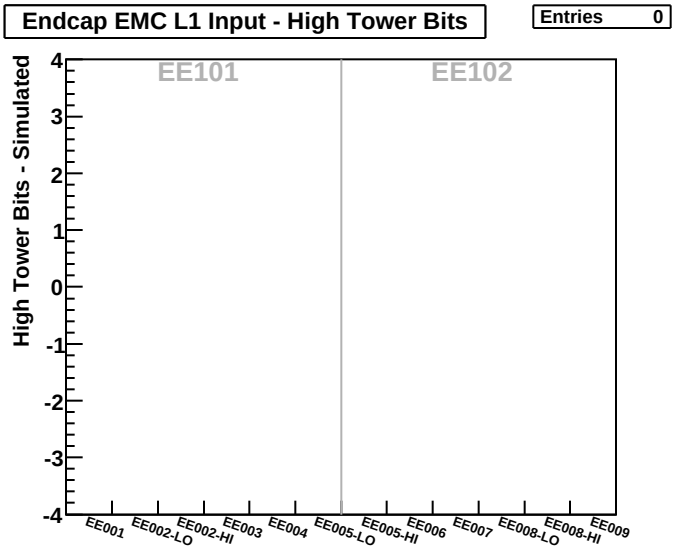

Endcap EMC L1 Input - Low Eta Sum
Entries 5968116
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

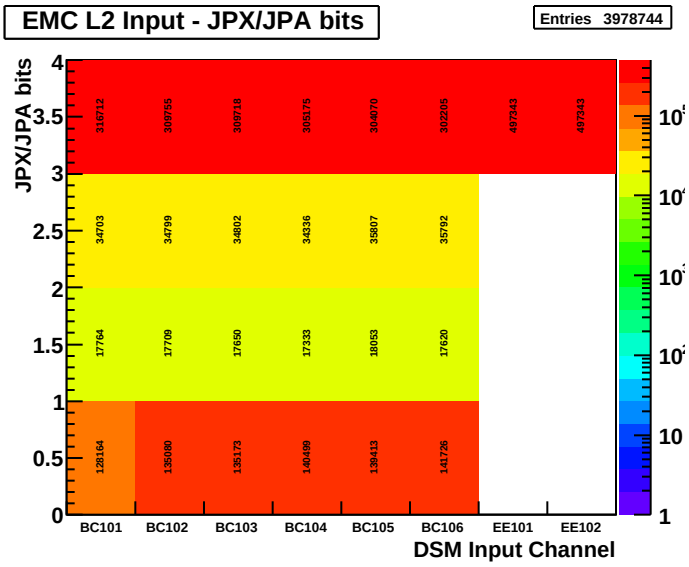

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

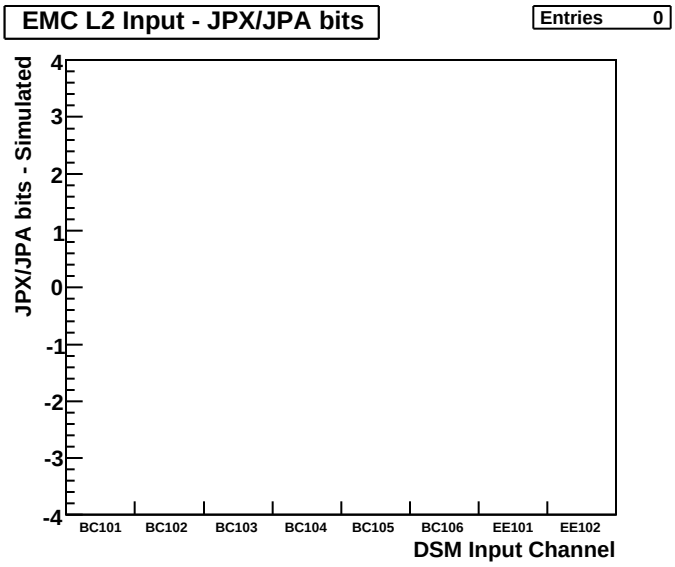

Endcap EMC L1 Input - High Eta Sum
Entries 5968116
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

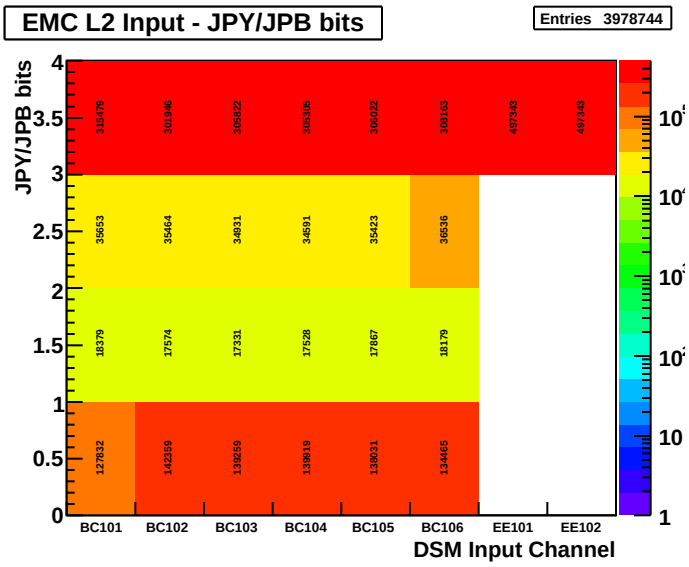

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

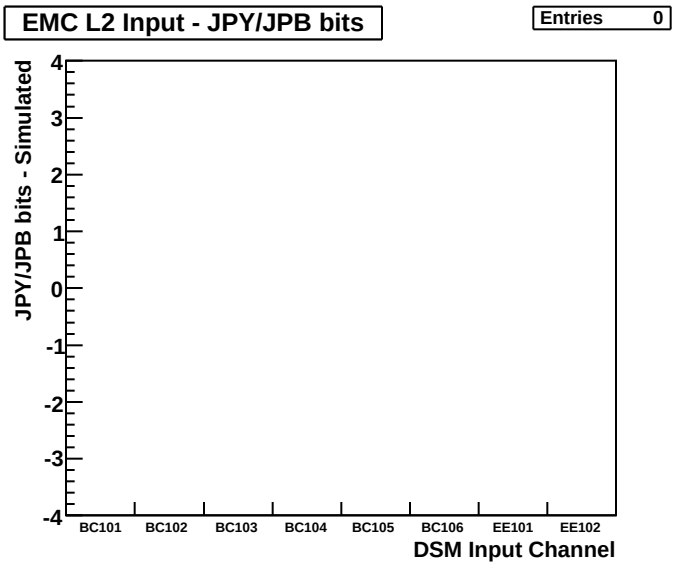

Endcap EMC L1 Input - High Tower Bits
Entries 5968116
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

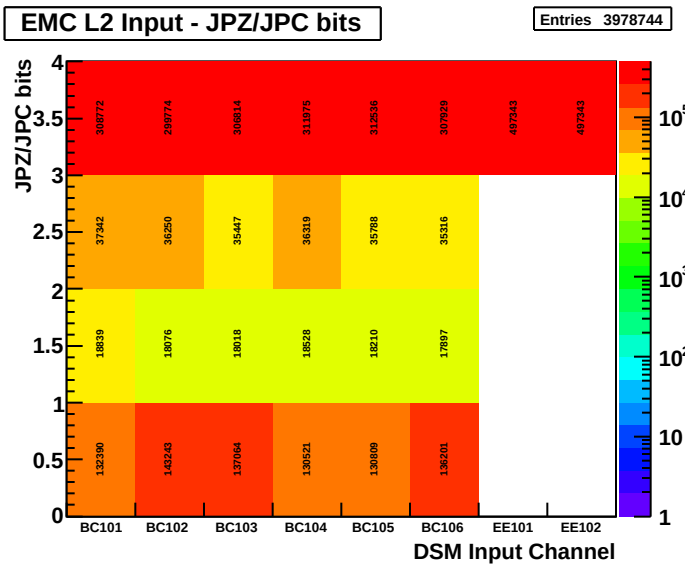

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

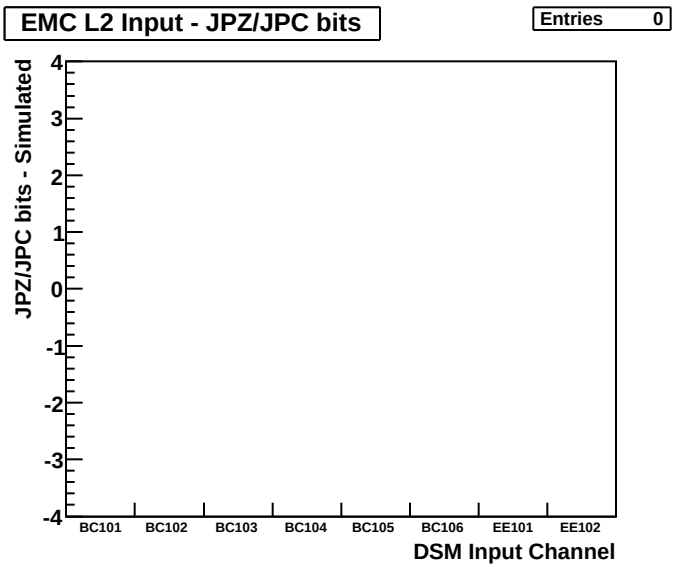

EMC L2 Input - JPX/JPA bits
Entries 3978744
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
316712 309755 309718 305175 304070 302205 497343 497343
34703 34799 34802 34336 35807 35792
17764 17709 17650 17333 18053 17620
128164 135080 135173 140499 139413 141726

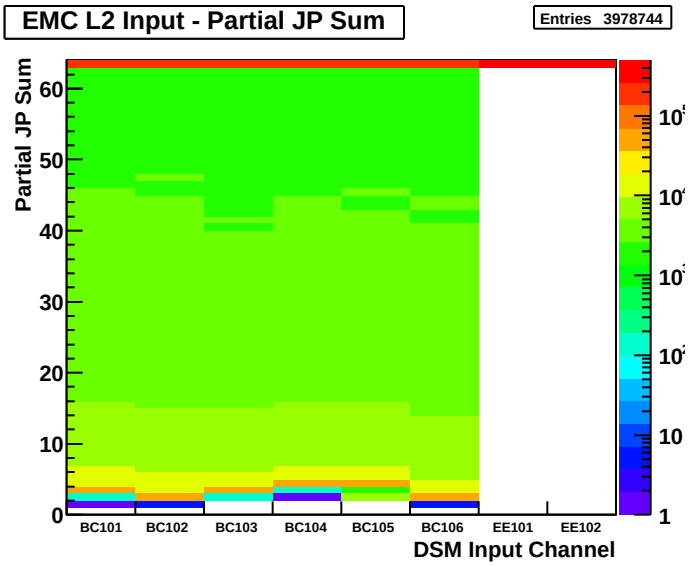

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

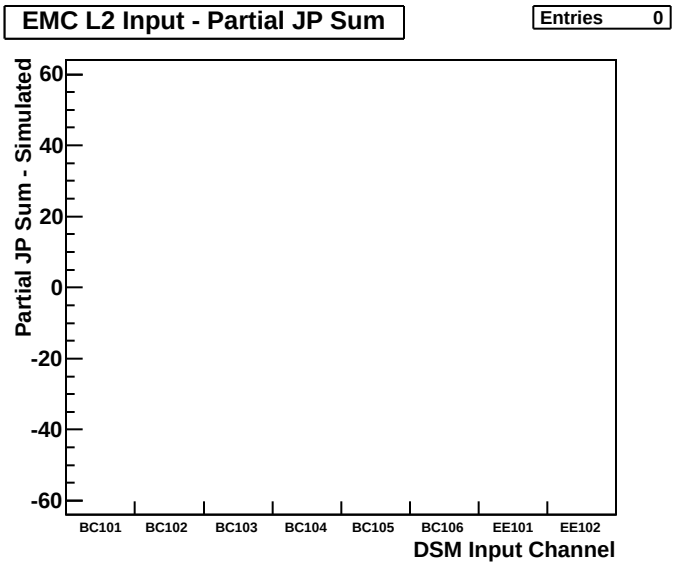

EMC L2 Input - JPY/JPB bits
Entries 3978744
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
315479 301946 305822 305305 306022 308163 497343 497343
35653 35464 34931 34591 35423 36536
18379 17574 17331 17528 17867 18179
127832 142359 138259 139919 138031 134465

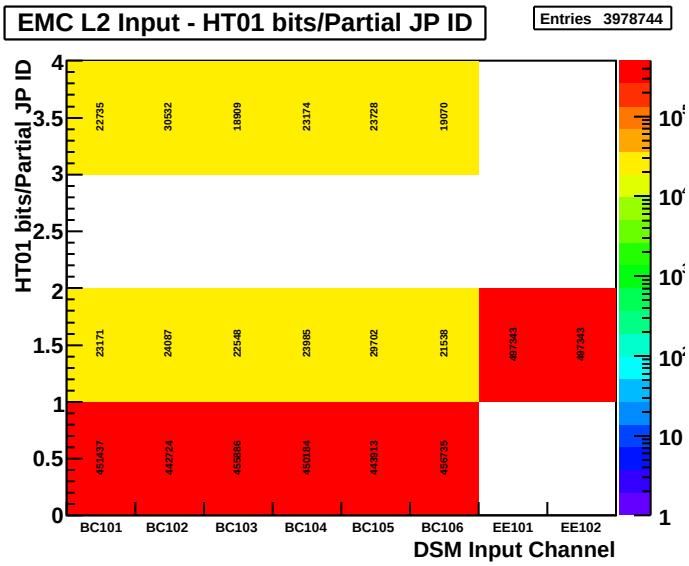

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

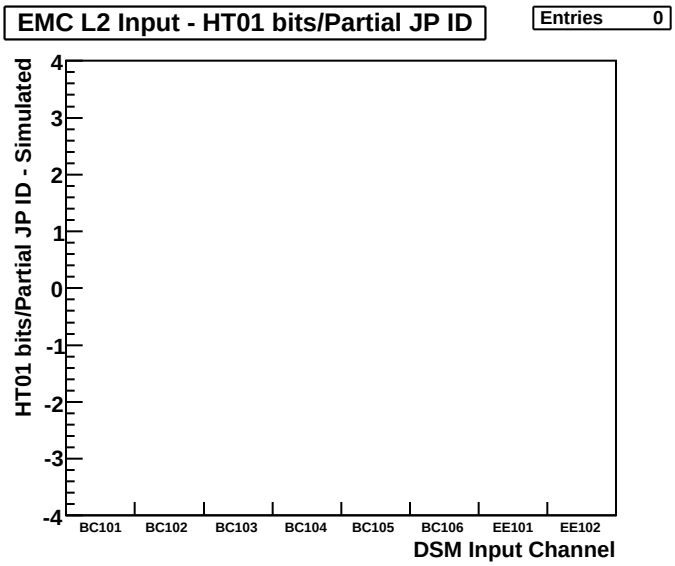

EMC L2 Input - JPZ/JPC bits
Entries 3978744
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
308772 299774 308814 311975 312536 307929 497343 497343
37342 36250 35447 36319 35788 35316
18839 18076 18018 18528 18210 17897
132390 143243 137064 130521 130809 136201

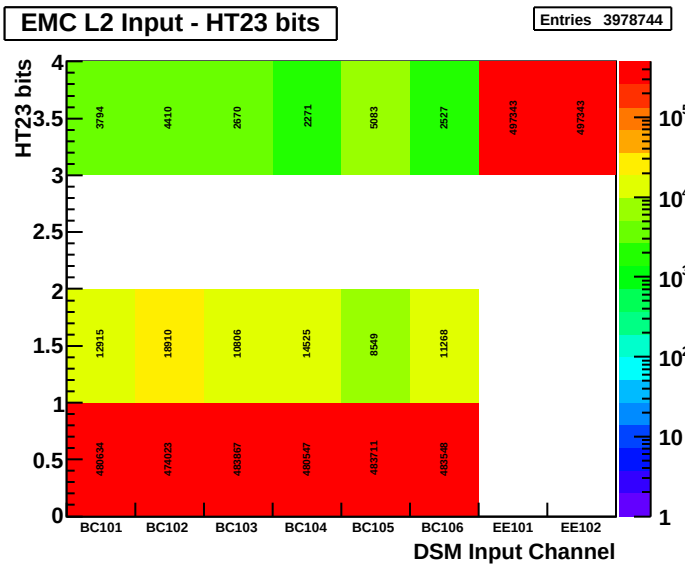

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

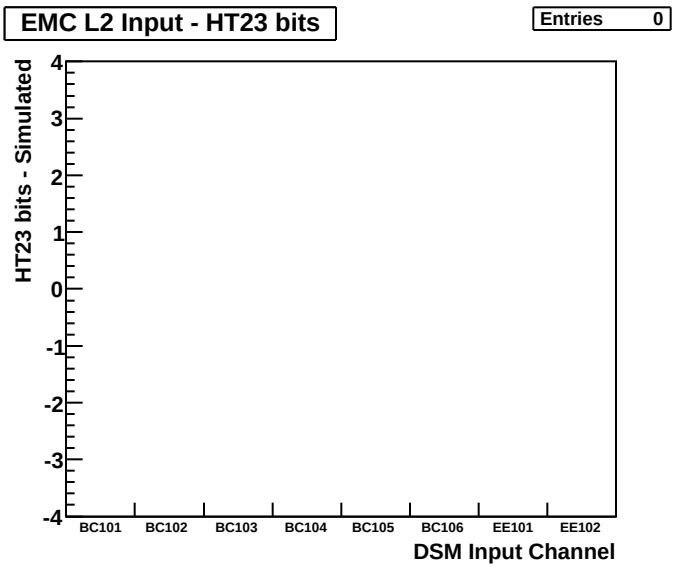

EMC L2 Input - Partial JP Sum
Entries 3978744
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

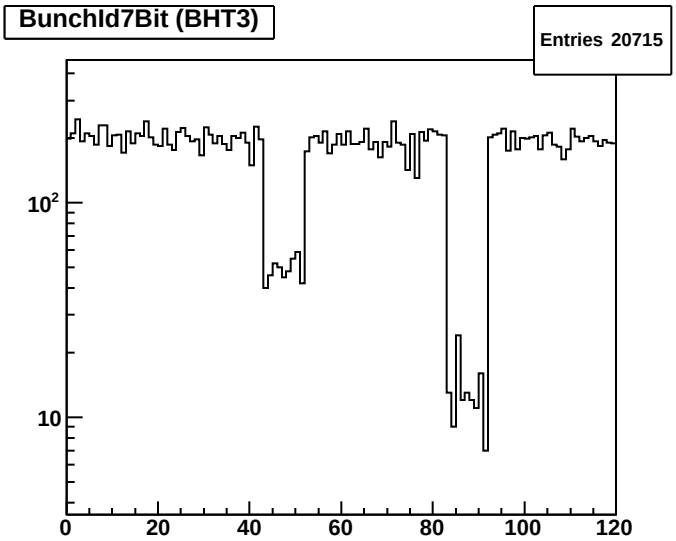

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

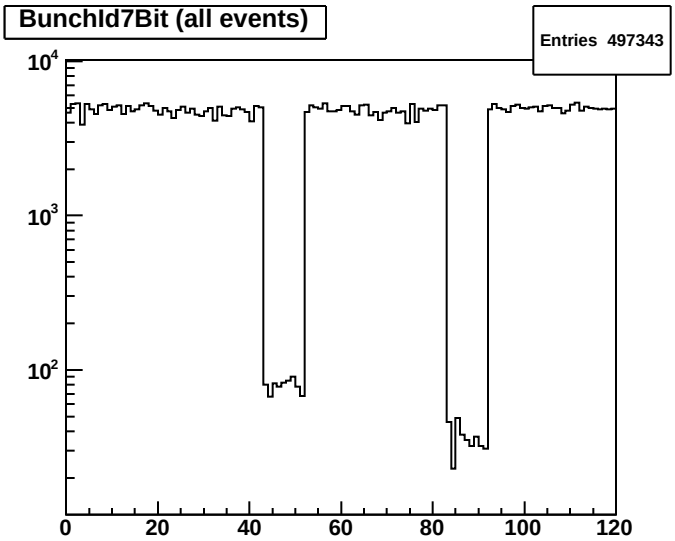

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3978744
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

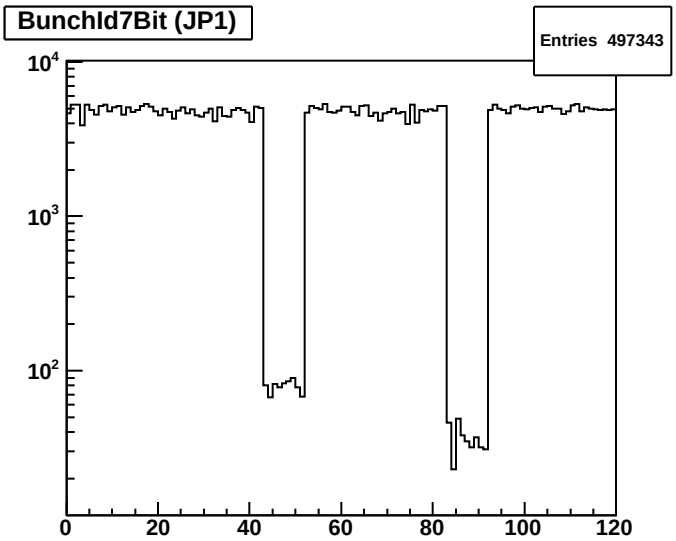

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

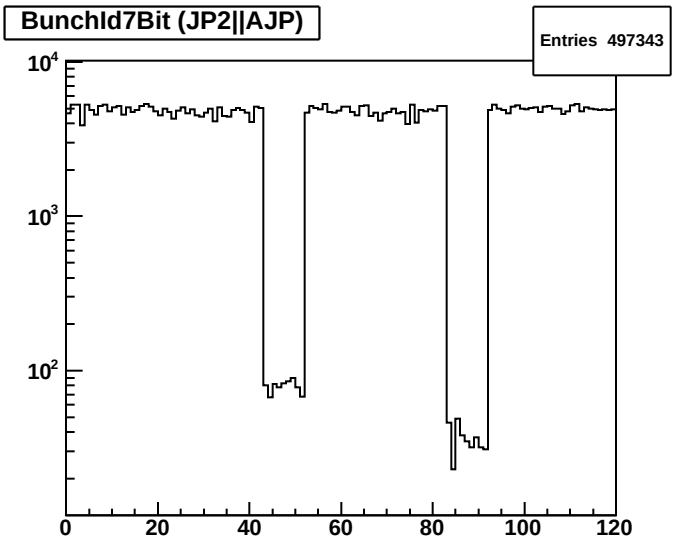

EMC L2 Input - HT23 bits
Entries 3978744
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

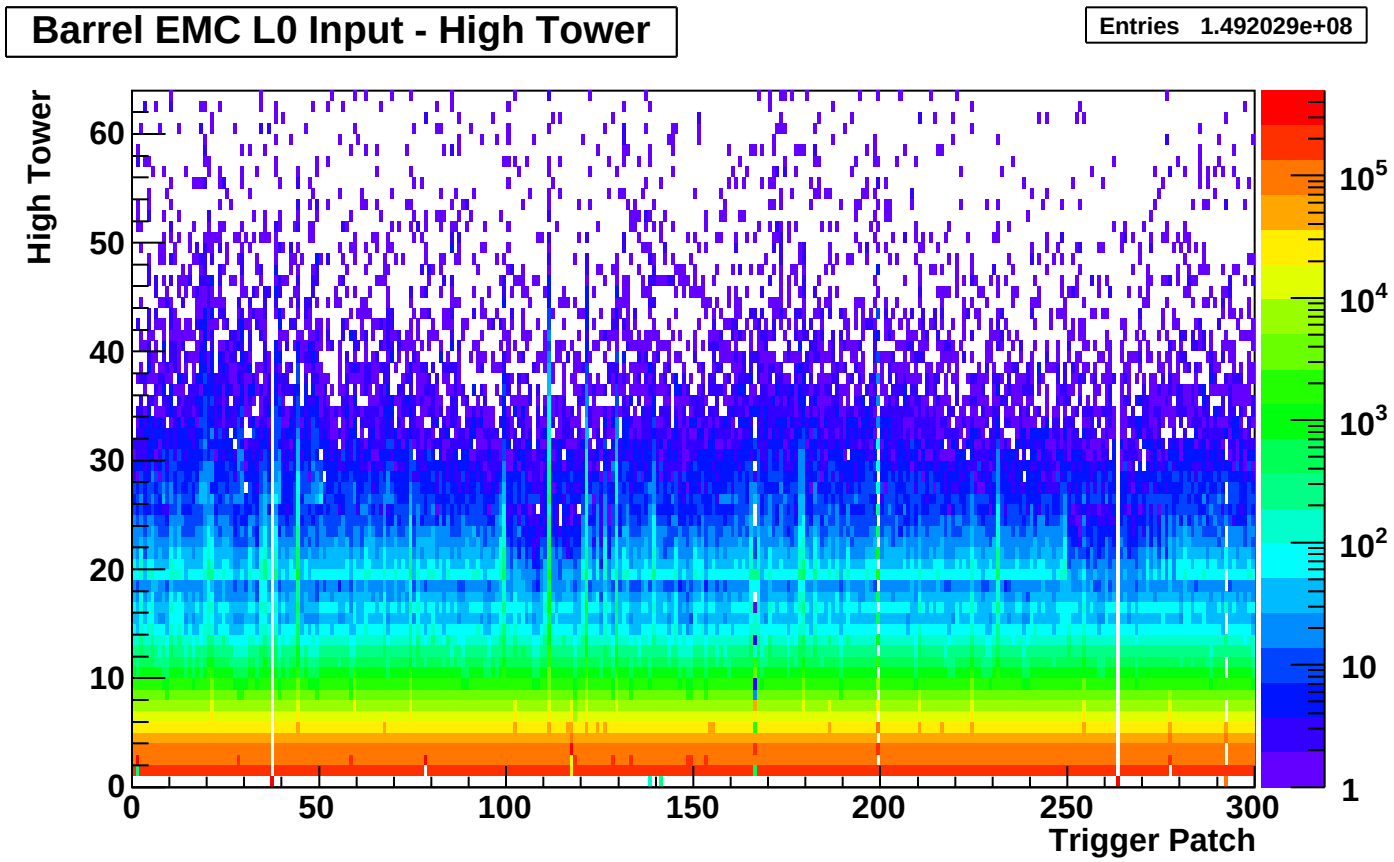

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

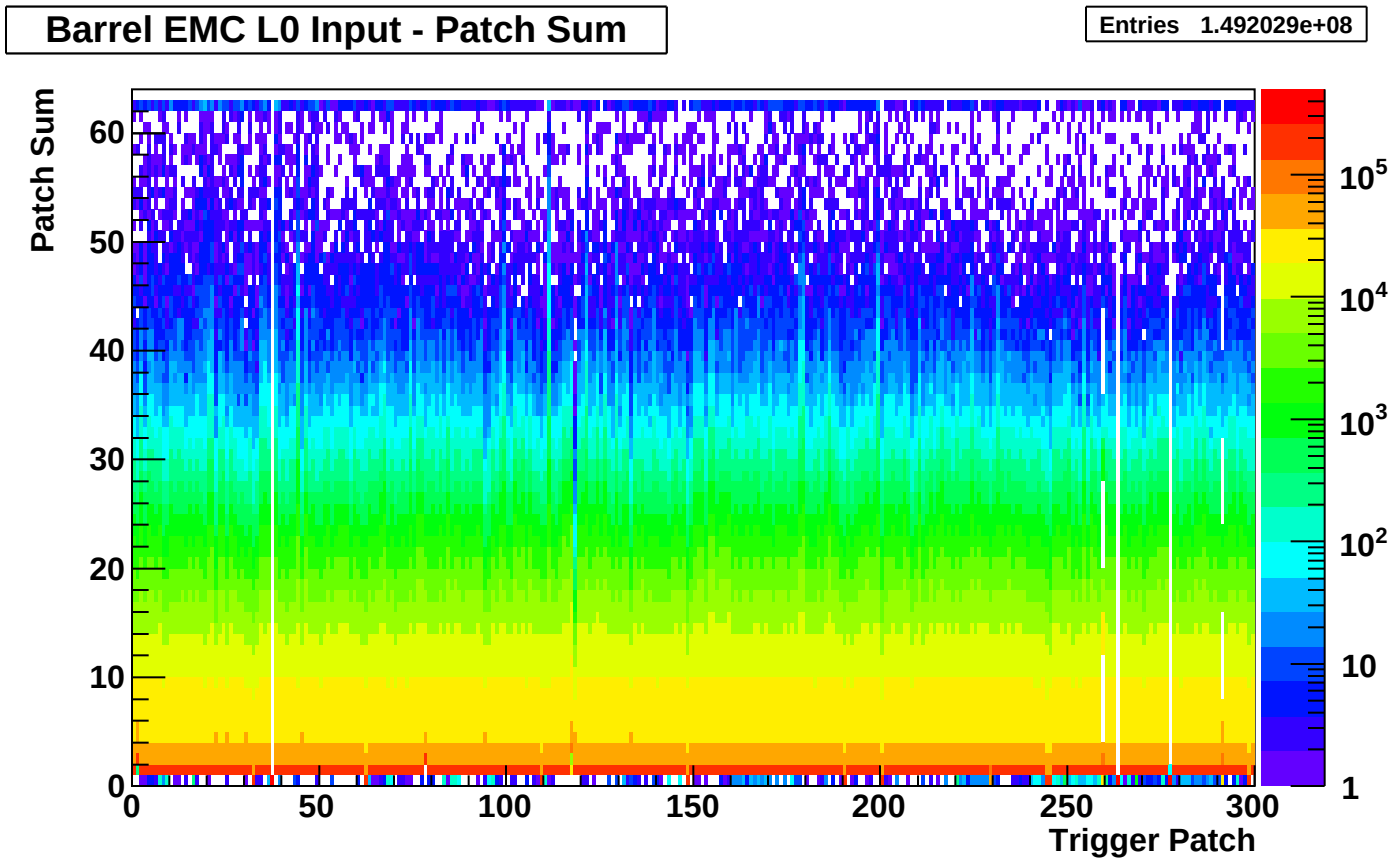

BunchId7Bit (BHT3)
Entries 20715
10^2
10
0
20
40
60
80
100
120

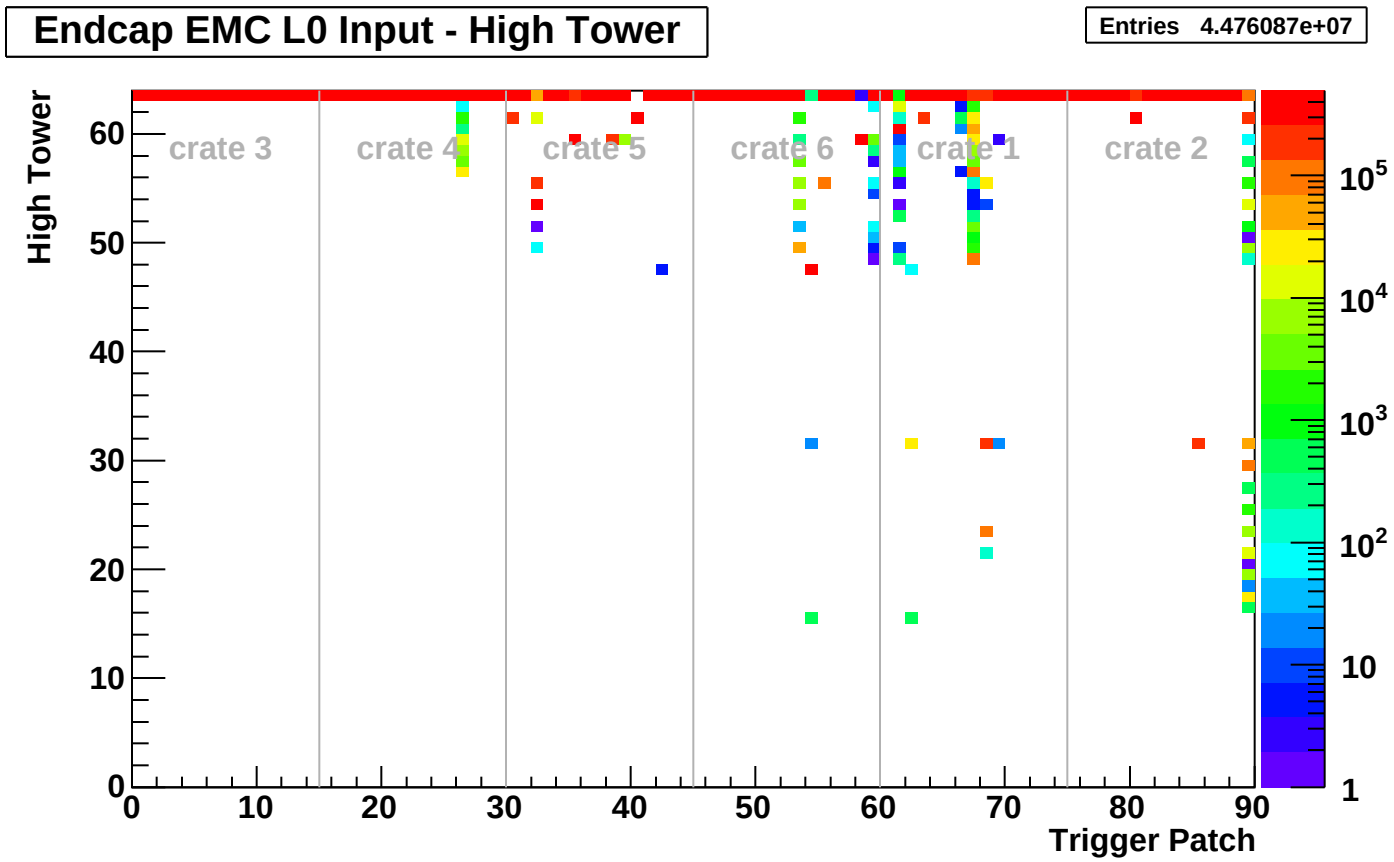

BunchId7Bit (all events)
Entries 497343
10^4
10^3
10^2
0
20
40
60
80
100
120

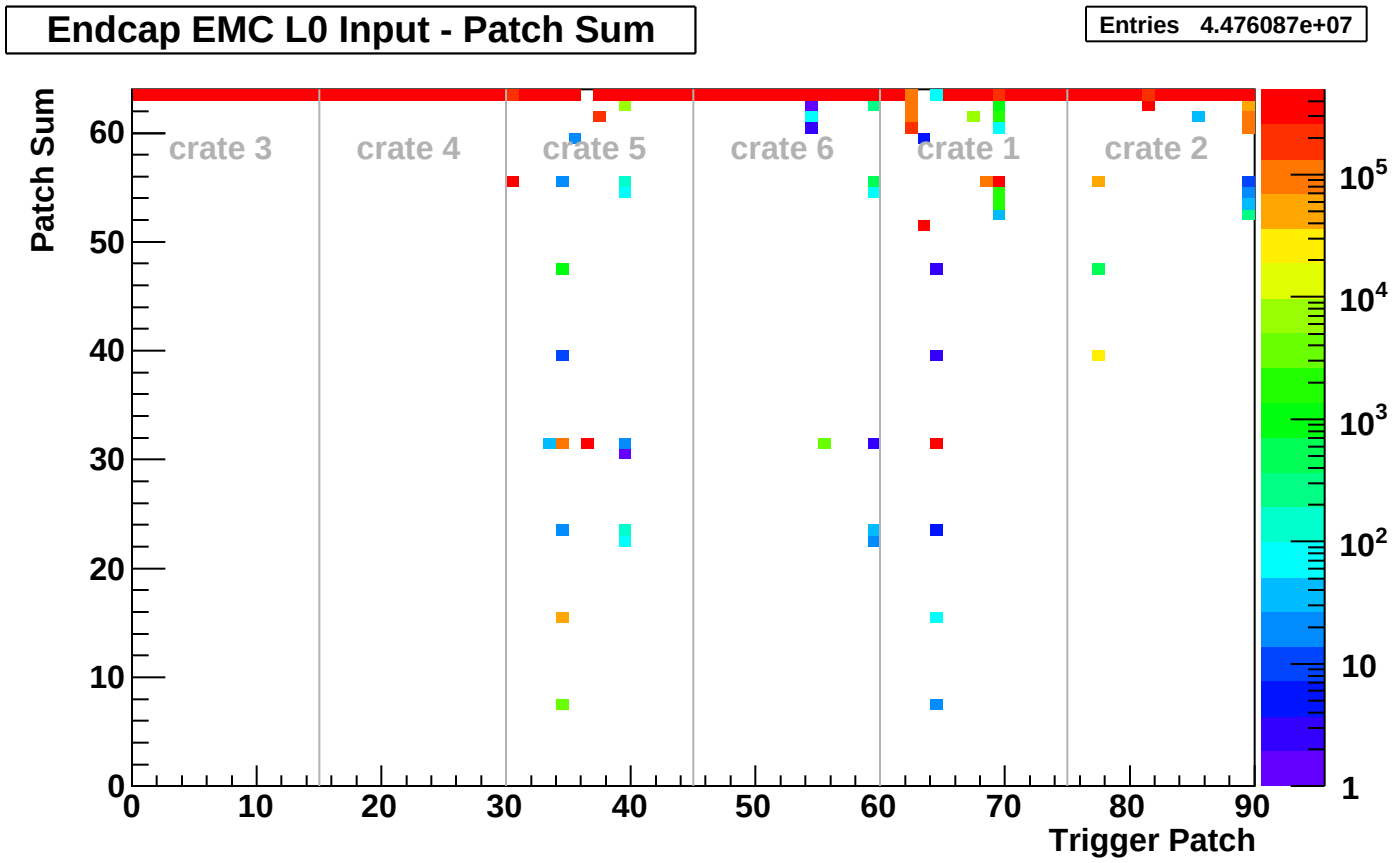

BunchId7Bit (JP1)
Entries 497343
10^4
10^3
10^2
0
20
40
60
80
100
120

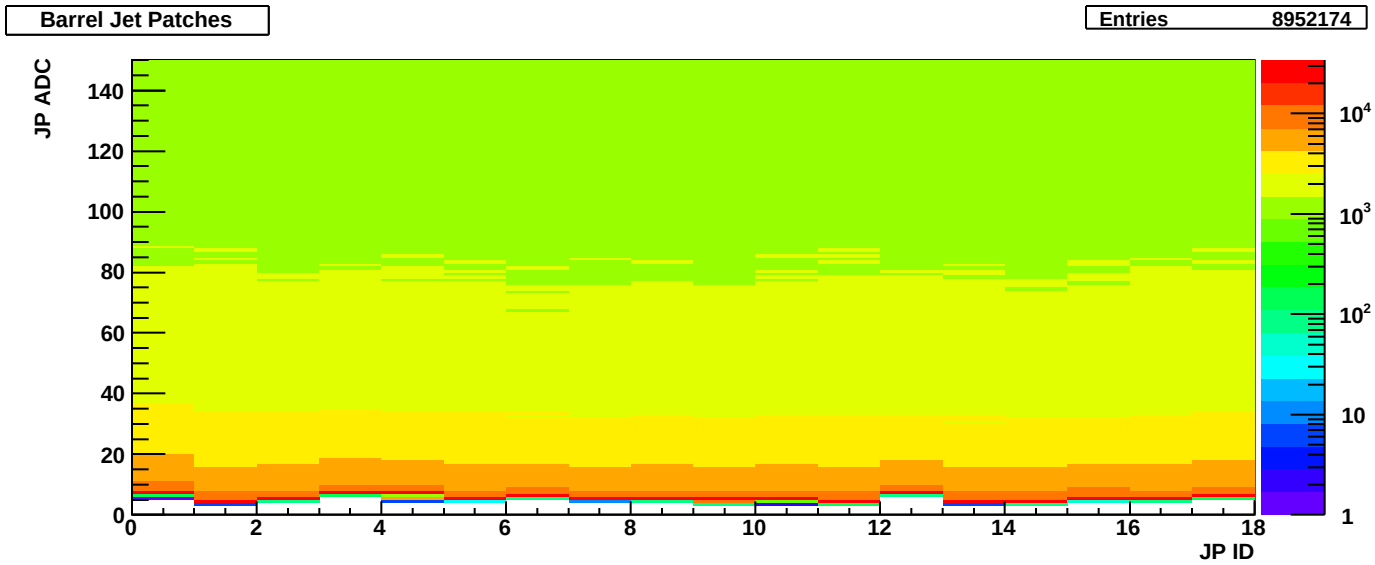

BunchId7Bit (JP2||AJP)
Entries 497343
10^4
10^3
10^2
0
20
40
60
80
100
120

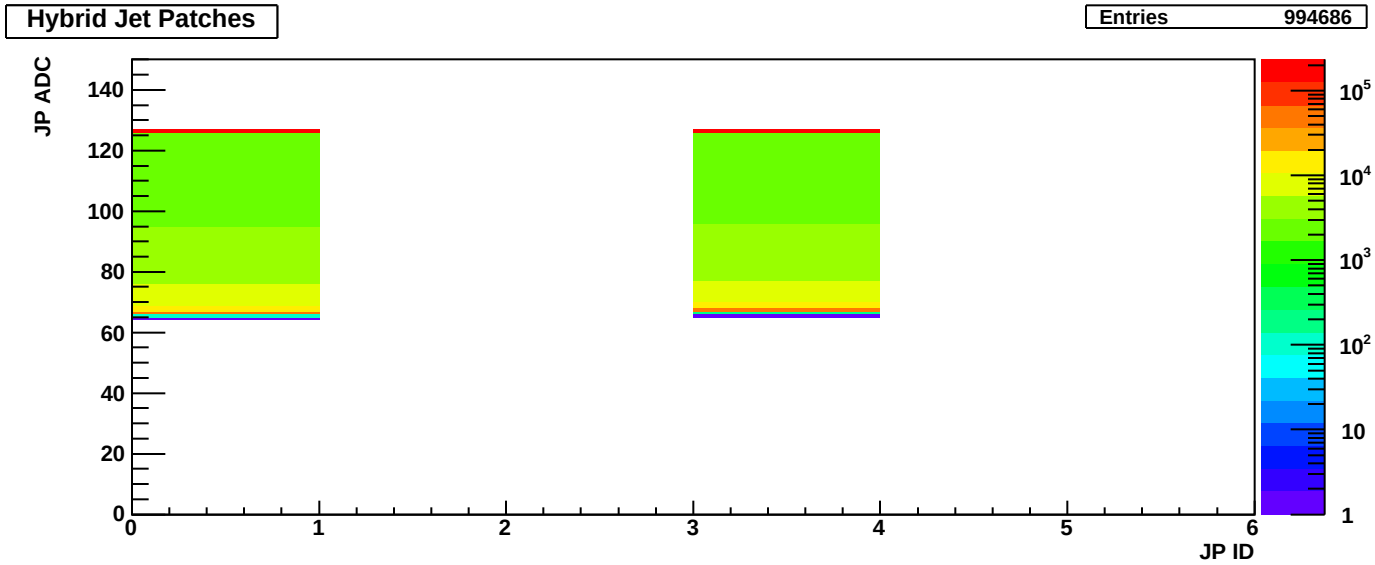

Barrel EMC L0 Input - High Tower
Entries 1.492029e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

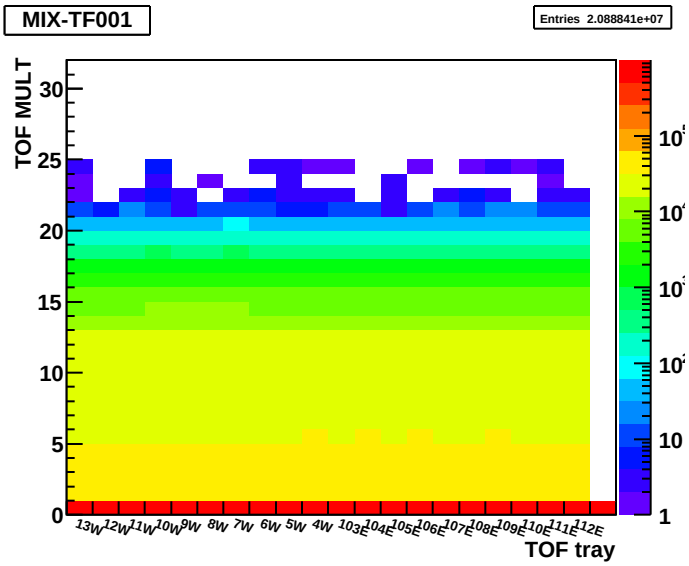

Barrel EMC L0 Input - Patch Sum
Entries 1.492029e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

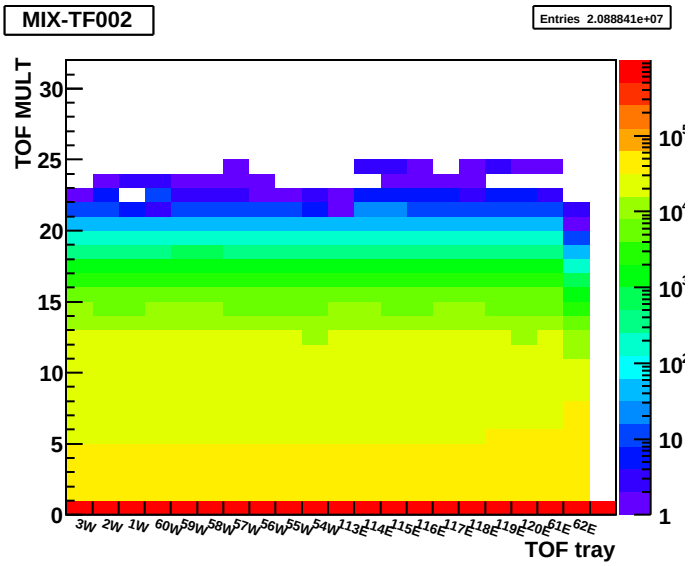

Endcap EMC L0 Input - High Tower
Entries 4.476087e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

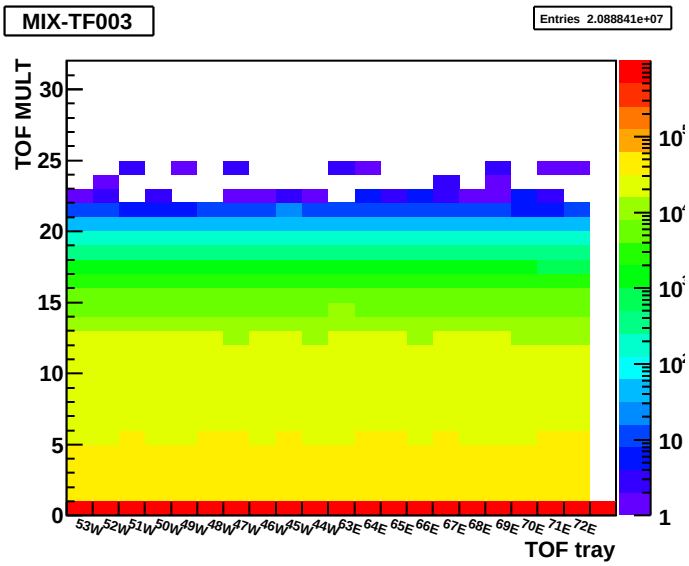

Endcap EMC L0 Input - Patch Sum
Entries 4.476087e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

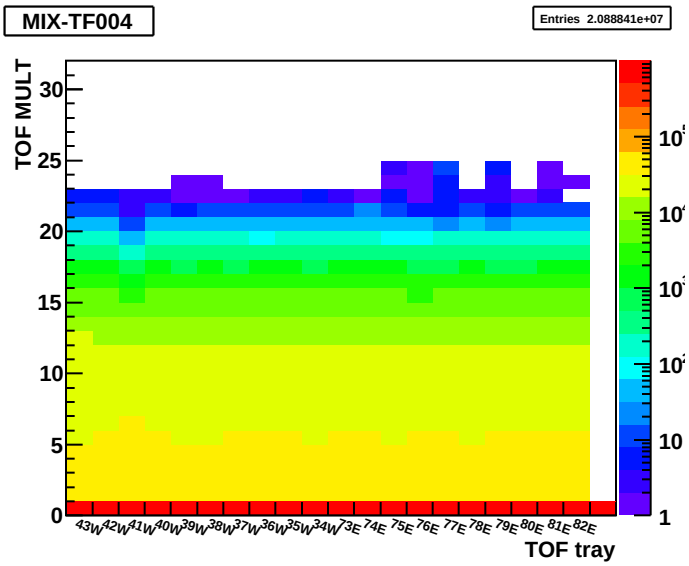

Barrel Jet Patches
Entries 8952174
JP ADC
JP ID

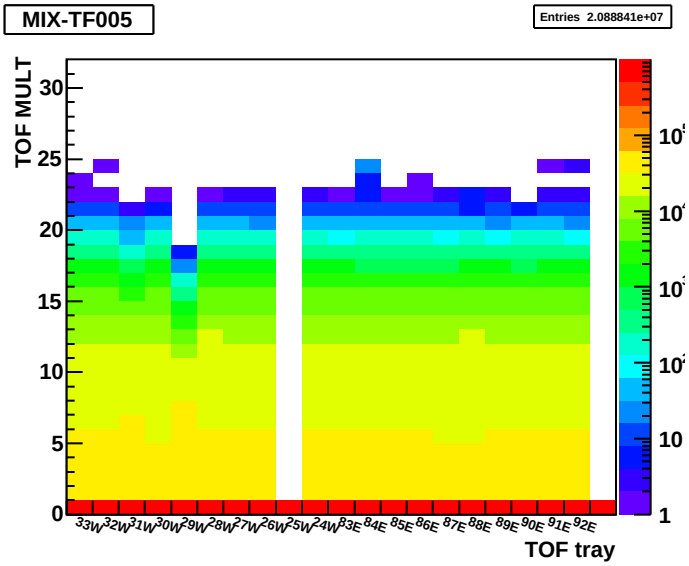

Hybrid Jet Patches
Entries 994686
JP ADC
JP ID

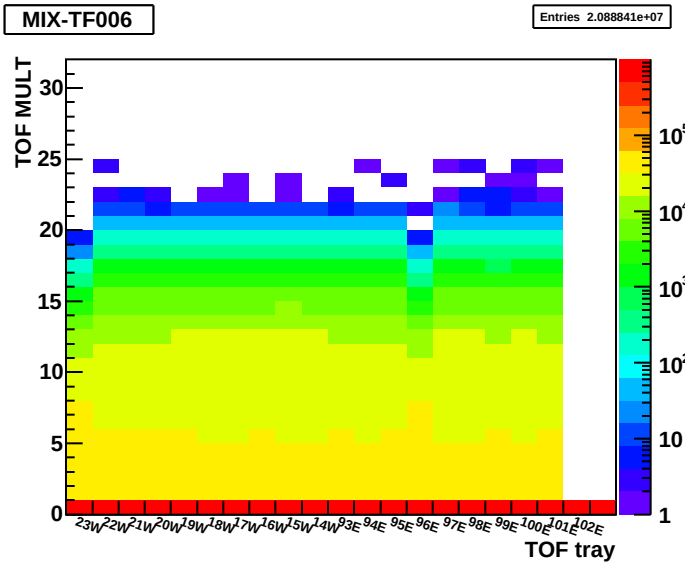

MIX-TF001
Entries 2.088841e+07
TOF MULT
TOF tray

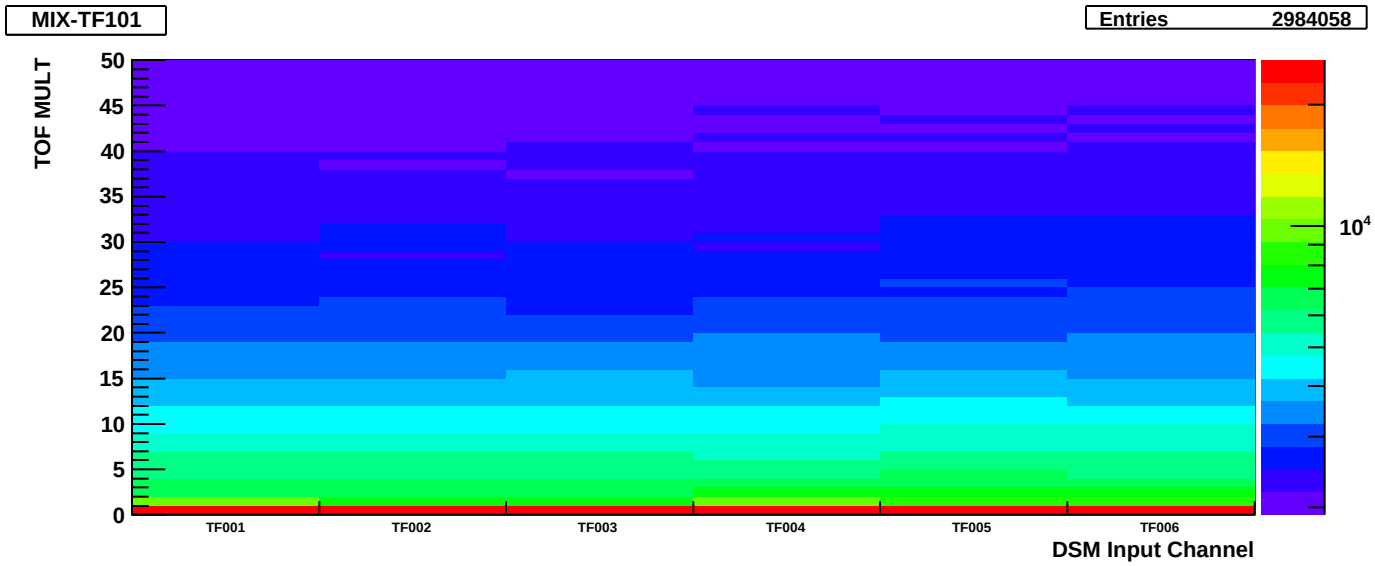

MIX-TF002
Entries 2.088841e+07
TOF MULT
TOF tray

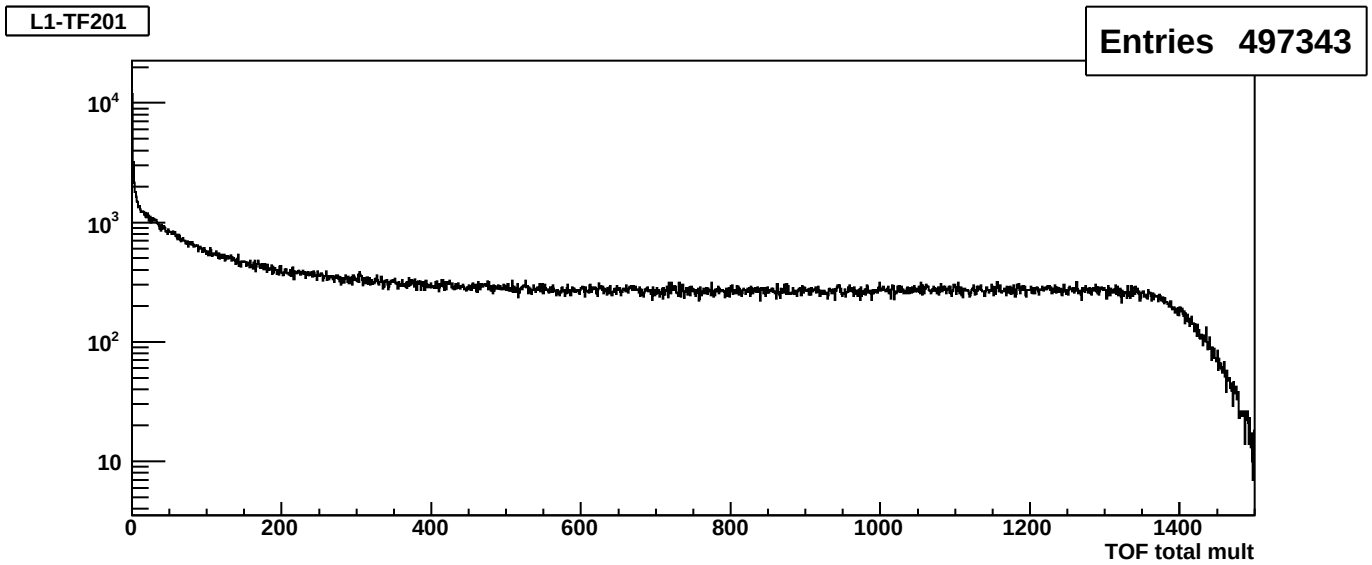

MIX-TF003
Entries 2.088841e+07
TOF MULT
TOF tray

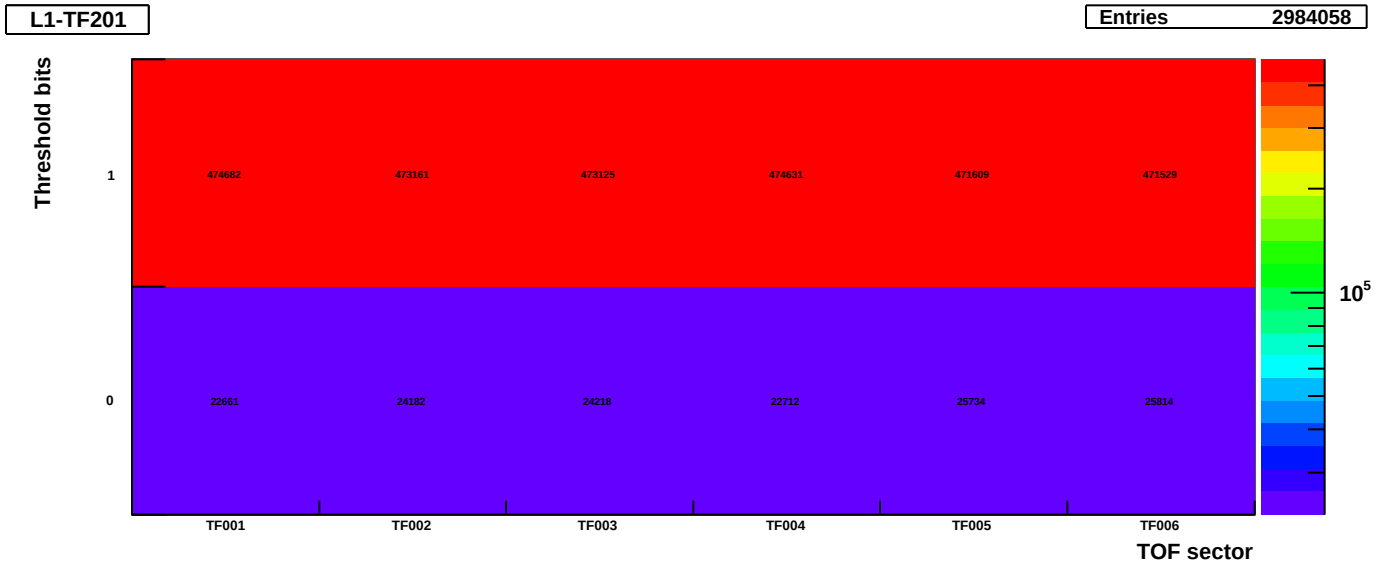

MIX-TF004
Entries 2.088841e+07
TOF MULT
TOF tray

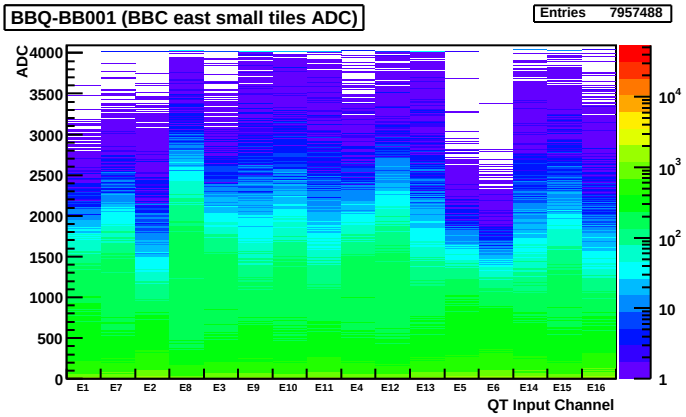

MIX-TF005
Entries 2.088841e+07
TOF MULT
TOF tray

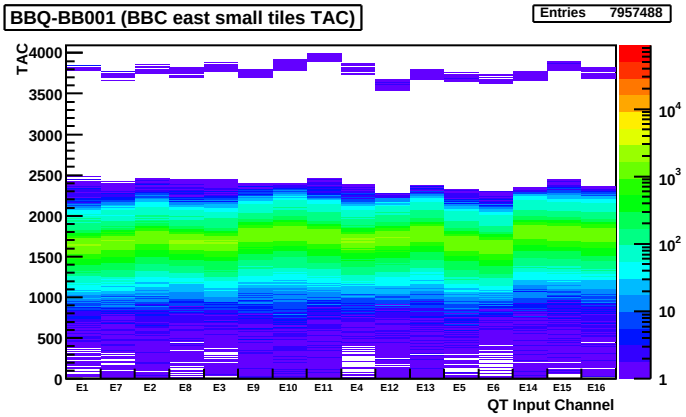

MIX-TF006
Entries 2.088841e+07
TOF MULT
TOF tray

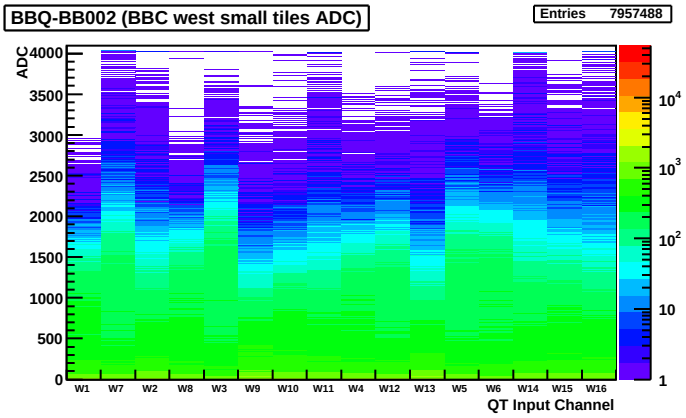

MIX-TF101
Entries 2984058
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

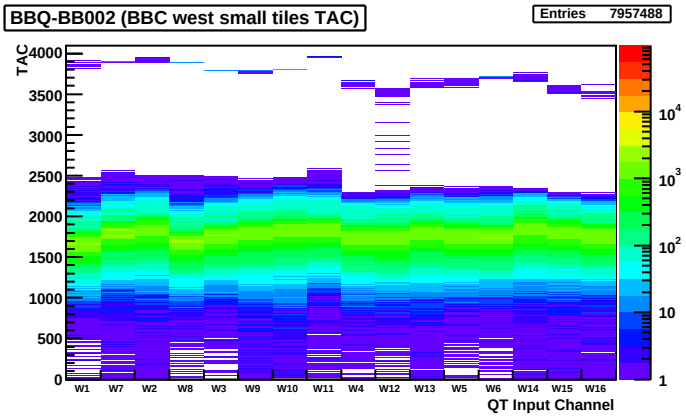

L1-TF201
Entries 497343
10^4
10^3
10^2
10
0
200
400
600
800
1000
1200
1400
TOF total mult

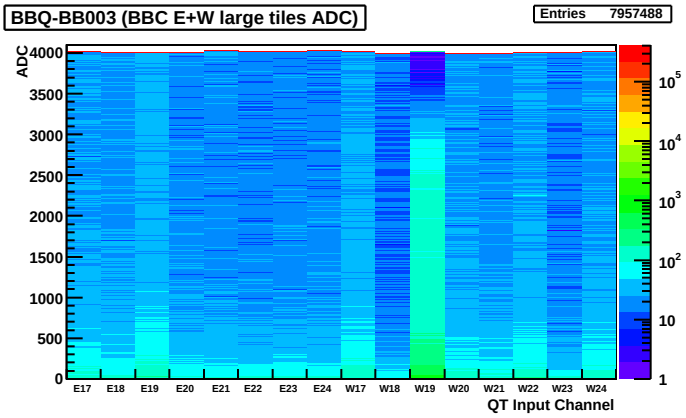

L1-TF201
Entries 2984058
Threshold bits
1
0
474682
473161
473125
474631
471609
471529
22661
24182
24218
22712
25734
25814
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

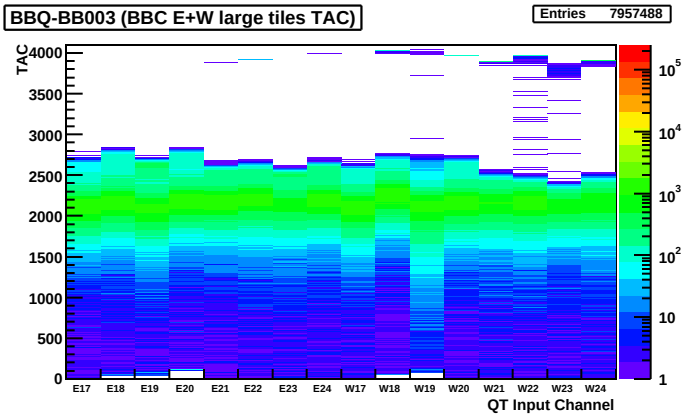

BBQ-BB001 (BBC east small tiles ADC)
Entries 7957488
ADC
QT Input Channel

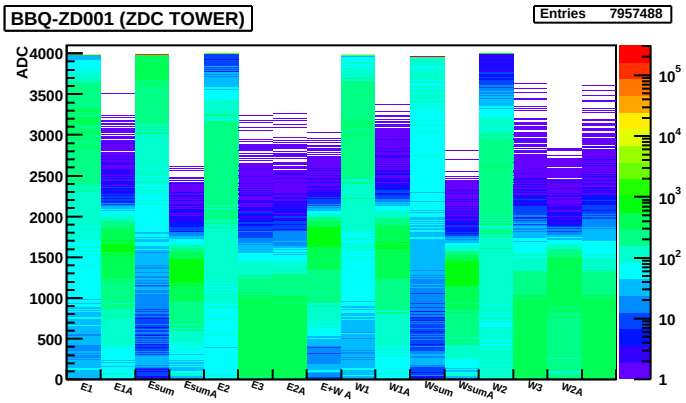

BBQ-BB001 (BBC east small tiles TAC)
Entries 7957488
TAC
QT Input Channel

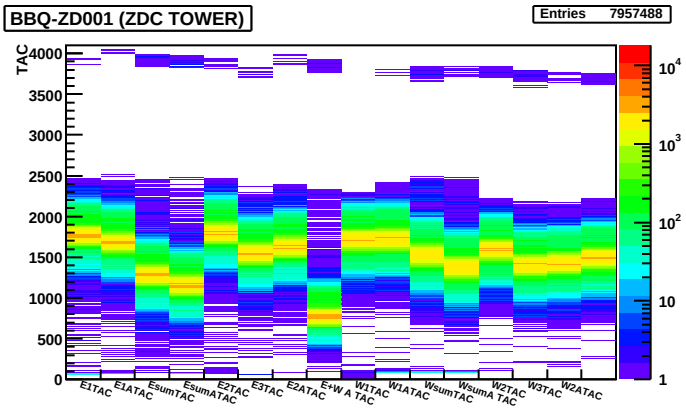

BBQ-BB002 (BBC west small tiles ADC)
Entries 7957488
ADC
QT Input Channel

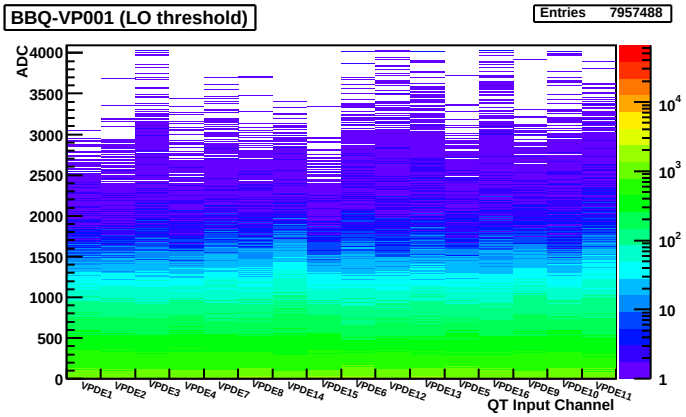

BBQ-BB002 (BBC west small tiles TAC)
Entries 7957488
TAC
QT Input Channel

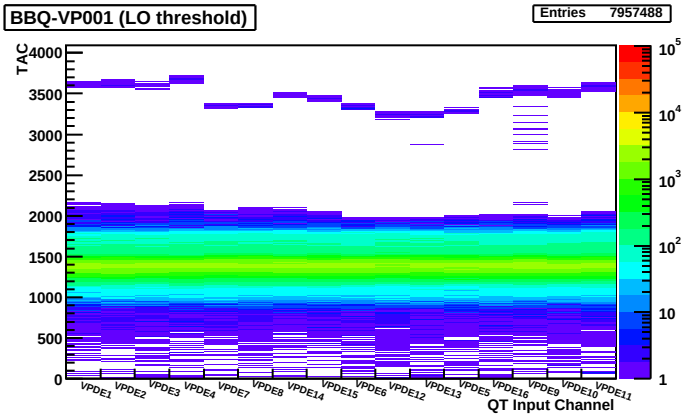

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 7957488
ADC
QT Input Channel

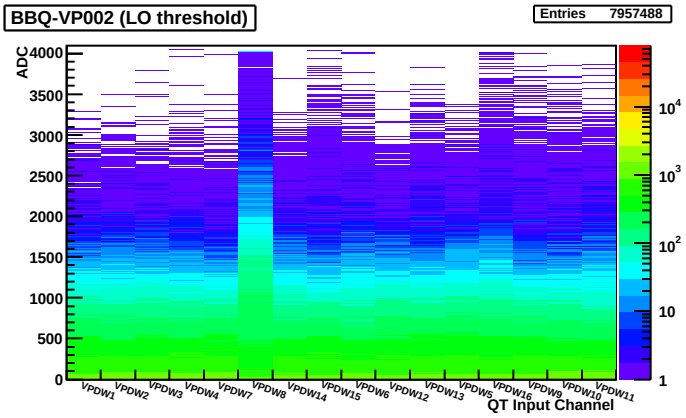

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 7957488
TAC
QT Input Channel

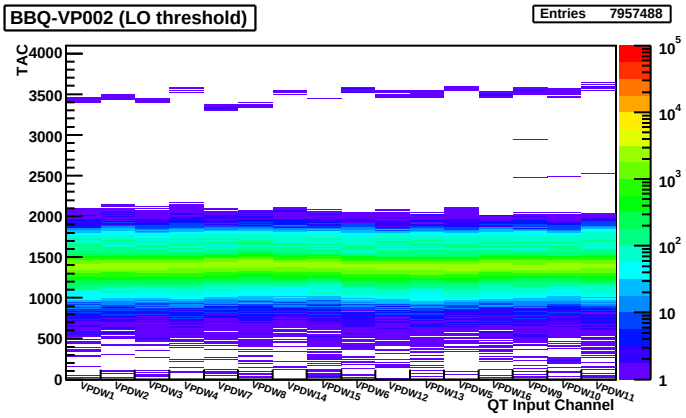

BBQ-ZD001 (ZDC TOWER)
Entries 7957488
ADC

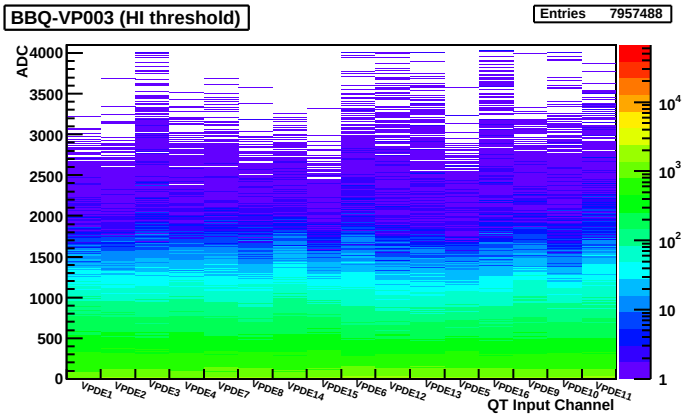

BBQ-ZD001 (ZDC TOWER)
Entries 7957488
TAC

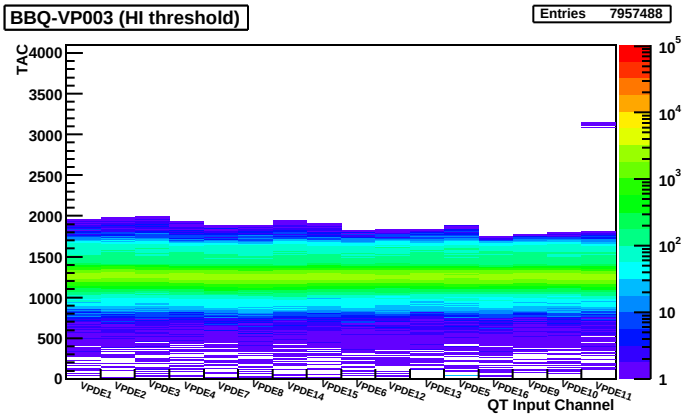

BBQ-VP001 (LO threshold)
Entries 7957488
ADC
QT Input Channel

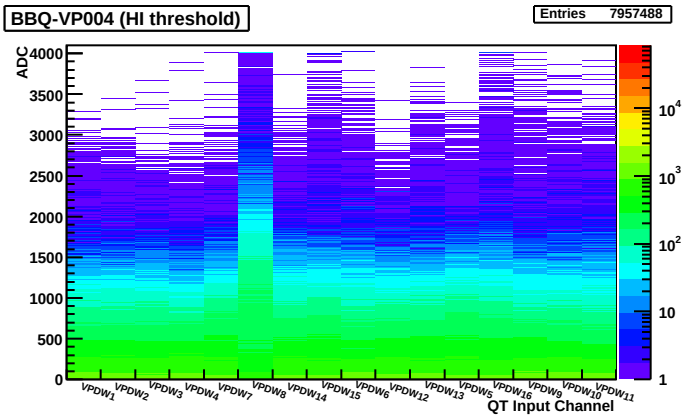

BBQ-VP001 (LO threshold)
Entries 7957488
TAC
QT Input Channel

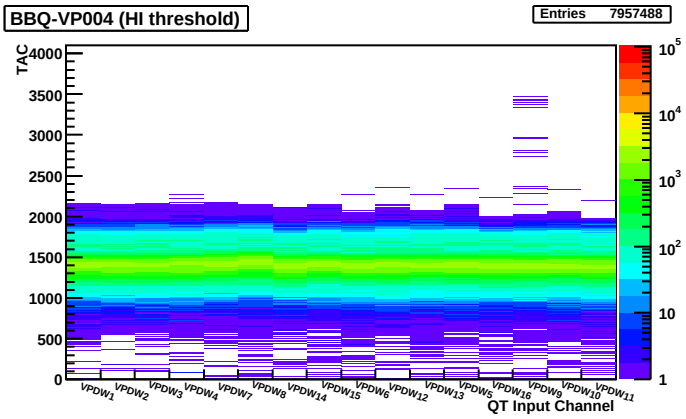

BBQ-VP002 (LO threshold)
Entries 7957488
ADC
QT Input Channel

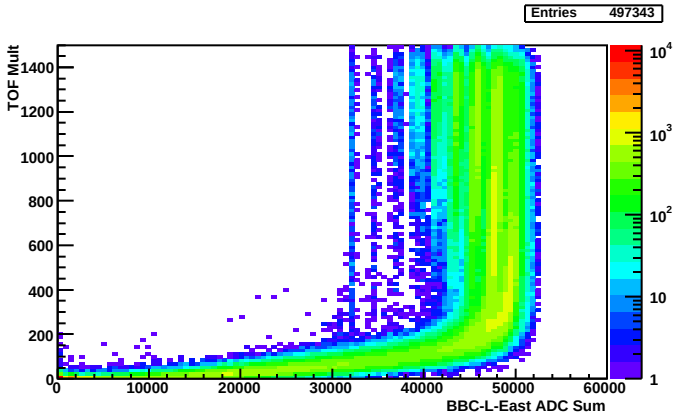

BBQ-VP002 (LO threshold)
Entries 7957488
TAC
QT Input Channel

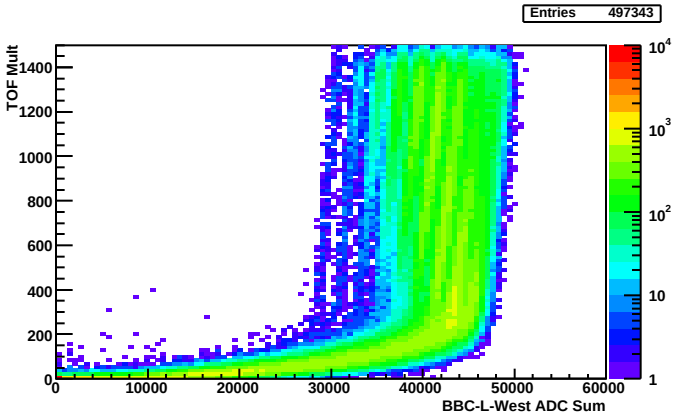

BBQ-VP003 (HI threshold)
Entries 7957488
ADC
QT Input Channel

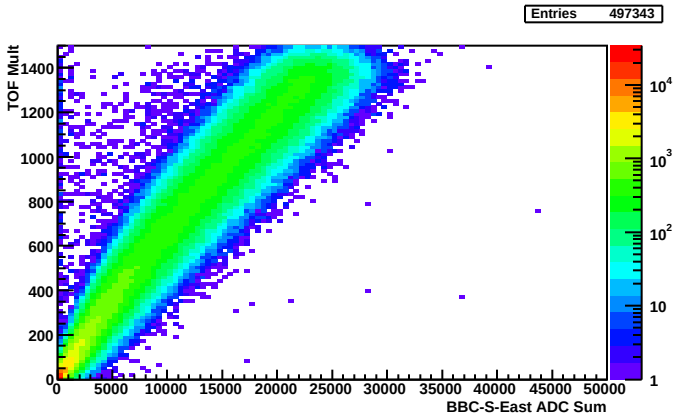

BBQ-VP003 (HI threshold)
Entries 7957488
TAC
QT Input Channel

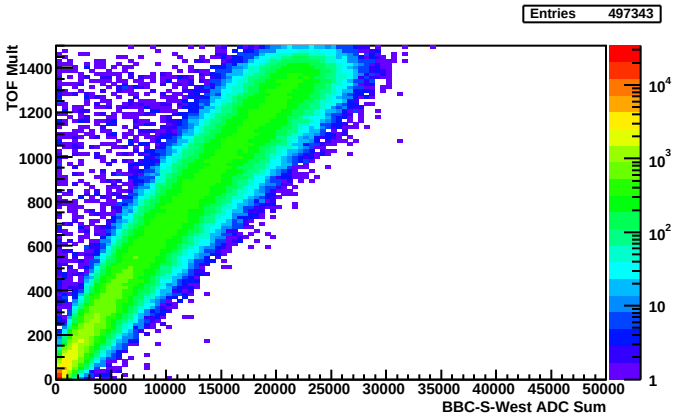

BBQ-VP004 (HI threshold)
Entries 7957488
ADC
QT Input Channel

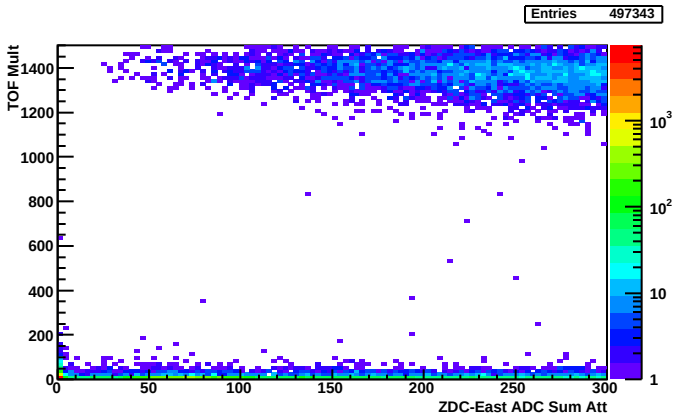

BBQ-VP004 (HI threshold)
Entries 7957488
TAC
QT Input Channel

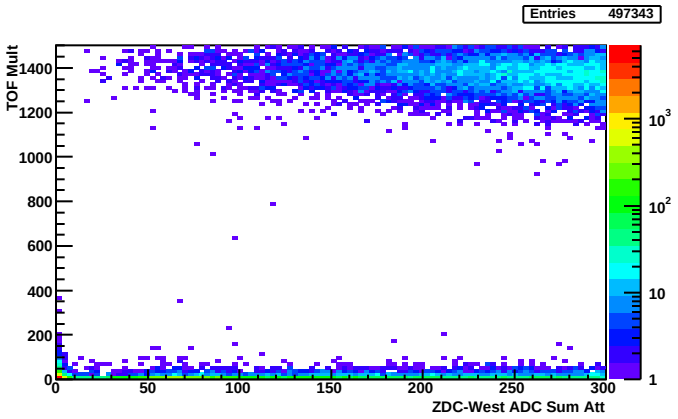

Entries 497343
TOF Mult
BBC-L-East ADC Sum

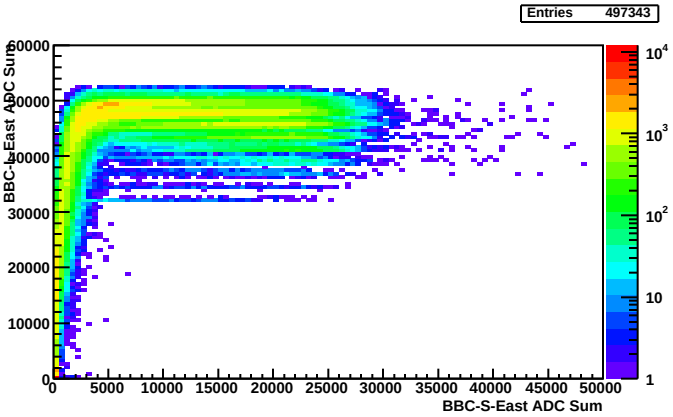

Entries 497343
TOF Mult
BBC-L-West ADC Sum

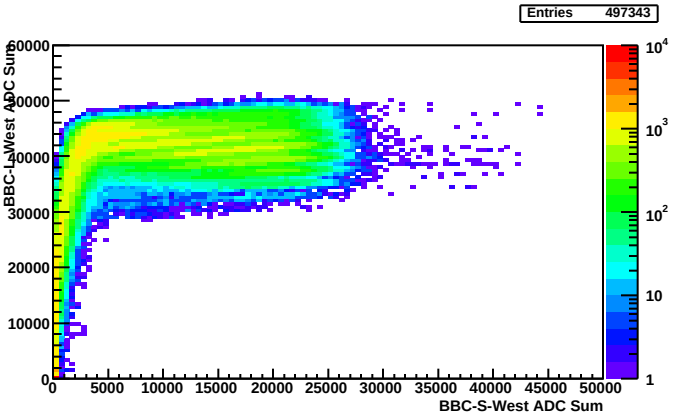

Entries 497343
TOF Mult
BBC-S-East ADC Sum

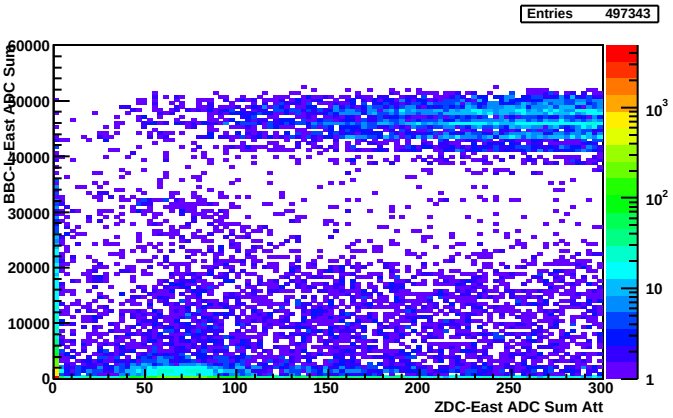

Entries 497343
TOF Mult
BBC-S-West ADC Sum

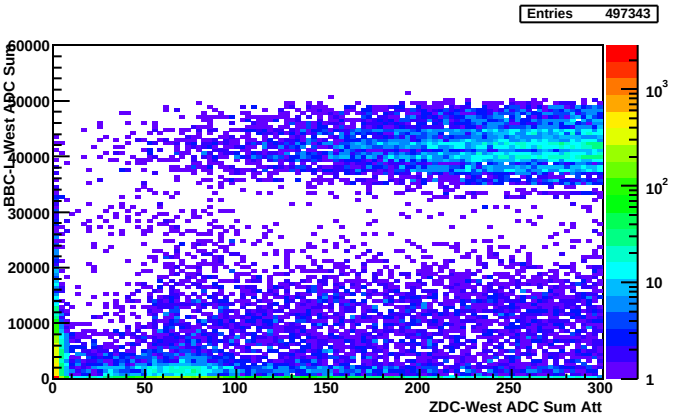

Entries 497343
TOF Mult
ZDC-East ADC Sum Att

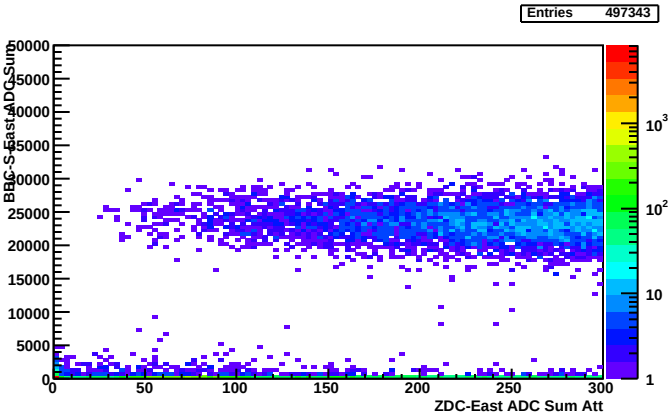

Entries 497343
TOF Mult
ZDC-West ADC Sum Att

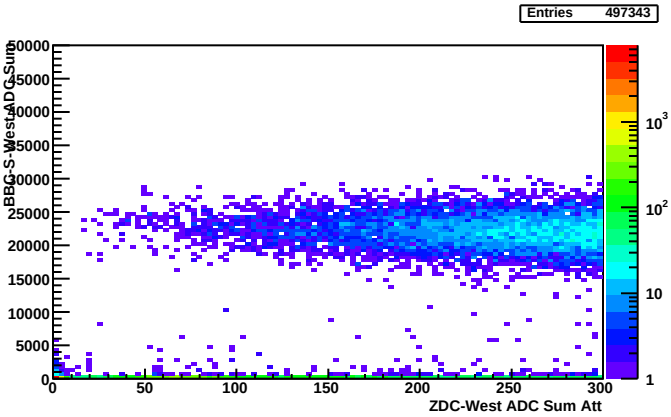

Entries 497343
BBC-L-East ADC Sum
BBC-S-East ADC Sum

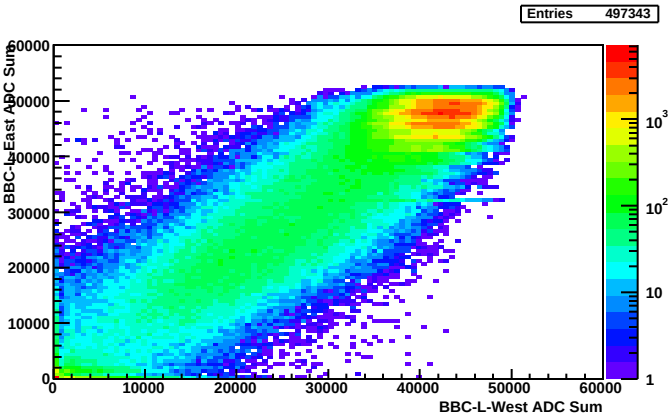

Entries 497343
BBC-L-West ADC Sum
BBC-S-West ADC Sum

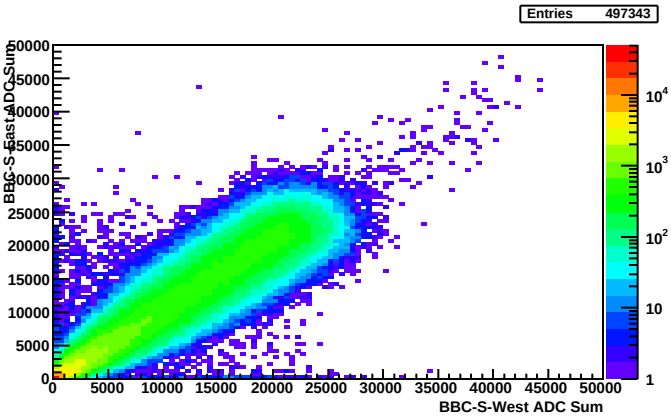

Entries 497343
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

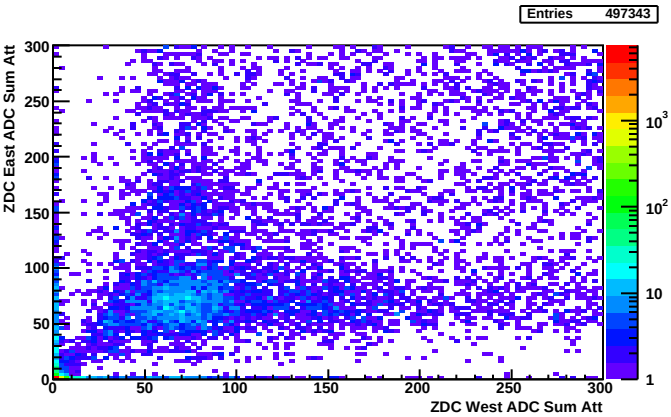

Entries 497343
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

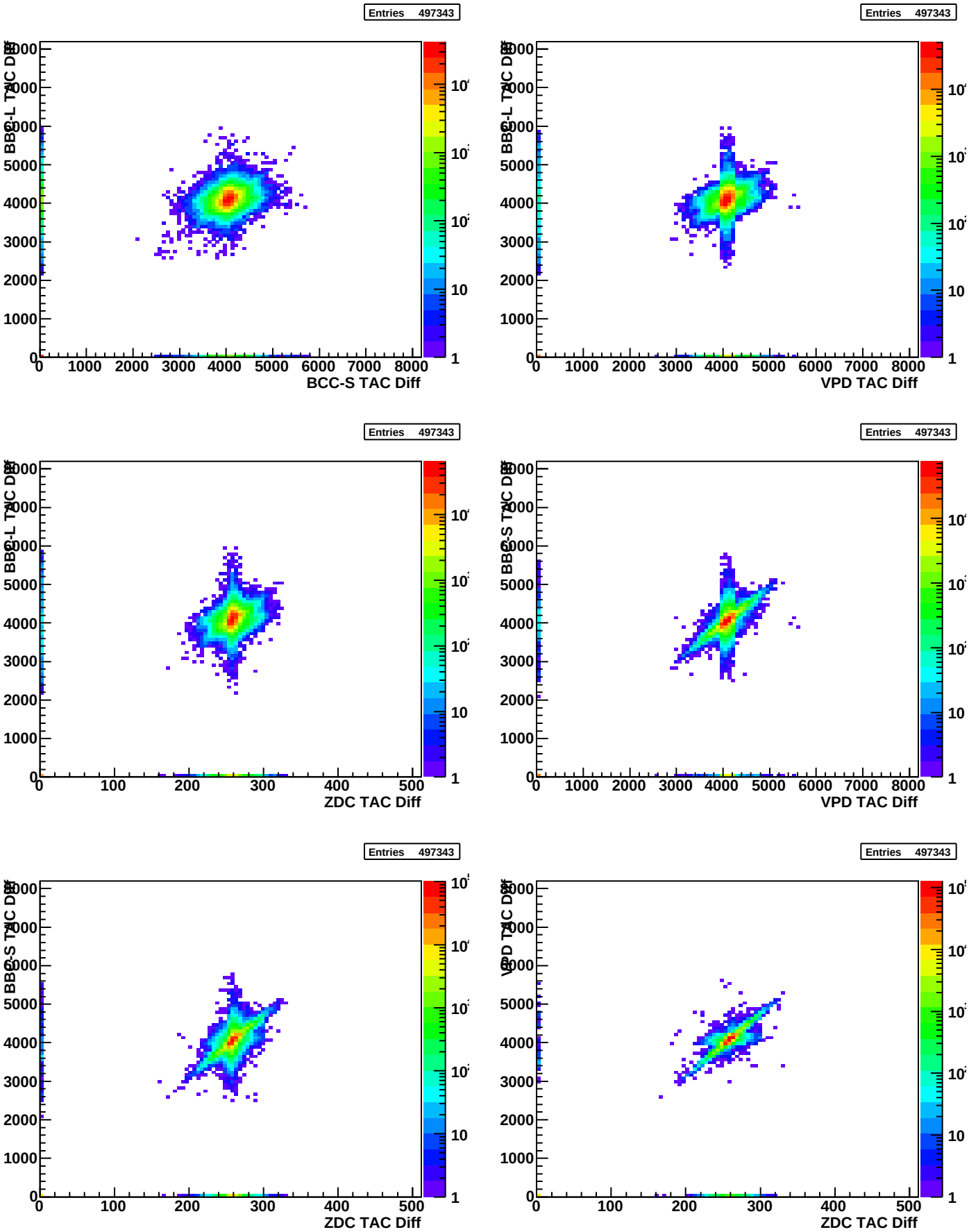

Entries 497343
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

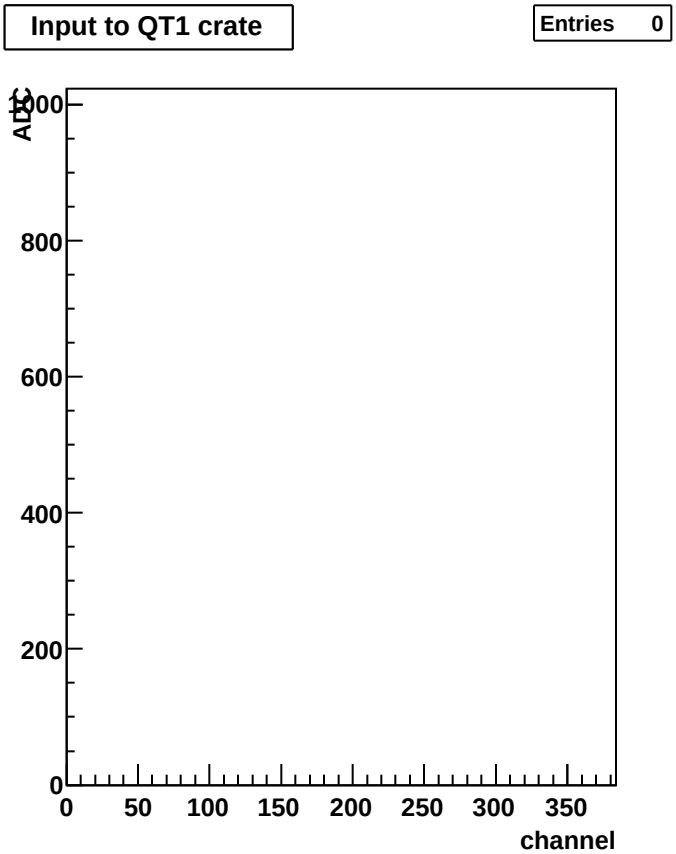

Entries 497343
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

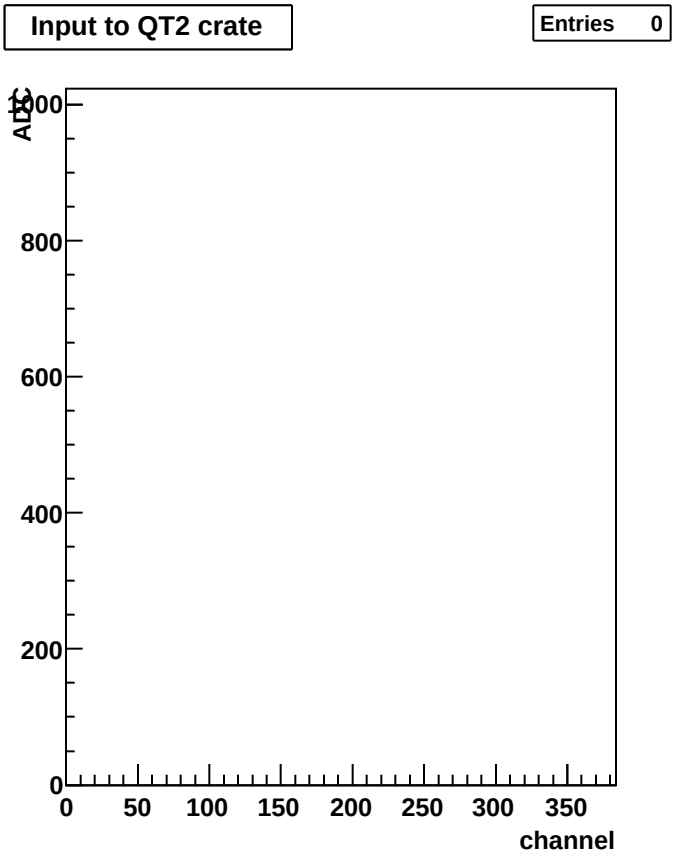

Entries 497343
BBC-L-East ADC Sum
BBC-L-West ADC Sum

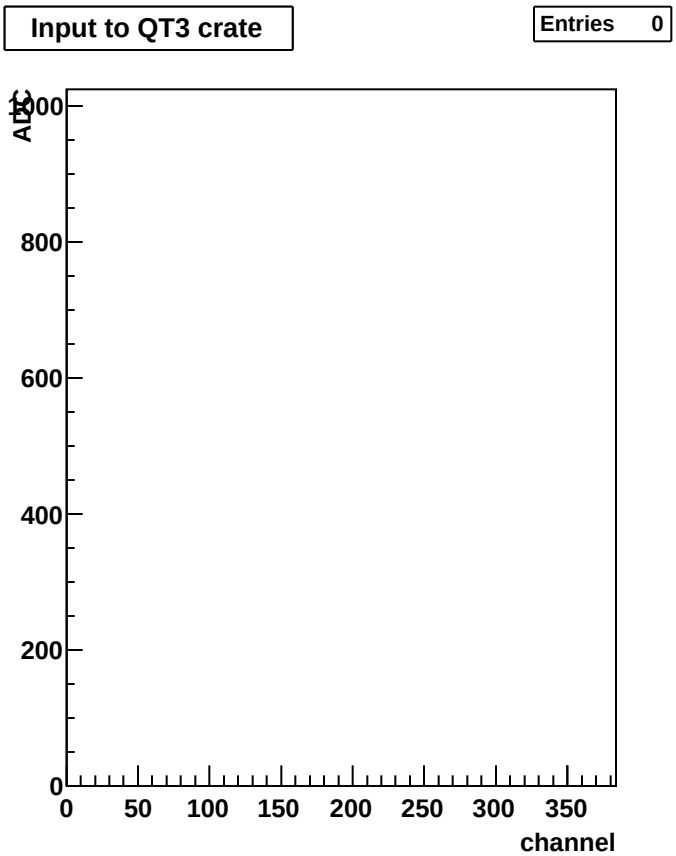

Entries 497343
BBC-S-East ADC Sum
BBC-S-West ADC Sum

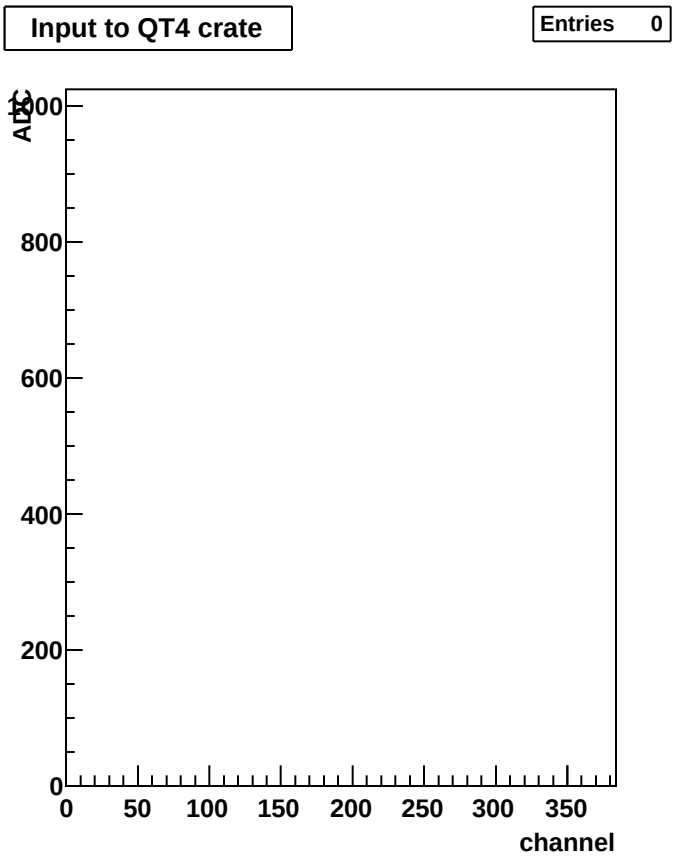

Entries 497343
ZDC East ADC Sum Att
ZDC West ADC Sum Att

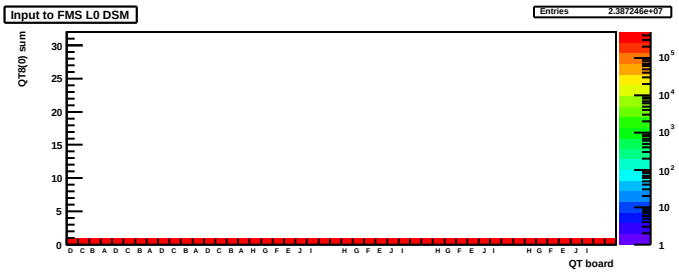

Entries 497343
BBC-L TAC Diff
BCC-S TAC Diff
Entries 497343
BBC-L TAC Diff
VPD TAC Diff
Entries 497343
BBC-L TAC Diff
ZDC TAC Diff
Entries 497343
BBC-S TAC Diff
VPD TAC Diff
Entries 497343
BBC-S TAC Diff
ZDC TAC Diff
Entries 497343
VPD TAC Diff
ZDC TAC Diff

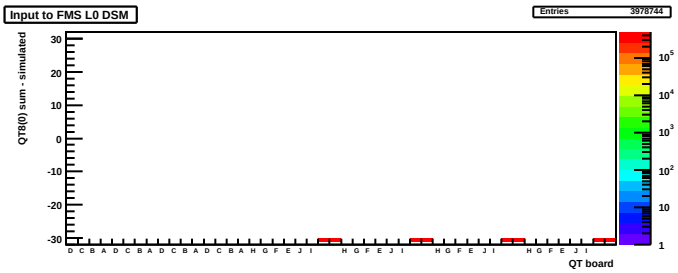

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

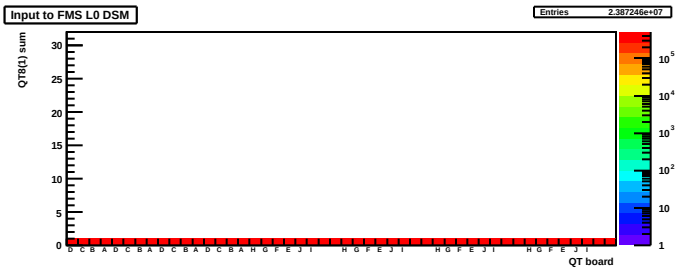

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

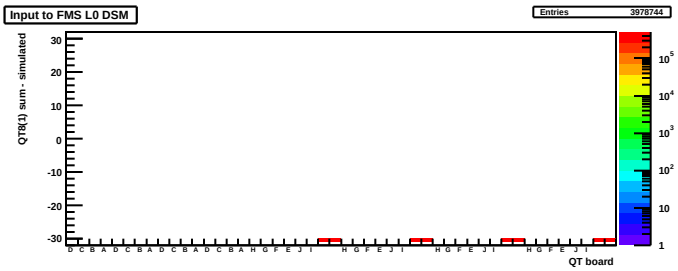

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

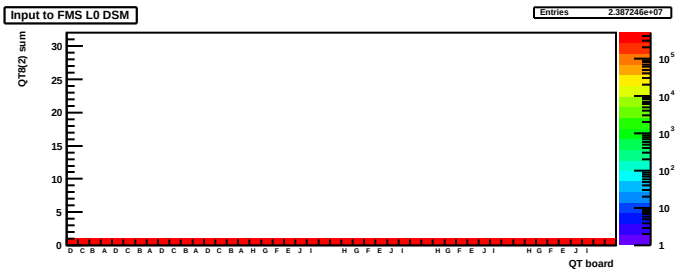

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

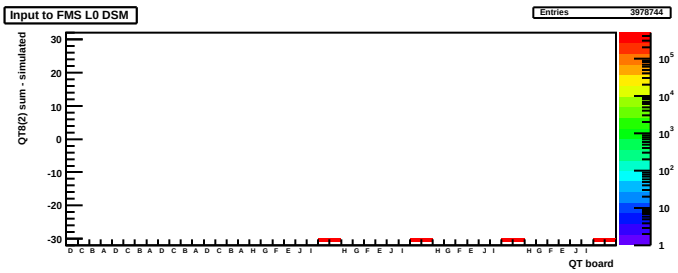

Input to FMS L0 DSM
Entries 2.387246e+07
QT8(0) sum
QT board

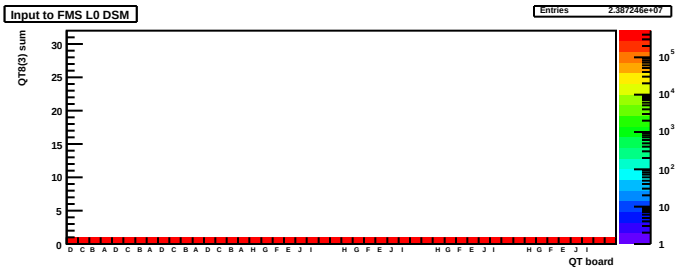

Input to FMS L0 DSM
Entries 3978744
QT8(0) sum - simulated
QT board

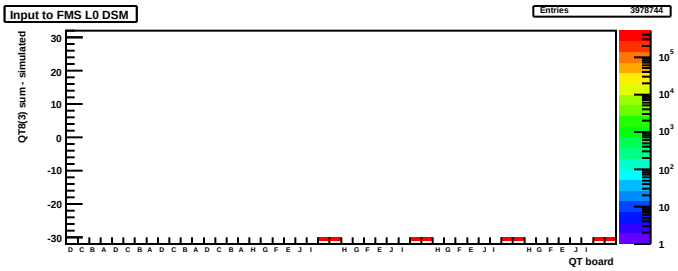

Input to FMS L0 DSM
Entries 2.387246e+07
QT8(1) sum
QT board

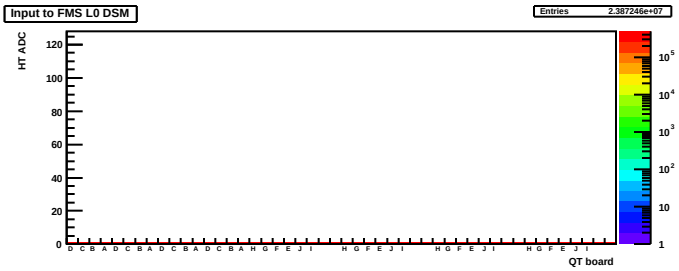

Input to FMS L0 DSM
Entries 3978744
QT8(1) sum - simulated
QT board

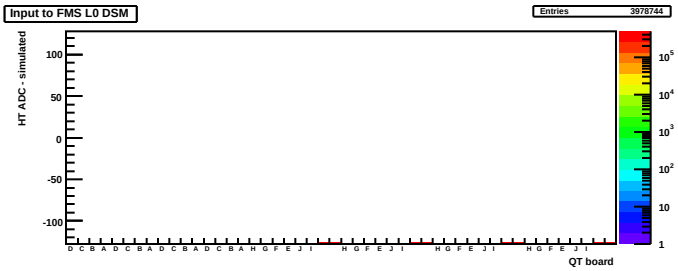

Input to FMS L0 DSM
Entries 2.387246e+07
QT8(2) sum
QT board

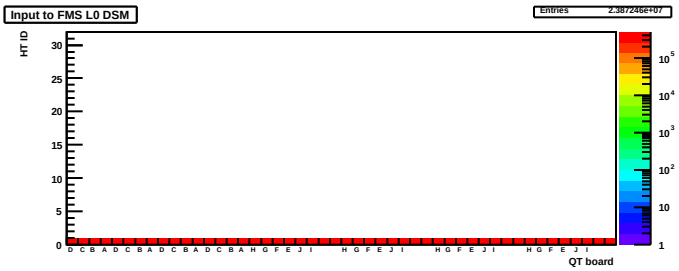

Input to FMS L0 DSM
Entries 3978744
QT8(2) sum - simulated
QT board

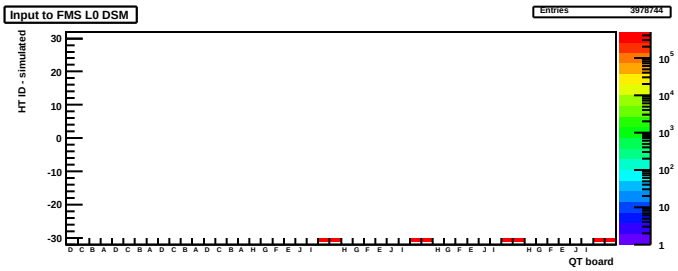

Input to FMS L0 DSM
Entries 2.387246e+07
QT8(3) sum
QT board

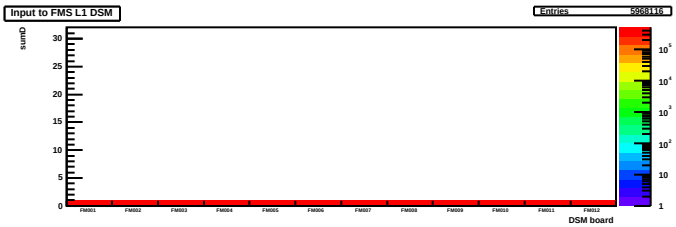

Input to FMS L0 DSM
Entries 3978744
QT8(3) sum - simulated
QT board

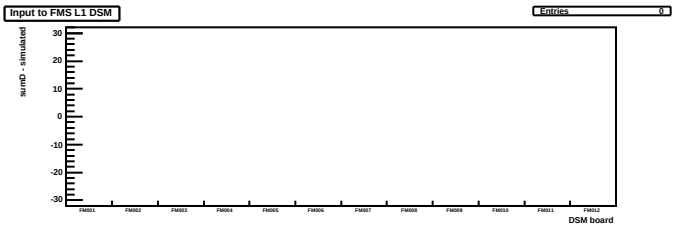

Input to FMS L0 DSM
Entries 2.387246e+07
HT ADC
QT board

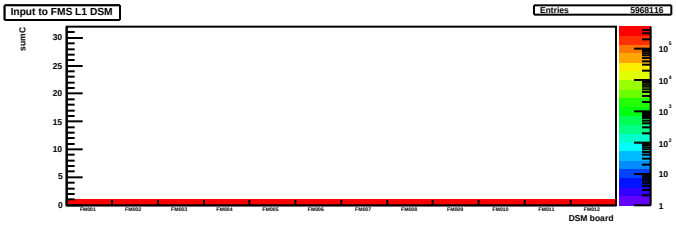

Input to FMS L0 DSM
Entries 3978744
HT ADC - simulated
QT board

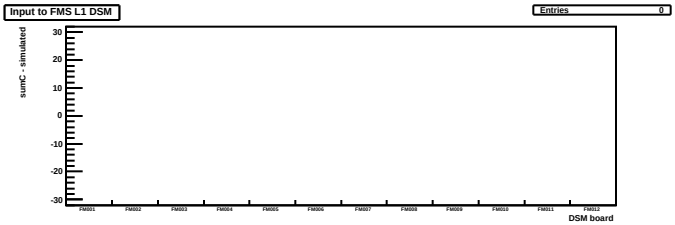

Input to FMS L0 DSM
Entries 2.387246e+07
HT ID
QT board

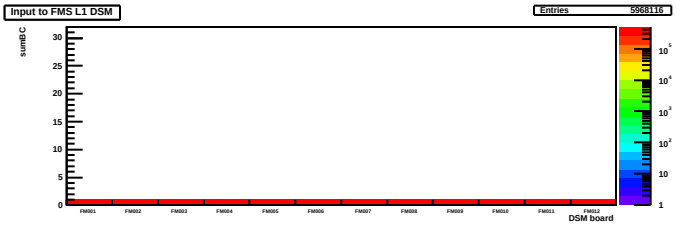

Input to FMS L0 DSM
Entries 3978744
HT ID - simulated
QT board

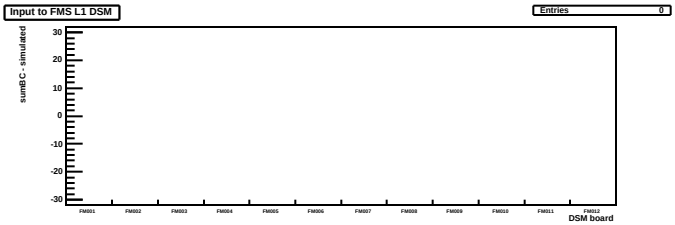

Input to FMS L1 DSM
Entries 5968116
sumD
DSM board

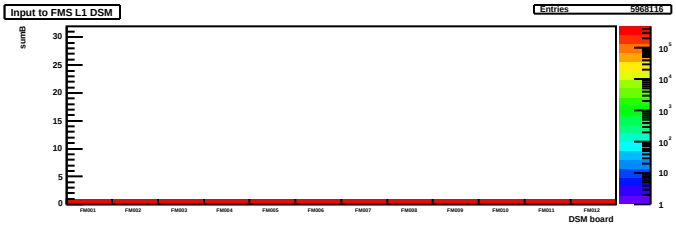

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

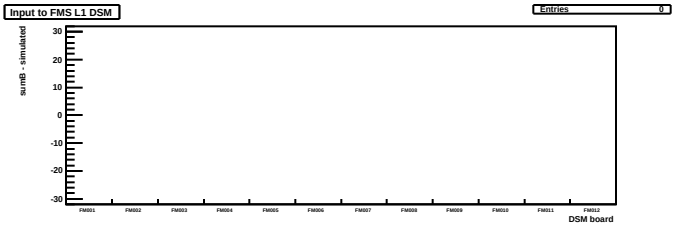

Input to FMS L1 DSM
Entries 5968116
sumC
DSM board

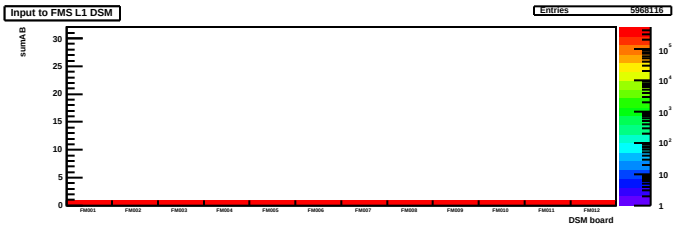

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

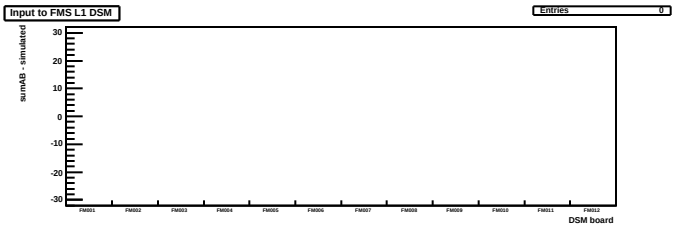

Input to FMS L1 DSM
Entries 5968116
sumBC
DSM board

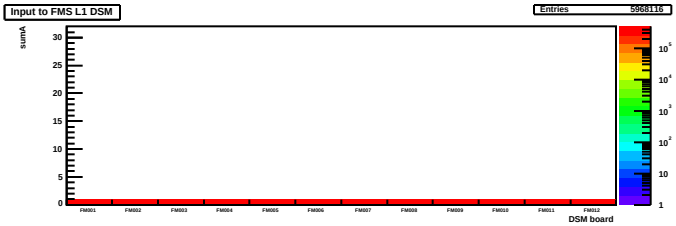

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

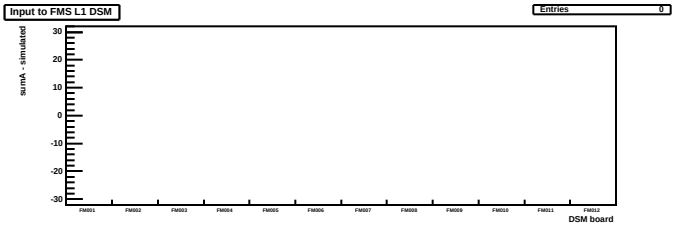

Input to FMS L1 DSM
Entries 5968116
sumB
DSM board

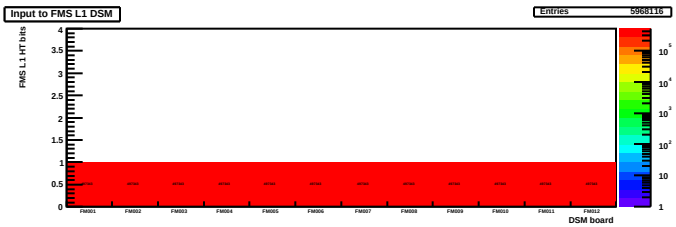

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

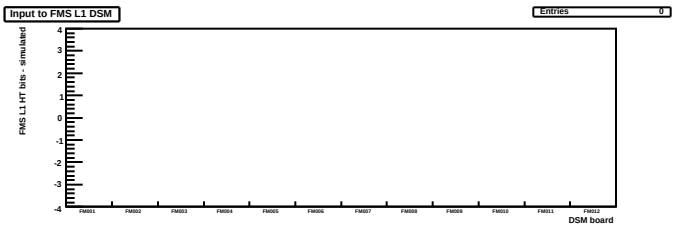

Input to FMS L1 DSM
Entries 5968116
sumAB
DSM board

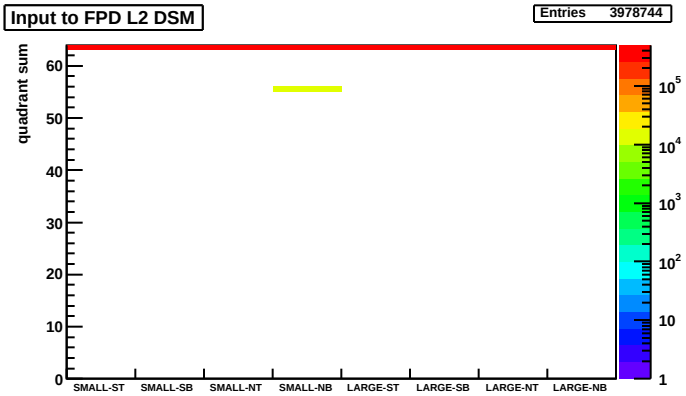

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

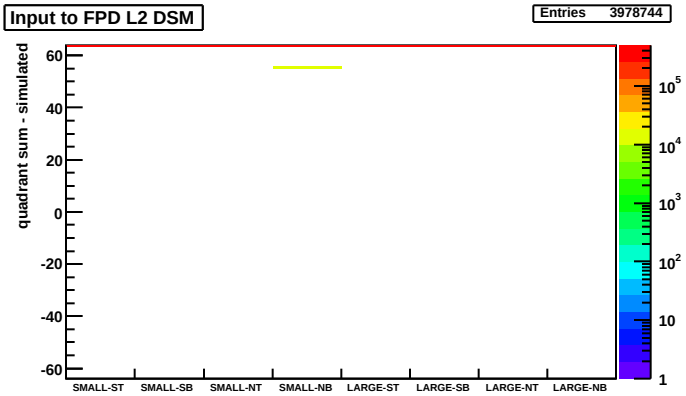

Input to FMS L1 DSM
Entries 5968116
sumA
DSM board

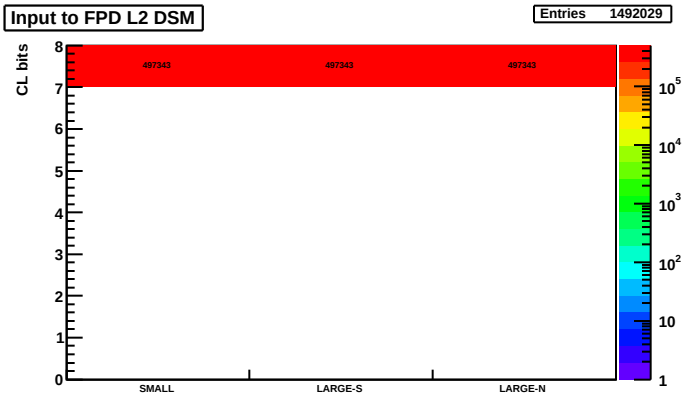

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

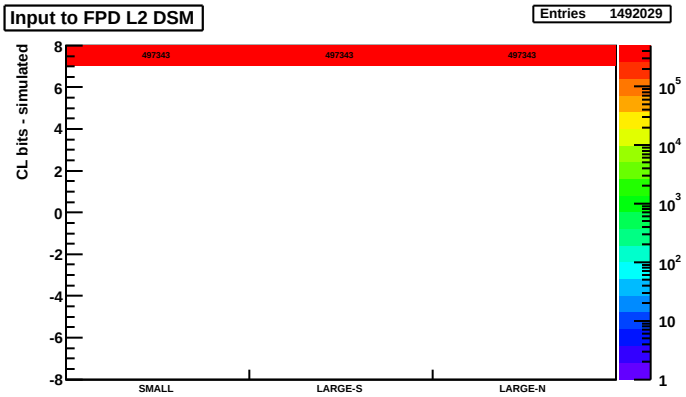

Input to FMS L1 DSM
Entries 5968116
FMS L1 HT bits
DSM board

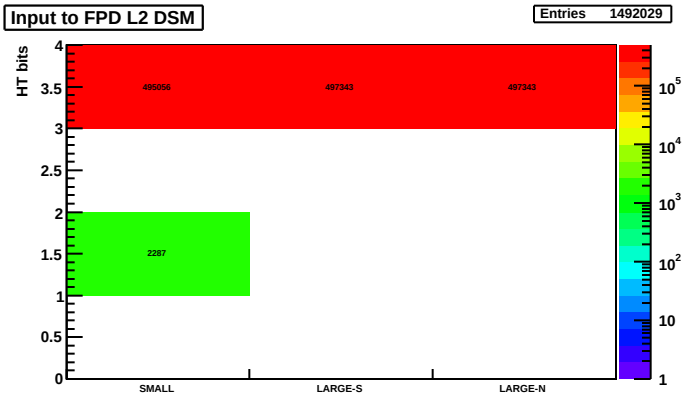

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

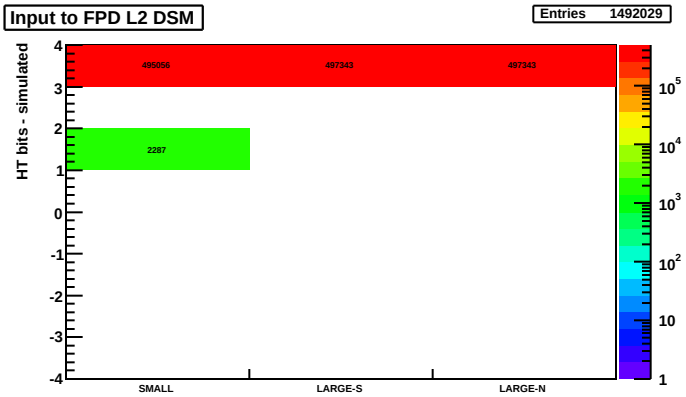

Input to FPD L2 DSM
Entries 3978744
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

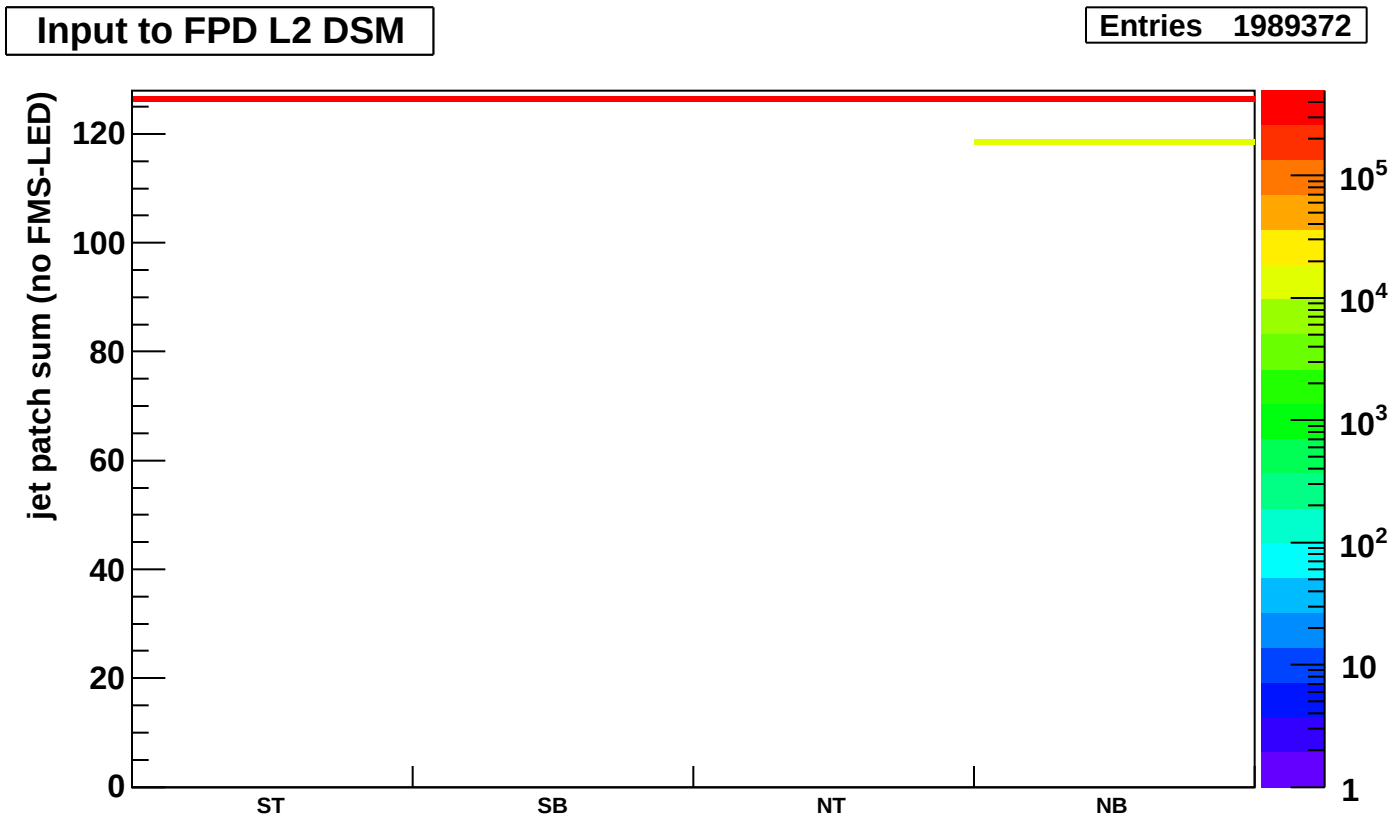

Input to FPD L2 DSM
Entries 3978744
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

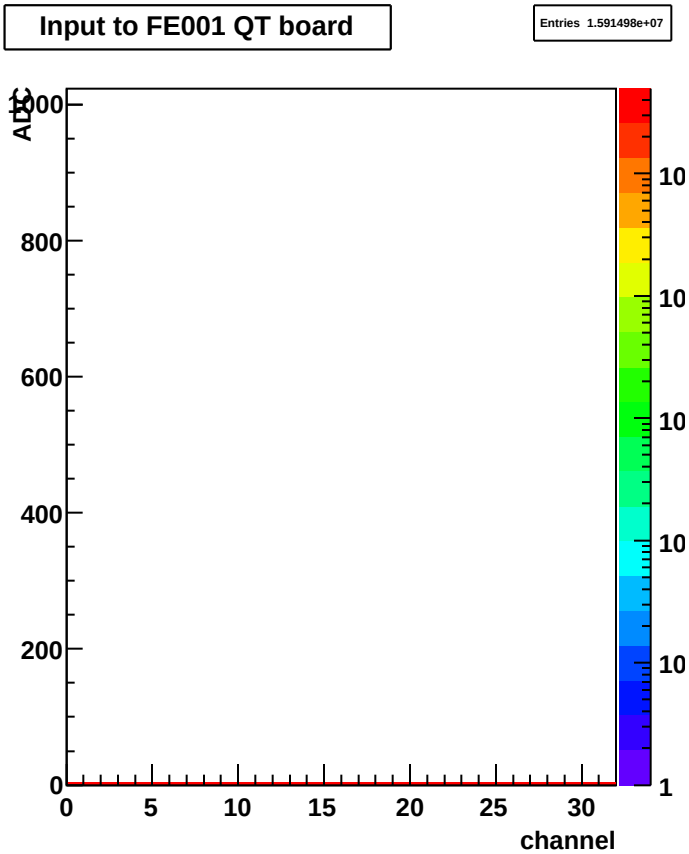

Input to FPD L2 DSM
Entries 1492029
CL bits
497343
497343
497343
SMALL
LARGE-S
LARGE-N

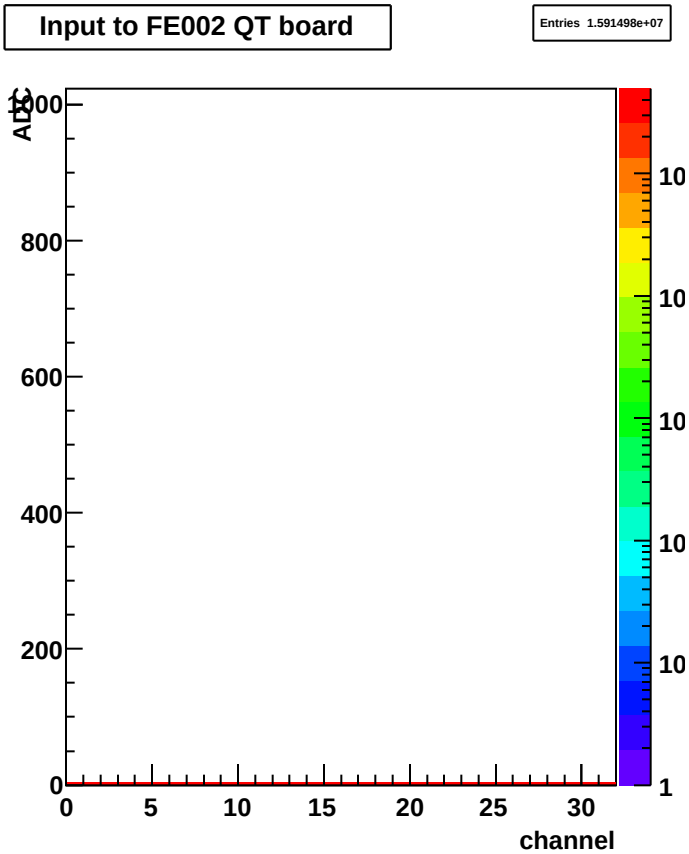

Input to FPD L2 DSM
Entries 1492029
CL bits - simulated
497343
497343
497343
SMALL
LARGE-S
LARGE-N

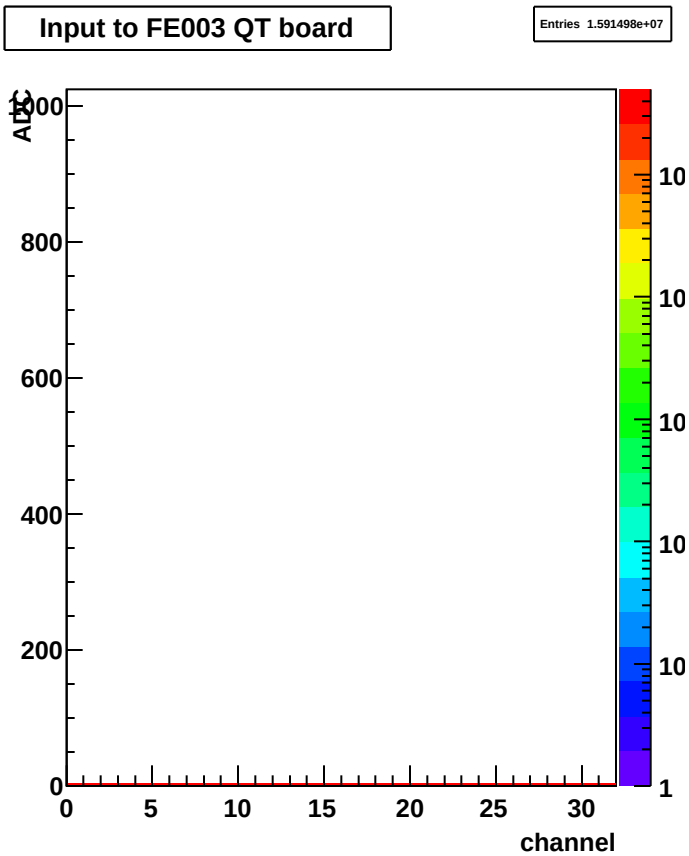

Input to FPD L2 DSM
Entries 1492029
HT bits
495056
497343
497343
2287
SMALL
LARGE-S
LARGE-N

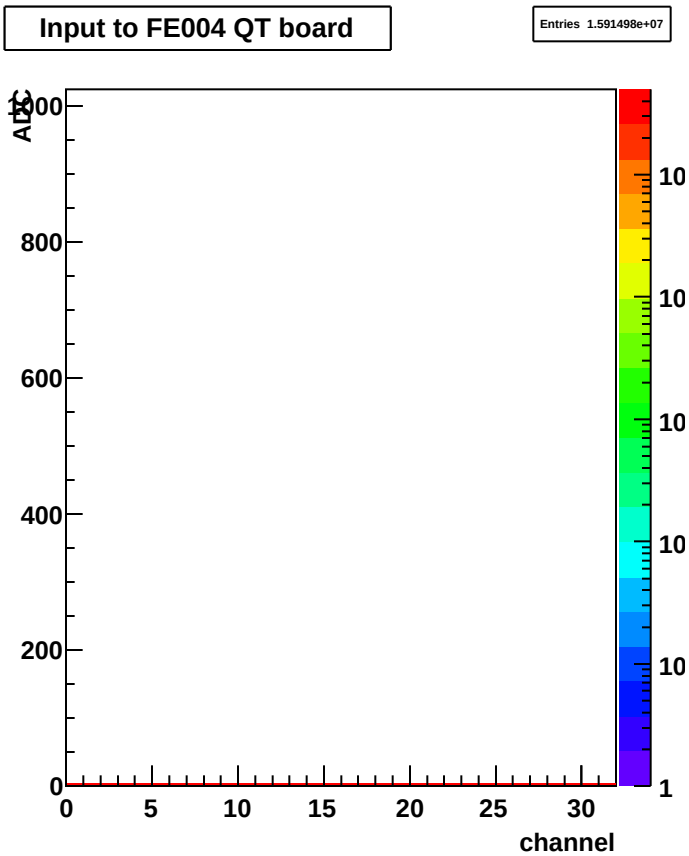

Input to FPD L2 DSM
Entries 1492029
HT bits - simulated
495056
497343
497343
2287
SMALL
LARGE-S
LARGE-N


Input to FPD L2 DSM
Entries 1989372
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

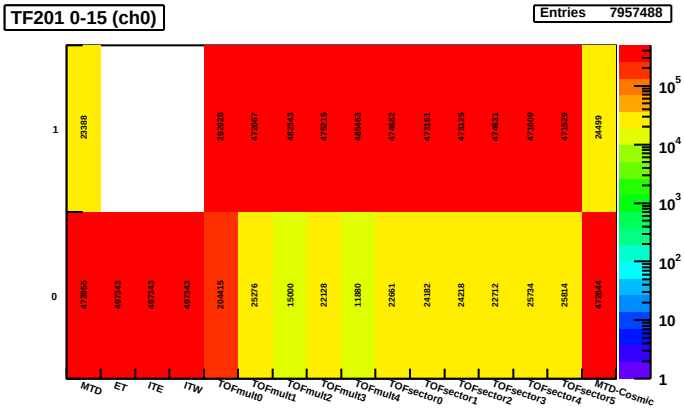

Input to FE001 QT board
Entries 1.591498e+07
ADC
channel


Input to FE002 QT board
Entries 1.591498e+07
ADC
channel

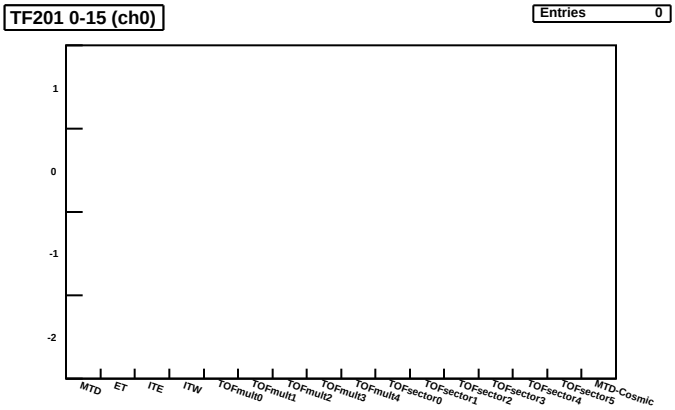

Input to FE003 QT board
Entries 1.591498e+07
ADC
channel


Input to FE004 QT board
Entries 1.591498e+07
ADC
channel

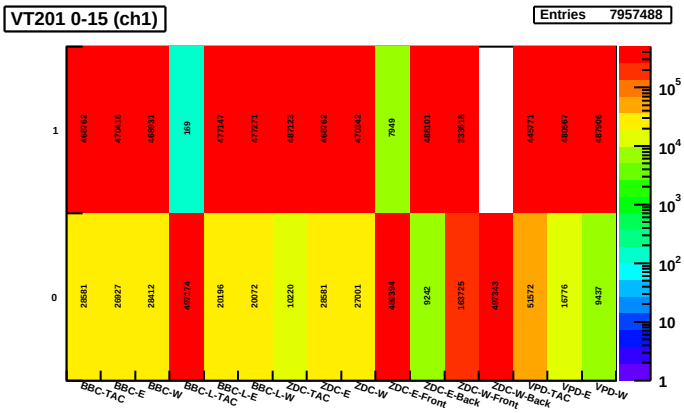
TF201 0-15 (ch0)



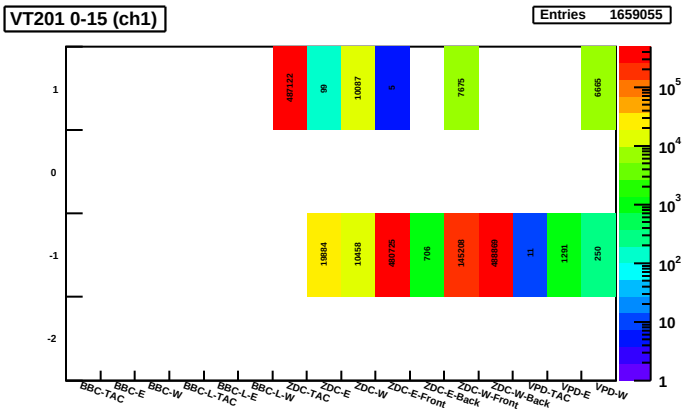
TF201 0-15 (ch0)



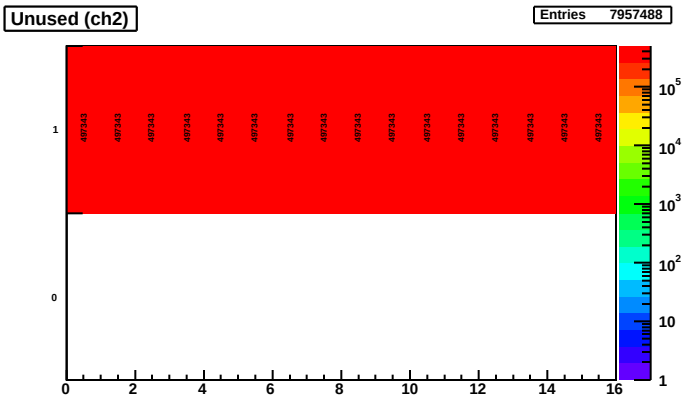
VT201 0-15 (ch1)

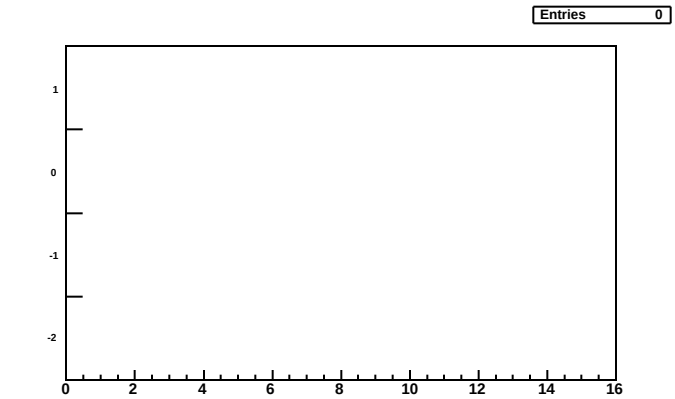


VT201 0-15 (ch1)

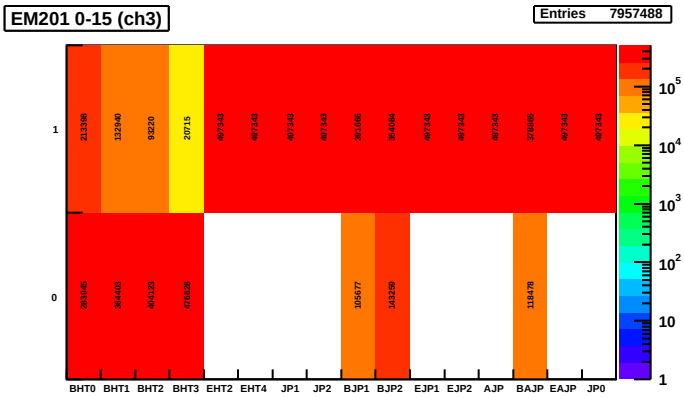


Unused (ch2)

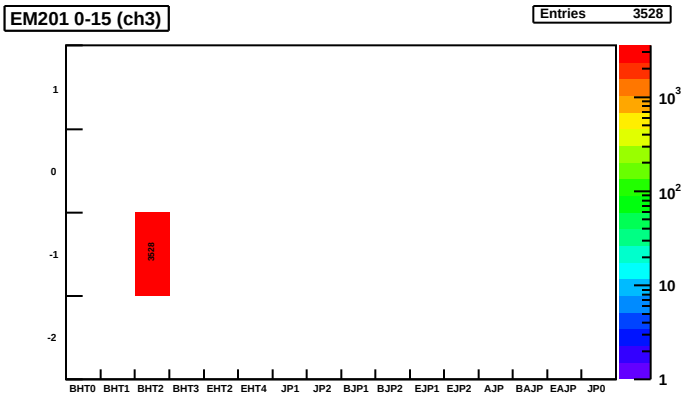




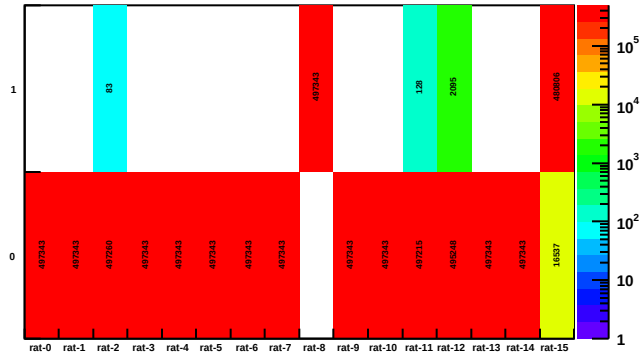
EM201 0-15 (ch3)

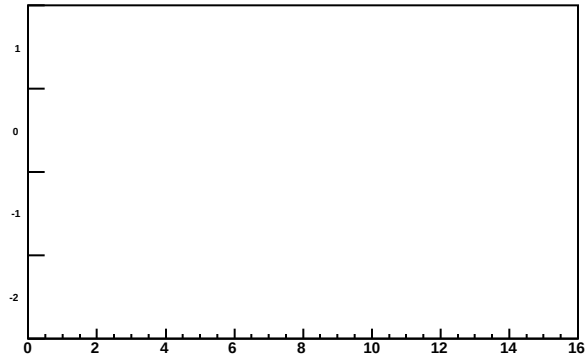


EM201 0-15 (ch3)

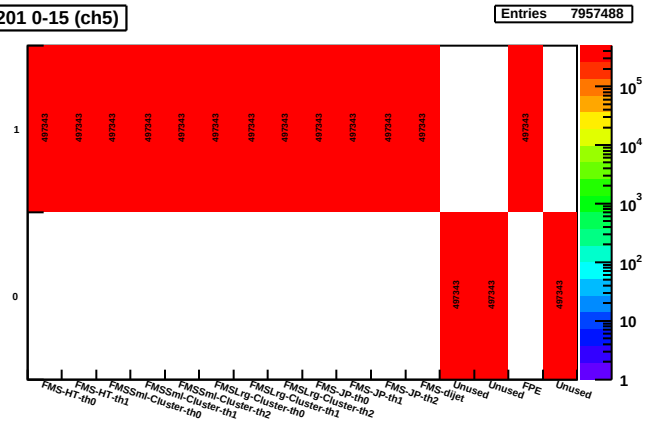


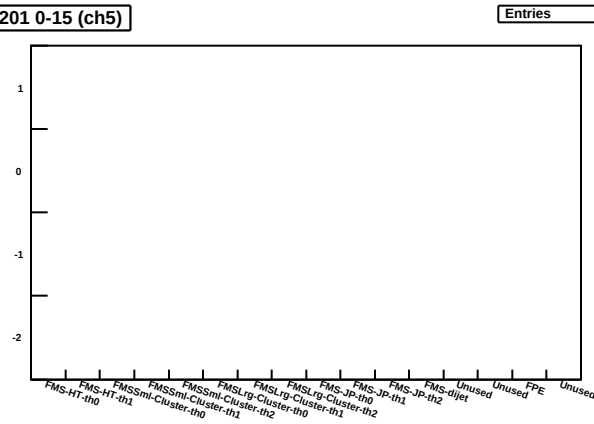
RAT board (ch4)



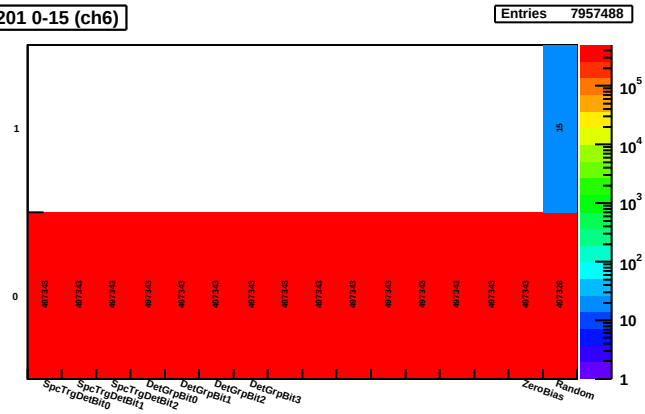


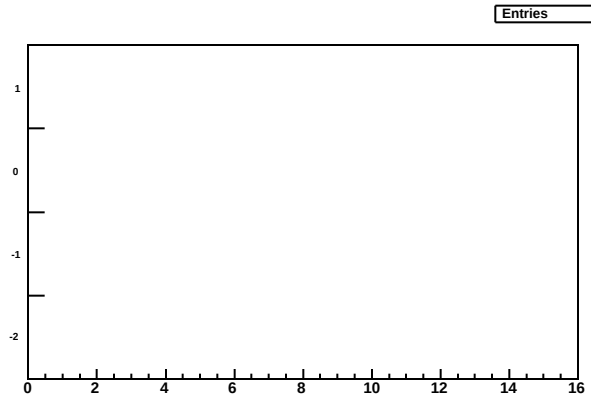
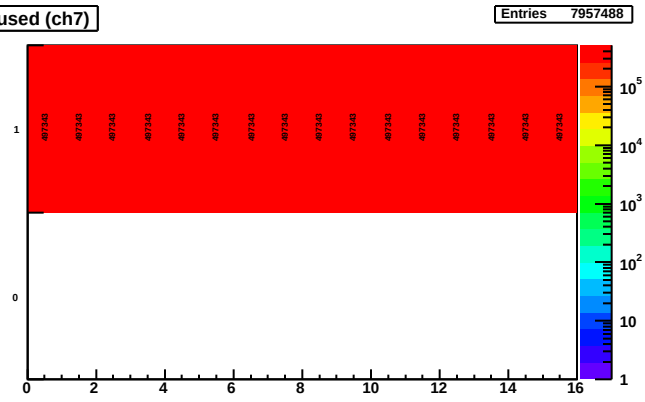
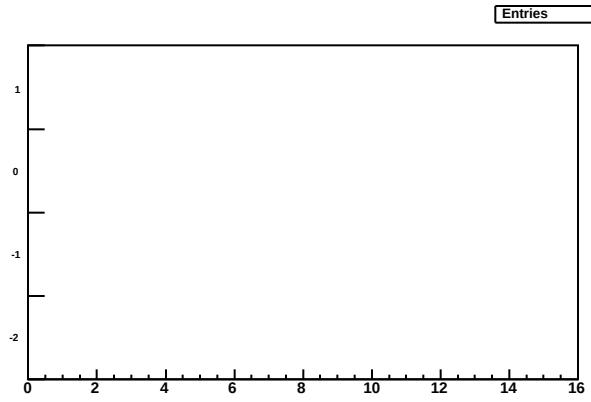
FP201 0-15 (ch5)





ST201 0-15 (ch6)





Unused (ch7)