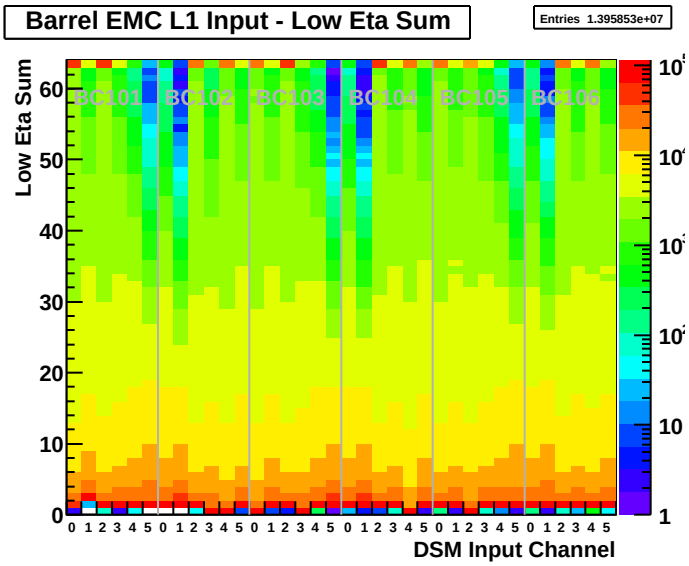
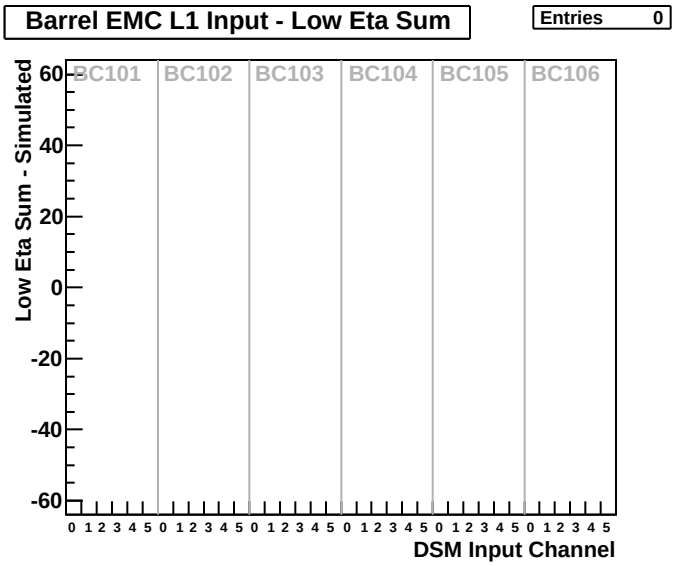
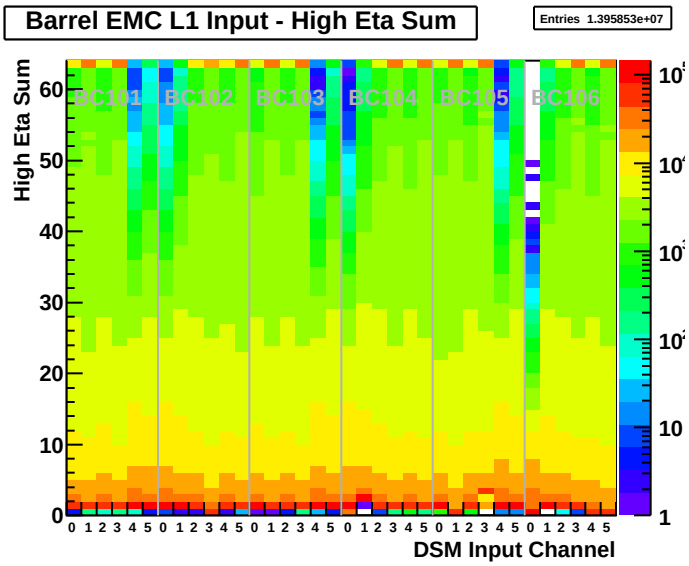
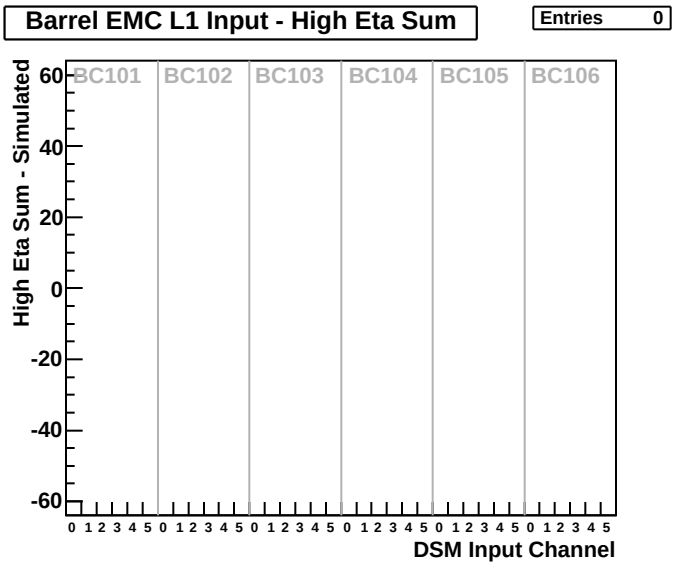
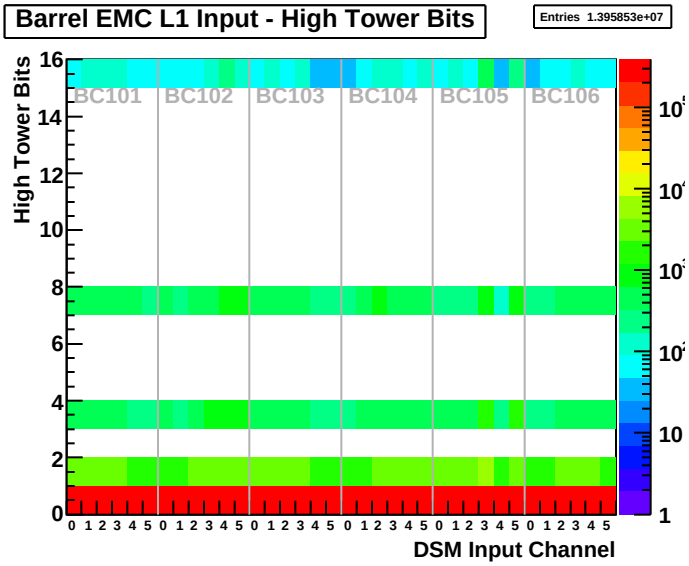
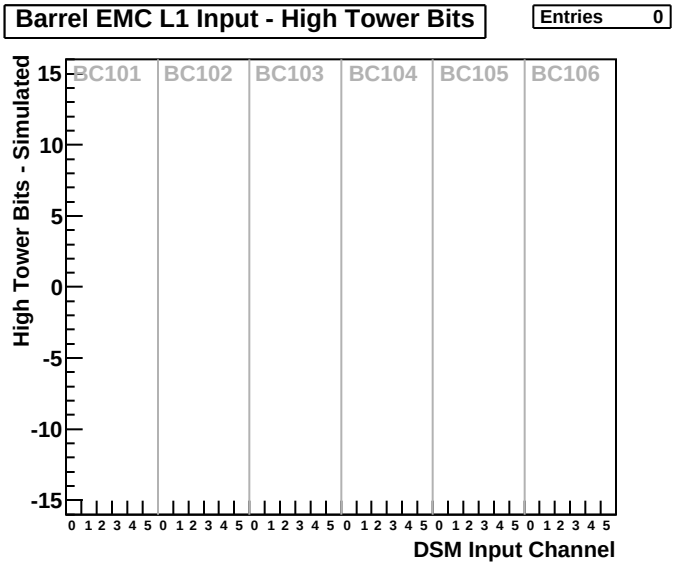

Barrel EMC L1 Input - Low Eta Sum
Entries 1.395853e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

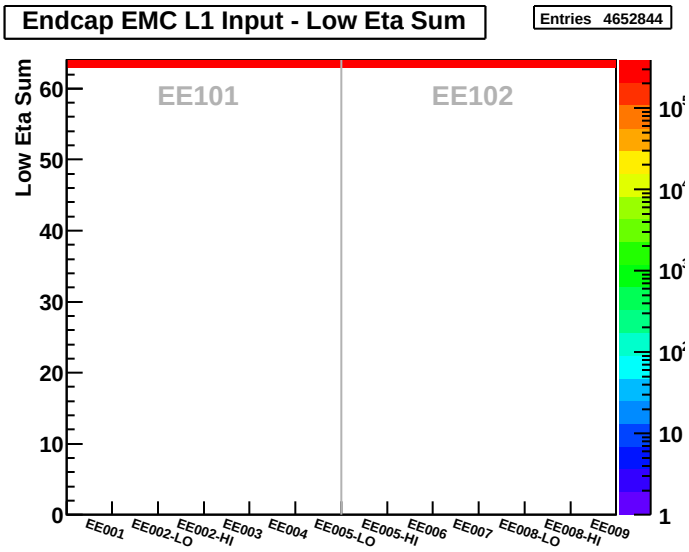

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

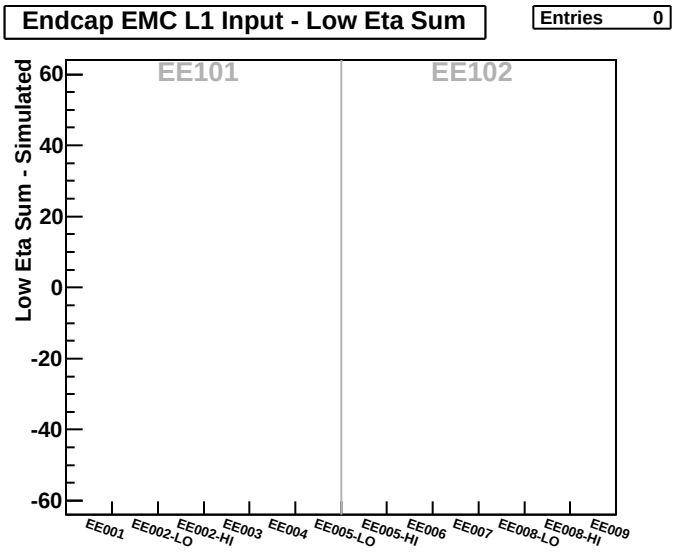

Barrel EMC L1 Input - High Eta Sum
Entries 1.395853e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

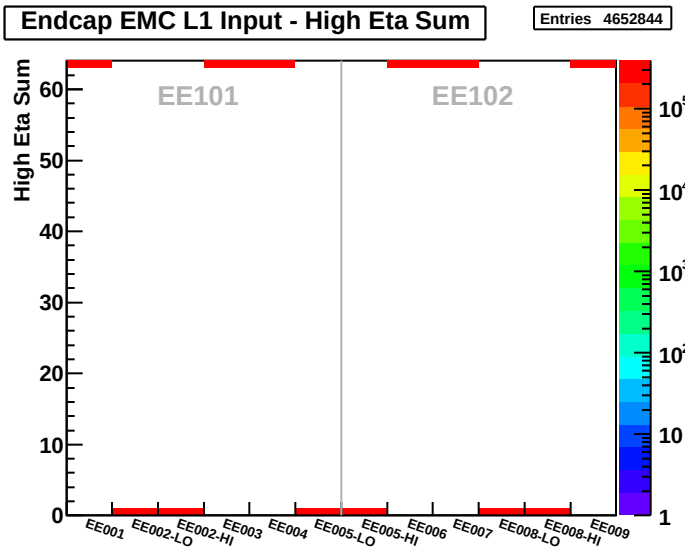

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

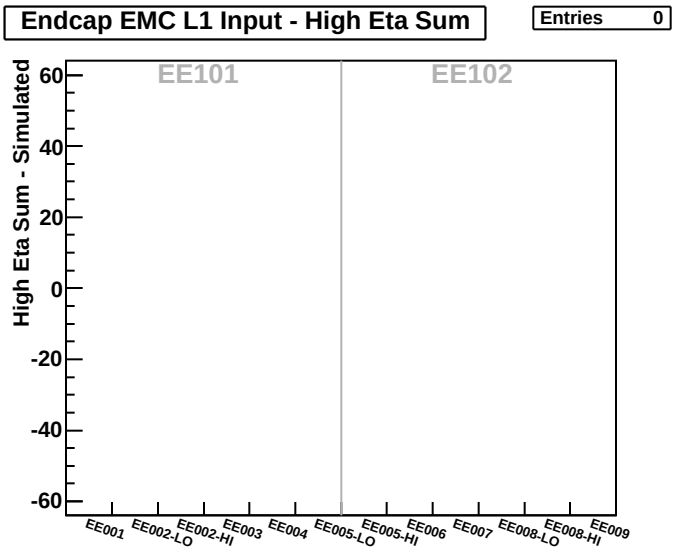

Barrel EMC L1 Input - High Tower Bits
Entries 1.395853e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

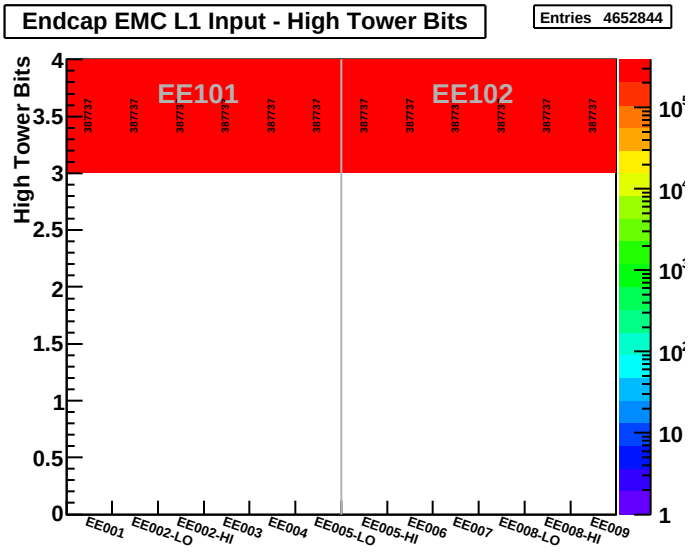

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

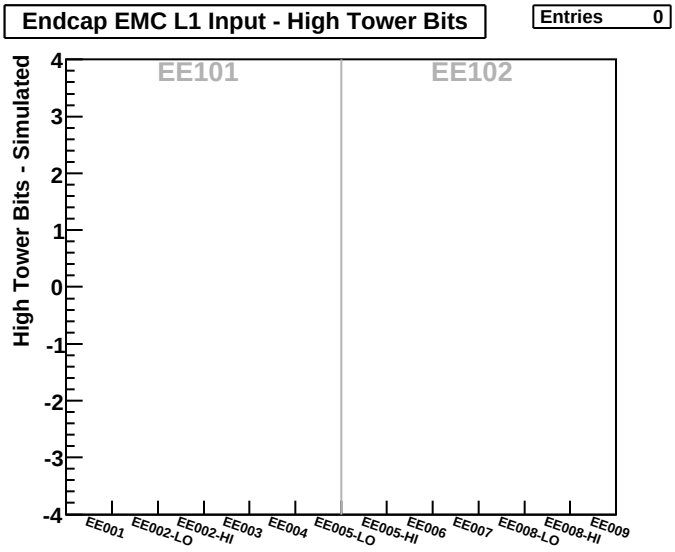

Endcap EMC L1 Input - Low Eta Sum
Entries 4652844
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

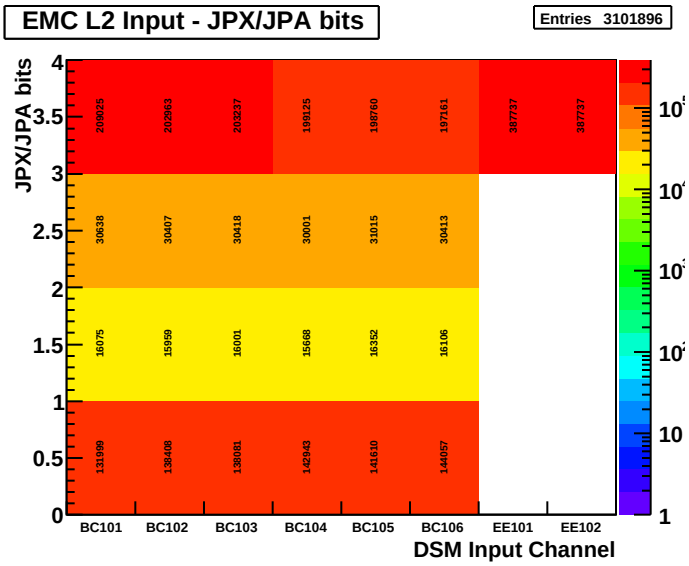

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

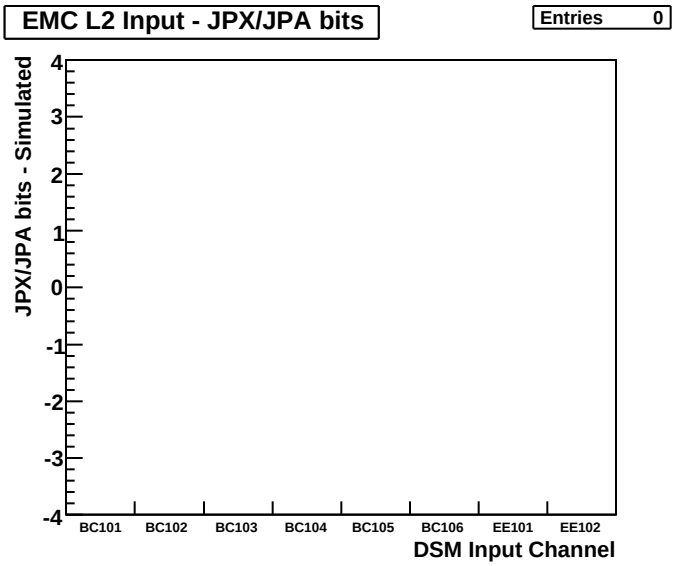

Endcap EMC L1 Input - High Eta Sum
Entries 4652844
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

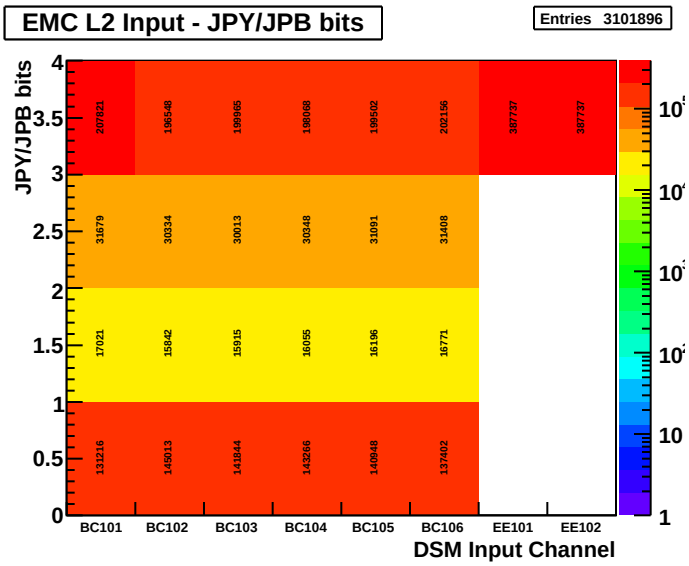

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

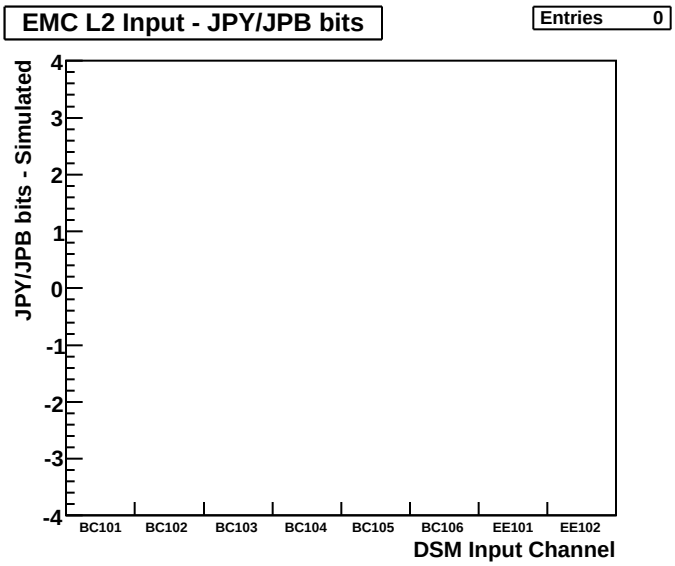

Endcap EMC L1 Input - High Tower Bits
Entries 4652844
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

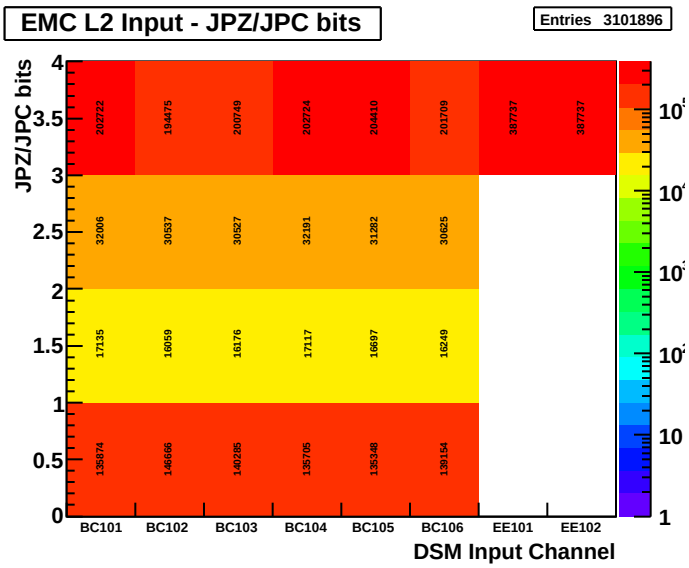

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

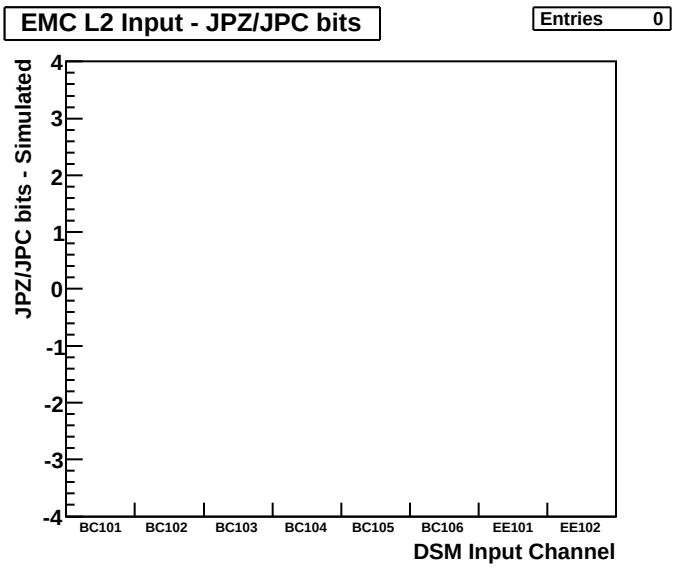

EMC L2 Input - JPX/JPA bits
Entries 3101896
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
209025 202563 203237 199125 198760 197161 387737 387737
30638 30407 30418 30001 31015 30413
16075 15959 16001 15668 16352 16106
131999 138408 138081 142943 141610 144057

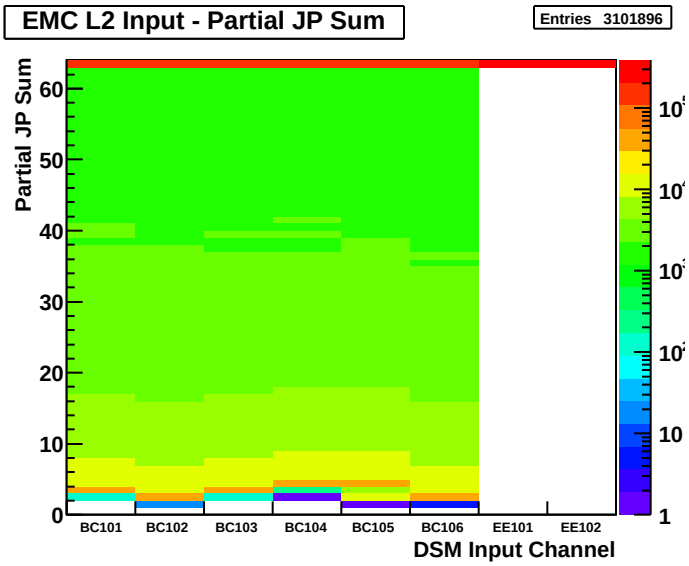

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

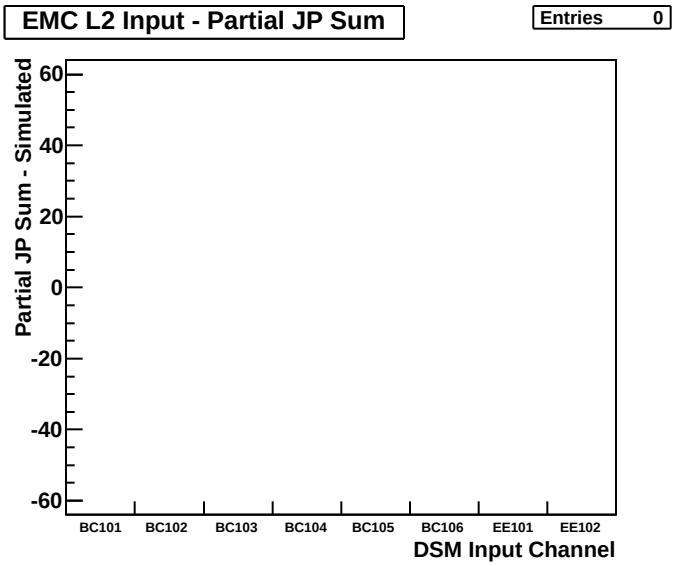

EMC L2 Input - JPY/JPB bits
Entries 3101896
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
207821 196548 199965 198088 199502 202156 387737 387737
31679 30334 30013 30348 31091 31408
17021 15842 15915 16055 16196 16771
133216 145013 141844 143266 140948 137402

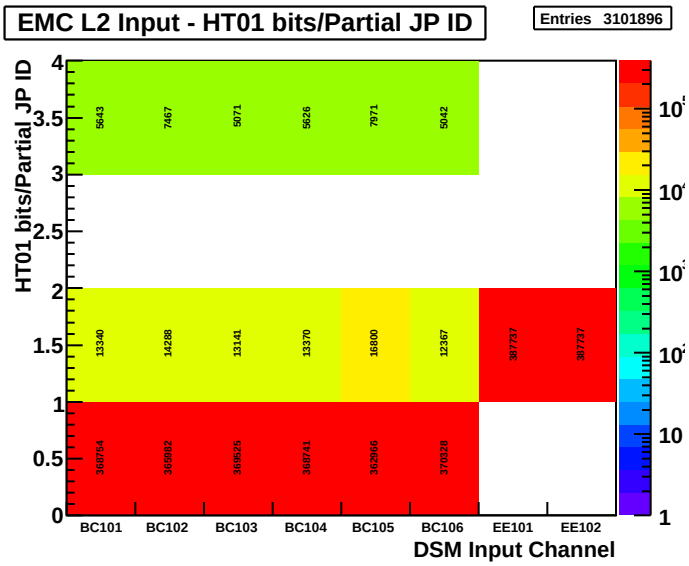

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

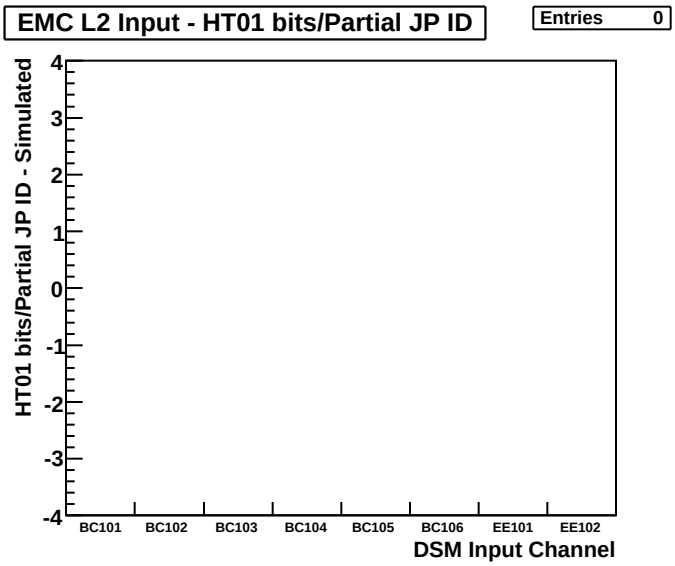

EMC L2 Input - JPZ/JPC bits
Entries 3101896
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
202722 194475 200749 202724 204410 201709 387737 387737
32006 30537 30527 32191 31282 30625
17135 16059 16176 17117 16697 16249
135874 146566 140285 135705 135348 139154

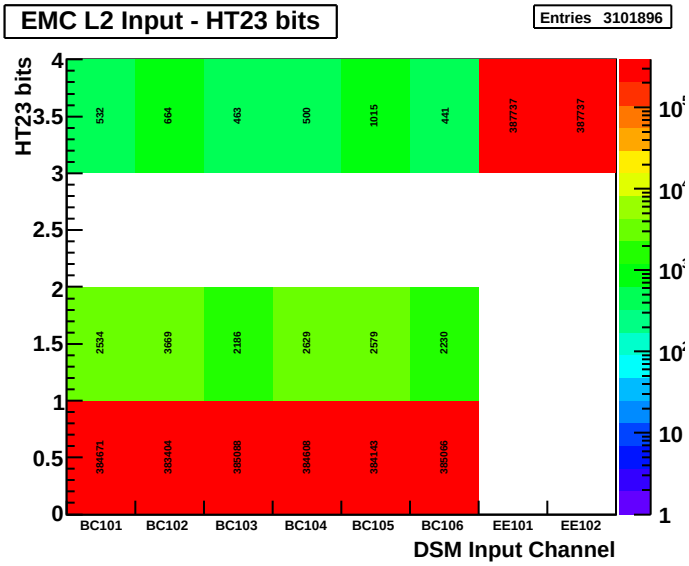

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

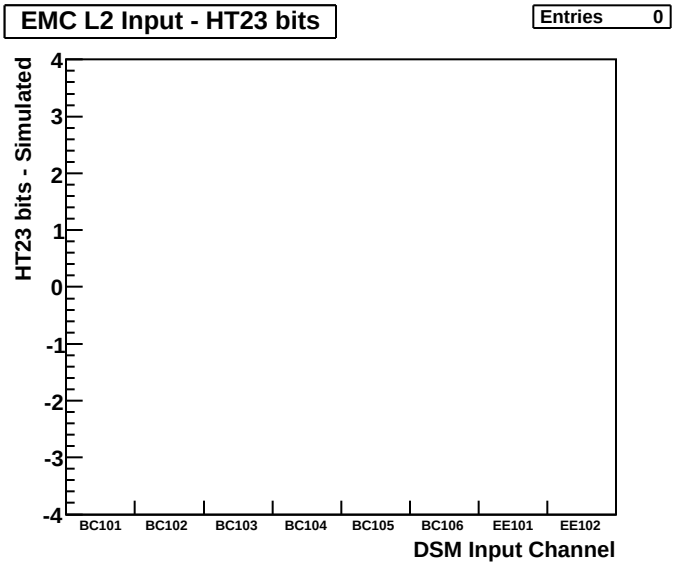

EMC L2 Input - Partial JP Sum
Entries 3101896
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

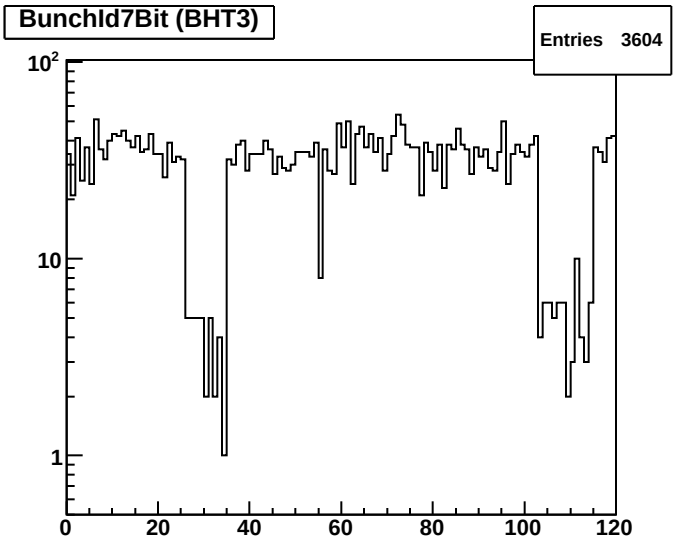

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

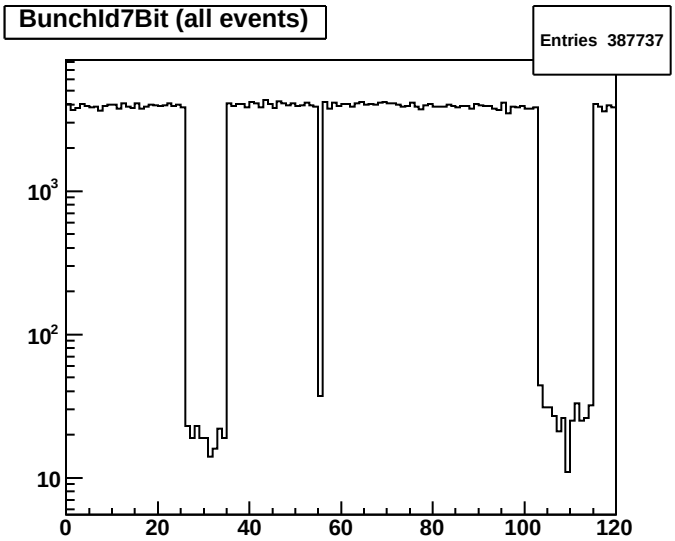

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3101896
HT01 bits/Partial JP ID
5643 7467 5071 5626 7971 5042
13340 14288 13141 13370 16800 12367 387737 387737
366754 366982 366525 366741 362966 370328
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

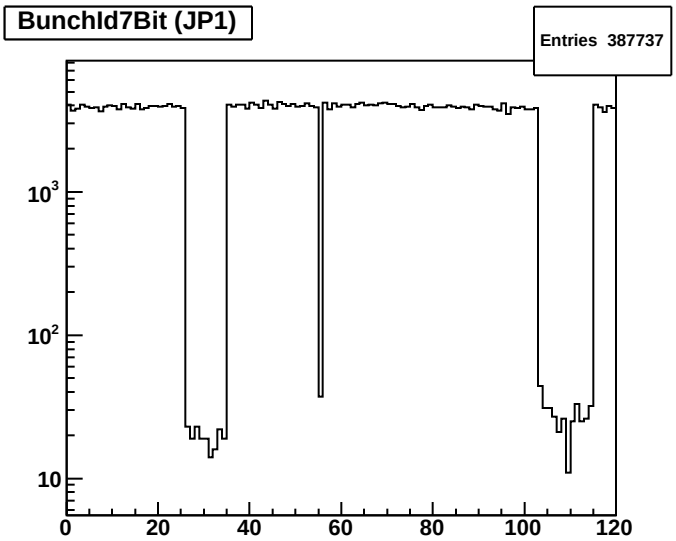

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

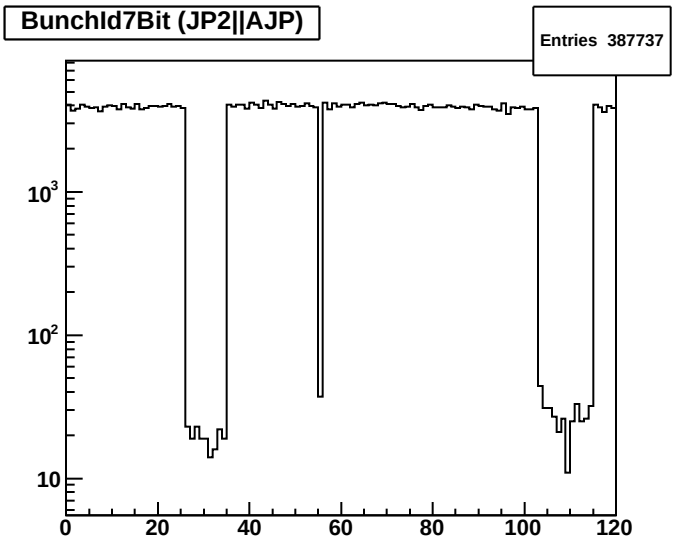

EMC L2 Input - HT23 bits
Entries 3101896
HT23 bits
532 664 463 500 1015 441 387737 387737
2534 3669 2186 2629 2579 2230
384671 383404 385088 384608 384143 385066
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

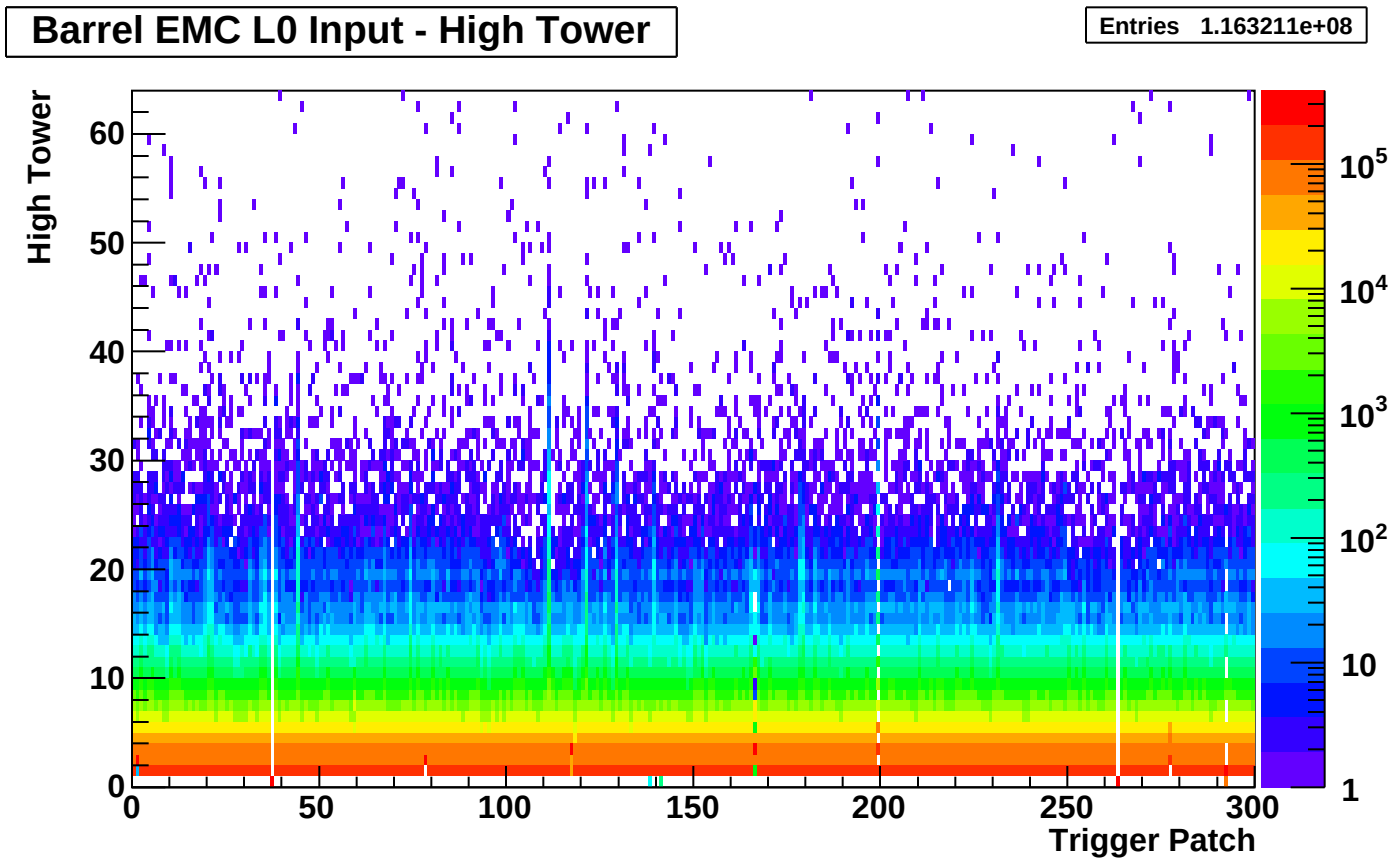

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

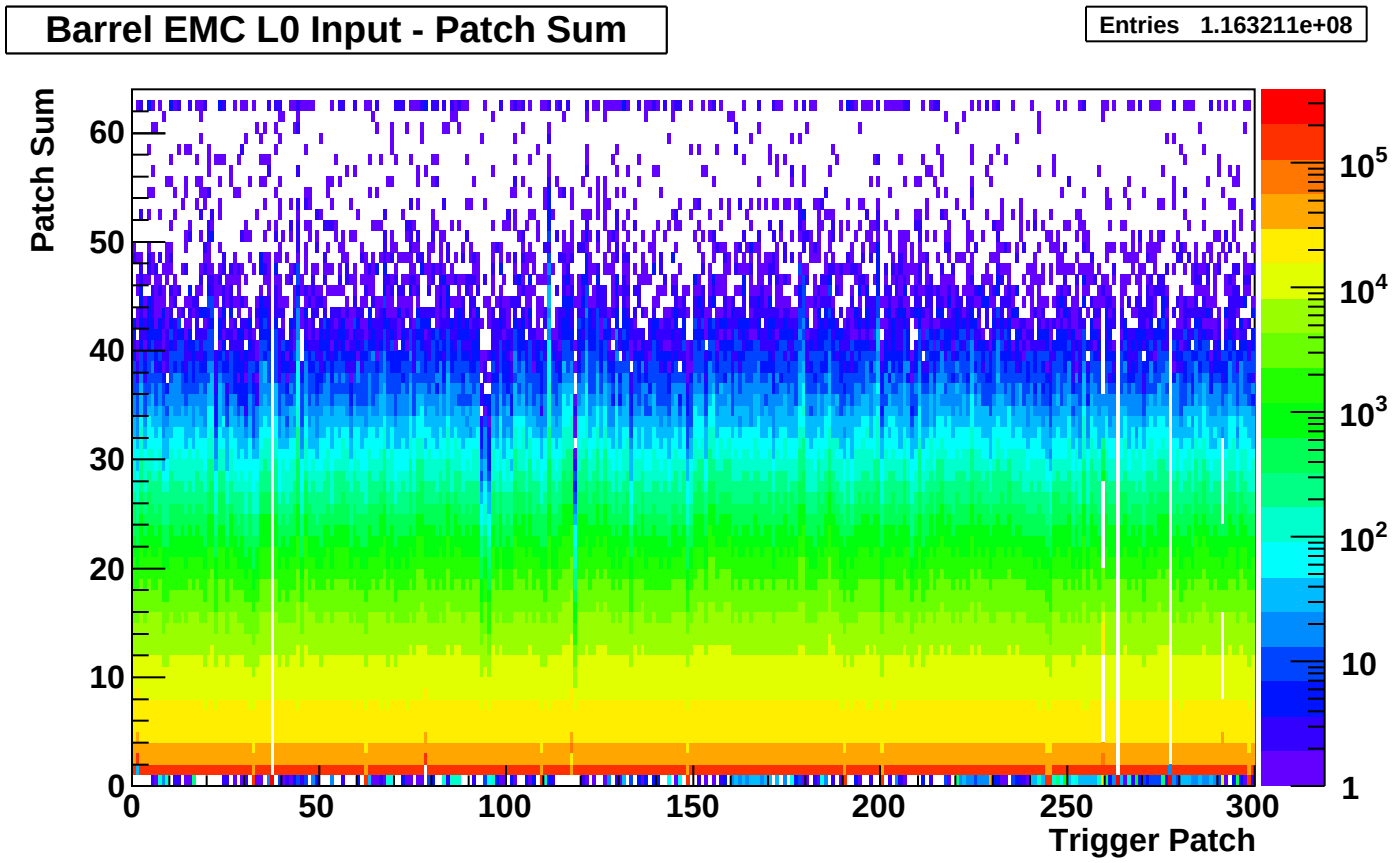

BunchId7Bit (BHT3)
Entries 3604
10^2
10
1
0
20
40
60
80
100
120

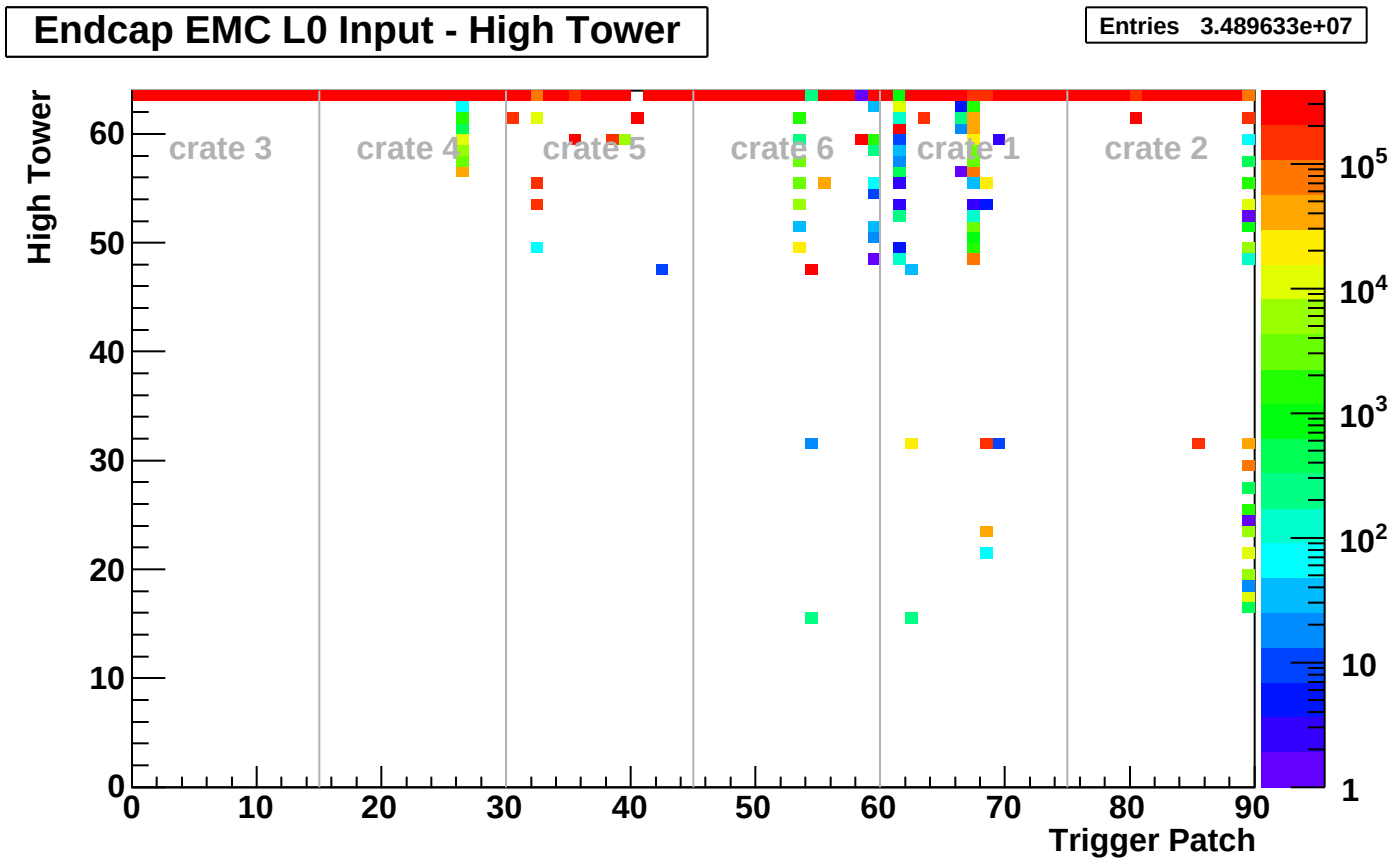

BunchId7Bit (all events)
Entries 387737
10^3
10^2
10
0
20
40
60
80
100
120

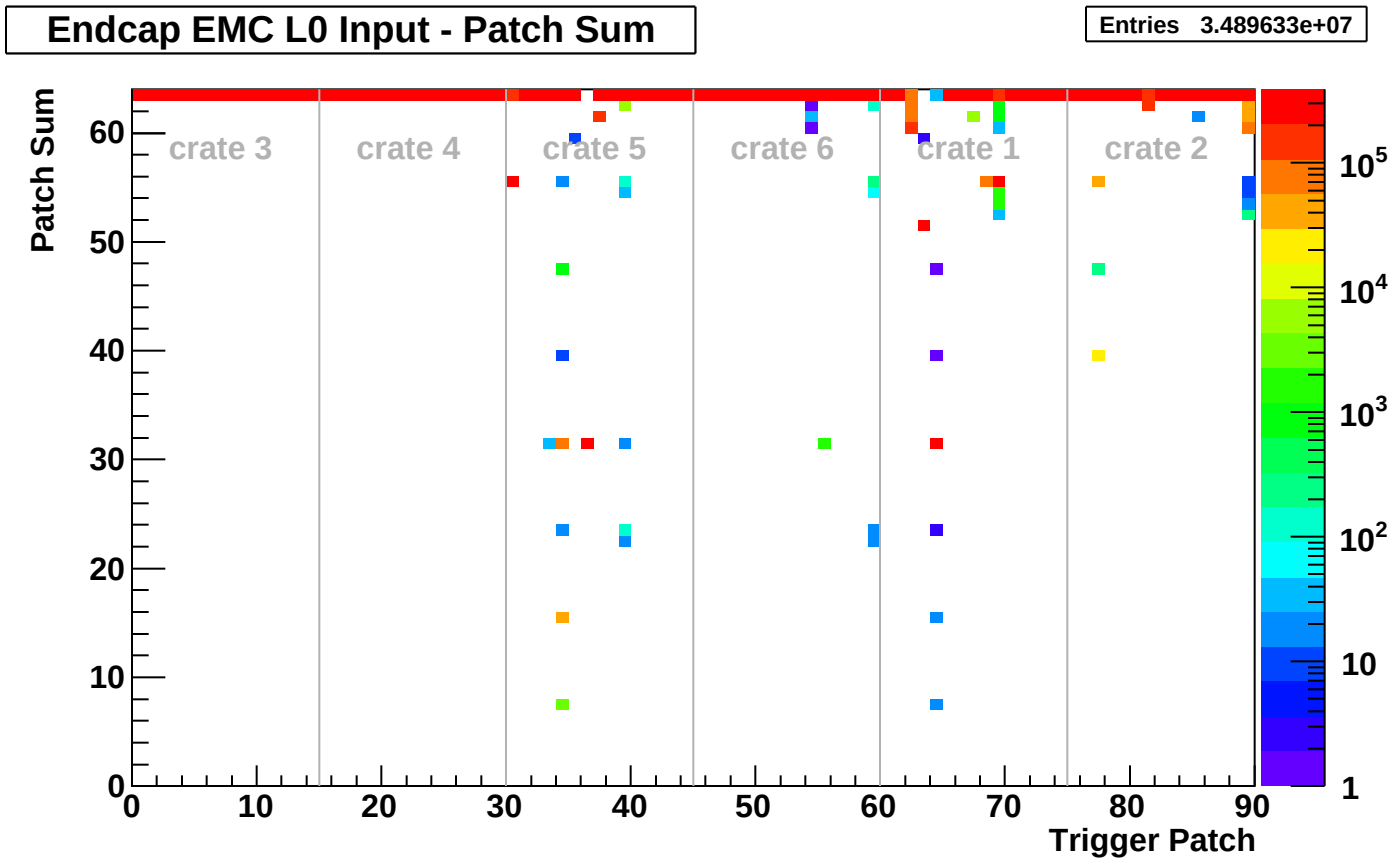

BunchId7Bit (JP1)
Entries 387737
10^3
10^2
10
0
20
40
60
80
100
120

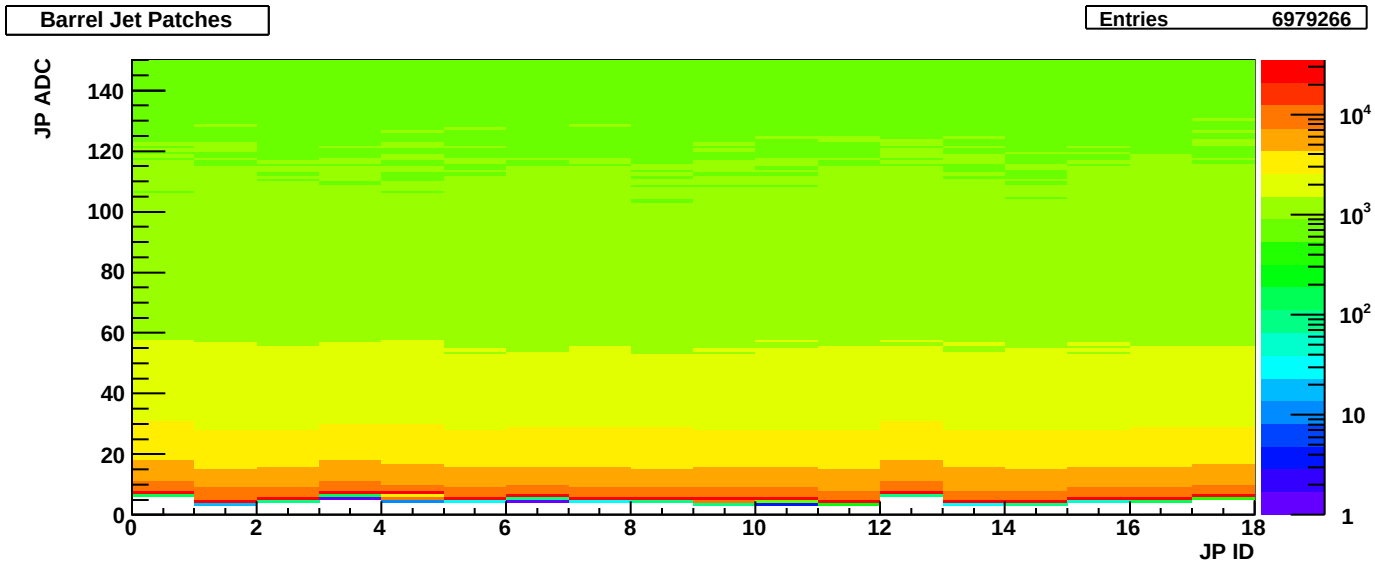

BunchId7Bit (JP2||AJP)
Entries 387737
10^3
10^2
10
0
20
40
60
80
100
120

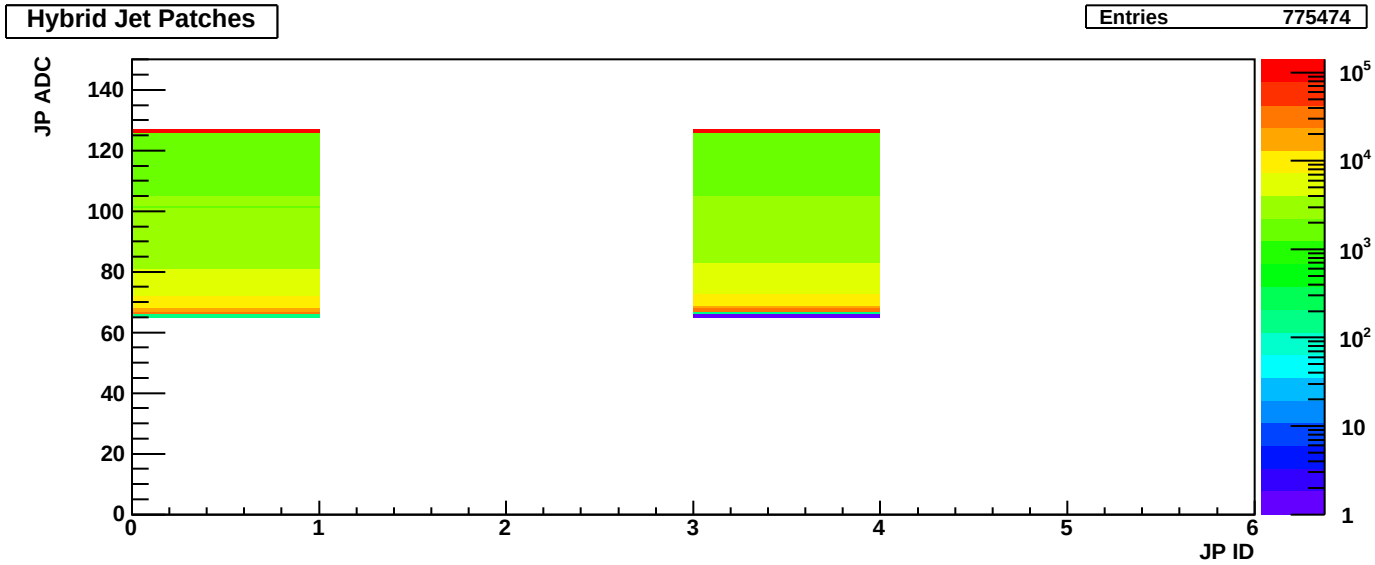

Barrel EMC L0 Input - High Tower
Entries 1.163211e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

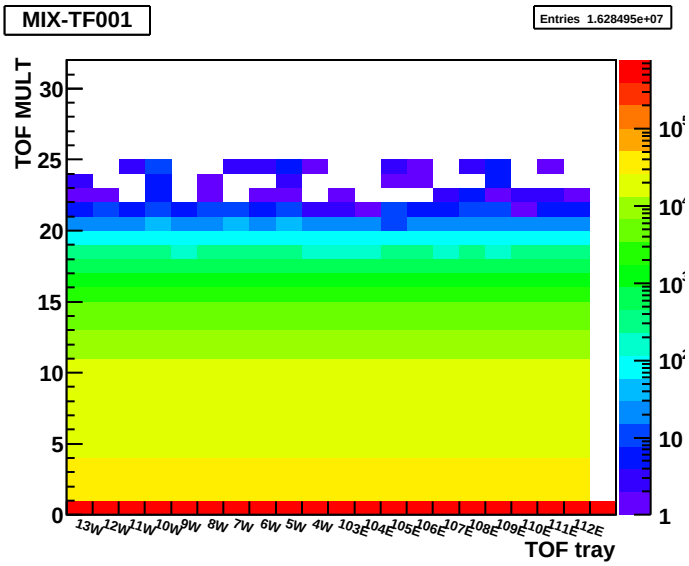

Barrel EMC L0 Input - Patch Sum
Entries 1.163211e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

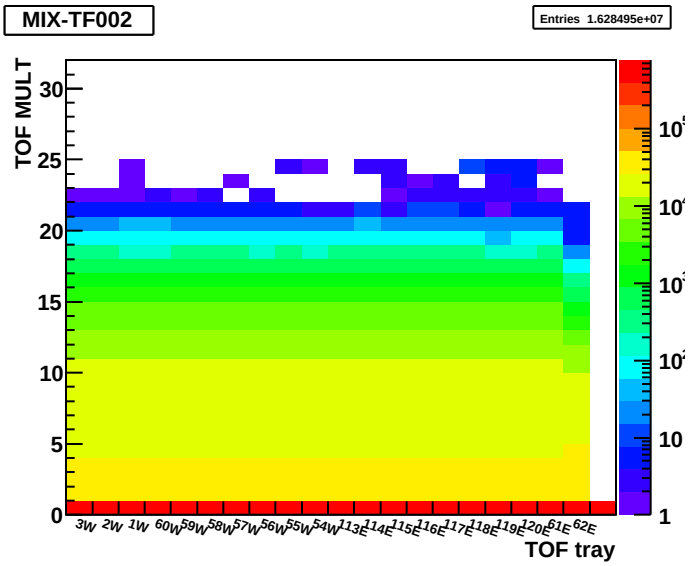

Endcap EMC L0 Input - High Tower
Entries 3.489633e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

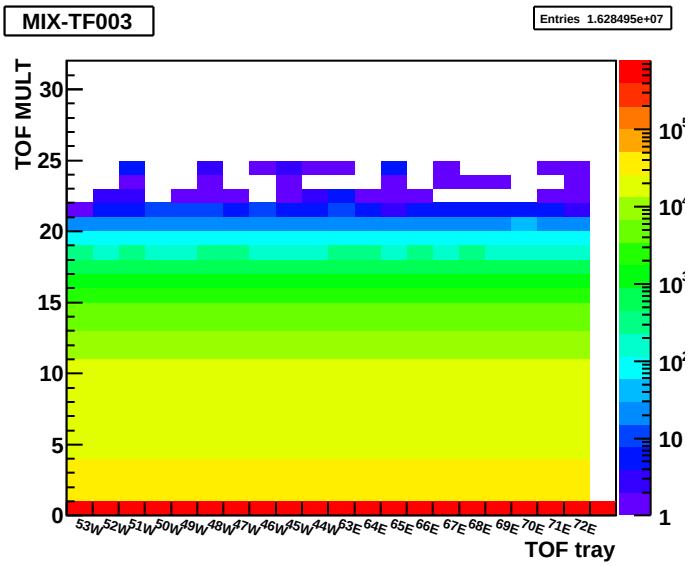

Endcap EMC L0 Input - Patch Sum
Entries 3.489633e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

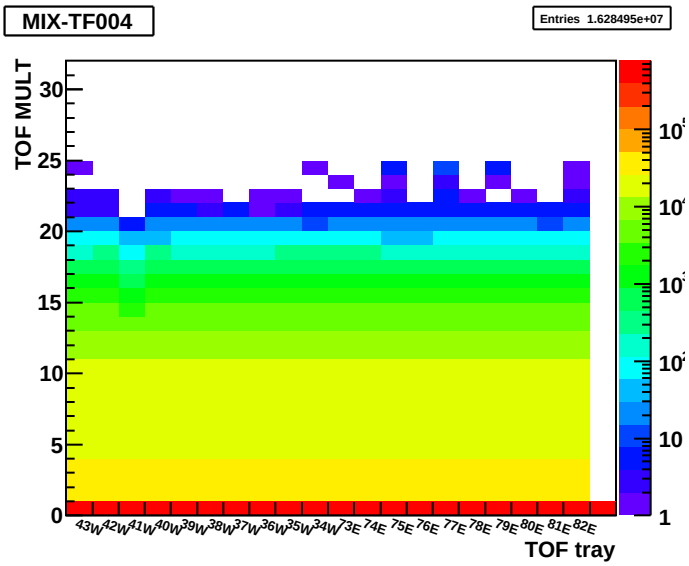

Barrel Jet Patches
Entries 6979266
JP ADC
JP ID

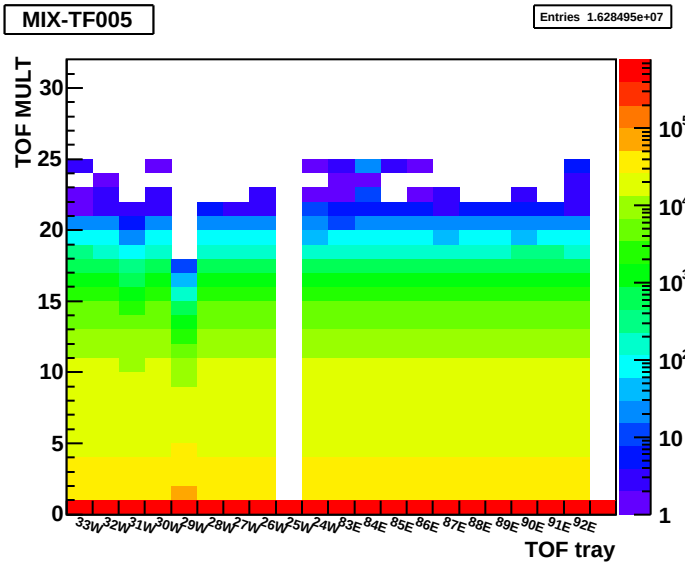

Hybrid Jet Patches
Entries 775474
JP ADC
JP ID

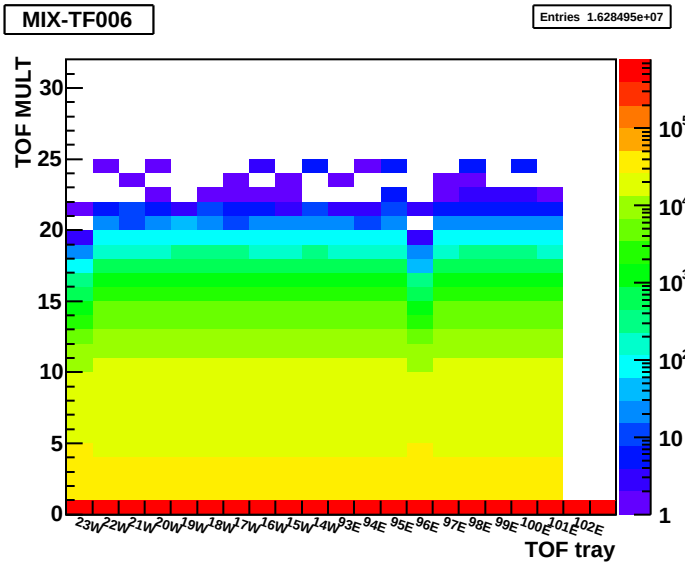

MIX-TF001
Entries 1.628495e+07
TOF MULT
TOF tray

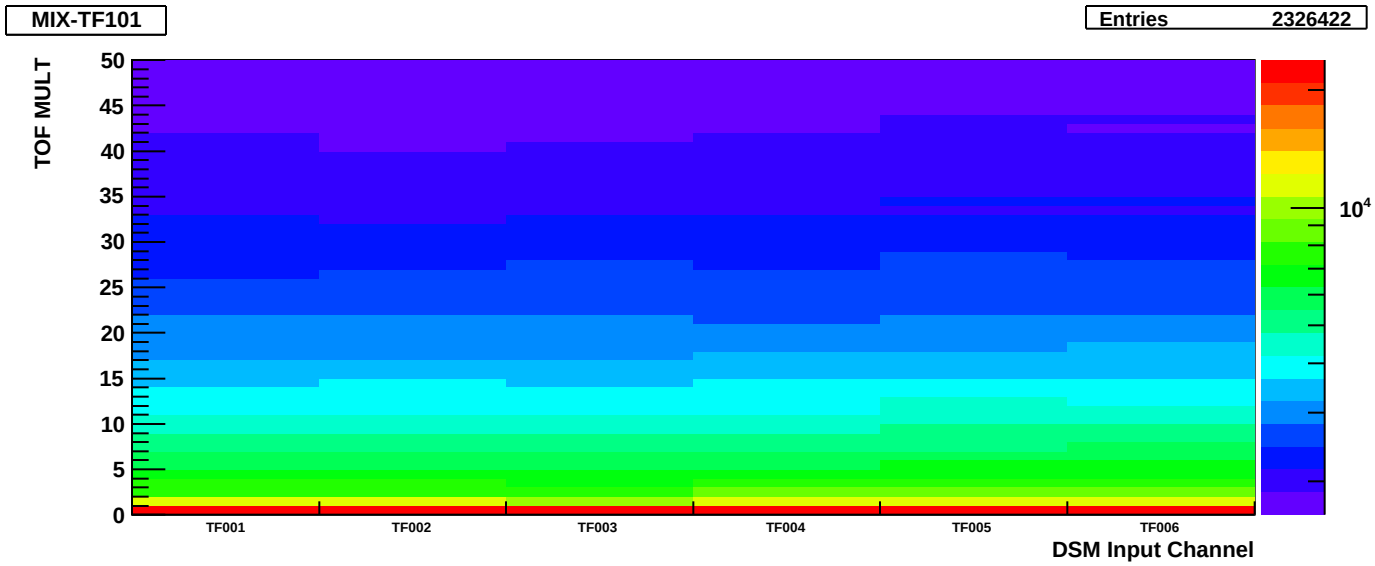

MIX-TF002
Entries 1.628495e+07
TOF MULT
TOF tray

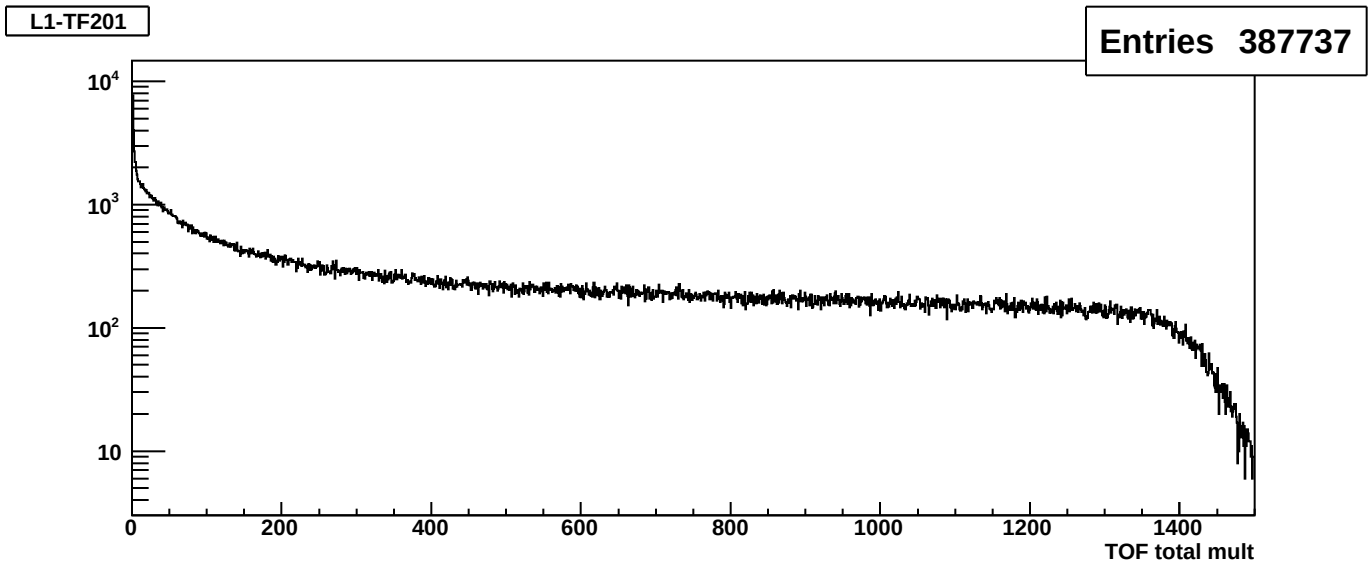

MIX-TF003
Entries 1.628495e+07
TOF MULT
TOF tray

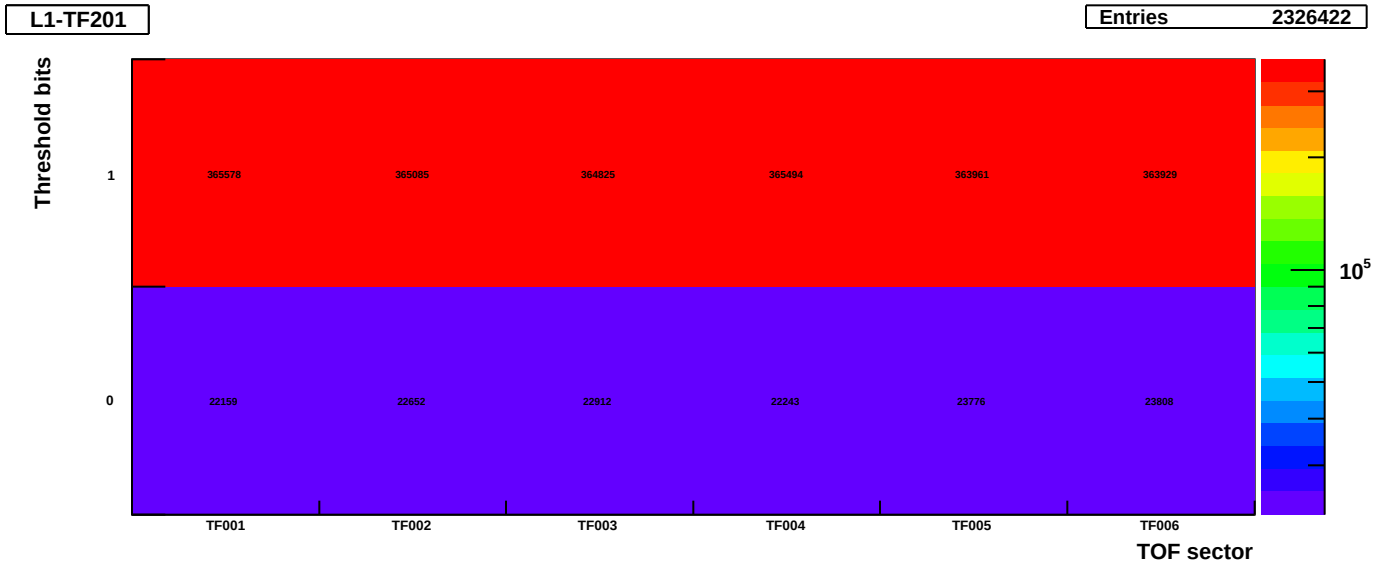

MIX-TF004
Entries 1.628495e+07
TOF MULT
TOF tray

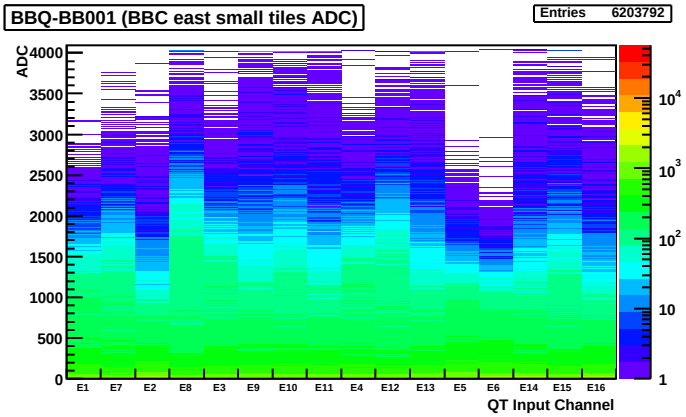

MIX-TF005
Entries 1.628495e+07
TOF MULT
TOF tray

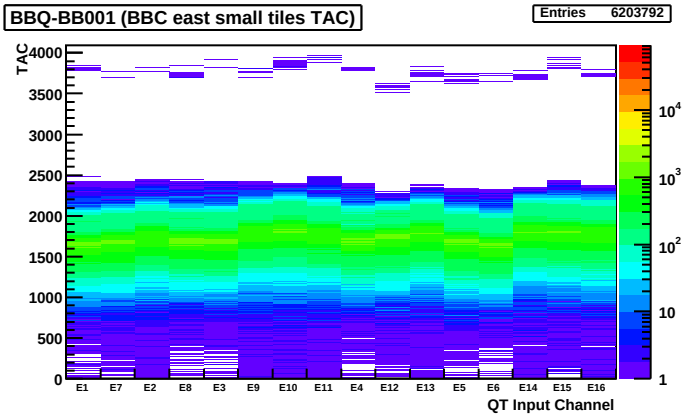

MIX-TF006
Entries 1.628495e+07
TOF MULT
TOF tray

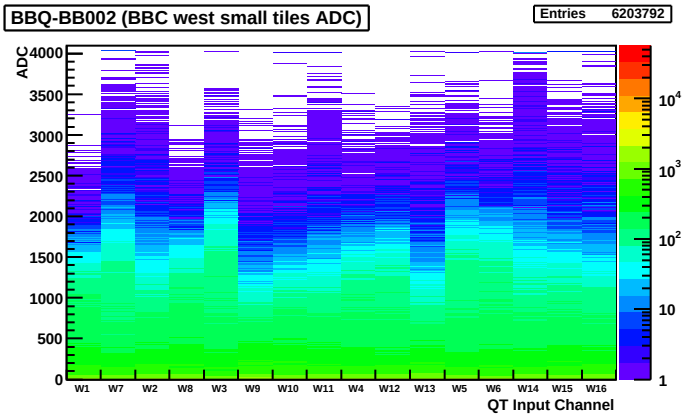

MIX-TF101
Entries 2326422
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

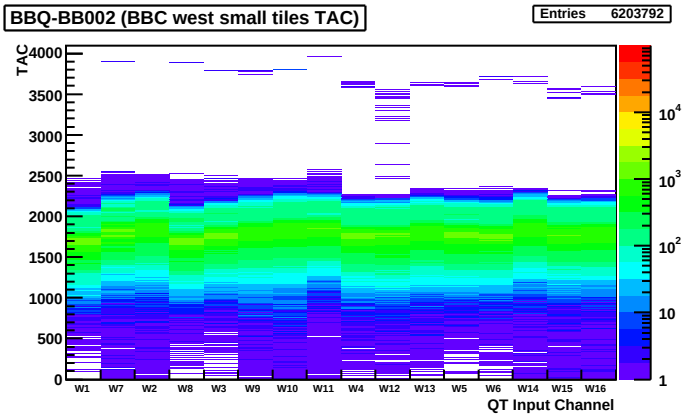

L1-TF201
Entries 387737
10^4
10^3
10^2
10
0
200
400
600
800
1000
1200
1400
TOF total mult

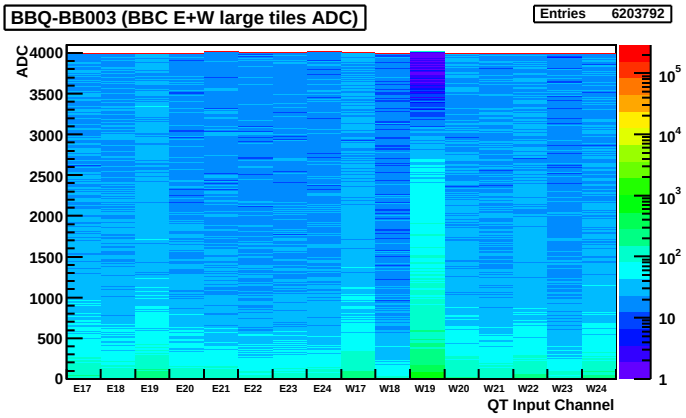

L1-TF201
Entries 2326422
Threshold bits
1
0
365578
365085
364825
365494
363961
363929
22159
22652
22912
22243
23776
23808
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

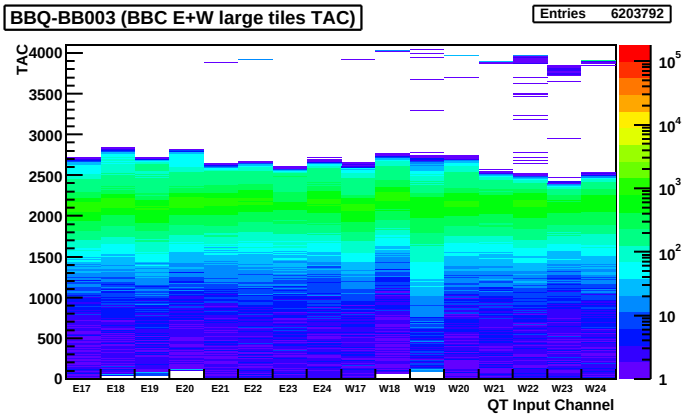

BBQ-BB001 (BBC east small tiles ADC)
Entries 6203792
ADC
QT Input Channel

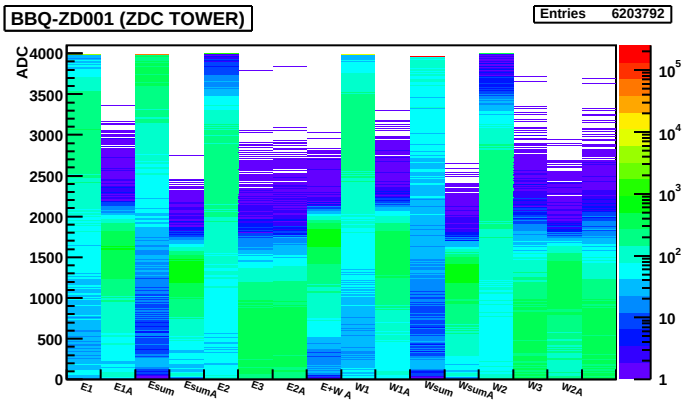

BBQ-BB001 (BBC east small tiles TAC)
Entries 6203792
TAC
QT Input Channel

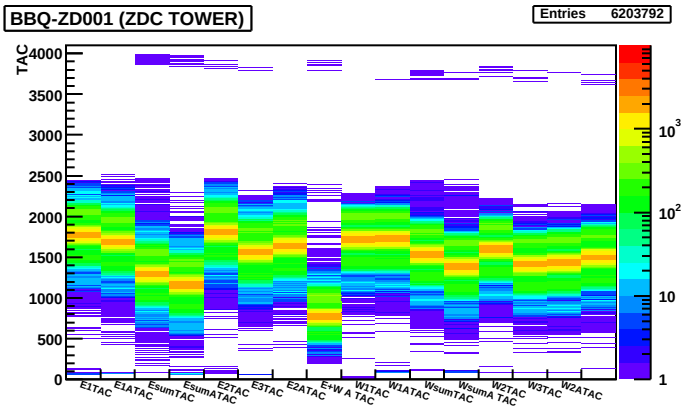

BBQ-BB002 (BBC west small tiles ADC)
Entries 6203792
ADC
QT Input Channel

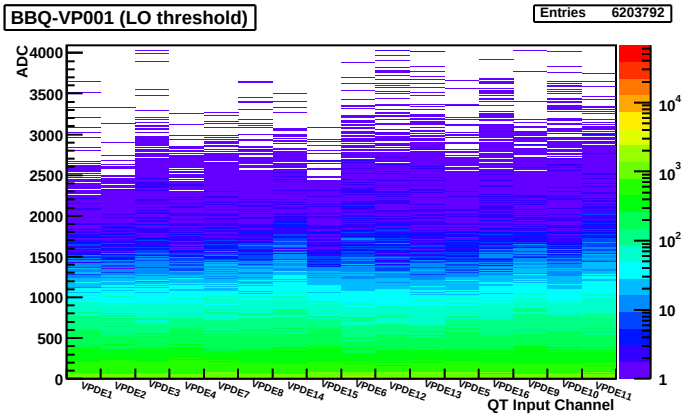

BBQ-BB002 (BBC west small tiles TAC)
Entries 6203792
TAC
QT Input Channel

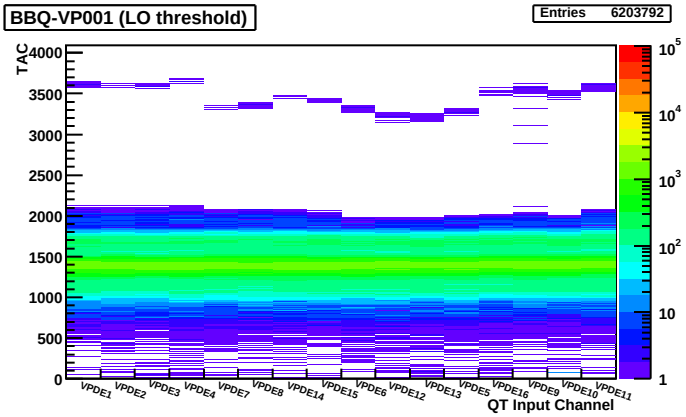

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 6203792
ADC
QT Input Channel

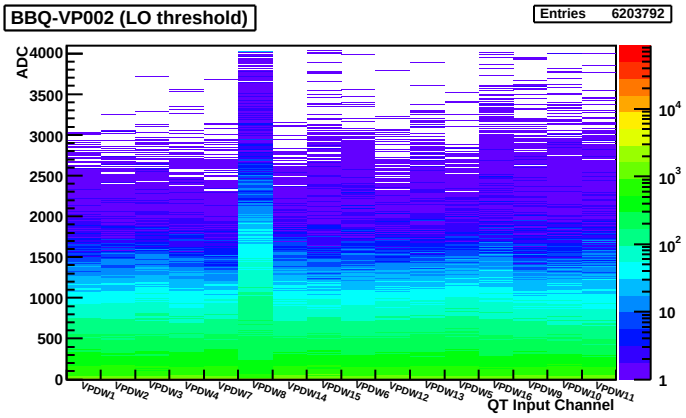

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 6203792
TAC
QT Input Channel

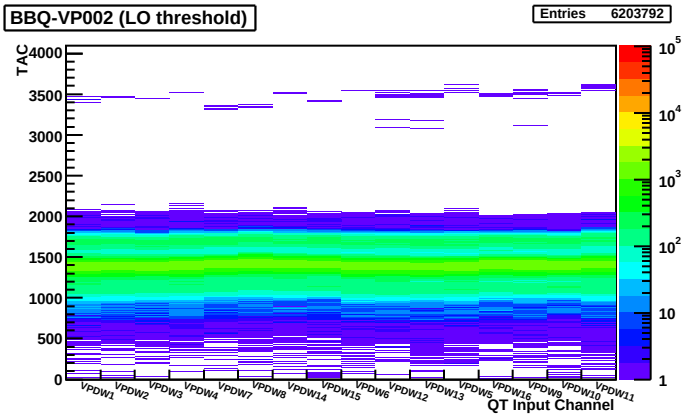

BBQ-ZD001 (ZDC TOWER)
Entries 6203792
ADC

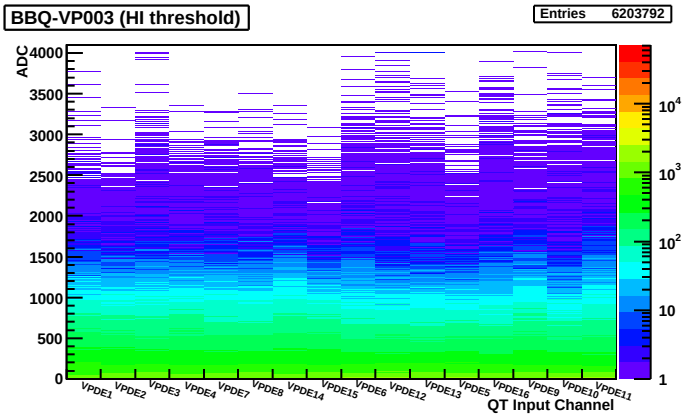

BBQ-ZD001 (ZDC TOWER)
Entries 6203792
TAC

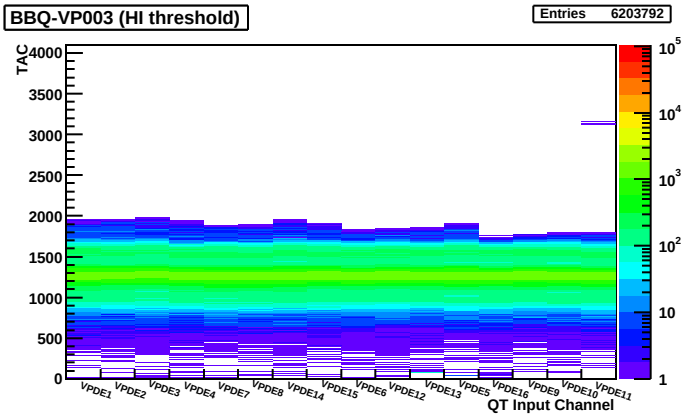

BBQ-VP001 (LO threshold)
Entries 6203792
ADC
QT Input Channel

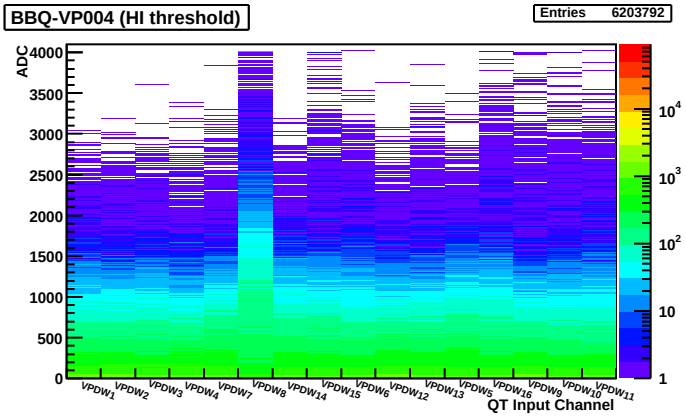

BBQ-VP001 (LO threshold)
Entries 6203792
TAC
QT Input Channel

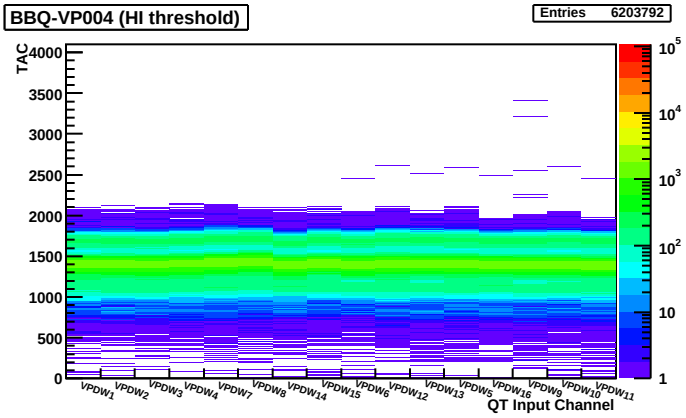

BBQ-VP002 (LO threshold)
Entries 6203792
ADC
QT Input Channel

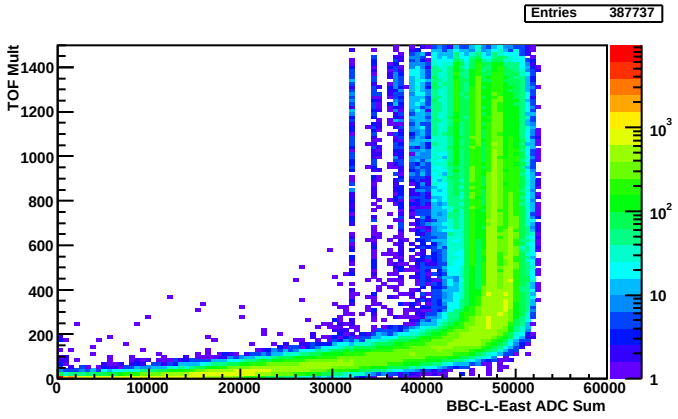

BBQ-VP002 (LO threshold)
Entries 6203792
TAC
QT Input Channel

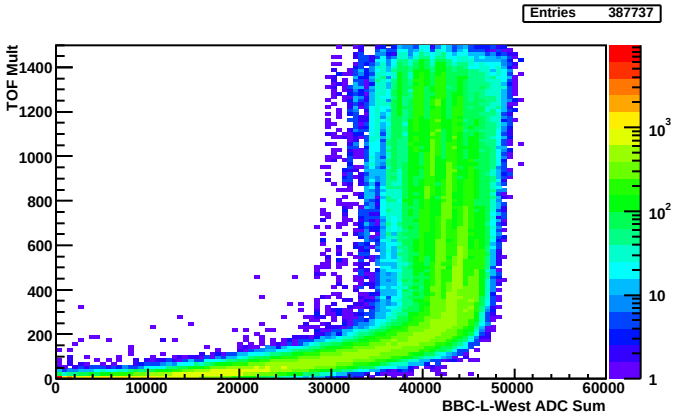

BBQ-VP003 (HI threshold)
Entries 6203792
ADC
QT Input Channel

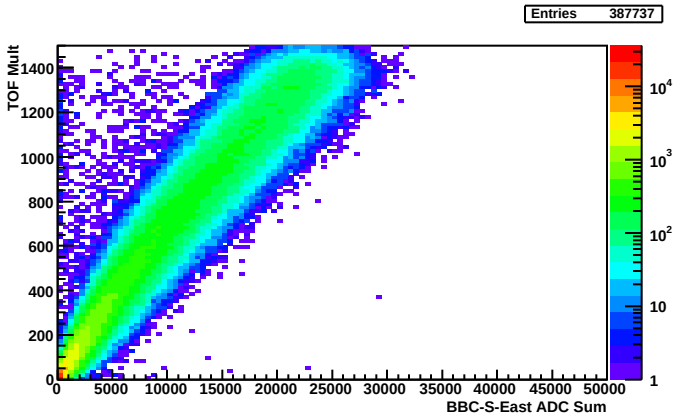

BBQ-VP003 (HI threshold)
Entries 6203792
TAC
QT Input Channel

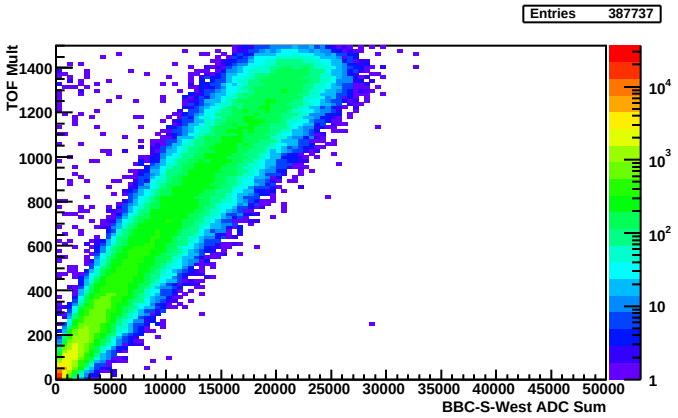

BBQ-VP004 (HI threshold)
Entries 6203792
ADC
QT Input Channel

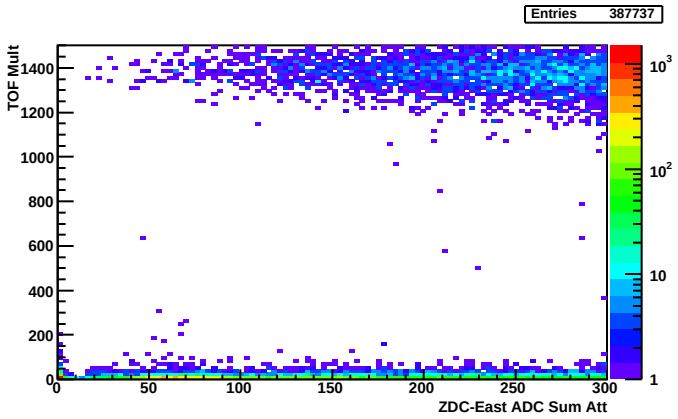

BBQ-VP004 (HI threshold)
Entries 6203792
TAC
QT Input Channel

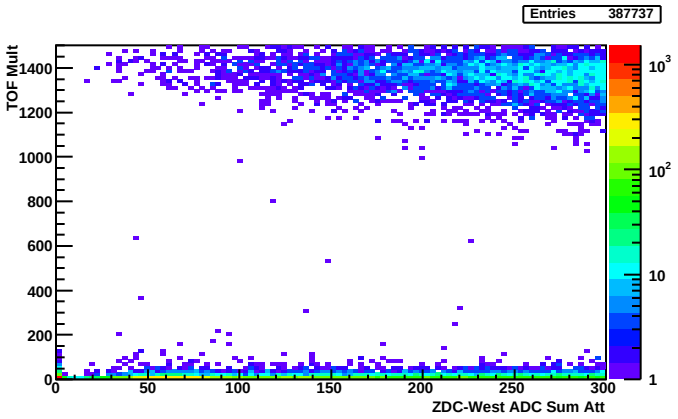

Entries 387737
TOF Mult
BBC-L-East ADC Sum

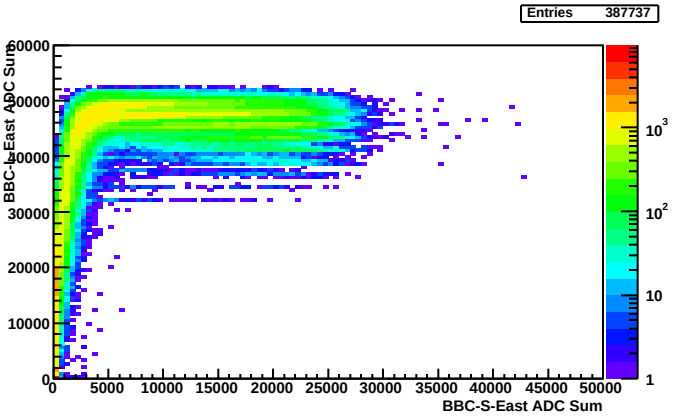

Entries 387737
TOF Mult
BBC-L-West ADC Sum

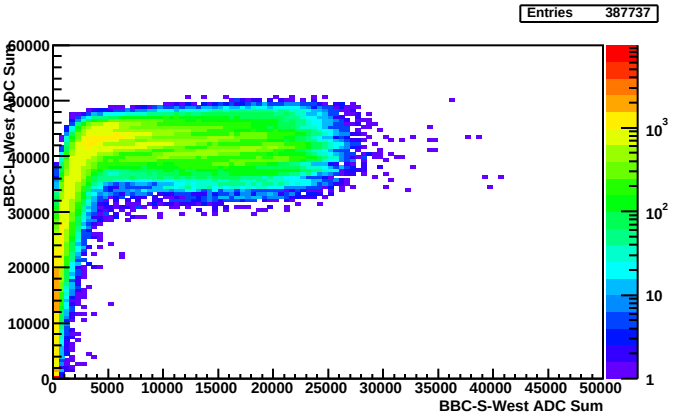

Entries 387737
TOF Mult
BBC-S-East ADC Sum

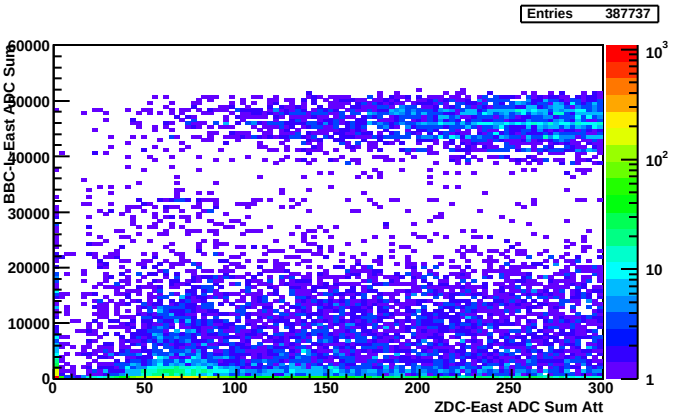

Entries 387737
TOF Mult
BBC-S-West ADC Sum

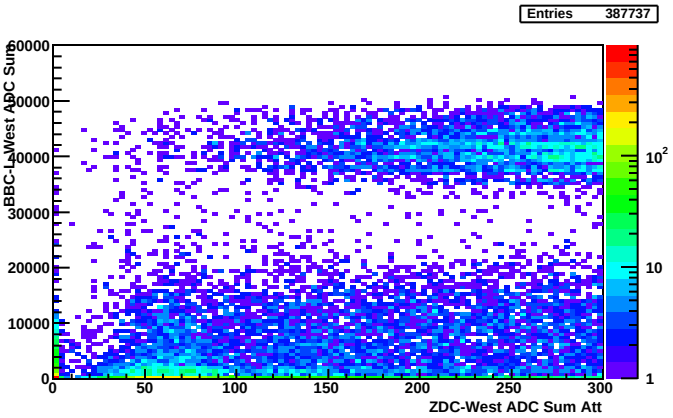

Entries 387737
TOF Mult
ZDC-East ADC Sum Att

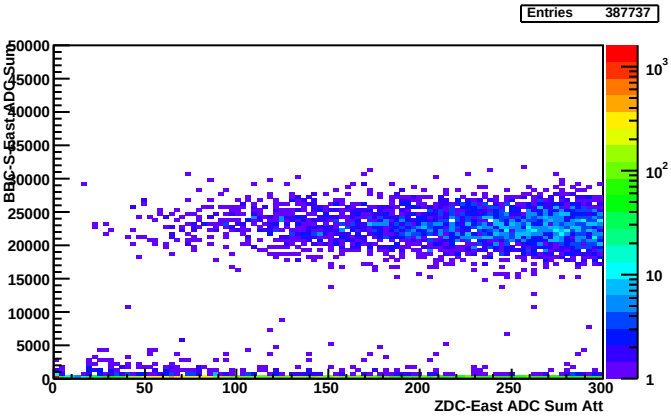

Entries 387737
TOF Mult
ZDC-West ADC Sum Att


Entries 387737
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 387737
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 387737
BBC-L-East ADC Sum
ZDC-East ADC Sum Att


Entries 387737
BBC-L-West ADC Sum
ZDC-West ADC Sum Att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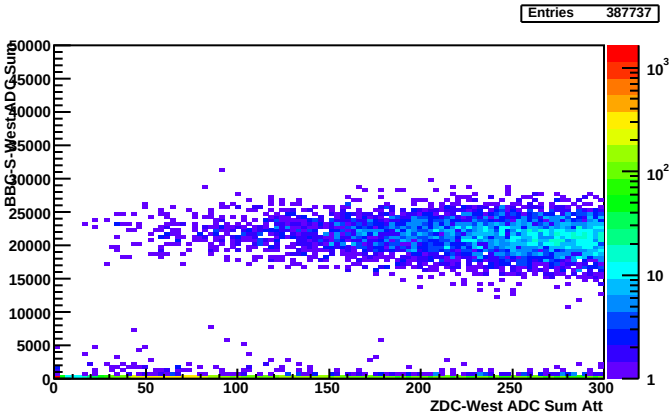
BBC-S-East ADC Sum
ZDC-East ADC Sum Att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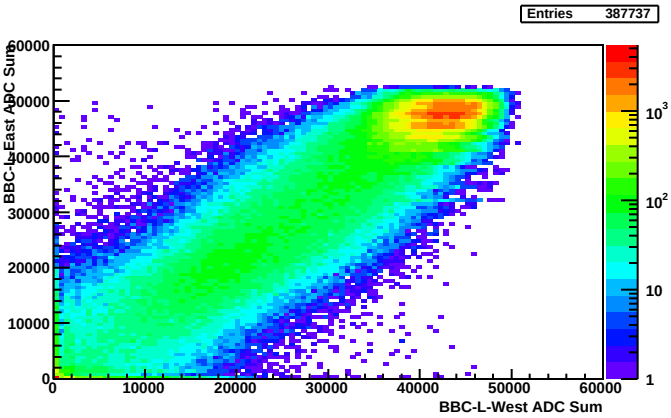
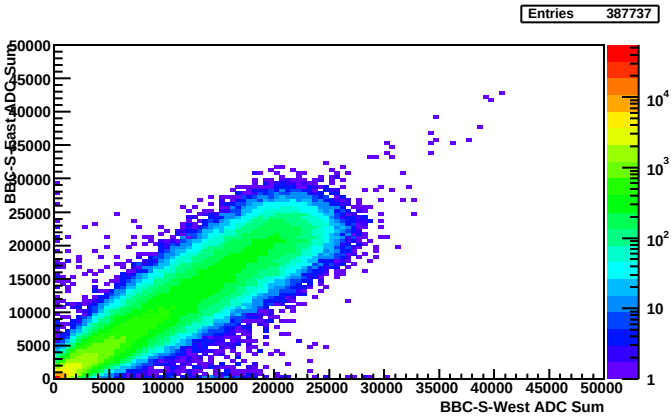
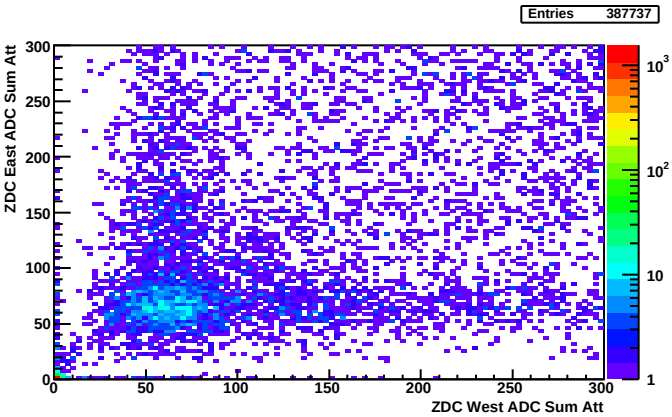
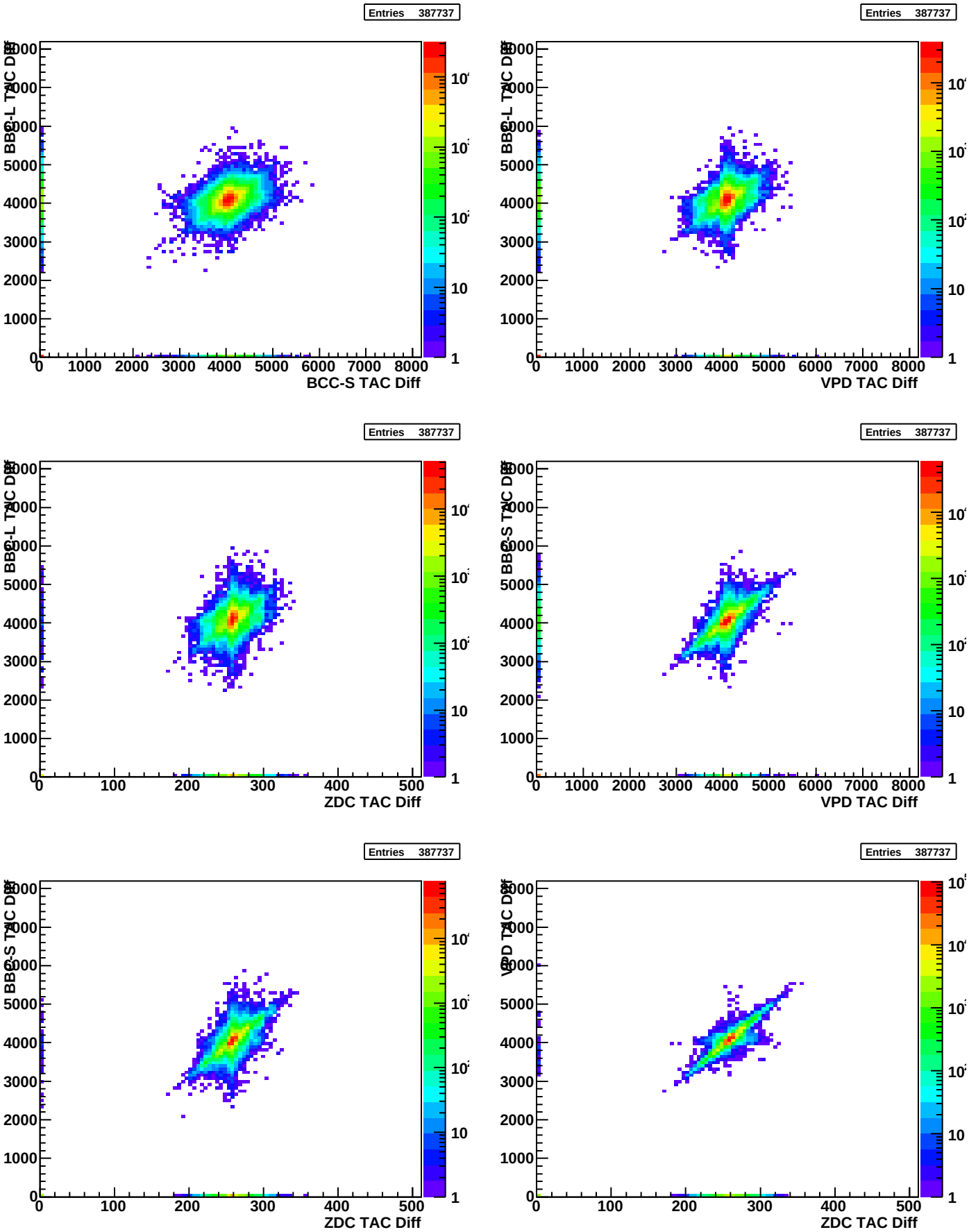
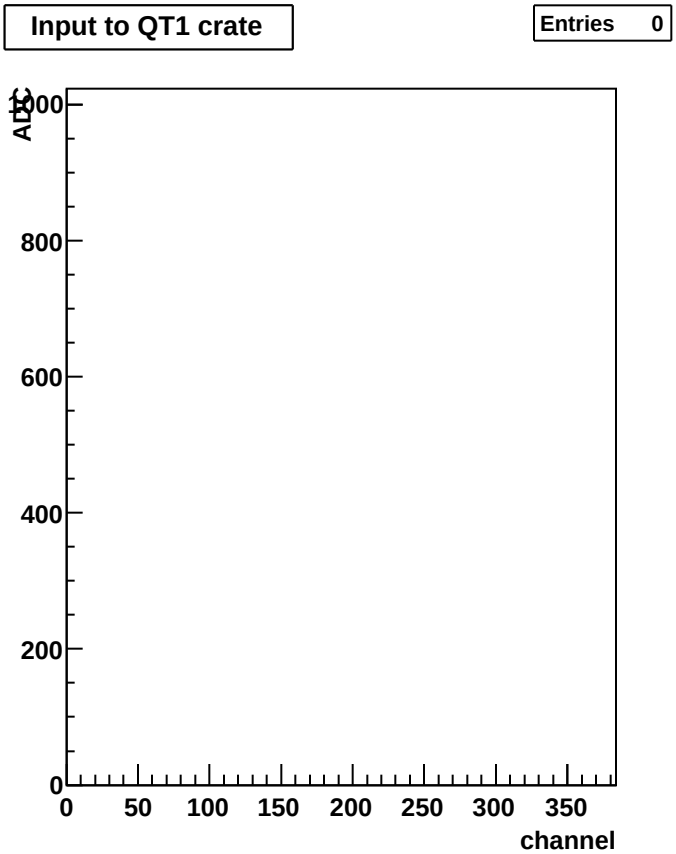
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

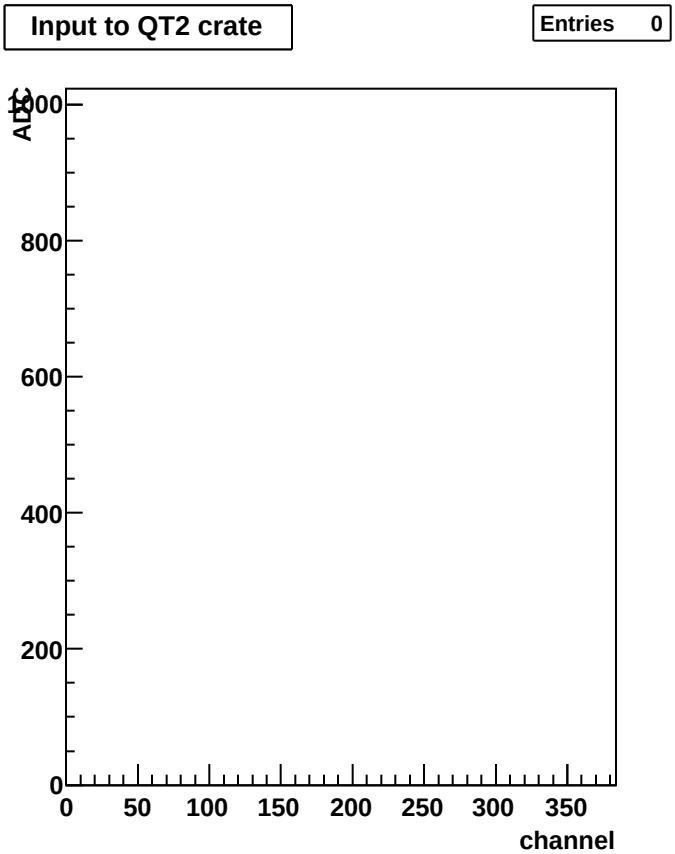

Entries 387737
BBC-L-East ADC Sum
BBC-L-West ADC Sum

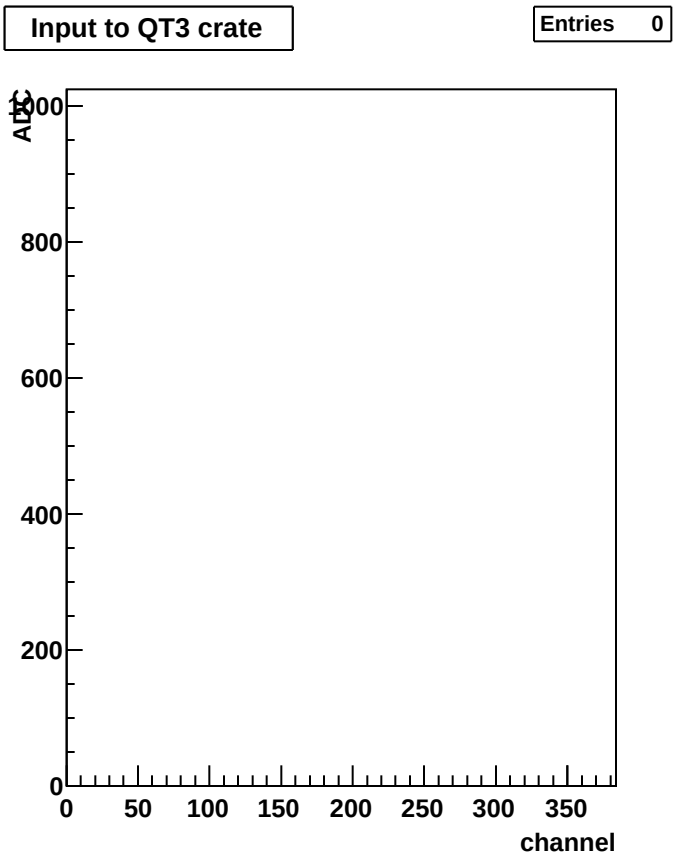

Entries 387737
BBC-S-East ADC Sum
BBC-S-West ADC Sum

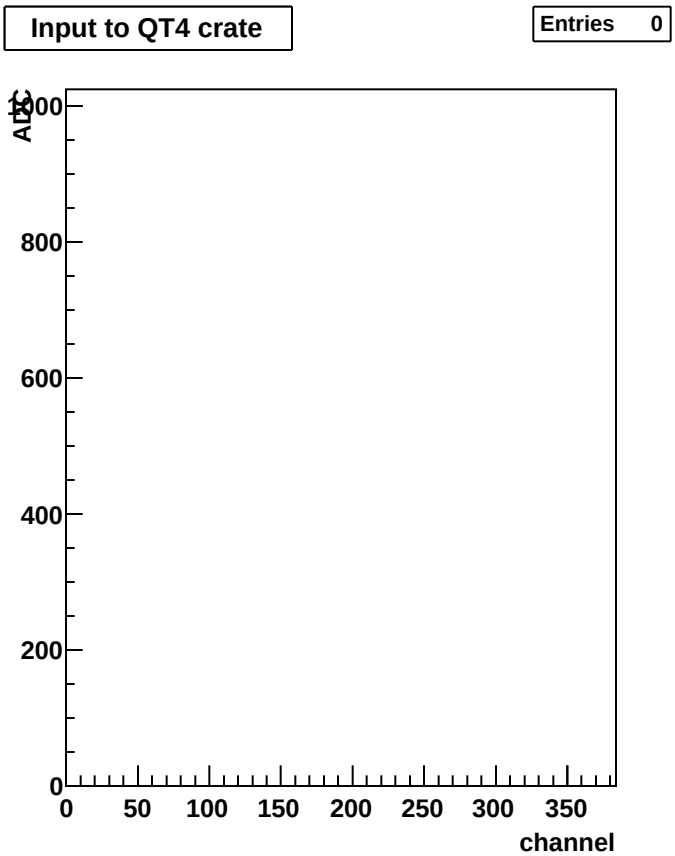

Entries 387737
ZDC East ADC Sum Att
ZDC West ADC Sum Att

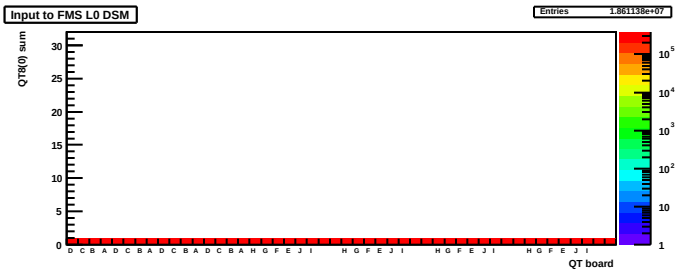

Entries 387737
BBC-L TAC Diff
BCC-S TAC Diff
Entries 387737
BBC-L TAC Diff
VPD TAC Diff
Entries 387737
BBC-L TAC Diff
ZDC TAC Diff
Entries 387737
BBC-S TAC Diff
VPD TAC Diff
Entries 387737
BBC-S TAC Diff
ZDC TAC Diff
Entries 387737
VPD TAC Diff
ZDC TAC Diff

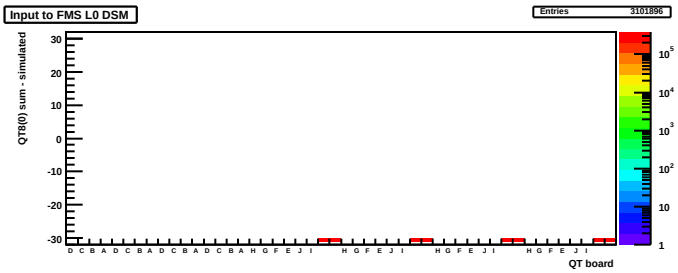

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

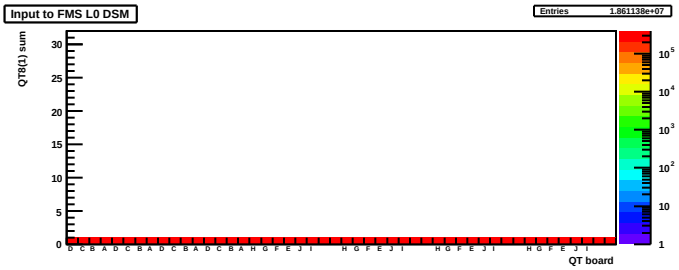

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

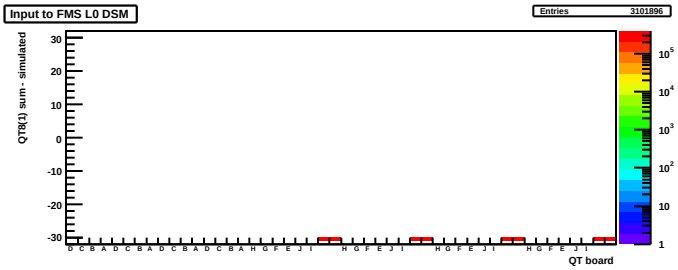

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

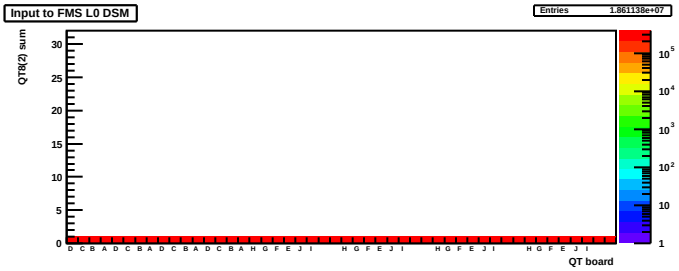

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

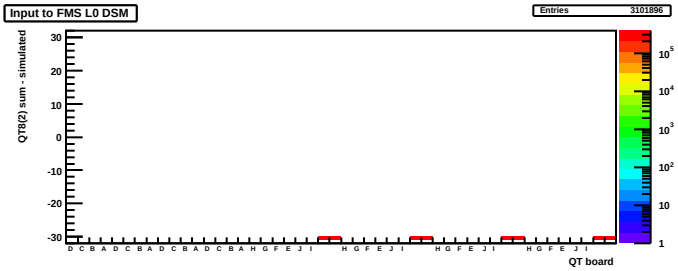

Input to FMS L0 DSM
Entries 1.861138e+07
QT8(0) sum
QT board

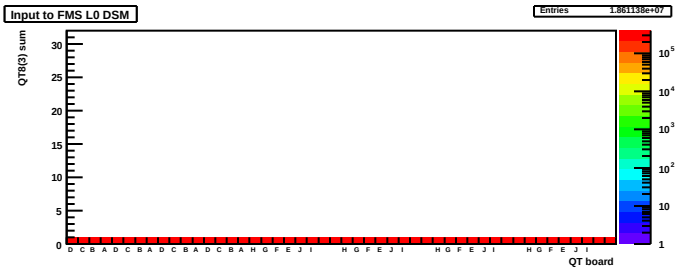

Input to FMS L0 DSM
Entries 3101896
QT8(0) sum - simulated
QT board

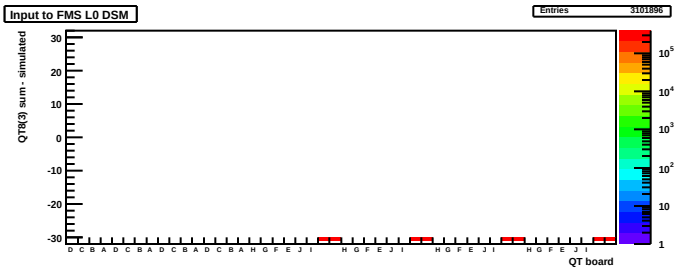

Input to FMS L0 DSM
Entries 1.861138e+07
QT8(1) sum
QT board

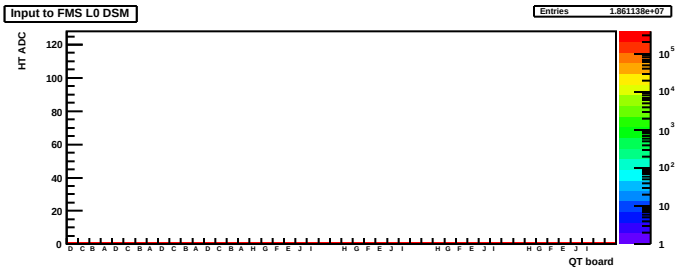

Input to FMS L0 DSM
Entries 3101896
QT8(1) sum - simulated
QT board

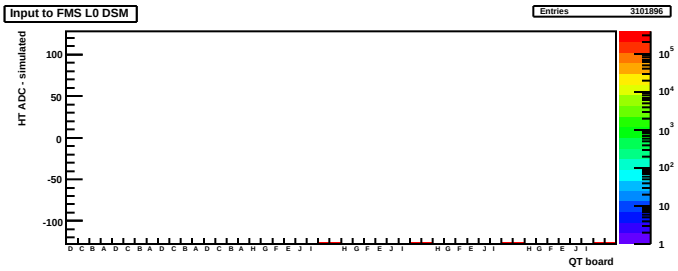

Input to FMS L0 DSM
Entries 1.861138e+07
QT8(2) sum
QT board

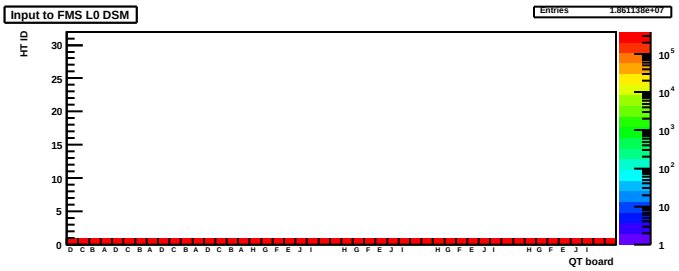

Input to FMS L0 DSM
Entries 3101896
QT8(2) sum - simulated
QT board

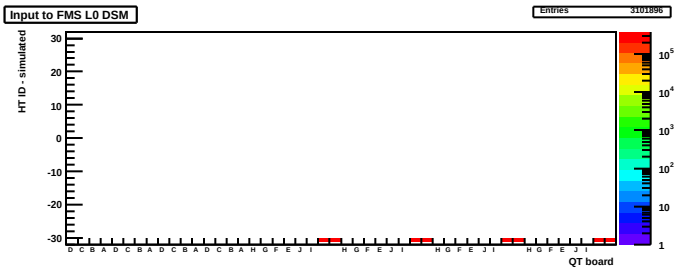

Input to FMS L0 DSM
Entries 1.861138e+07
QT8(3) sum
QT board

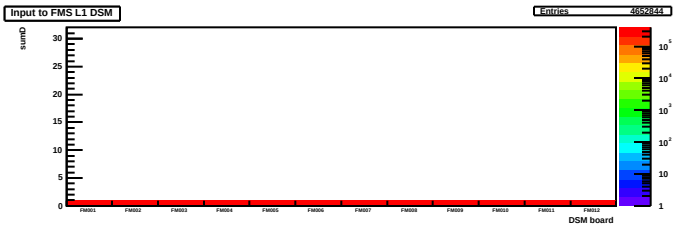

Input to FMS L0 DSM
Entries 3101896
QT8(3) sum - simulated
QT board

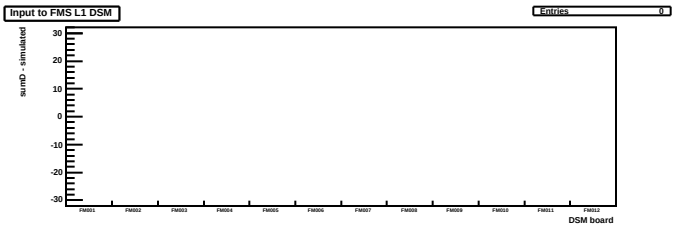

Input to FMS L0 DSM
Entries 1.861138e+07
HT ADC
QT board

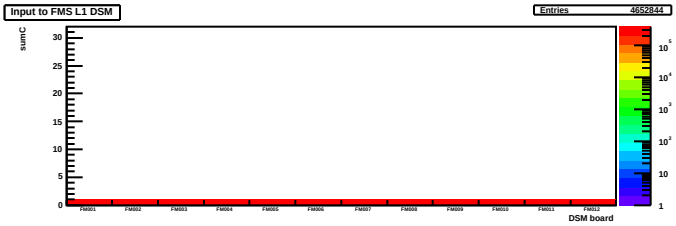

Input to FMS L0 DSM
Entries 3101896
HT ADC - simulated
QT board

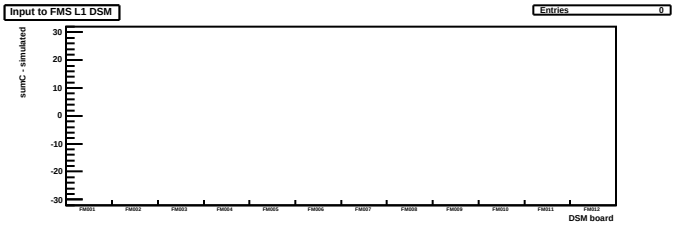

Input to FMS L0 DSM
Entries 1.861138e+07
HT ID
QT board

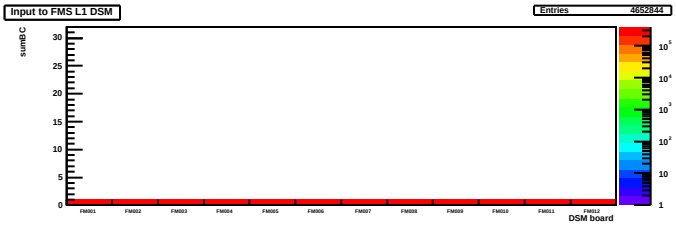

Input to FMS L0 DSM
Entries 3101896
HT ID - simulated
QT board

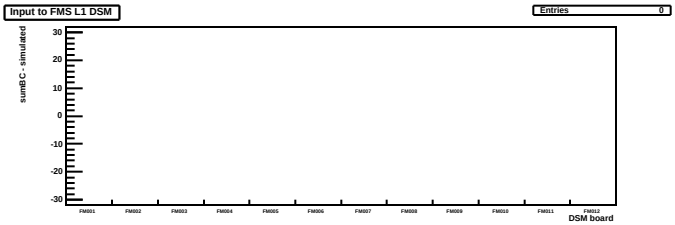

Input to FMS L1 DSM
Entries 4652844
sumD
DSM board

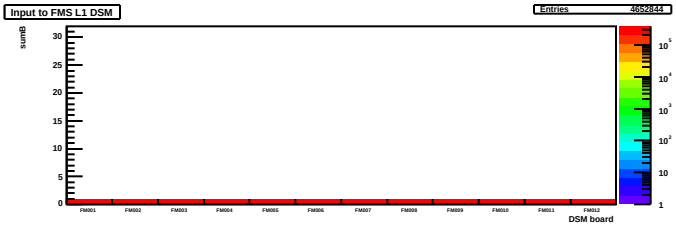

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

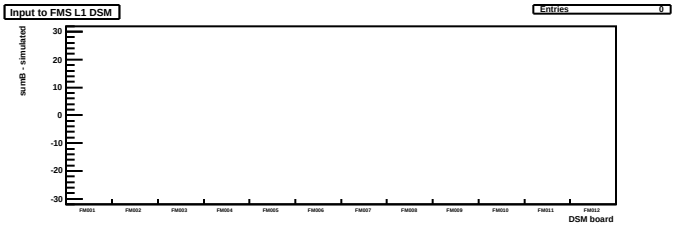

Input to FMS L1 DSM
Entries 4652844
sumC
DSM board

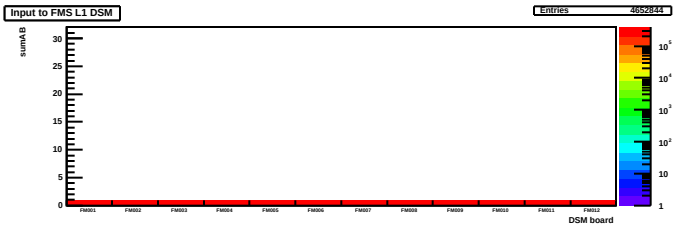

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

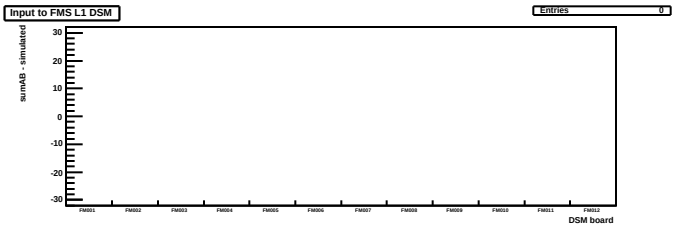

Input to FMS L1 DSM
Entries 4652844
sumBC
DSM board

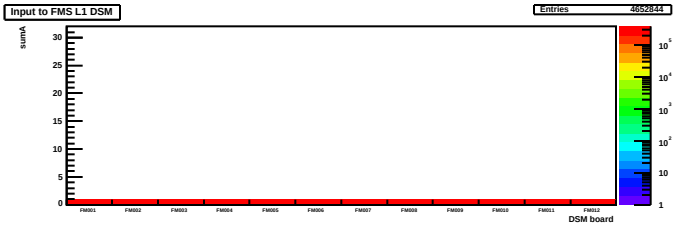

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

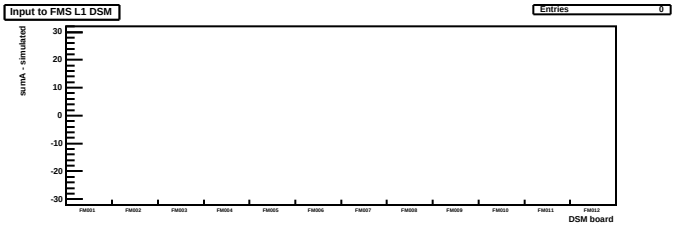

Input to FMS L1 DSM
Entries 4652844
sumB
DSM board

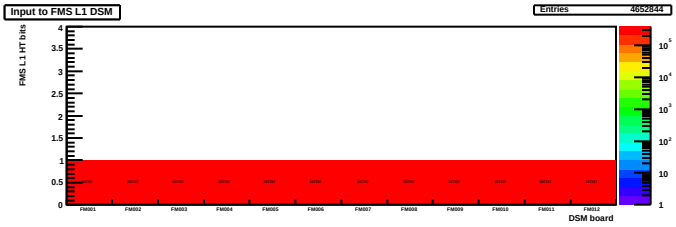

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

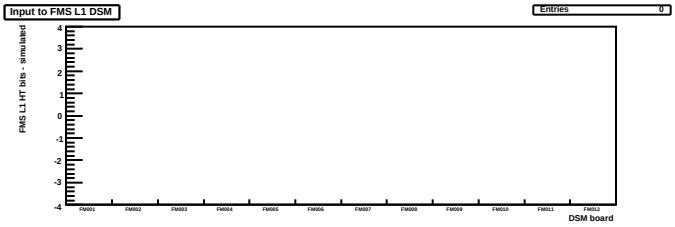

Input to FMS L1 DSM
Entries 4652844
sumAB
DSM board

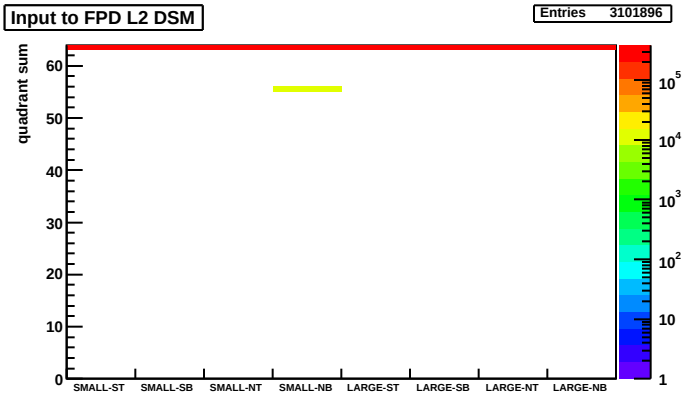

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

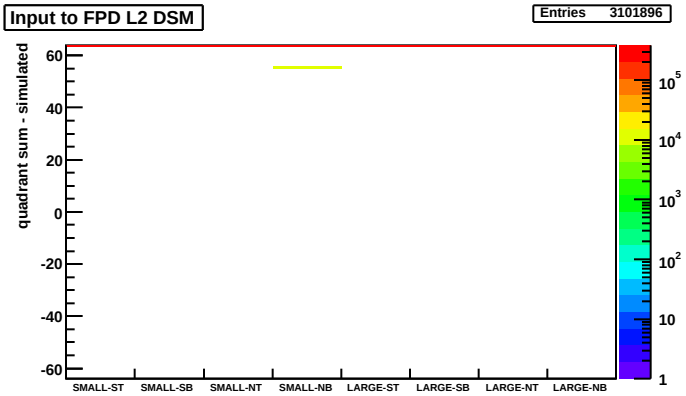

Input to FMS L1 DSM
Entries 4652844
sumA
DSM board

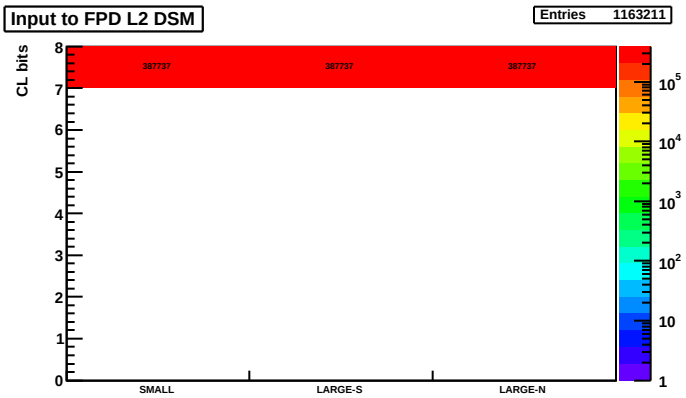

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

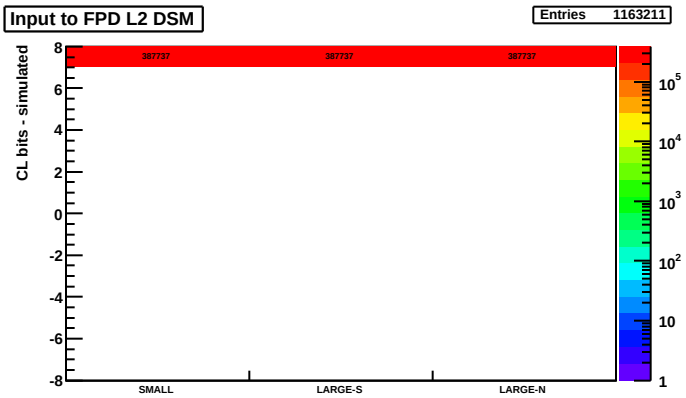

Input to FMS L1 DSM
Entries 4652844
FMS L1 HT bits
DSM board

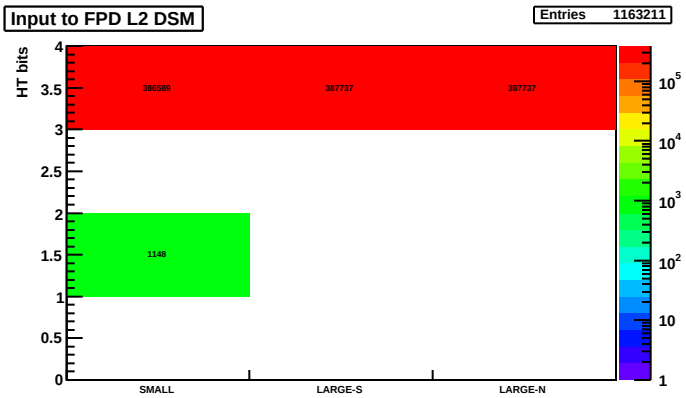

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

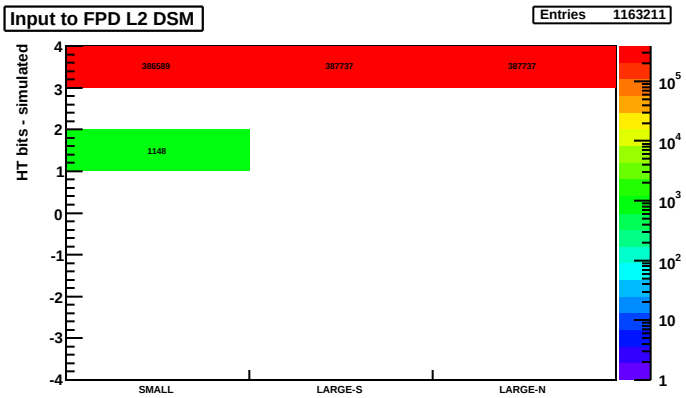

Input to FPD L2 DSM
Entries 3101896
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

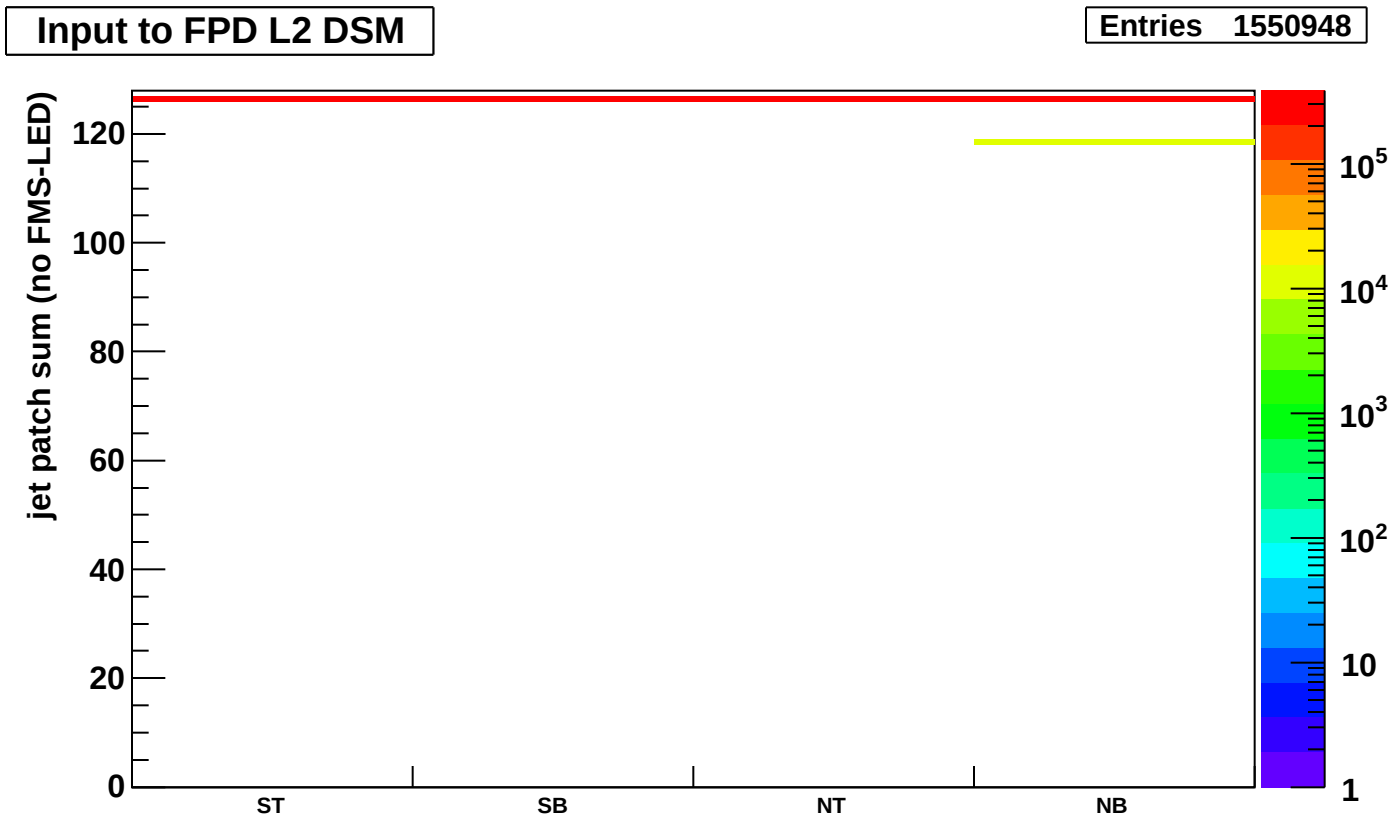

Input to FPD L2 DSM
Entries 3101896
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

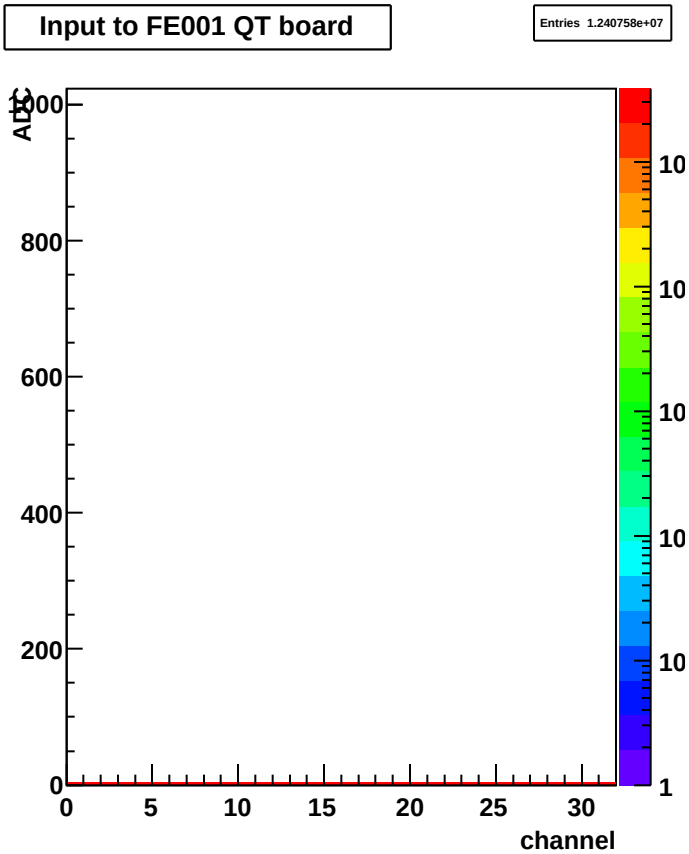

Input to FPD L2 DSM
Entries 1163211
CL bits
387737
387737
387737
SMALL
LARGE-S
LARGE-N

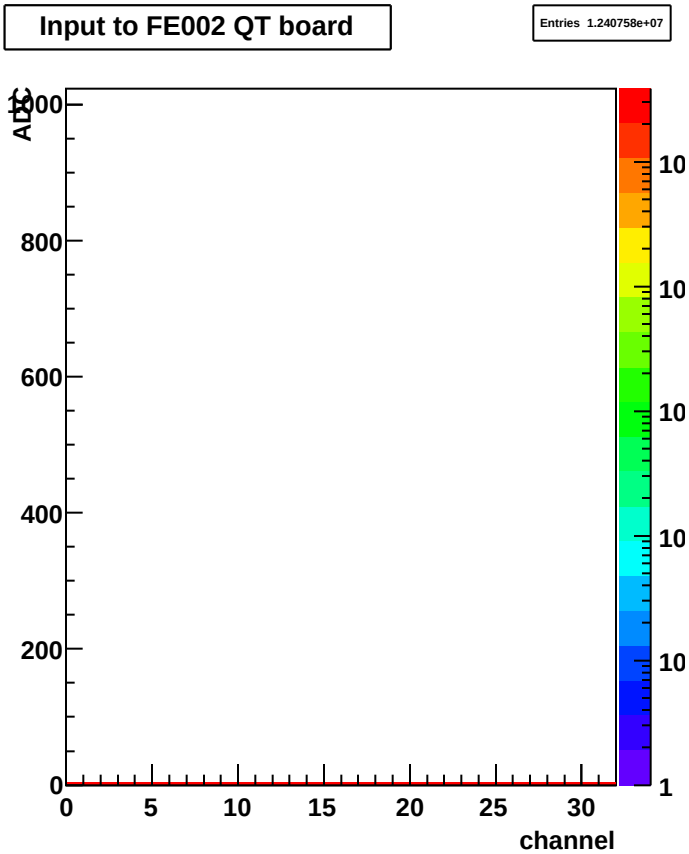

Input to FPD L2 DSM
Entries 1163211
CL bits - simulated
387737
387737
387737
SMALL
LARGE-S
LARGE-N

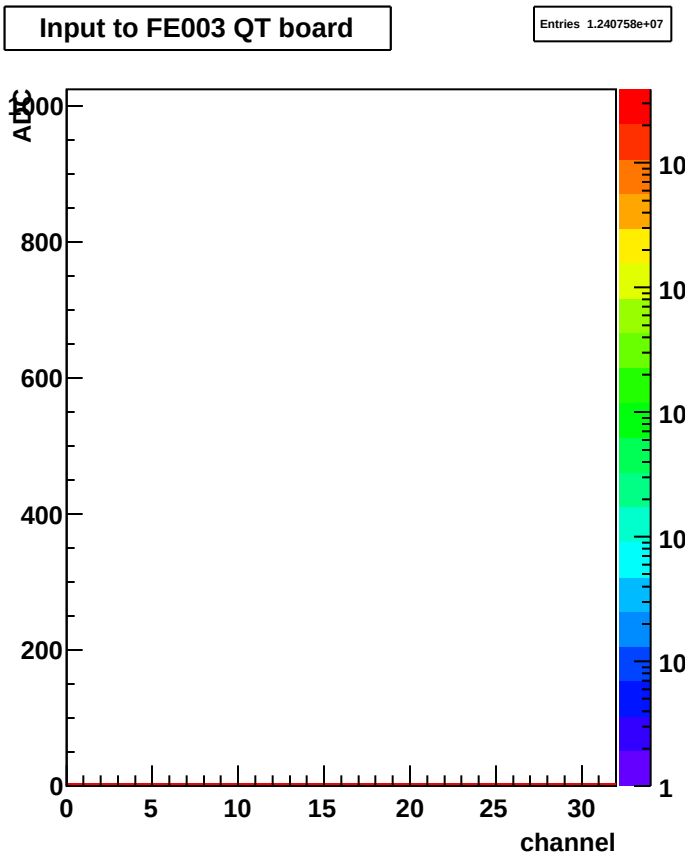

Input to FPD L2 DSM
Entries 1163211
HT bits
386589
387737
387737
1148
SMALL
LARGE-S
LARGE-N

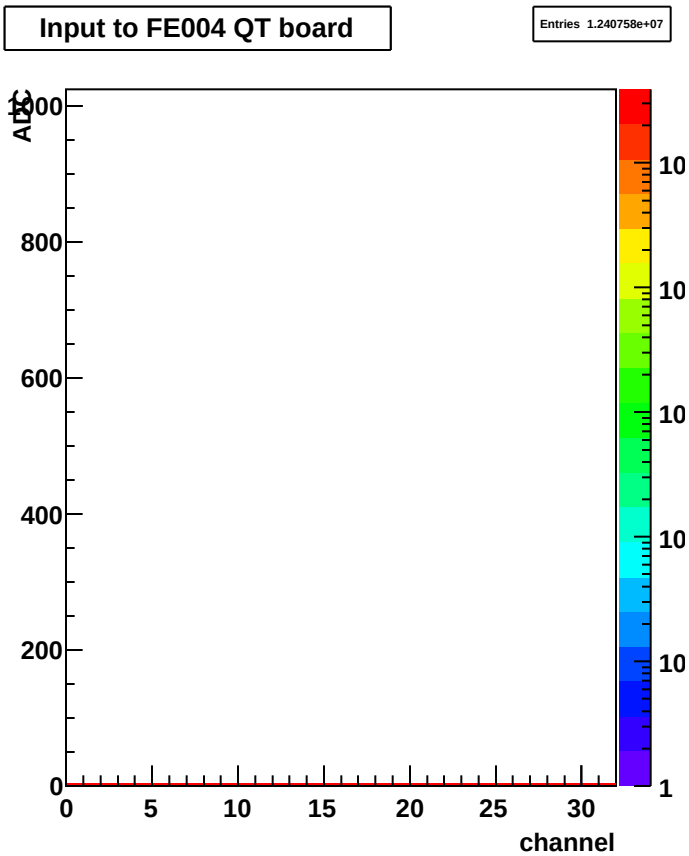

Input to FPD L2 DSM
Entries 1163211
HT bits - simulated
386589
387737
387737
1148
SMALL
LARGE-S
LARGE-N

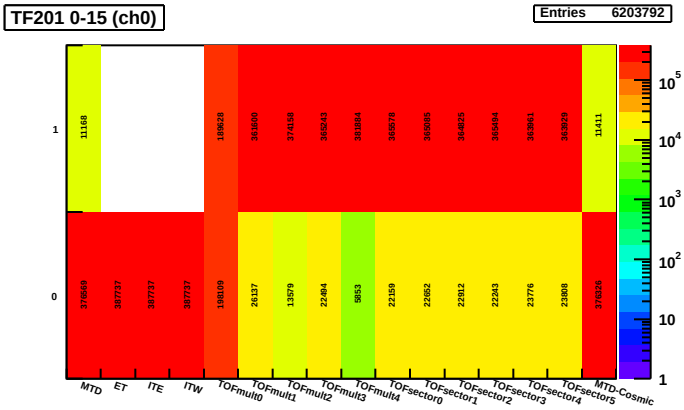

Input to FPD L2 DSM
Entries 1550948
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

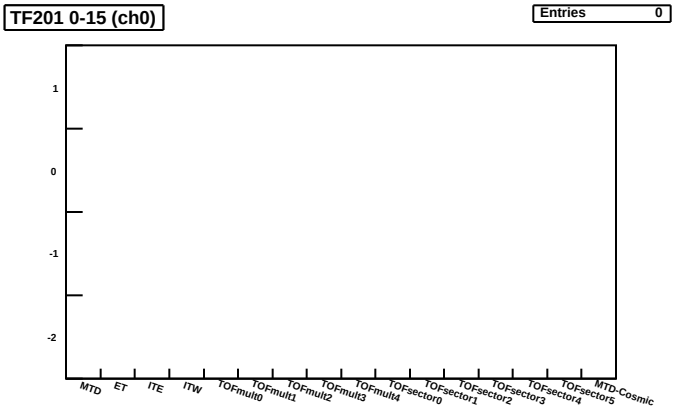

Input to FE001 QT board
Entries 1.240758e+07
ADC
channel

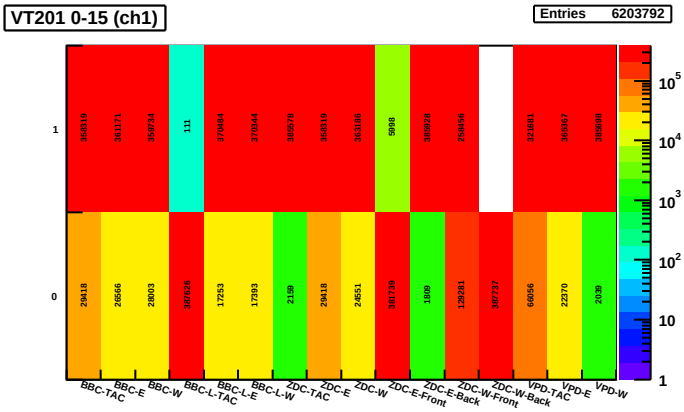

Input to FE002 QT board
Entries 1.240758e+07
ADC
channel

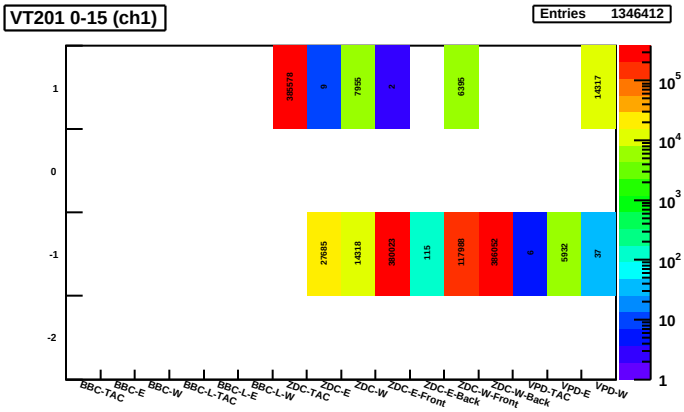

Input to FE003 QT board
Entries 1.240758e+07
ADC
channel

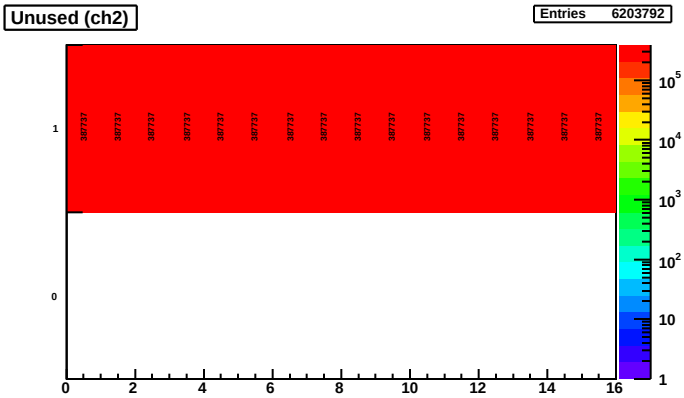

Input to FE004 QT board
Entries 1.240758e+07
ADC
channel

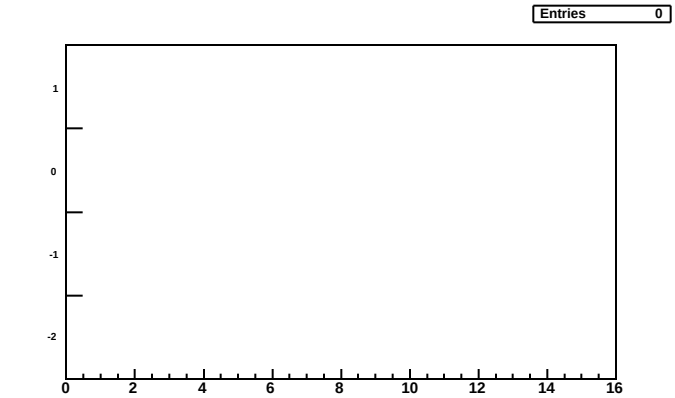

TF201 0-15 (ch0)
Entries 6203792
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

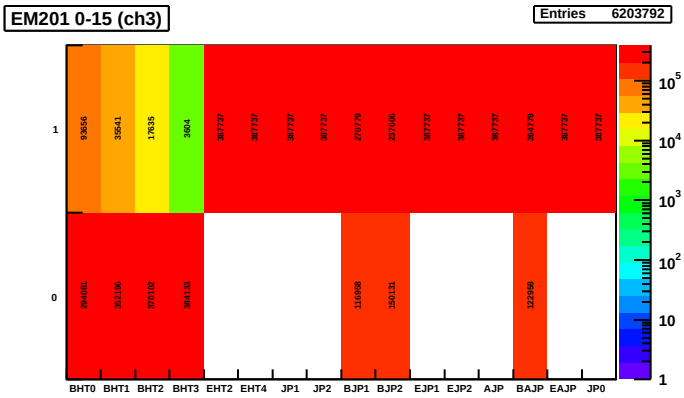

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

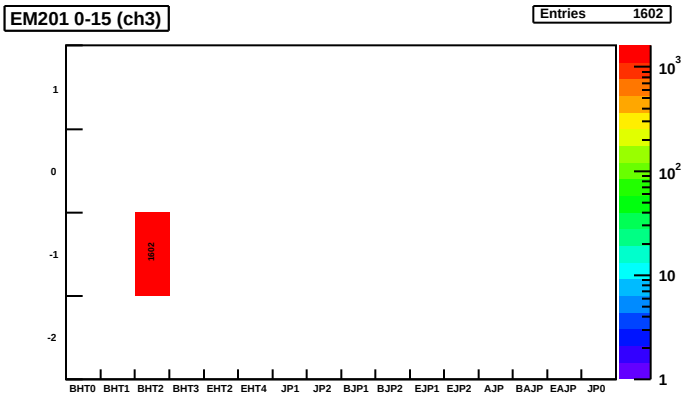

VT201 0-15 (ch1)
Entries 6203792
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 1346412
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

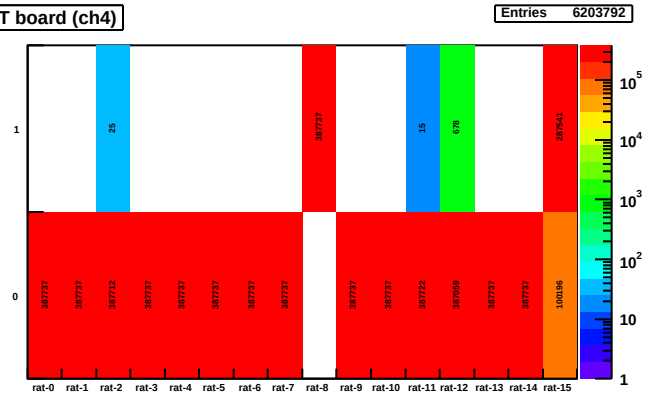

Unused (ch2)
Entries 6203792

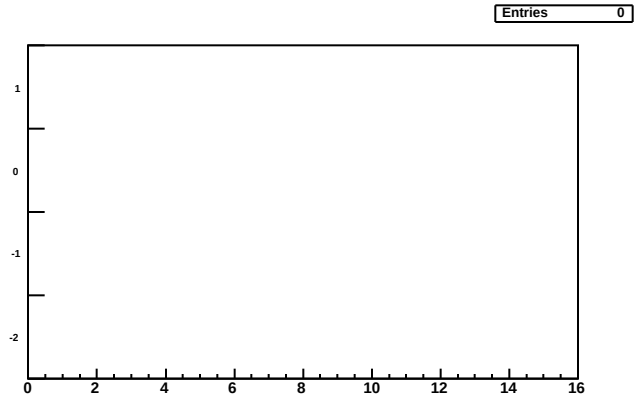

Entries 0


EM201 0-15 (ch3)
Entries 6203792
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

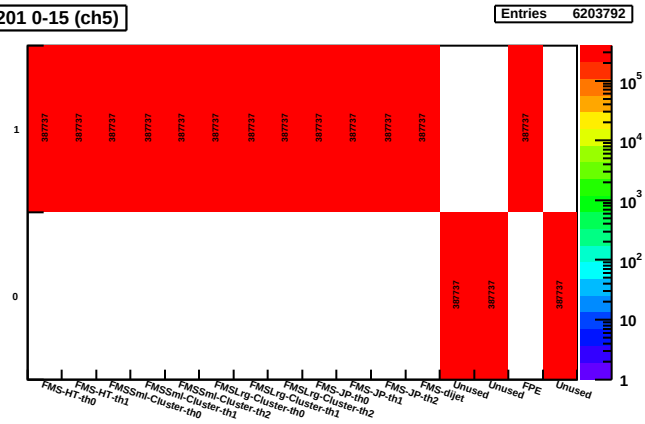

EM201 0-15 (ch3)
Entries 1602
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

RAT board (ch4)

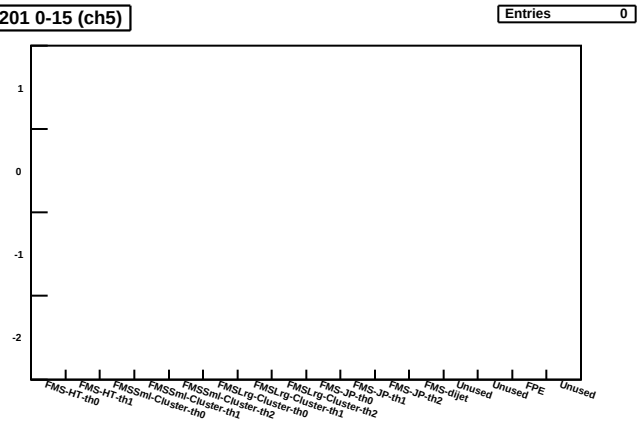




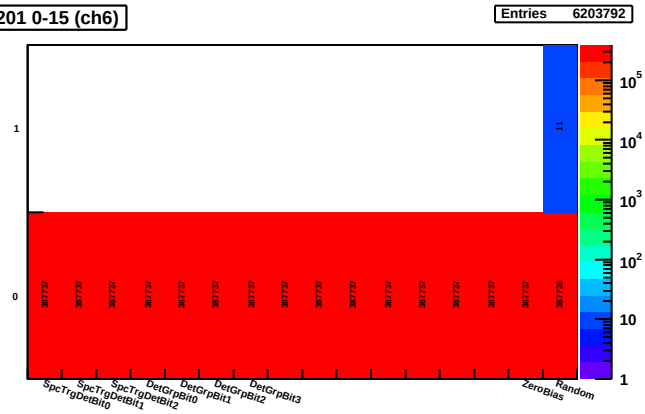
FP201 0-15 (ch5)

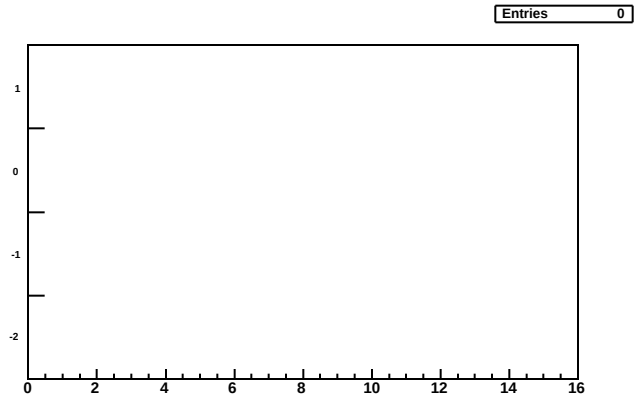
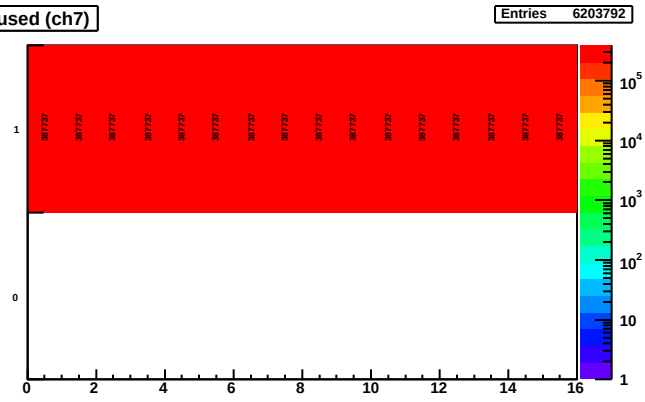
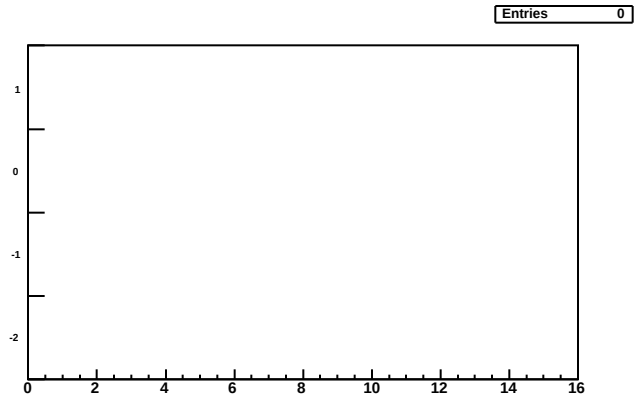


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)