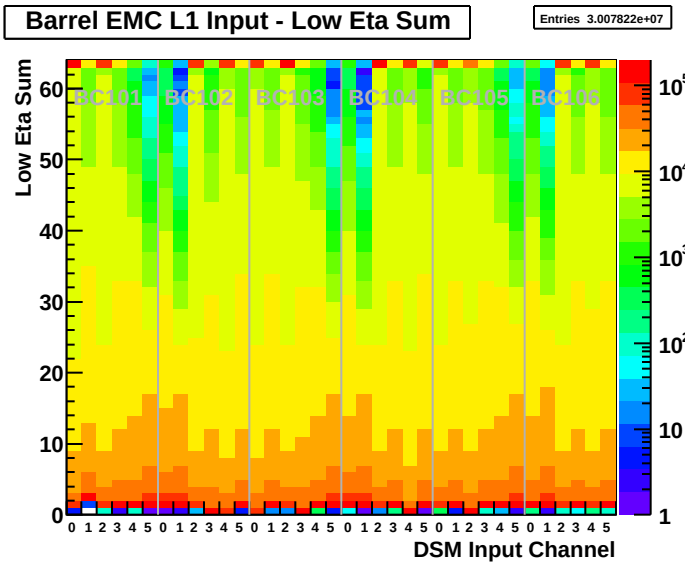
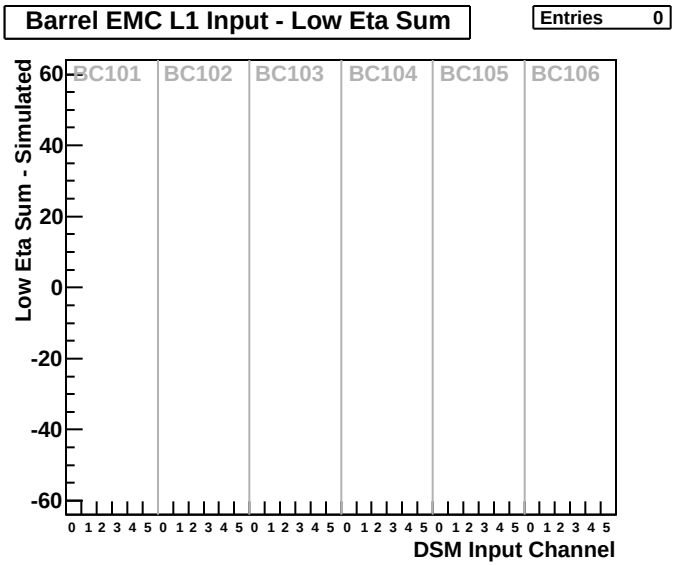
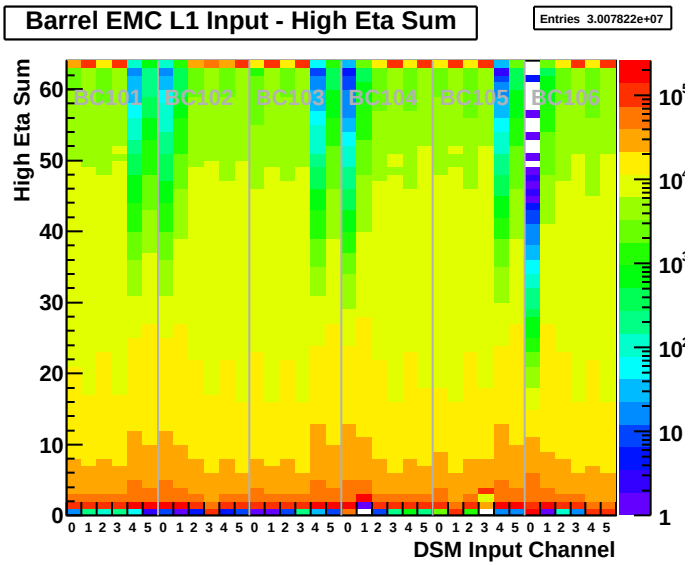
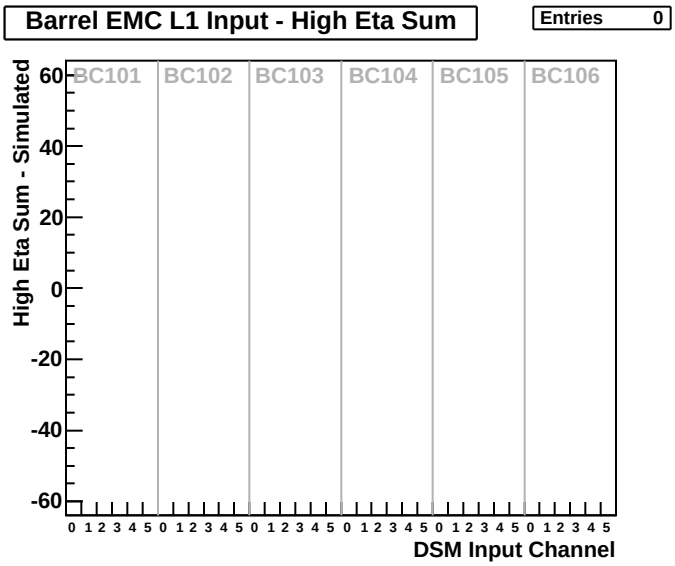
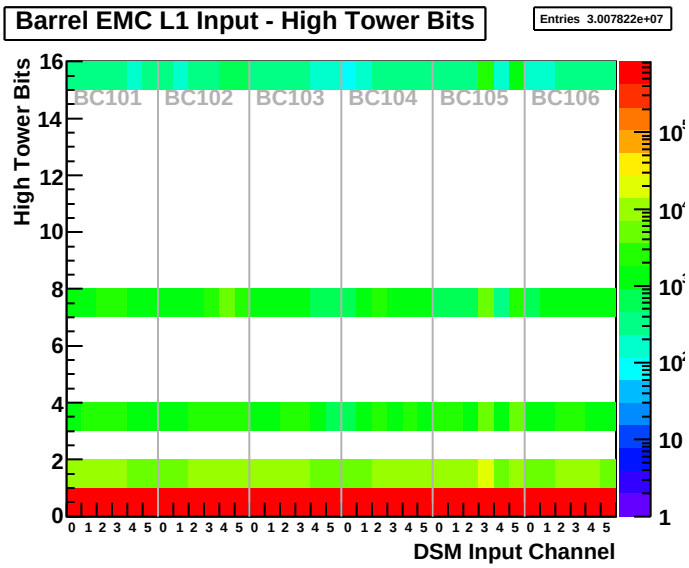
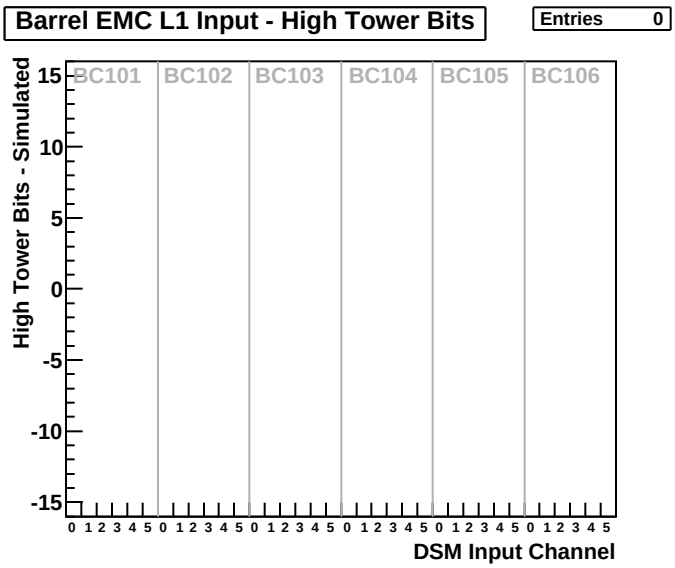

Barrel EMC L1 Input - Low Eta Sum
Entries 3.007822e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

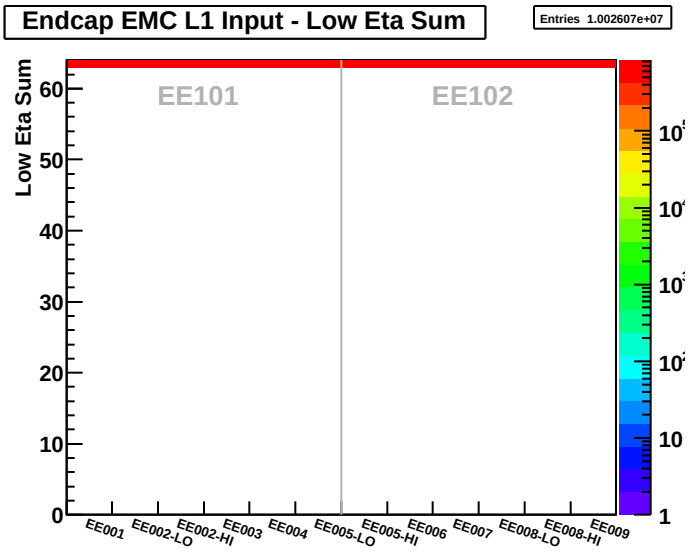

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

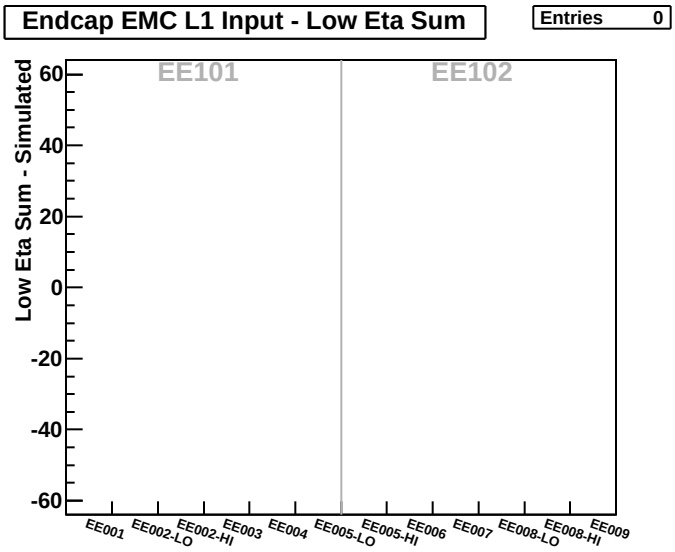

Barrel EMC L1 Input - High Eta Sum
Entries 3.007822e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

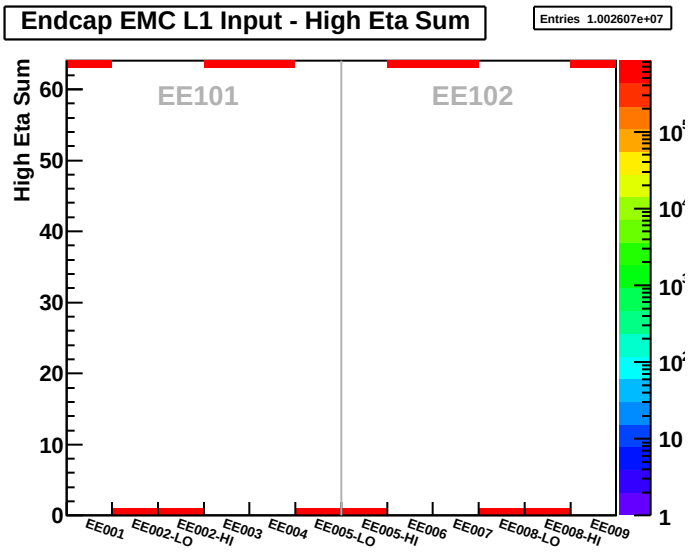

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

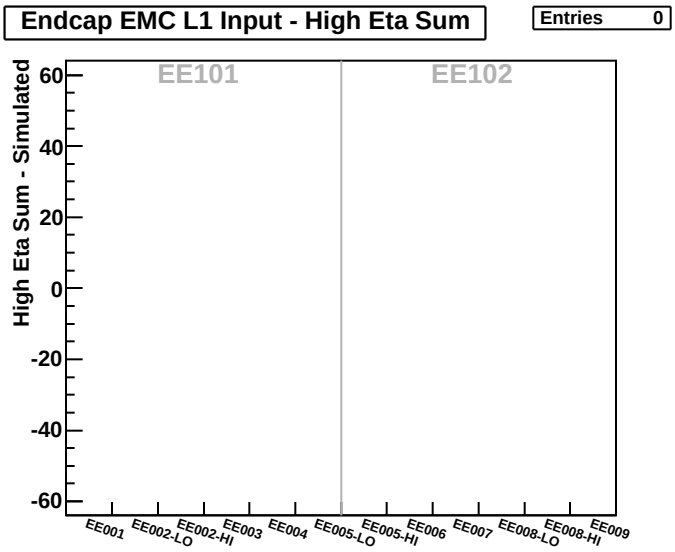

Barrel EMC L1 Input - High Tower Bits
Entries 3.007822e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

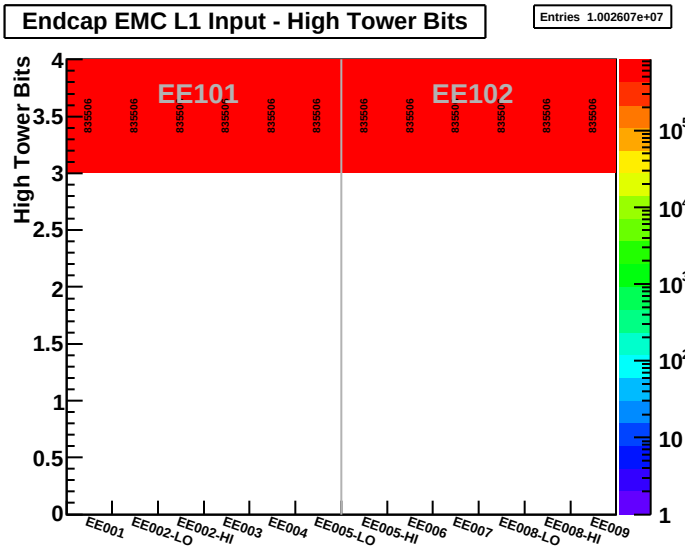

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

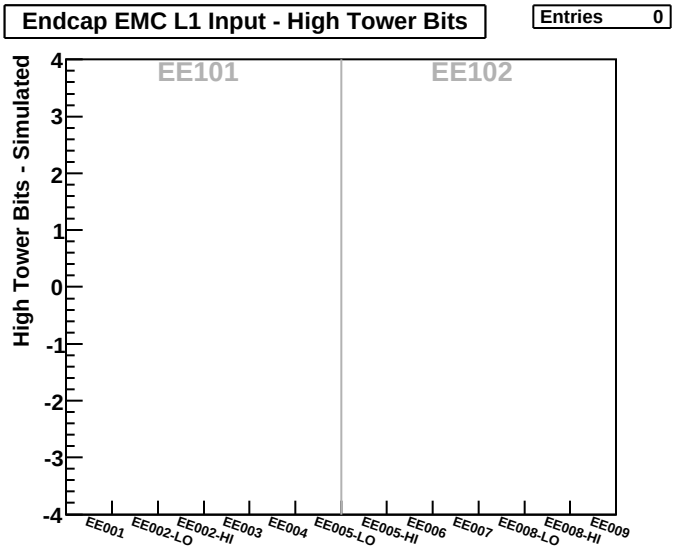

Endcap EMC L1 Input - Low Eta Sum
Entries 1.002607e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

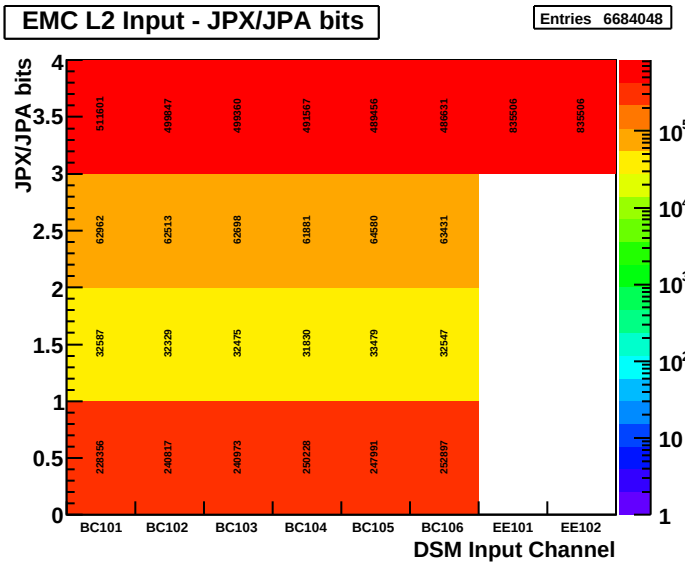

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

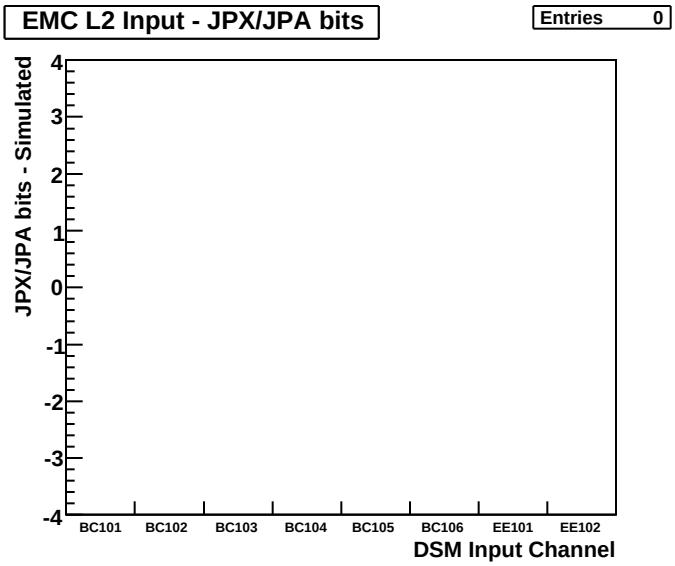

Endcap EMC L1 Input - High Eta Sum
Entries 1.002607e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

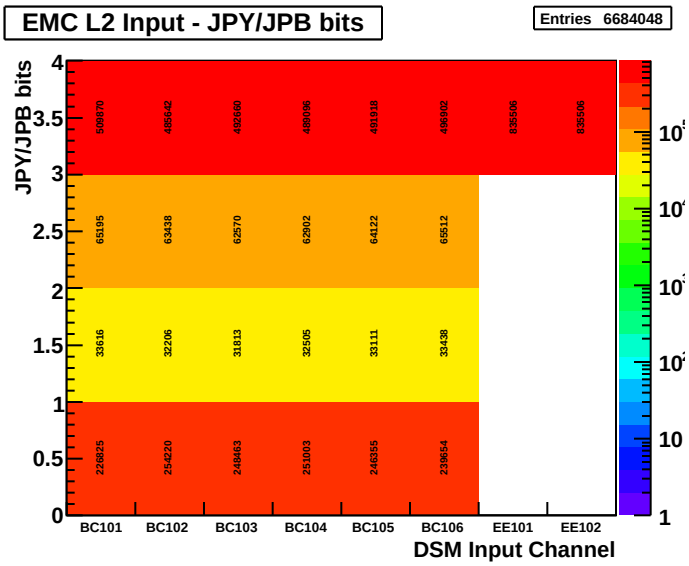

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

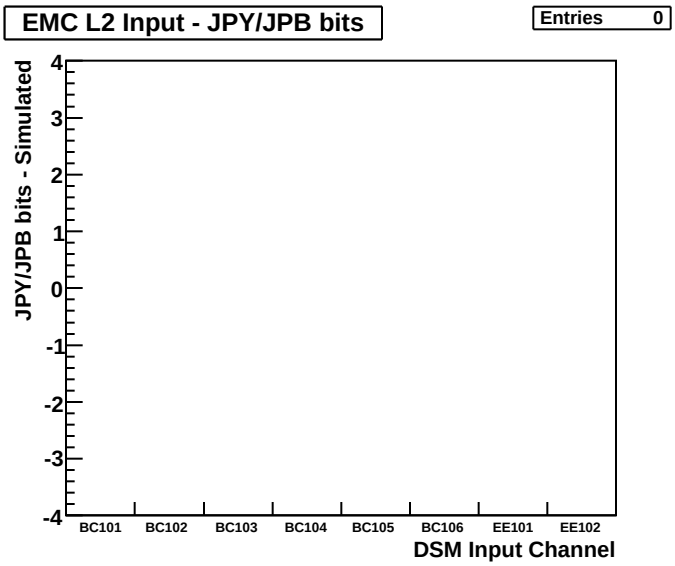

Endcap EMC L1 Input - High Tower Bits
Entries 1.002607e+07
High Tower Bits
EE101
EE102
835506
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

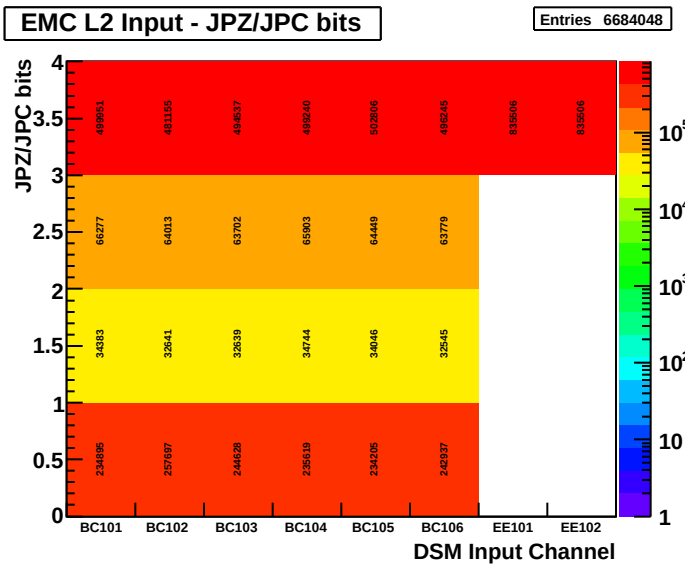

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

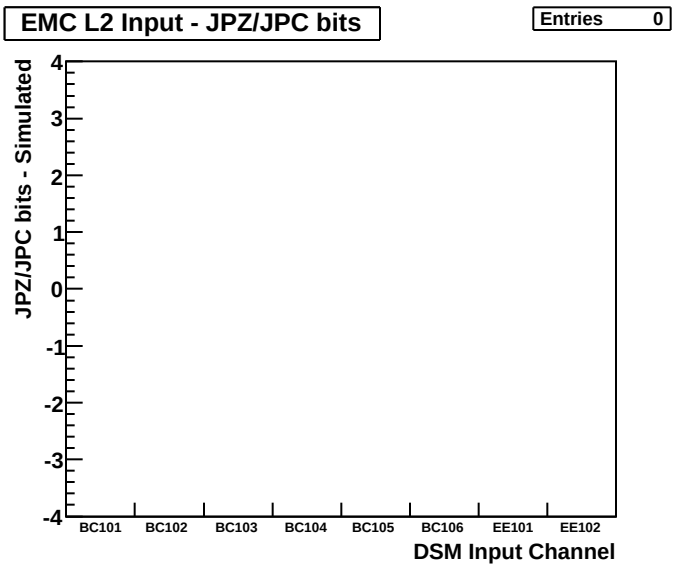

EMC L2 Input - JPX/JPA bits
Entries 6684048
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
511601 498647 498360 491567 489456 486631 835506 835506
62962 62513 62698 61881 64580 63431
32567 32329 32475 31830 33479 32547
228356 240817 240973 250228 247991 252897

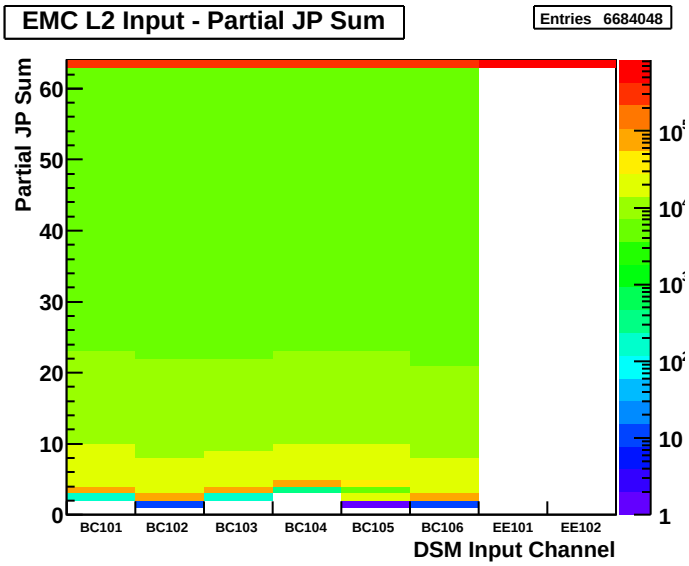

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

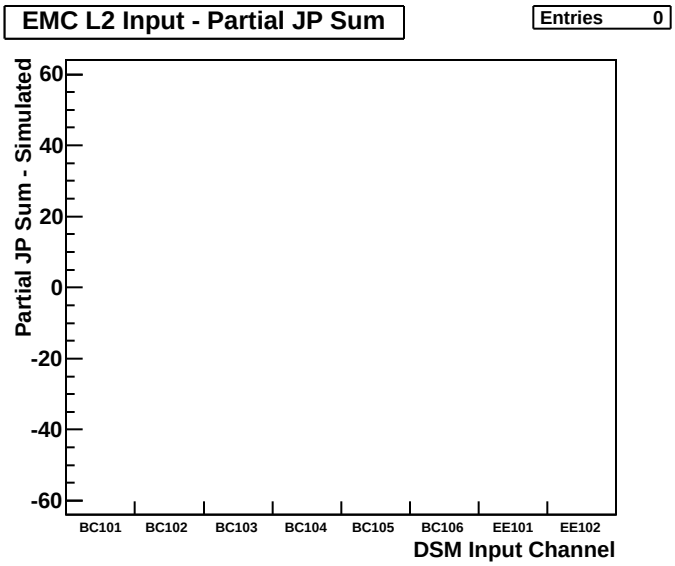

EMC L2 Input - JPY/JPB bits
Entries 6684048
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
509870 485642 492680 489086 491918 496902 835506 835506
65195 63438 62570 62902 64122 65512
33616 32206 31813 32505 33111 33438
226825 254220 248463 251003 246355 239654

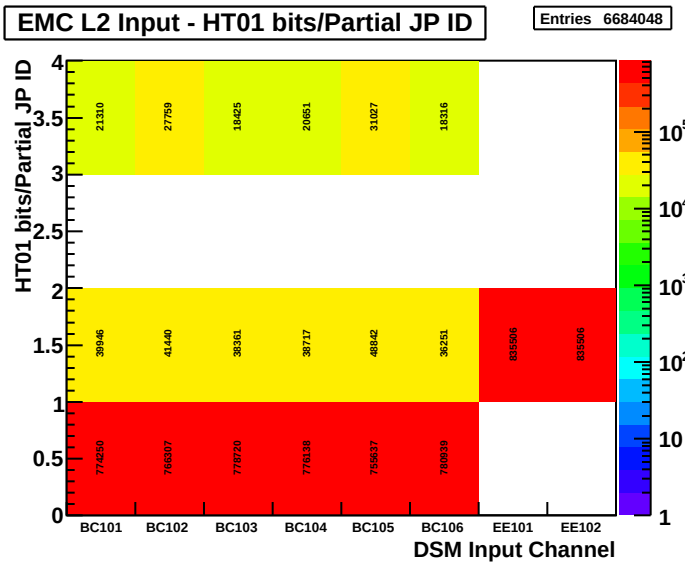

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

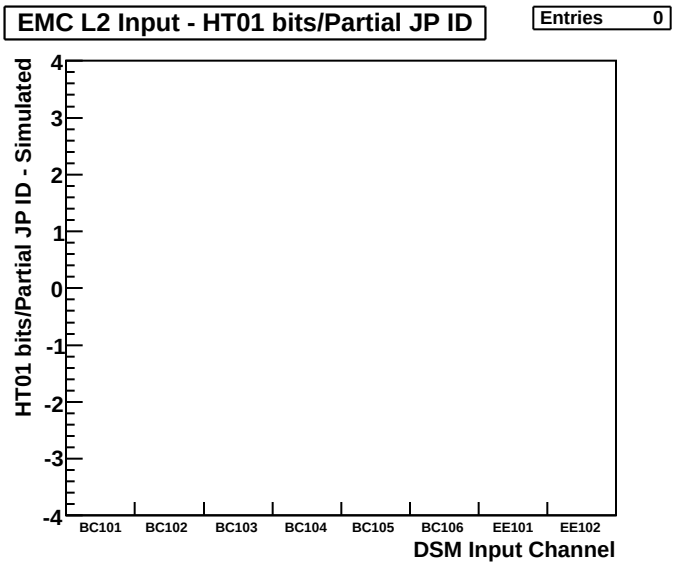

EMC L2 Input - JPZ/JPC bits
Entries 6684048
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
499951 481155 496537 492240 502806 496245 835506 835506
66277 64013 63702 65903 64449 63779
34883 32641 32639 34744 34046 32545
234895 257697 244628 235619 234205 242937

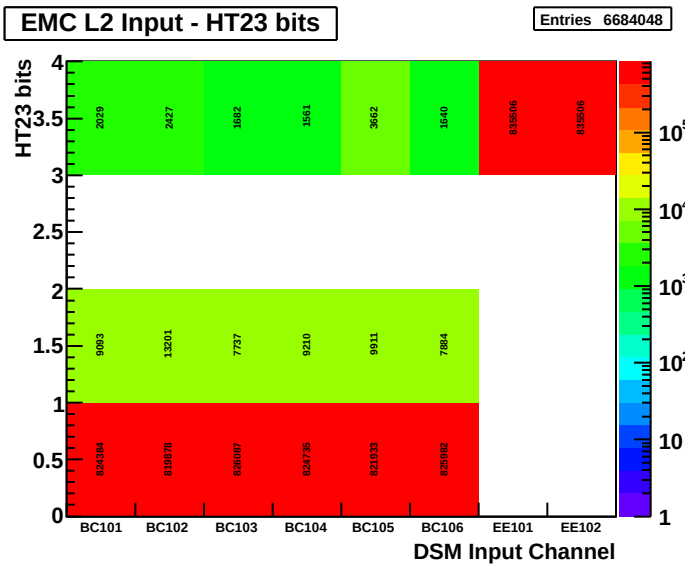

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

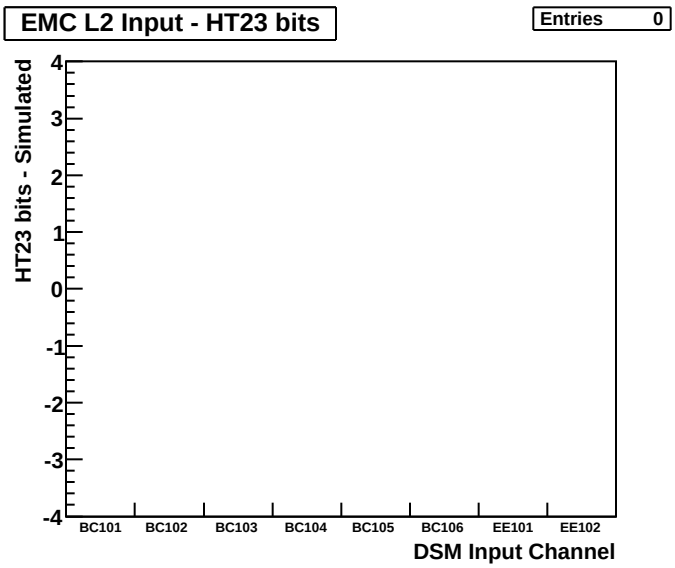

EMC L2 Input - Partial JP Sum
Entries 6684048
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

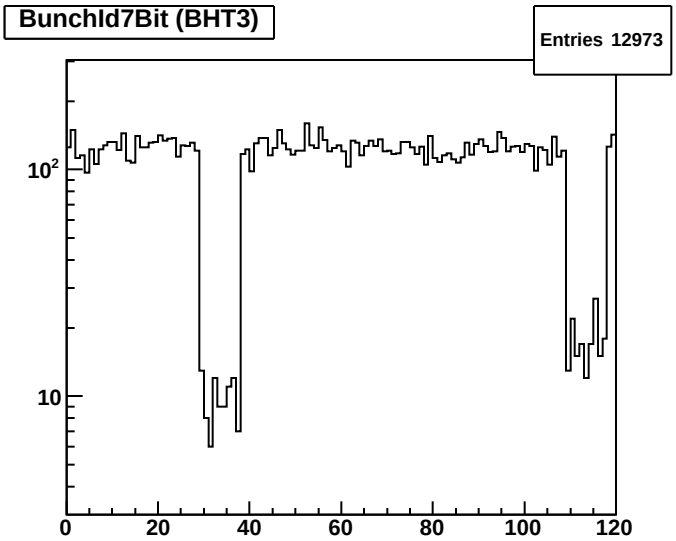

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

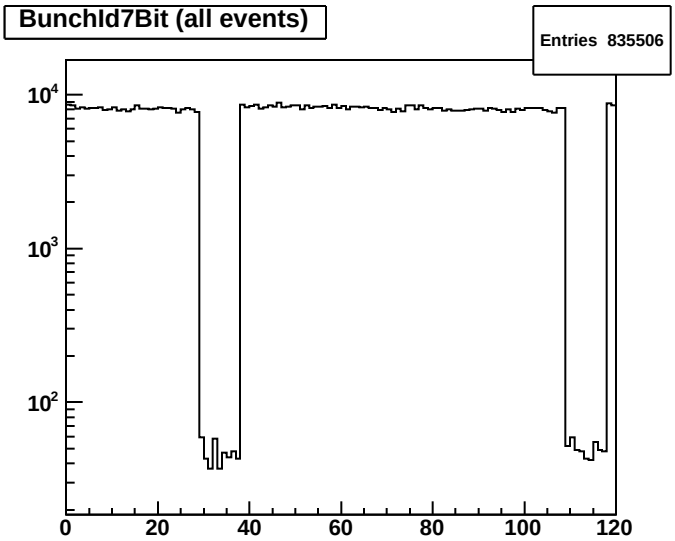

EMC L2 Input - HT01 bits/Partial JP ID
Entries 6684048
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

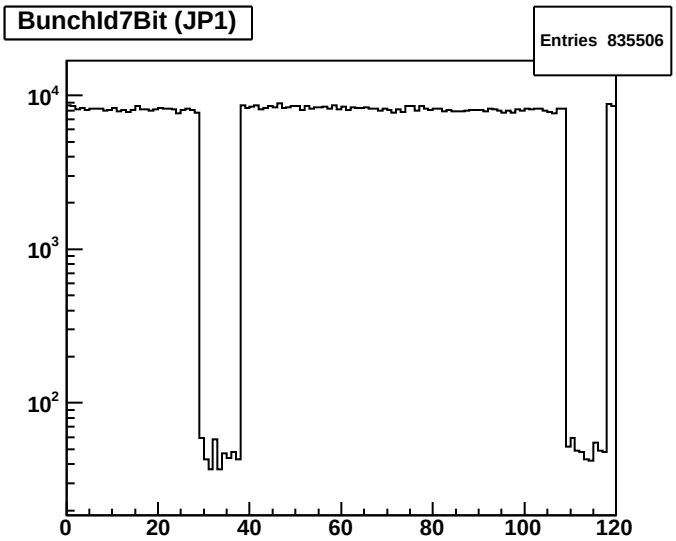

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

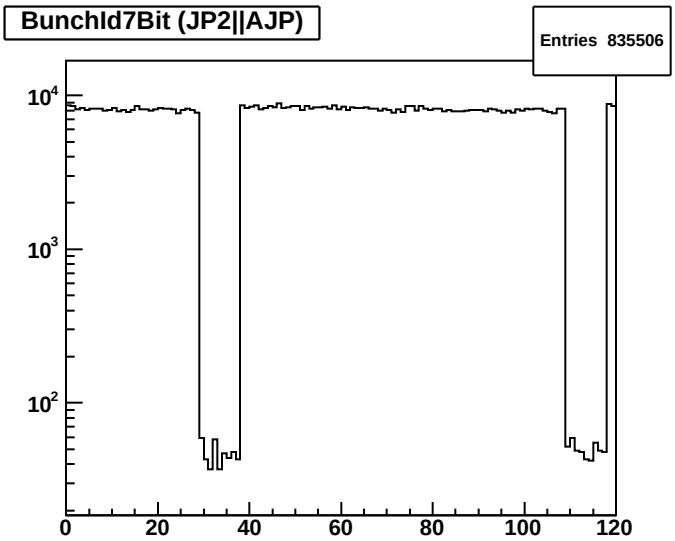

EMC L2 Input - HT23 bits
Entries 6684048
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

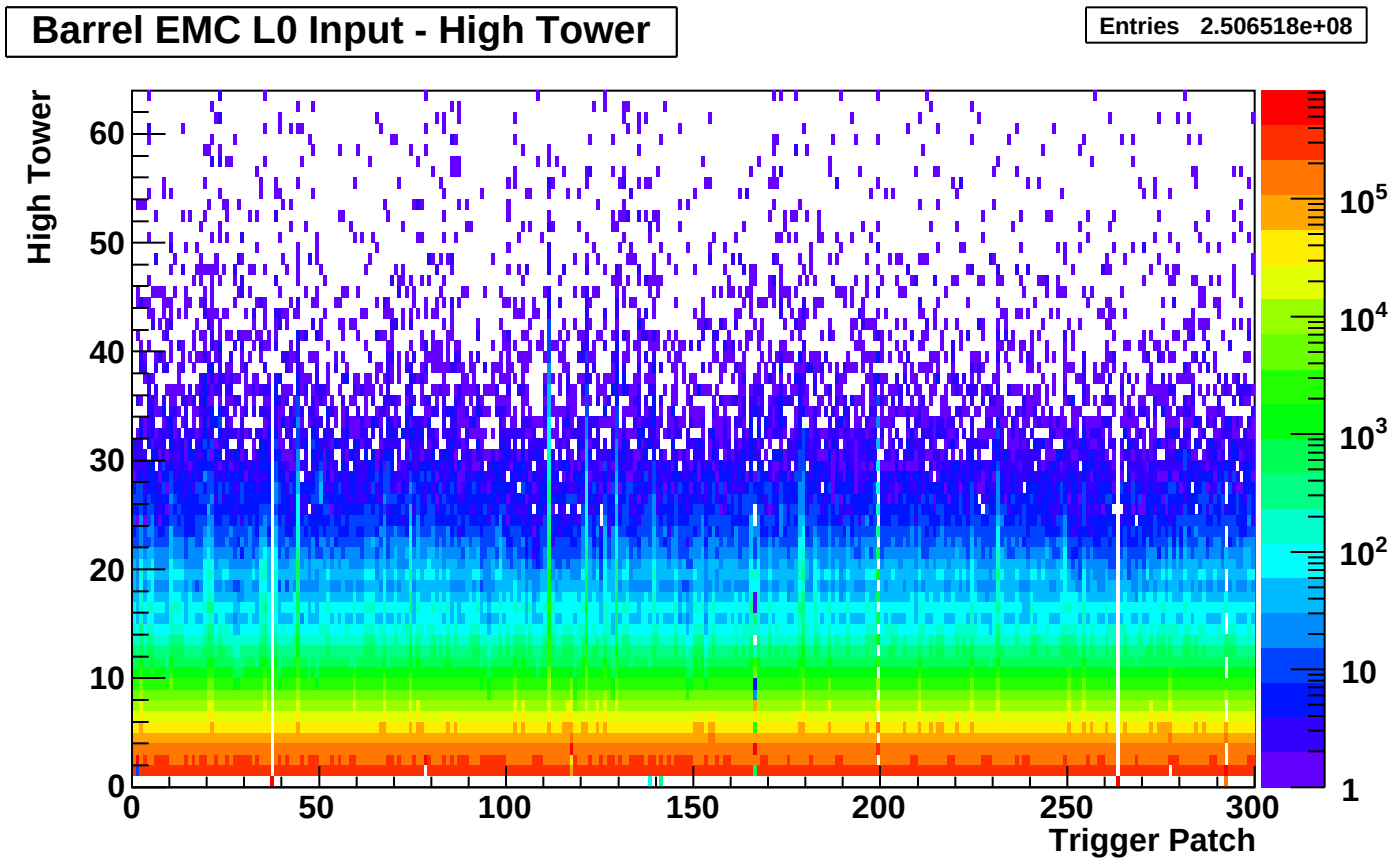

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

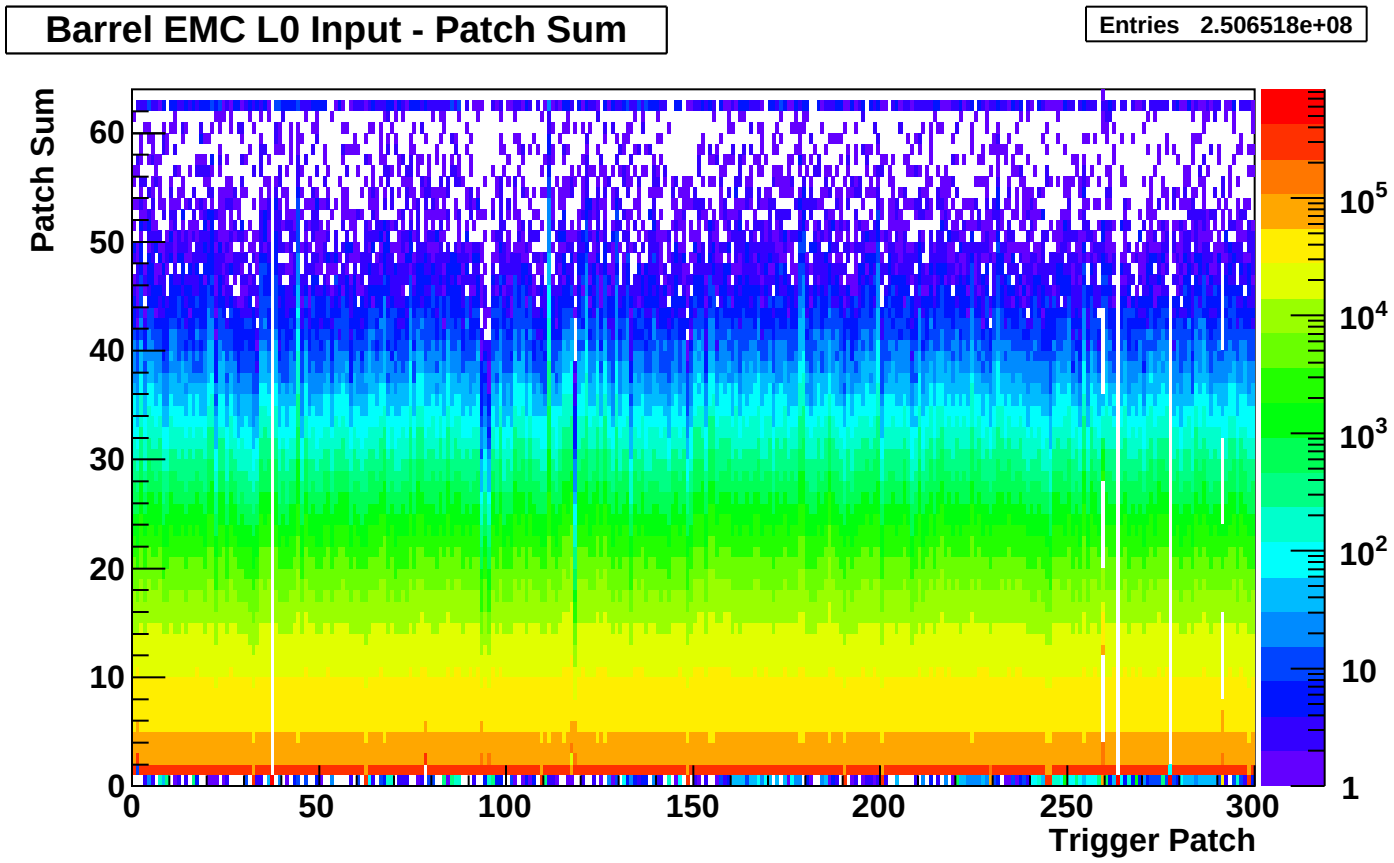

BunchId7Bit (BHT3)
Entries 12973
10^2
10
0
20
40
60
80
100
120

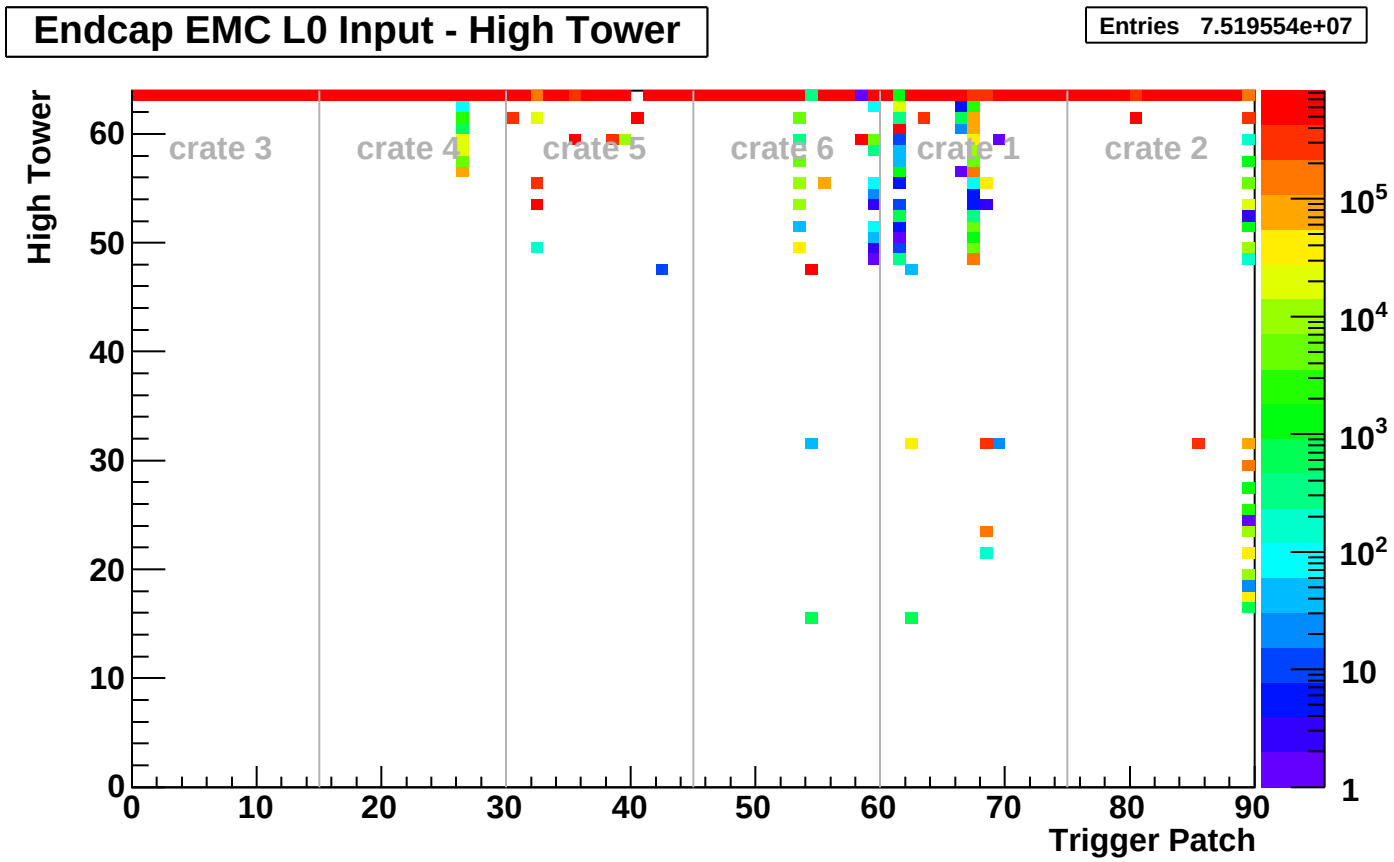

BunchId7Bit (all events)
Entries 835506
10^4
10^3
10^2
0
20
40
60
80
100
120

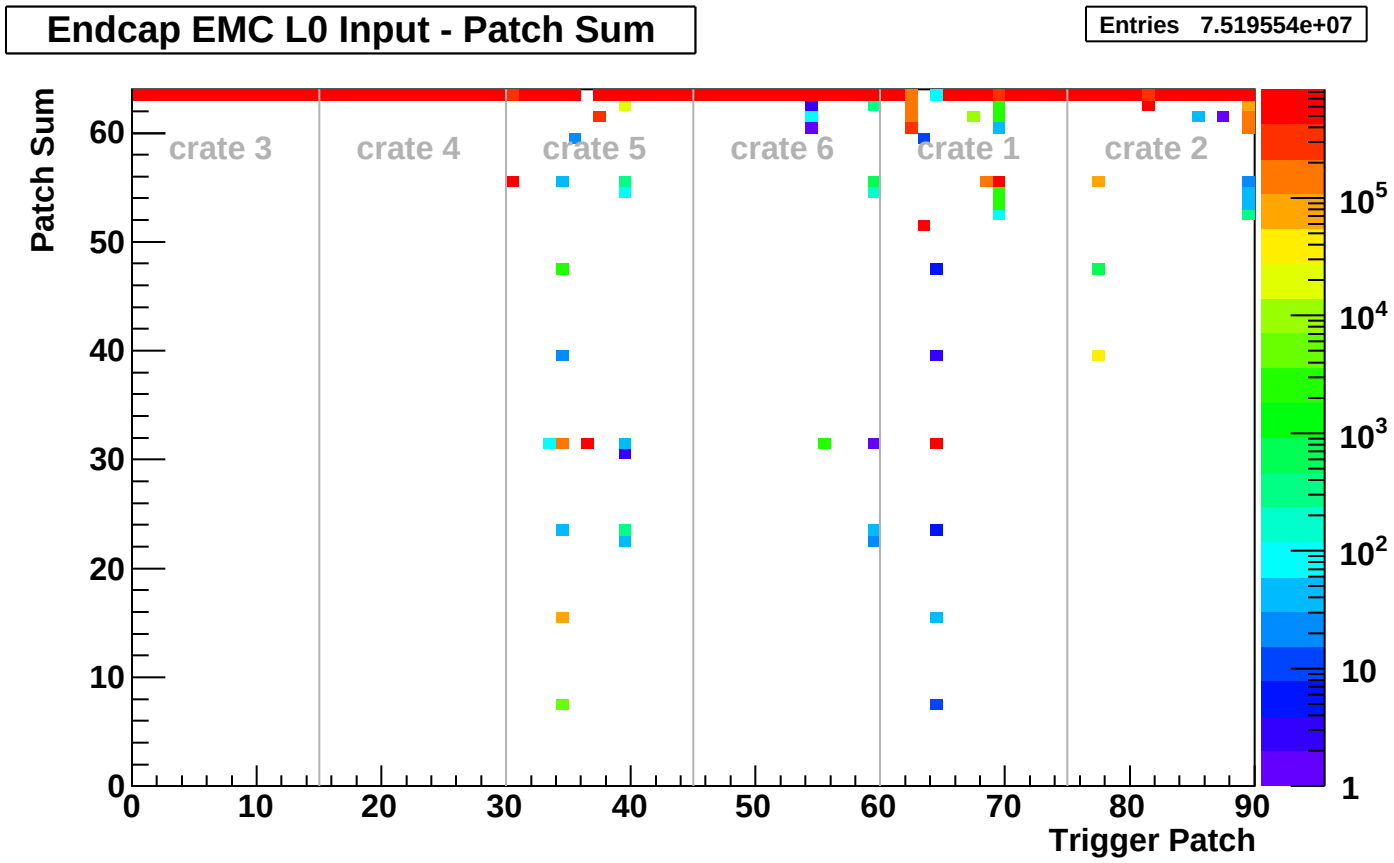

BunchId7Bit (JP1)
Entries 835506
10^4
10^3
10^2
0
20
40
60
80
100
120

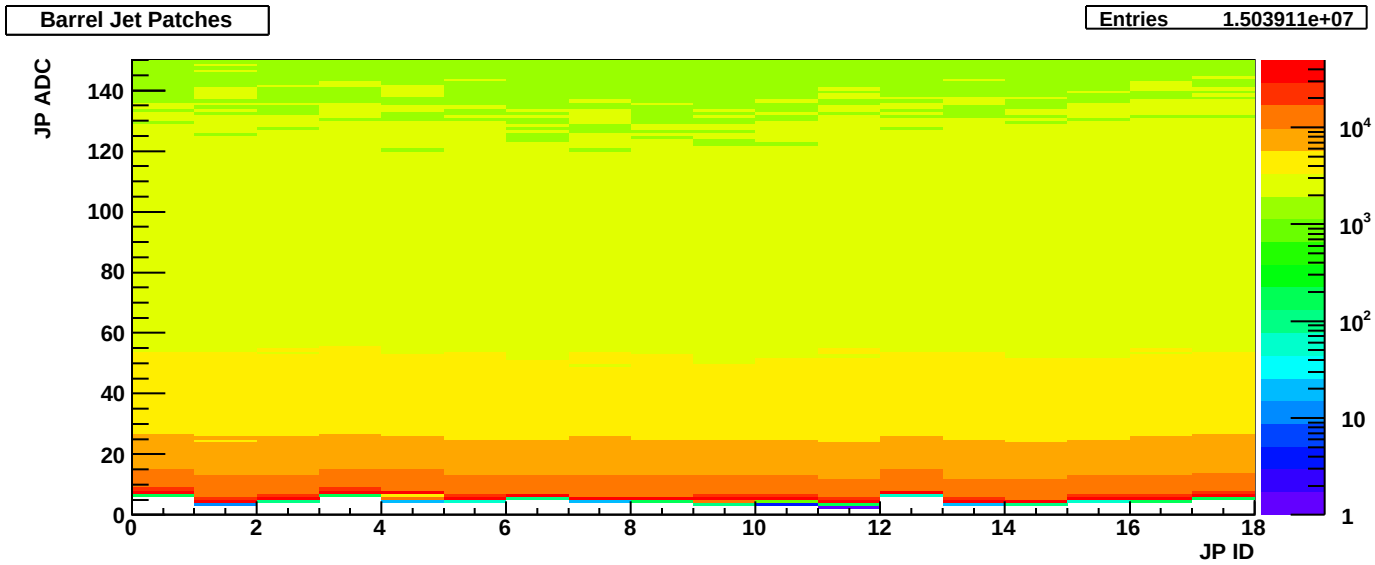

BunchId7Bit (JP2||AJP)
Entries 835506
10^4
10^3
10^2
0
20
40
60
80
100
120

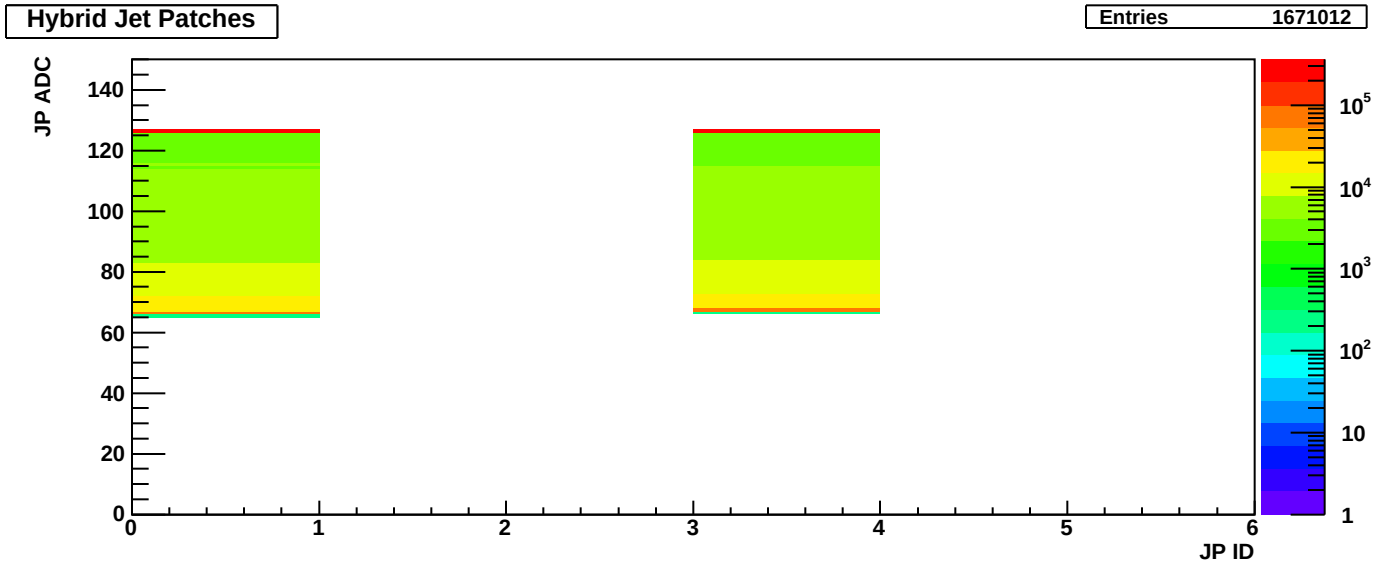

Barrel EMC L0 Input - High Tower
Entries 2.506518e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

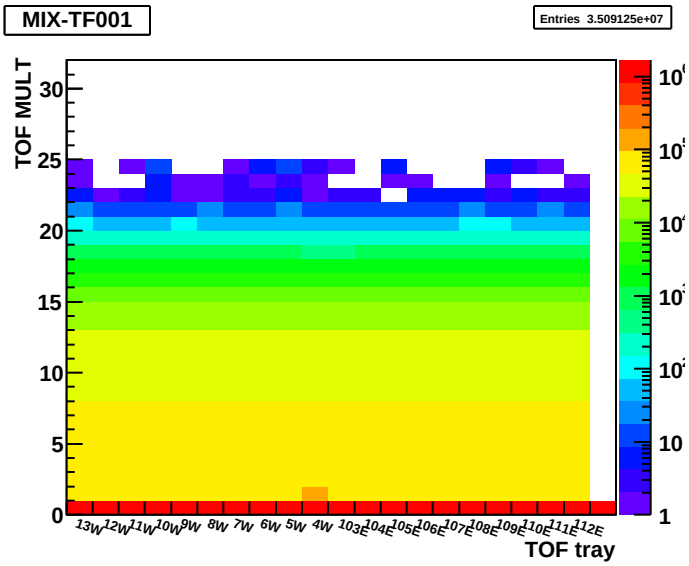

Barrel EMC L0 Input - Patch Sum
Entries 2.506518e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

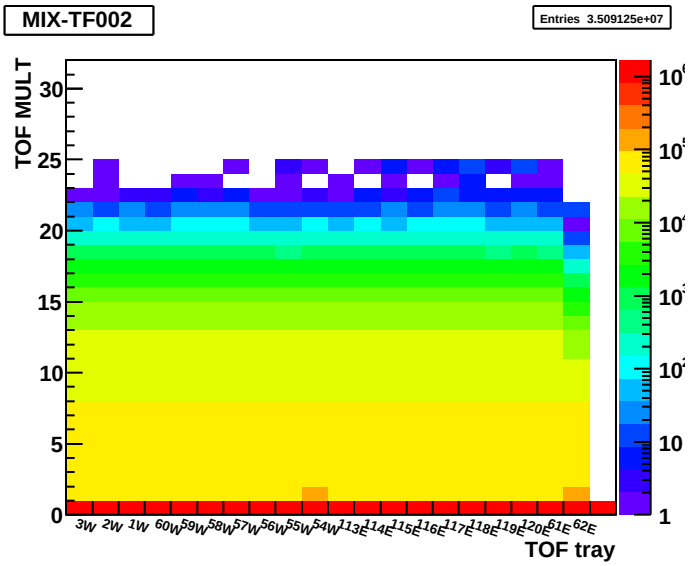

Endcap EMC L0 Input - High Tower
Entries 7.519554e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

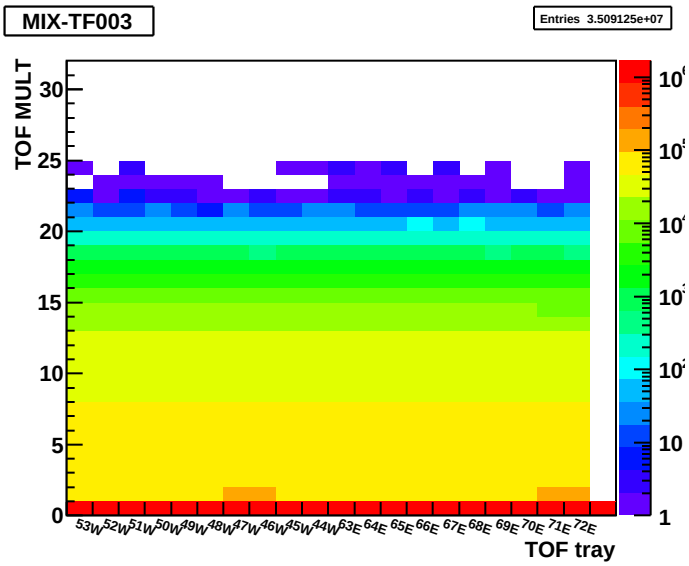

Endcap EMC L0 Input - Patch Sum
Entries 7.519554e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

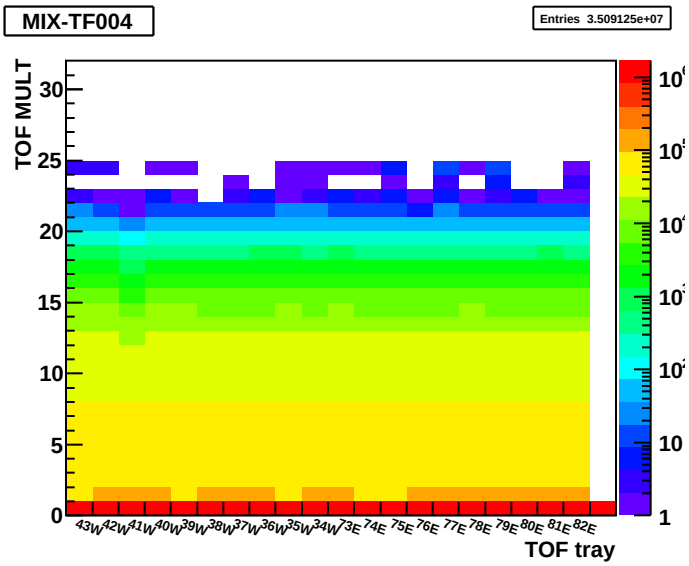

Barrel Jet Patches
Entries 1.503911e+07
JP ADC
JP ID

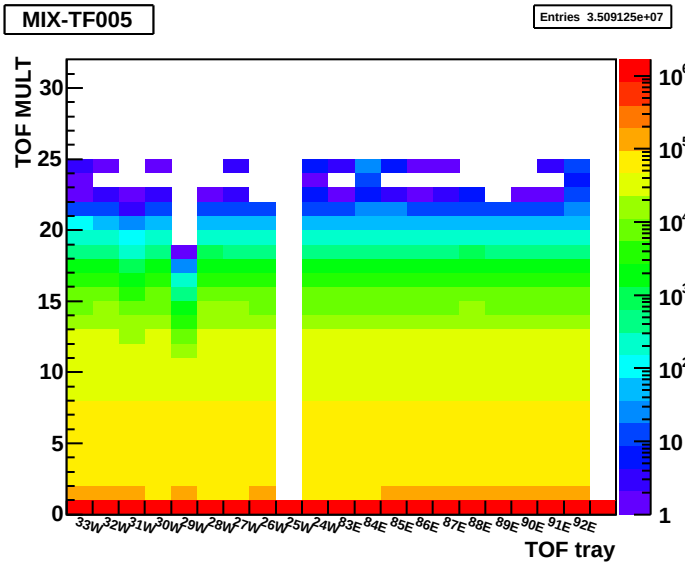

Hybrid Jet Patches
Entries 1671012
JP ADC
JP ID

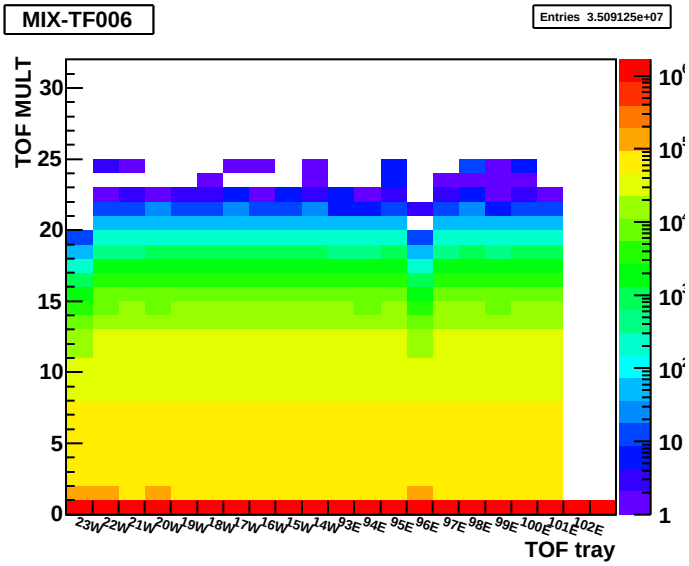

MIX-TF001
Entries 3.509125e+07
TOF MULT
TOF tray

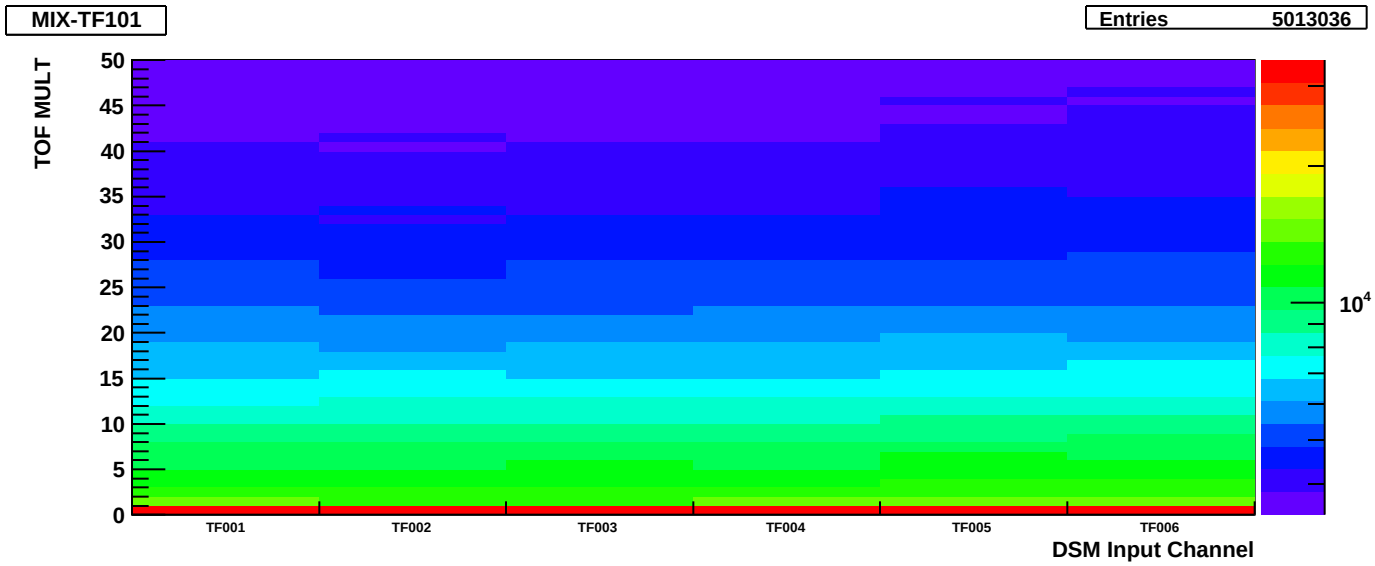

MIX-TF002
Entries 3.509125e+07
TOF MULT
TOF tray

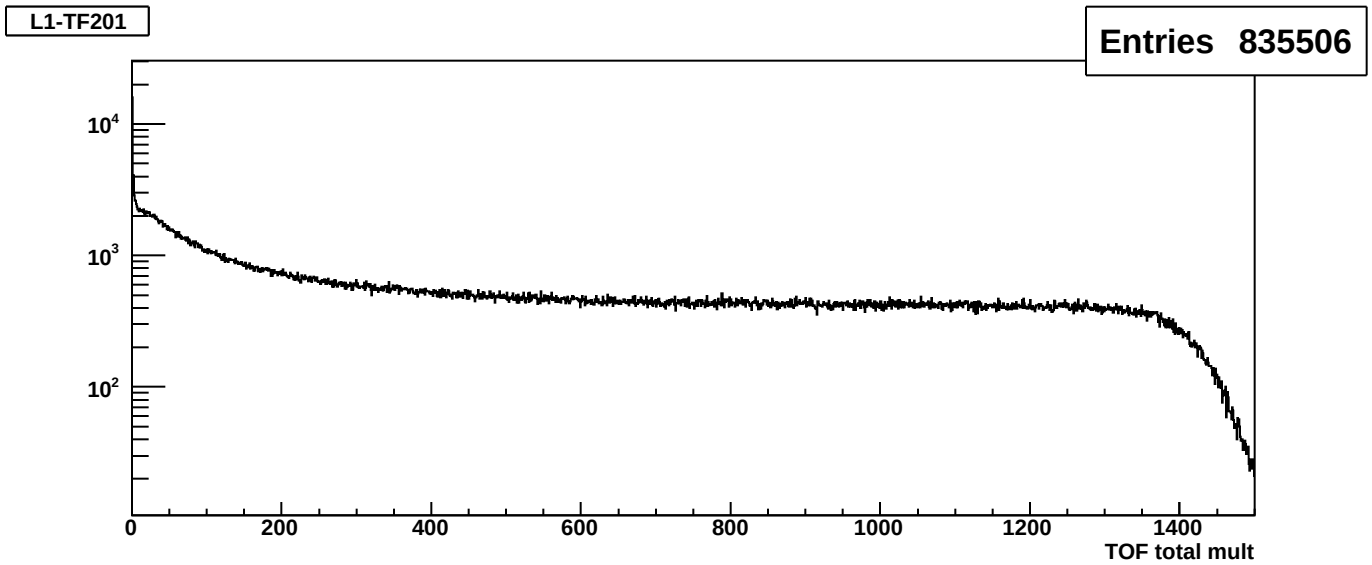

MIX-TF003
Entries 3.509125e+07
TOF MULT
TOF tray

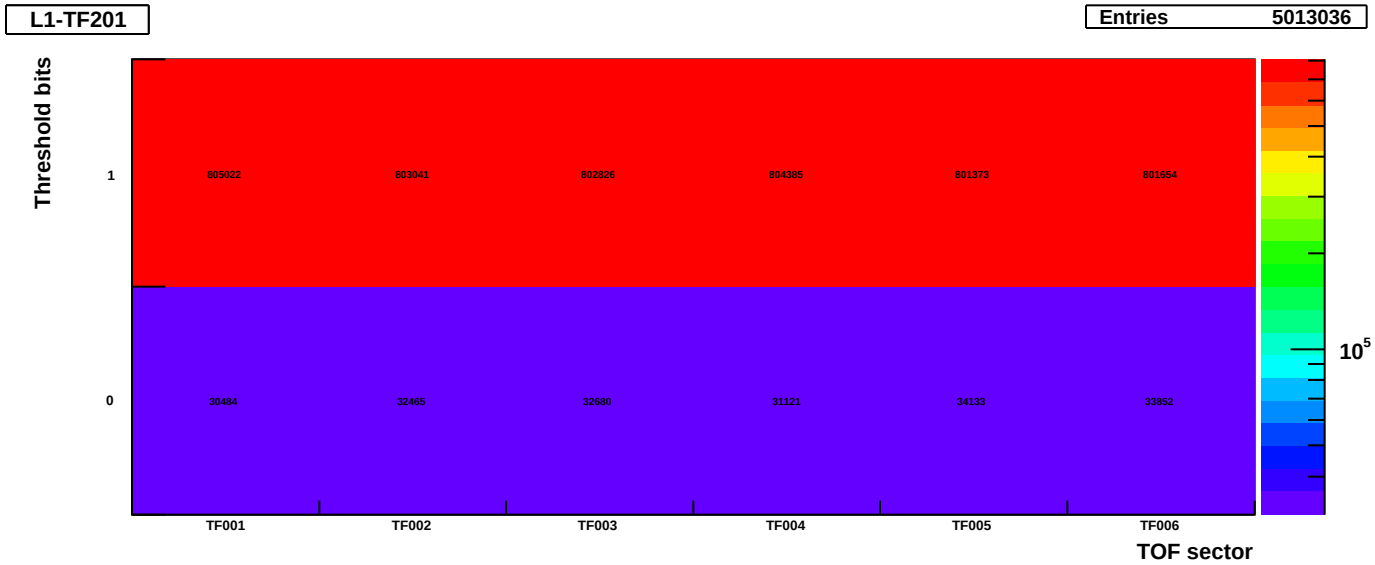

MIX-TF004
Entries 3.509125e+07
TOF MULT
TOF tray

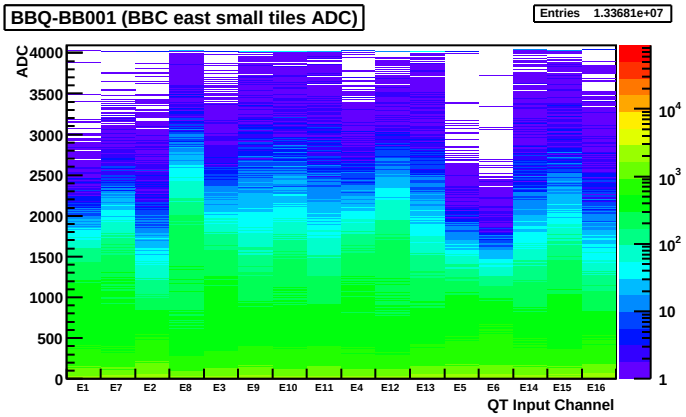

MIX-TF005
Entries 3.509125e+07
TOF MULT
TOF tray

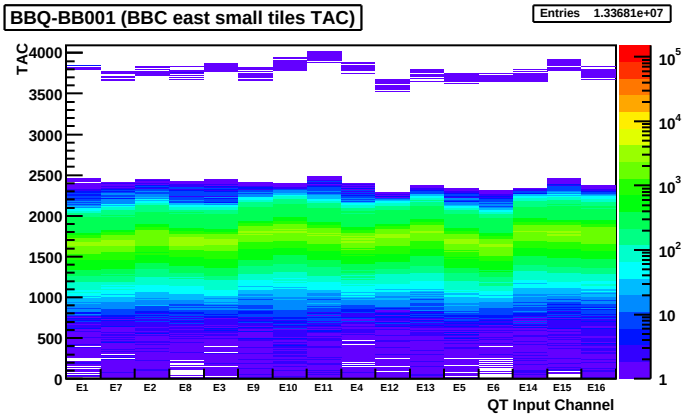

MIX-TF006
Entries 3.509125e+07
TOF MULT
TOF tray

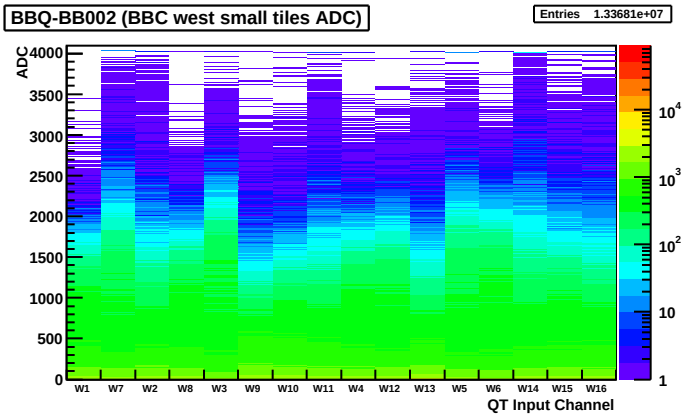

MIX-TF101
Entries 5013036
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

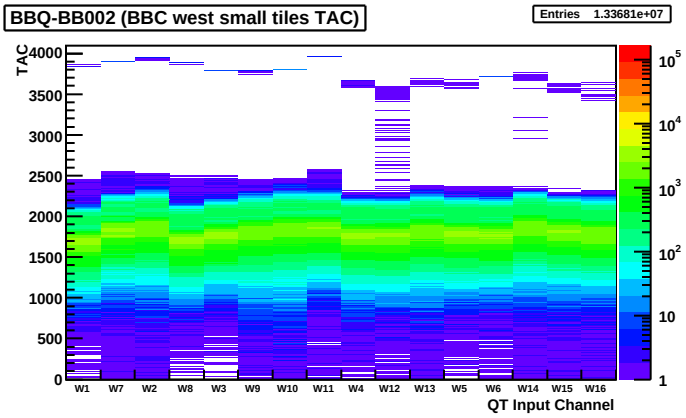

L1-TF201
Entries 835506
10^4
10^3
10^2
0
200
400
600
800
1000
1200
1400
TOF total mult

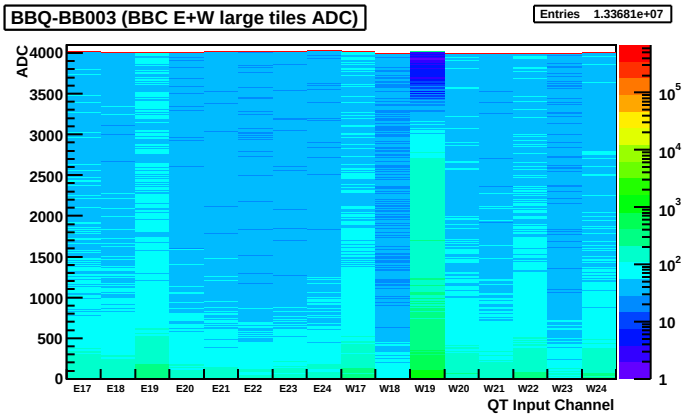

L1-TF201
Entries 5013036
Threshold bits
1
0
805022
803041
802826
804385
801373
801654
30484
32465
32680
31121
34133
33852
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

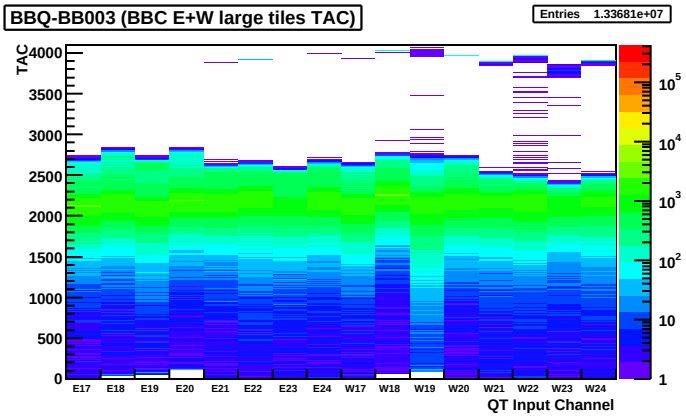

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.33681e+07
ADC
QT Input Channel

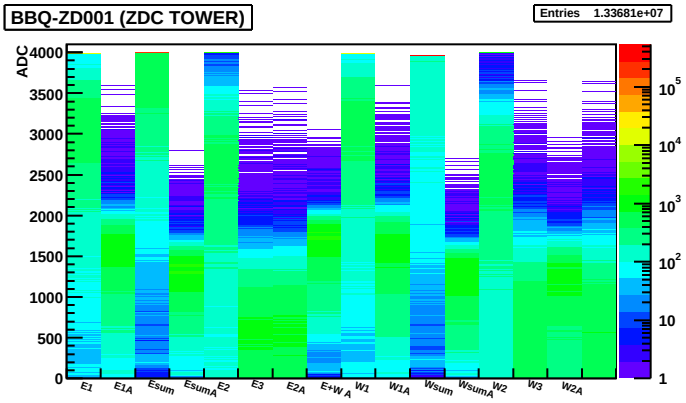

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.33681e+07
TAC
QT Input Channel

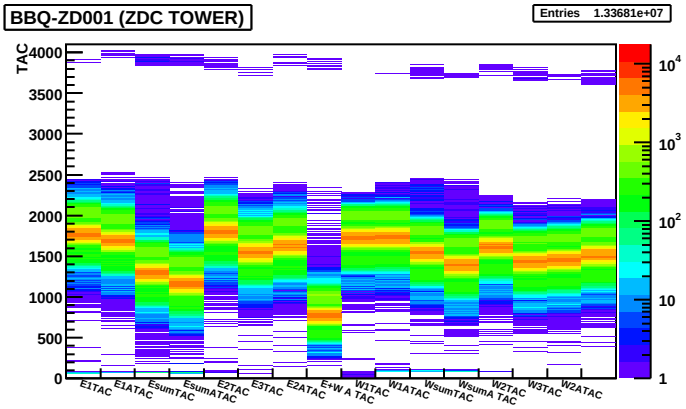

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.33681e+07
ADC
QT Input Channel

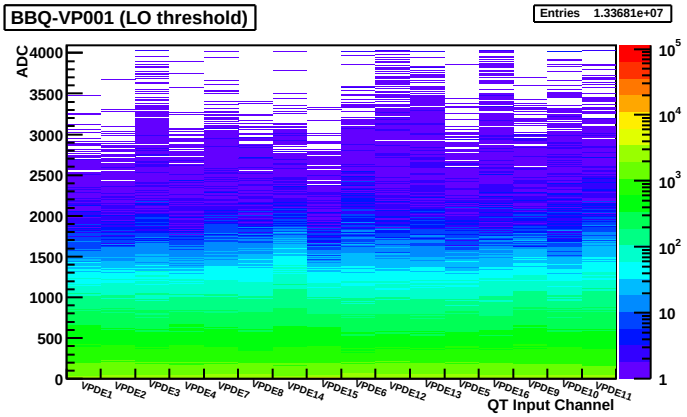

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.33681e+07
TAC
QT Input Channel

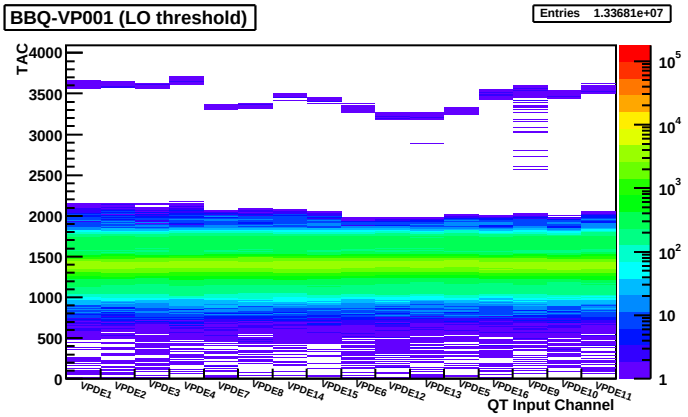

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.33681e+07
ADC
QT Input Channel

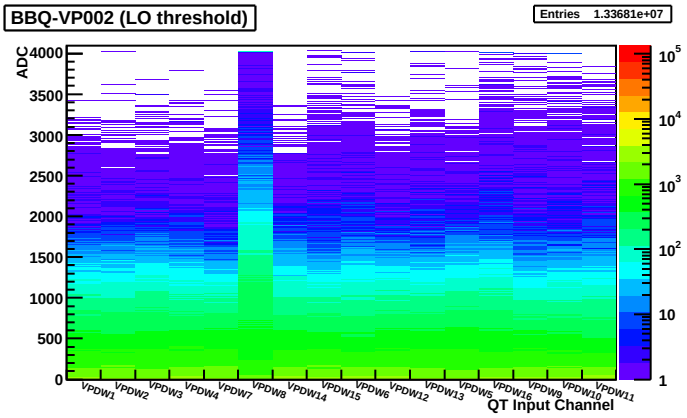

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.33681e+07
TAC
QT Input Channel

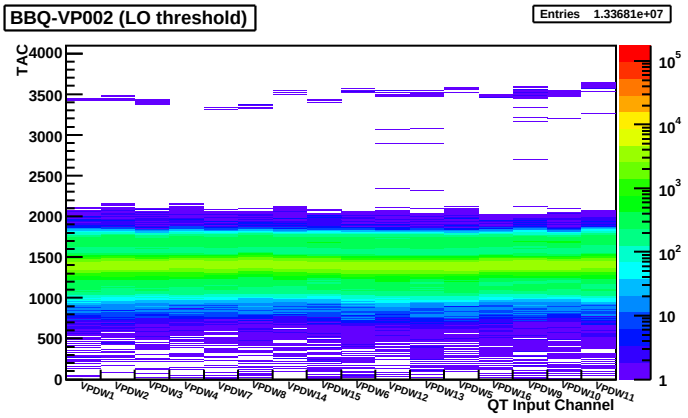

BBQ-ZD001 (ZDC TOWER)
Entries 1.33681e+07
ADC

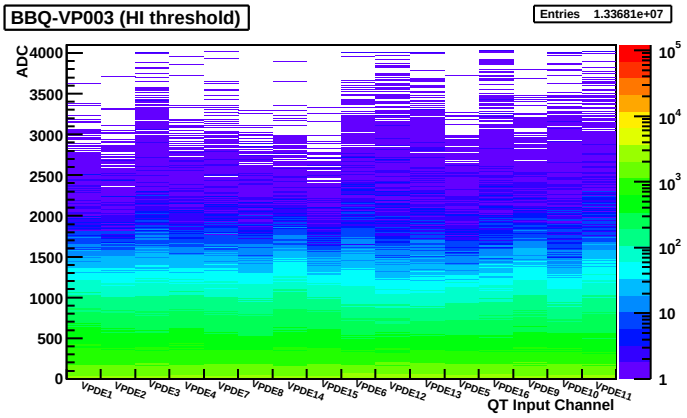

BBQ-ZD001 (ZDC TOWER)
Entries 1.33681e+07
TAC

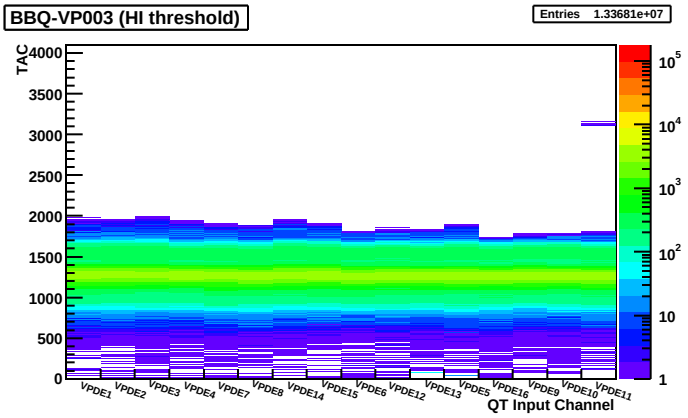

BBQ-VP001 (LO threshold)
Entries 1.33681e+07
ADC
QT Input Channel

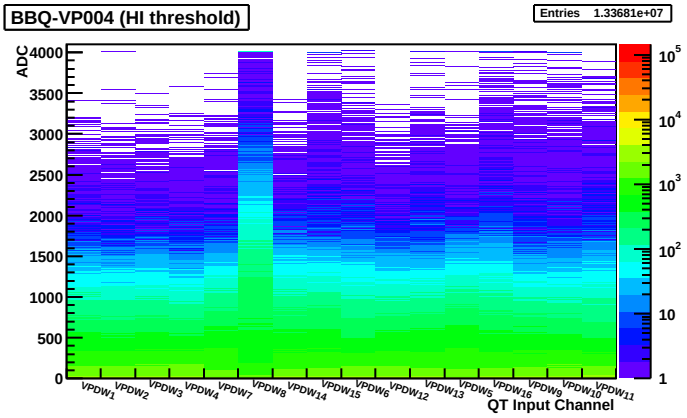

BBQ-VP001 (LO threshold)
Entries 1.33681e+07
TAC
QT Input Channel

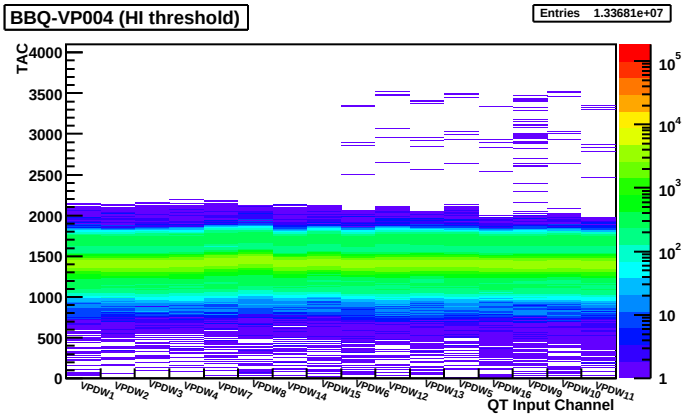

BBQ-VP002 (LO threshold)
Entries 1.33681e+07
ADC
QT Input Channel

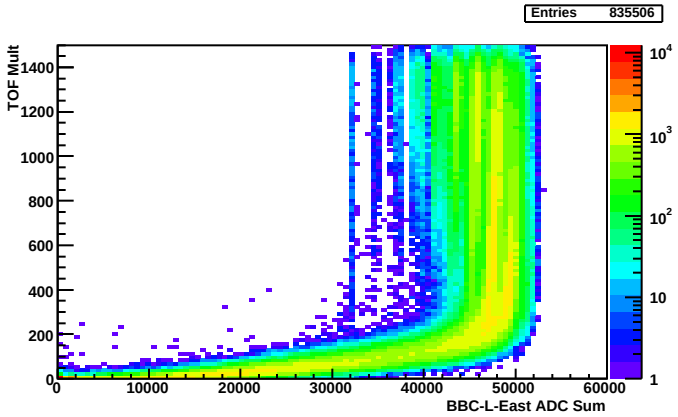

BBQ-VP002 (LO threshold)
Entries 1.33681e+07
TAC
QT Input Channel

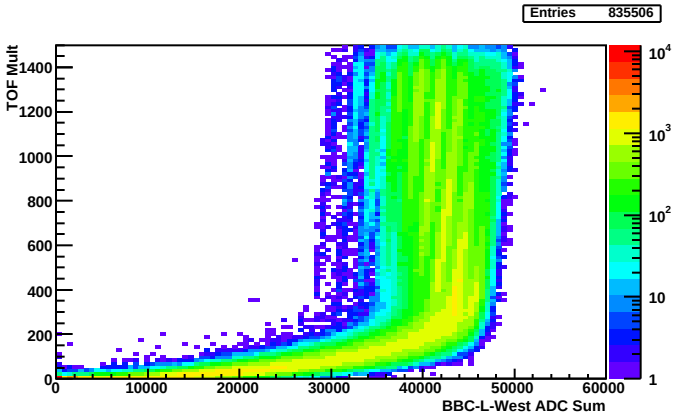

BBQ-VP003 (HI threshold)
Entries 1.33681e+07
ADC
QT Input Channel

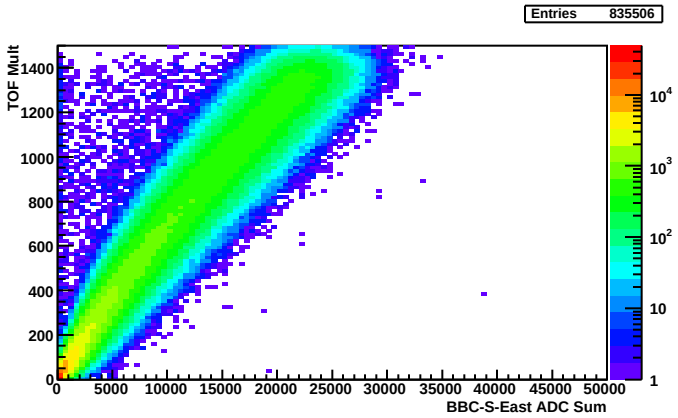

BBQ-VP003 (HI threshold)
Entries 1.33681e+07
TAC
QT Input Channel

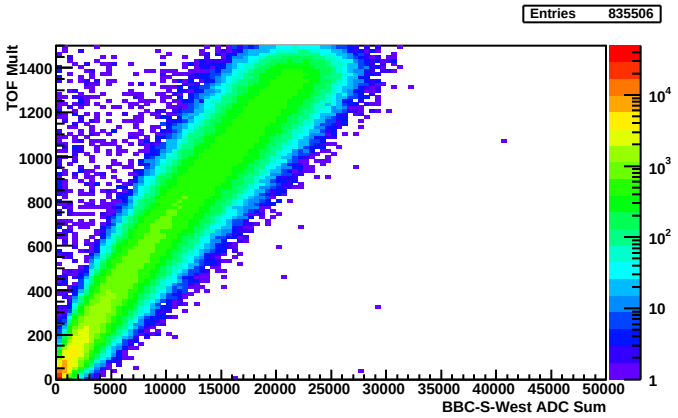

BBQ-VP004 (HI threshold)
Entries 1.33681e+07
ADC
QT Input Channel

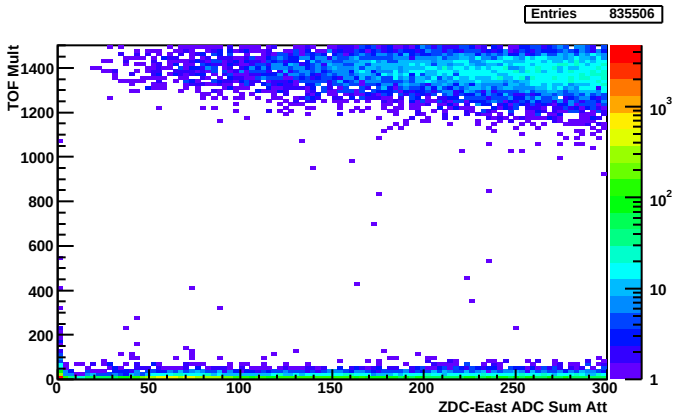

BBQ-VP004 (HI threshold)
Entries 1.33681e+07
TAC
QT Input Channel

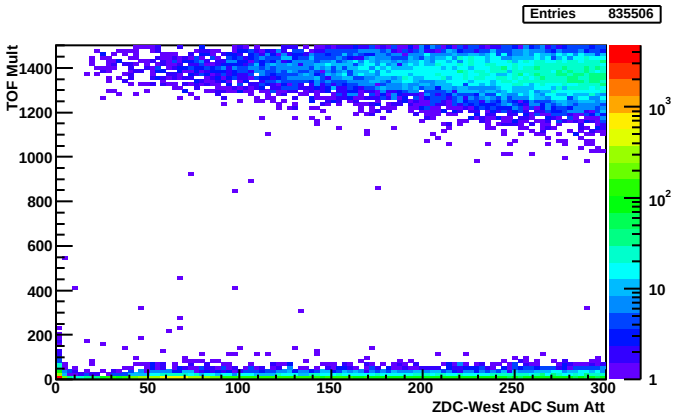

Entries 835506
TOF Mult
BBC-L-East ADC Sum

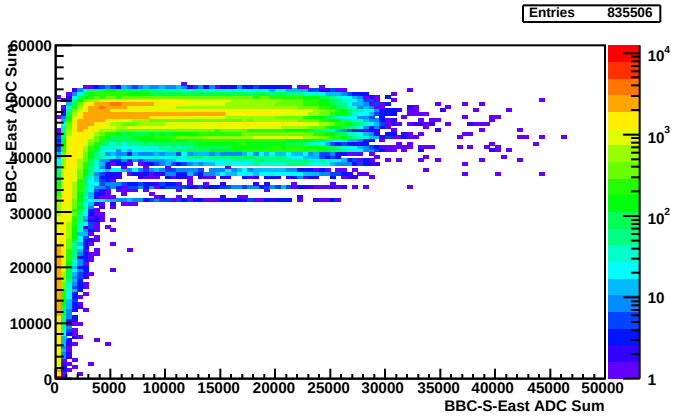

Entries 835506
TOF Mult
BBC-L-West ADC Sum

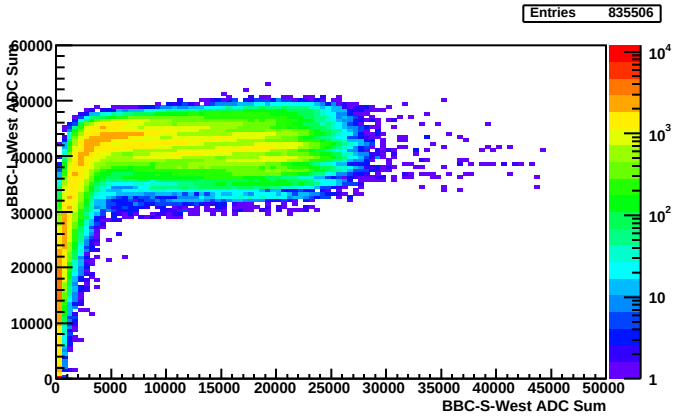

Entries 835506
TOF Mult
BBC-S-East ADC Sum

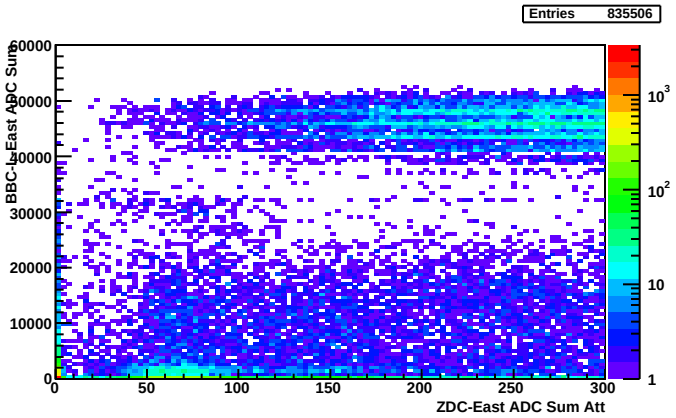

Entries 835506
TOF Mult
BBC-S-West ADC Sum

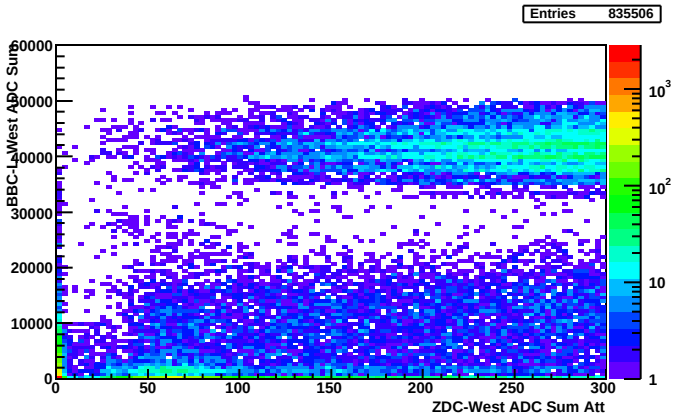

Entries 835506
TOF Mult
ZDC-East ADC Sum Att


Entries 835506
TOF Mult
ZDC-West ADC Sum Att


Entries 835506
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 835506
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 835506
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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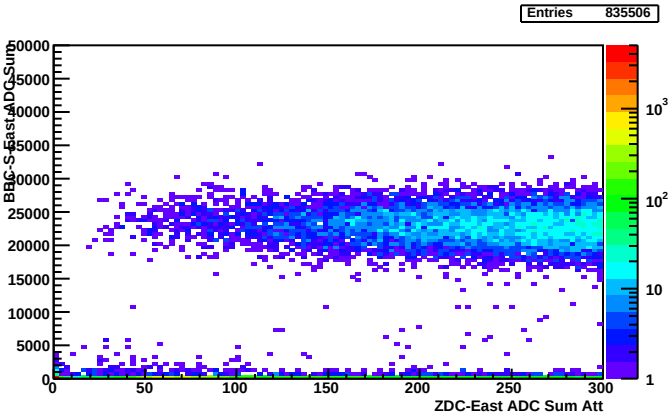
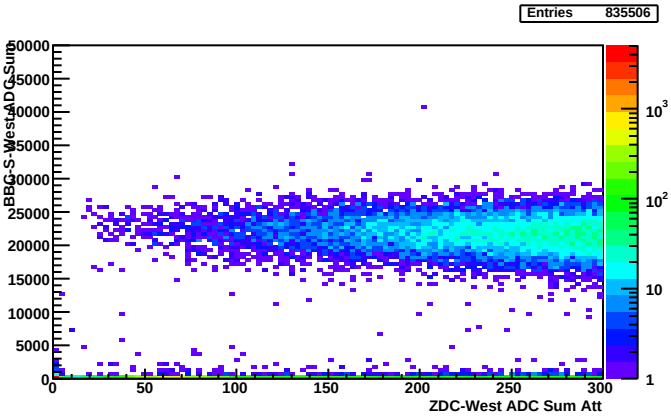
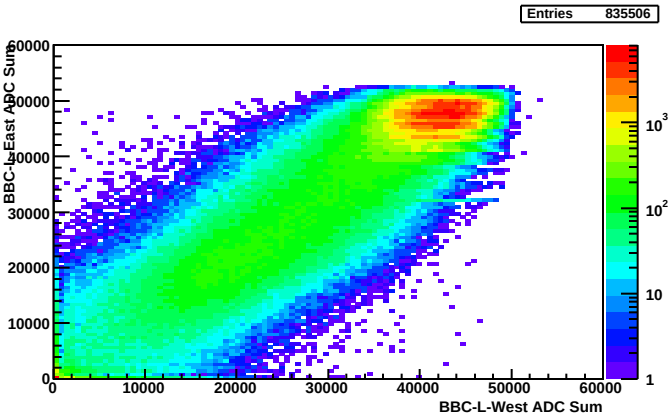
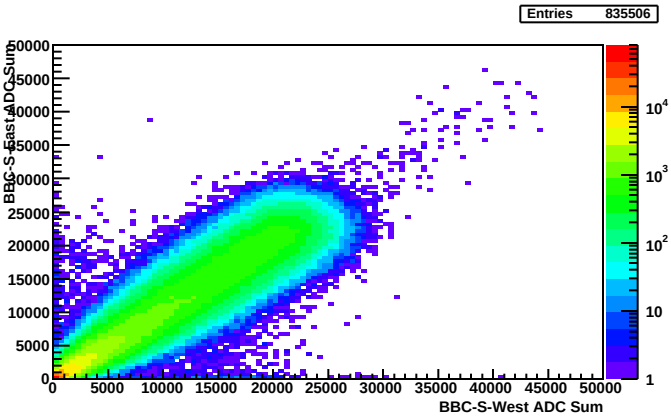
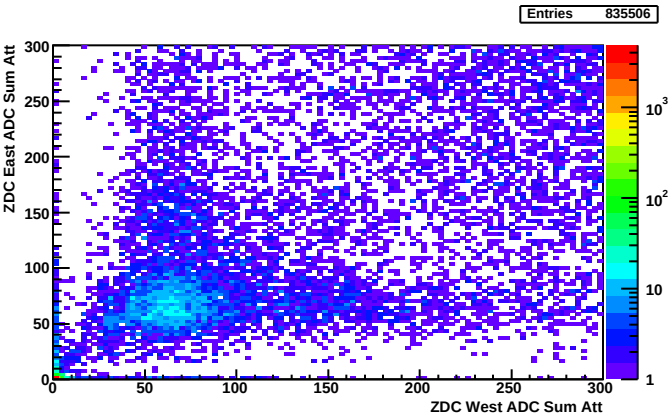
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

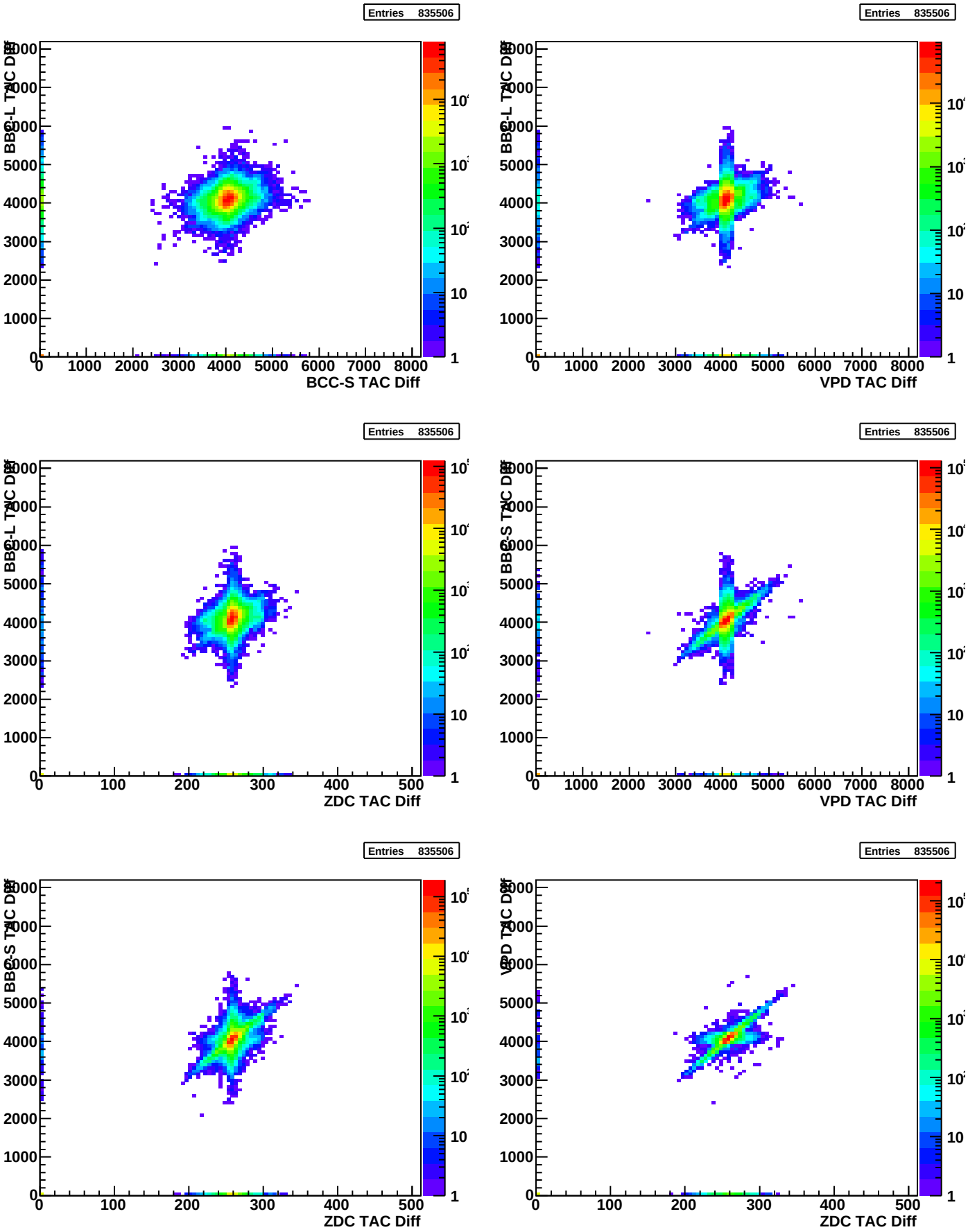

Entries 835506
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

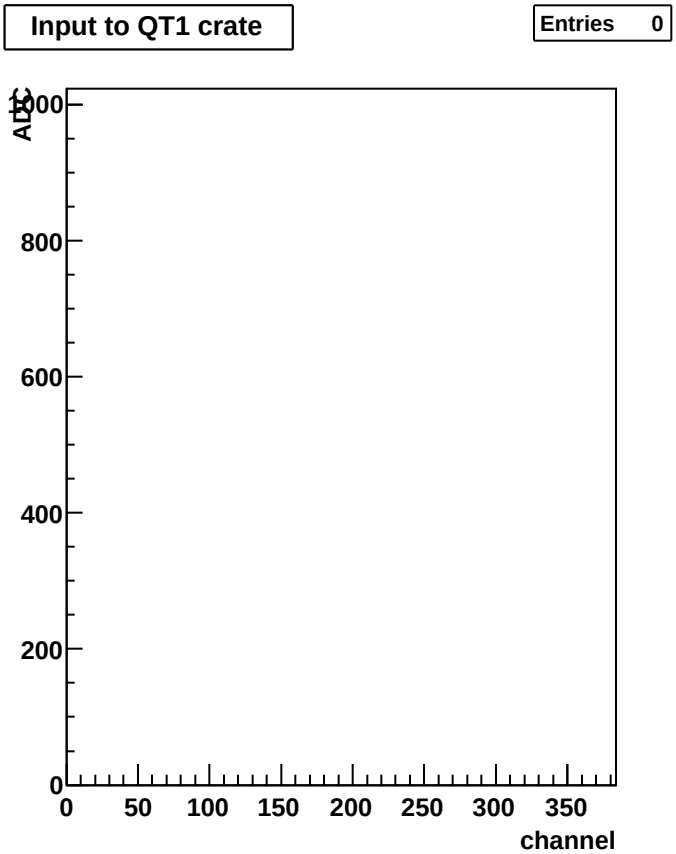

Entries 835506
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

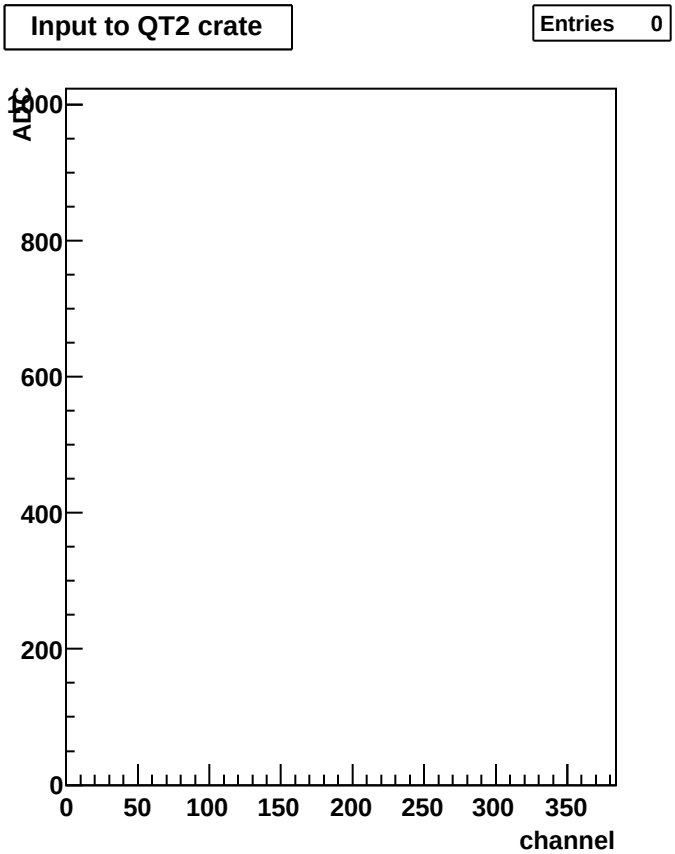

Entries 835506
BBC-L-East ADC Sum
BBC-L-West ADC Sum

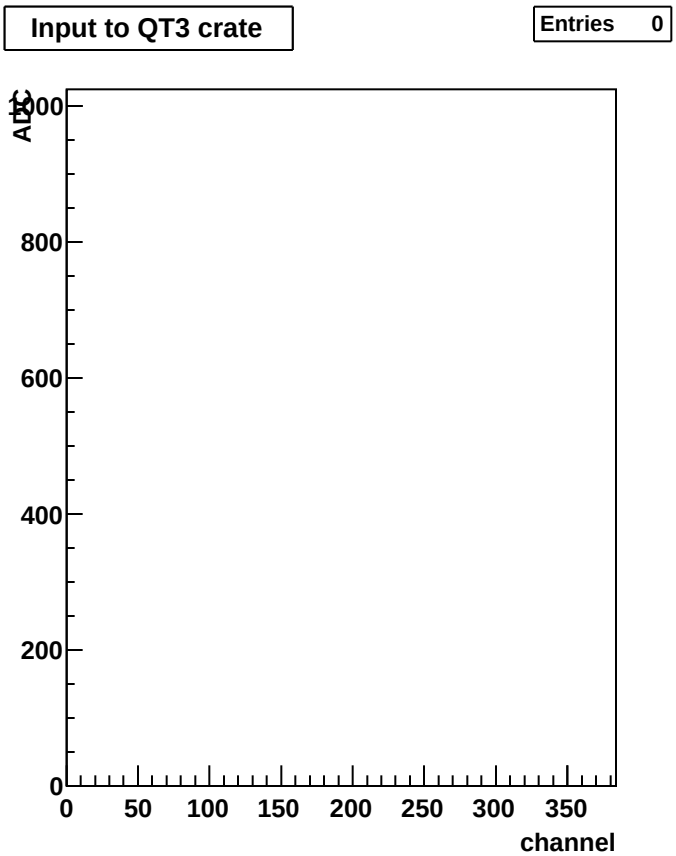

Entries 835506
BBC-S-East ADC Sum
BBC-S-West ADC Sum

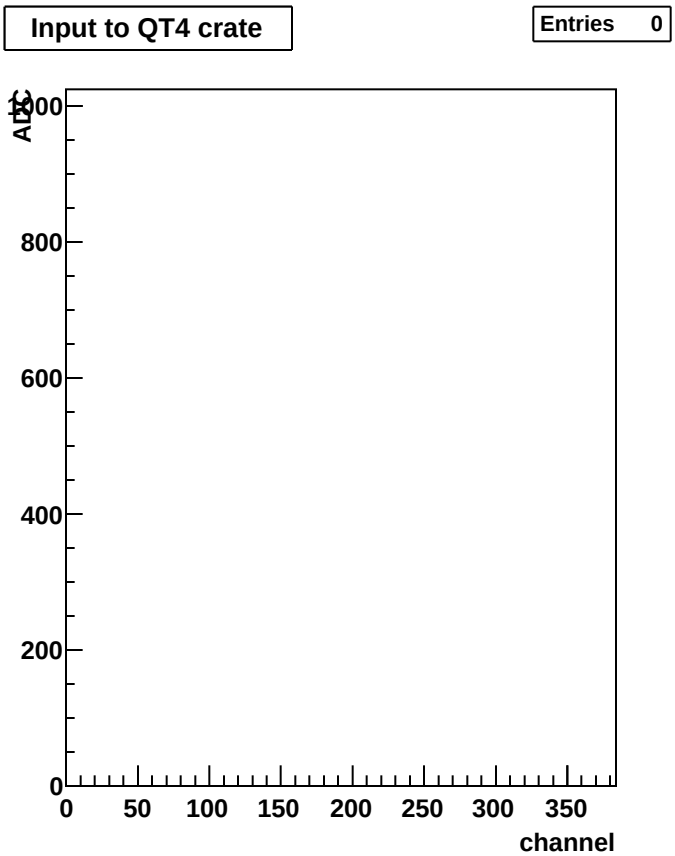

Entries 835506
ZDC East ADC Sum Att
ZDC West ADC Sum Att

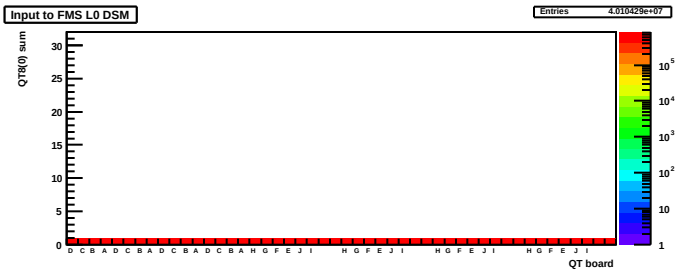

Entries 835506
BBC-L TAC Diff
BCC-S TAC Diff
Entries 835506
BBC-L TAC Diff
VPD TAC Diff
Entries 835506
BBC-L TAC Diff
ZDC TAC Diff
Entries 835506
BBC-S TAC Diff
VPD TAC Diff
Entries 835506
BBC-S TAC Diff
ZDC TAC Diff
Entries 835506
VPD TAC Diff
ZDC TAC Diff

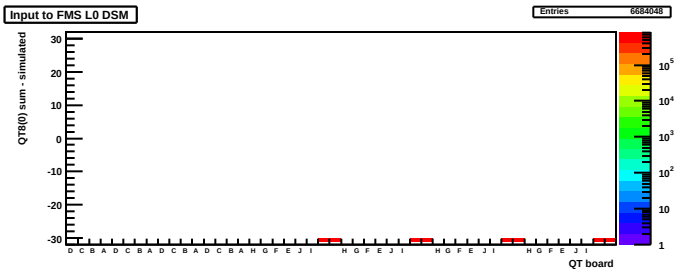

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

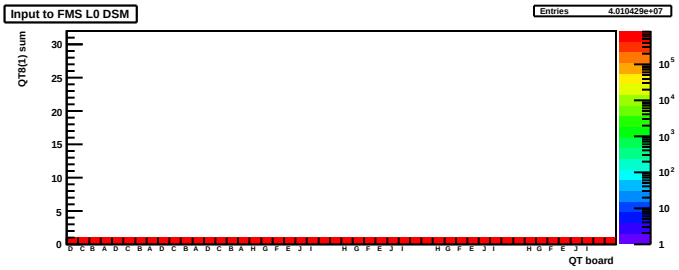

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

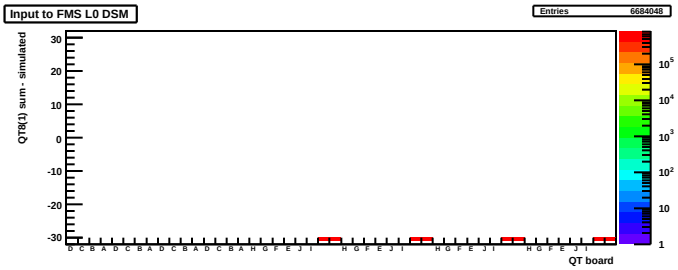

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

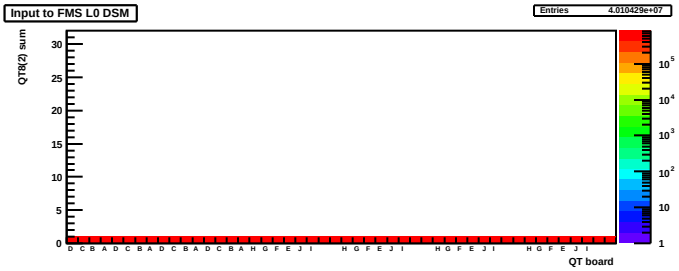

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

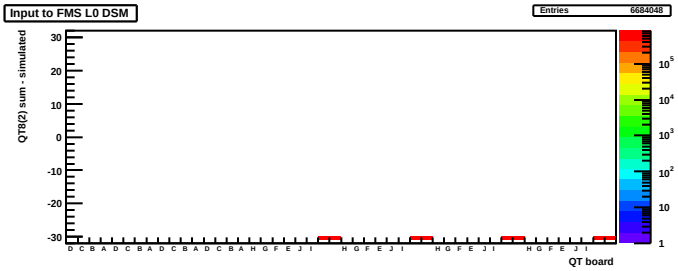

Input to FMS L0 DSM
Entries 4.010429e+07
QT8(0) sum
QT board

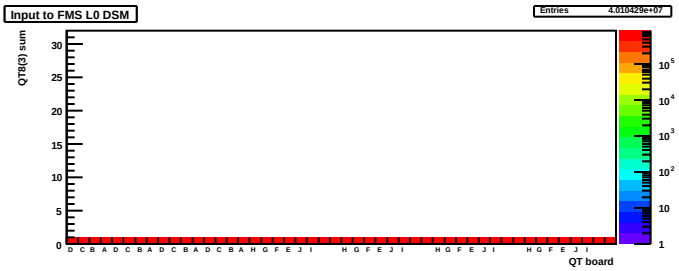

Input to FMS L0 DSM
Entries 6684048
QT8(0) sum - simulated
QT board

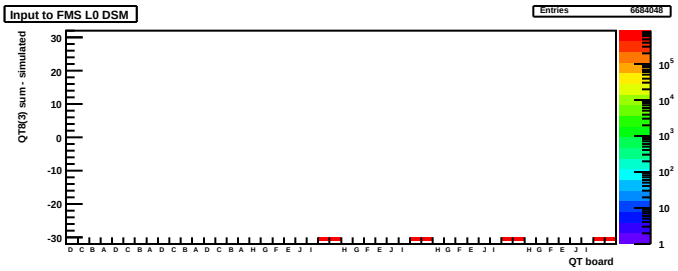

Input to FMS L0 DSM
Entries 4.010429e+07
QT8(1) sum
QT board

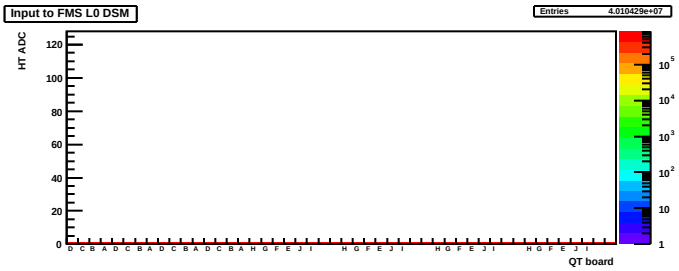

Input to FMS L0 DSM
Entries 6684048
QT8(1) sum - simulated
QT board

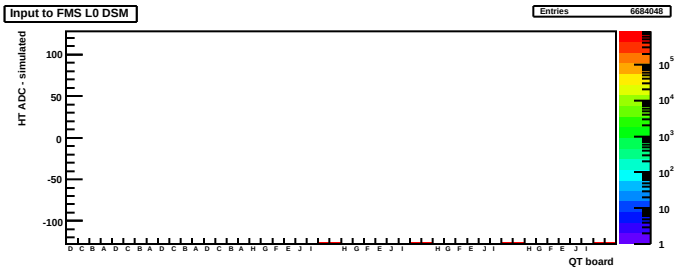

Input to FMS L0 DSM
Entries 4.010429e+07
QT8(2) sum
QT board

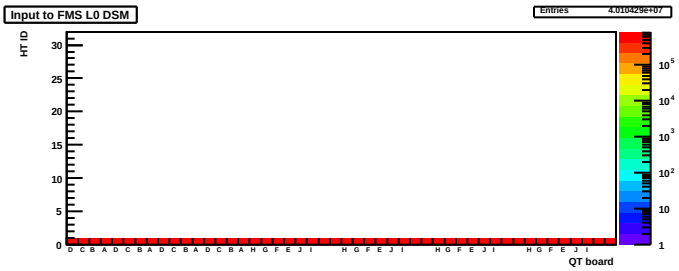

Input to FMS L0 DSM
Entries 6684048
QT8(2) sum - simulated
QT board

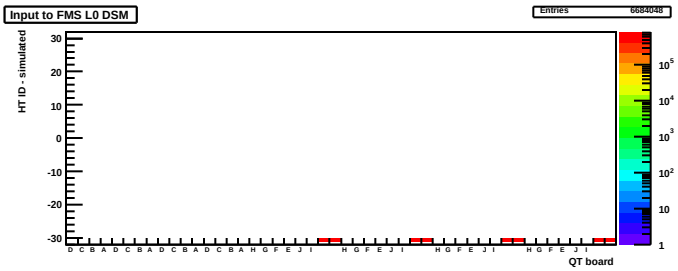

Input to FMS L0 DSM
Entries 4.010429e+07
QT8(3) sum
QT board

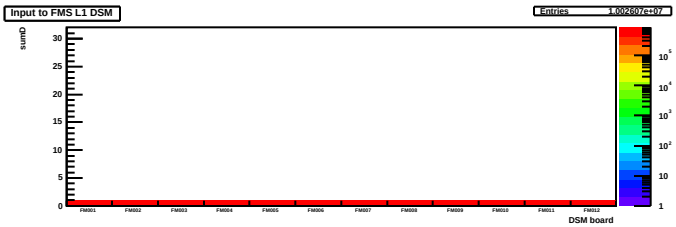

Input to FMS L0 DSM
Entries 6684048
QT8(3) sum - simulated
QT board

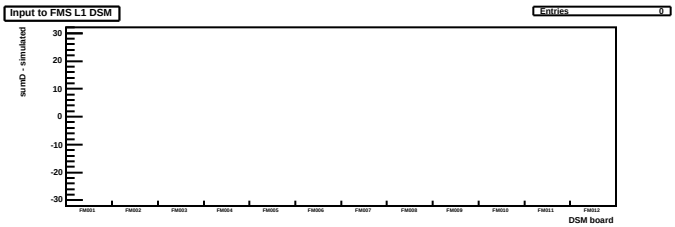

Input to FMS L0 DSM
Entries 4.010429e+07
HT ADC
QT board

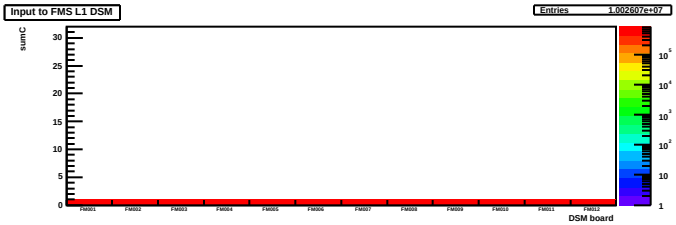

Input to FMS L0 DSM
Entries 6684048
HT ADC - simulated
QT board

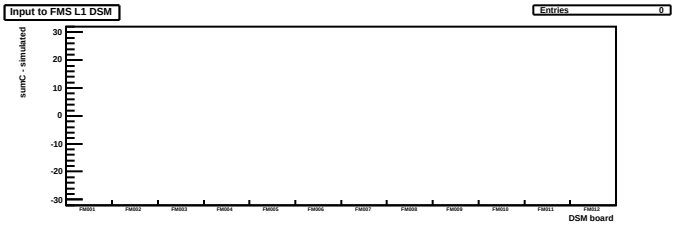

Input to FMS L0 DSM
Entries 4.010429e+07
HT ID
QT board

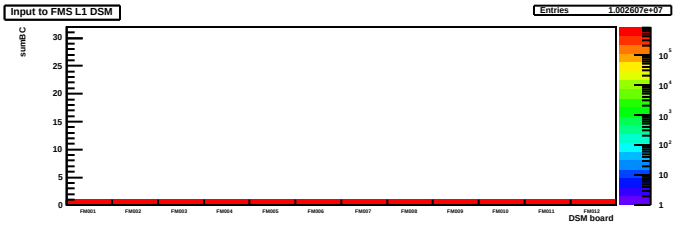

Input to FMS L0 DSM
Entries 6684048
HT ID - simulated
QT board

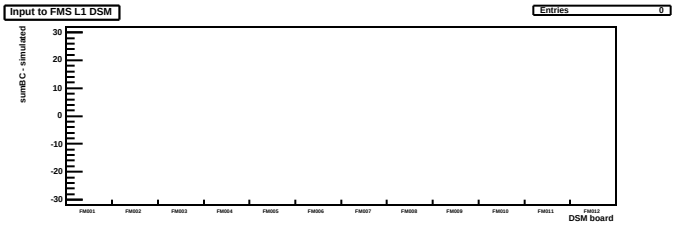

Input to FMS L1 DSM
Entries 1.002607e+07
sumD
DSM board

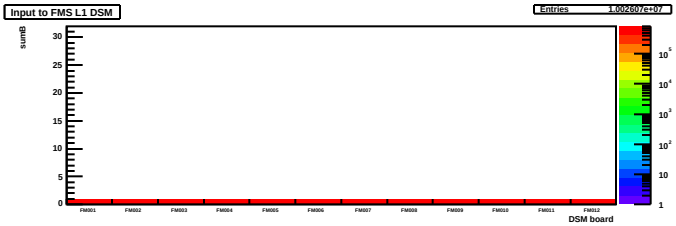

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

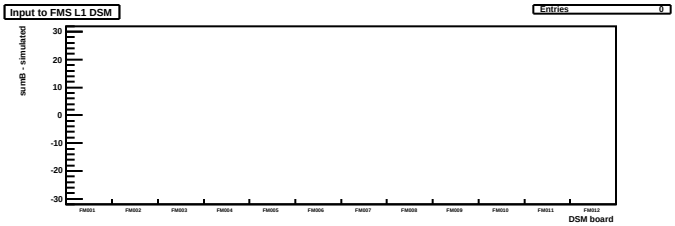

Input to FMS L1 DSM
Entries 1.002607e+07
sumC
DSM board

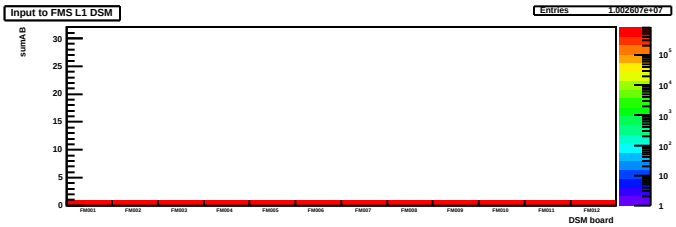

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

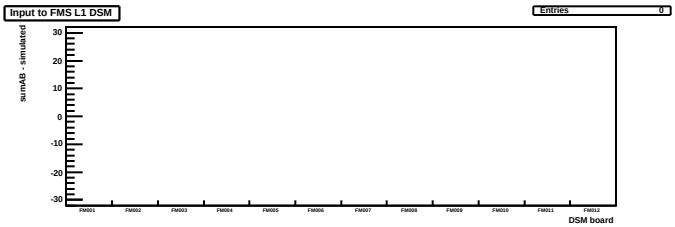

Input to FMS L1 DSM
Entries 1.002607e+07
sumBC
DSM board

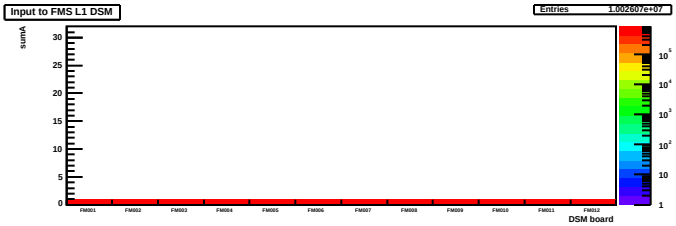

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

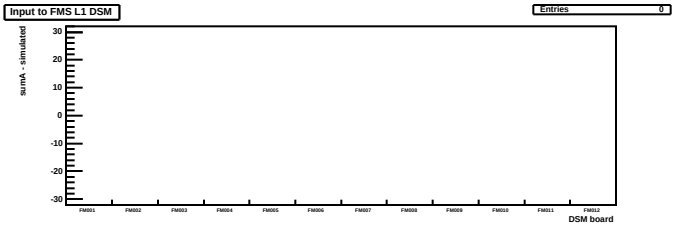

Input to FMS L1 DSM
Entries 1.002607e+07
sumB
DSM board

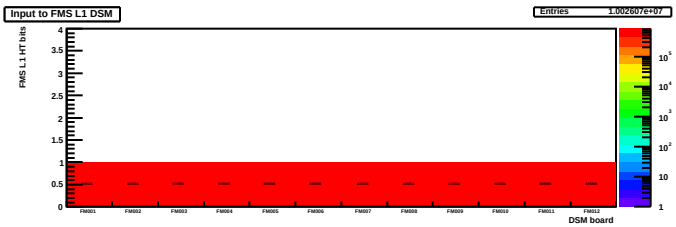

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

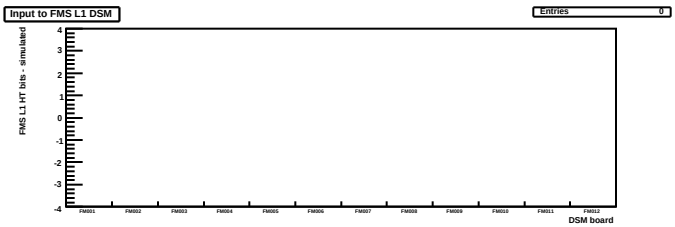

Input to FMS L1 DSM
Entries 1.002607e+07
sumAB
DSM board

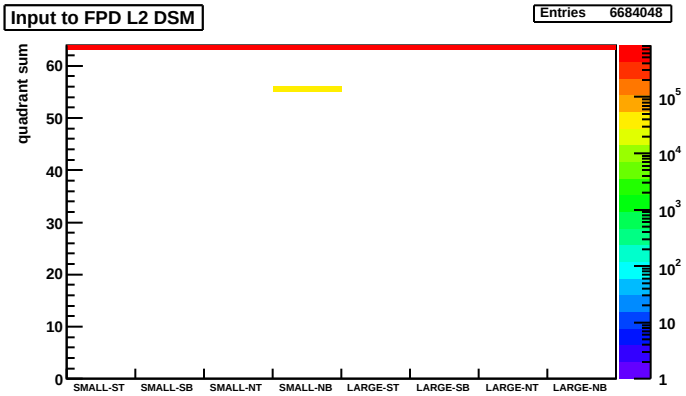

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

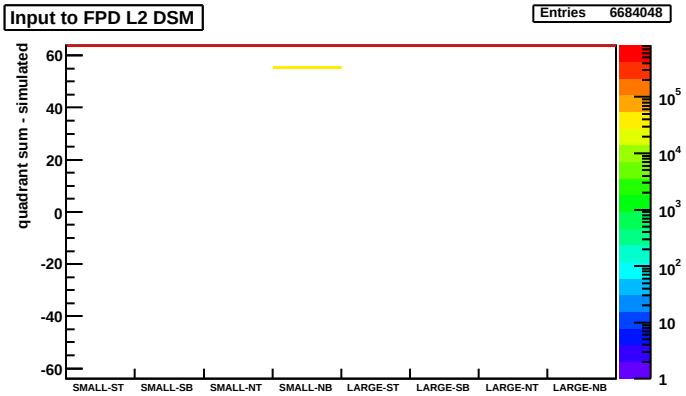

Input to FMS L1 DSM
Entries 1.002607e+07
sumA
DSM board

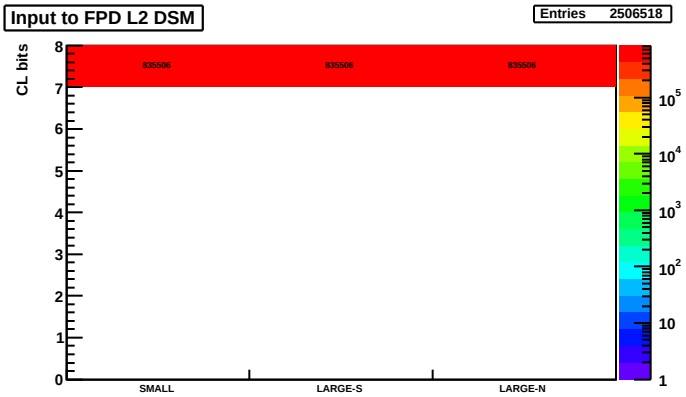

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

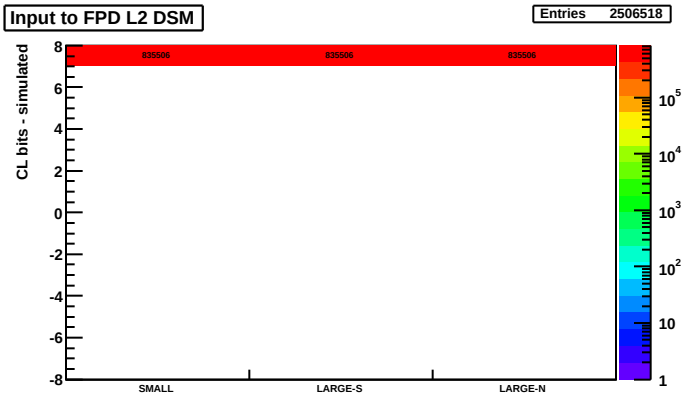

Input to FMS L1 DSM
Entries 1.002607e+07
FMS L1 HT bits
DSM board

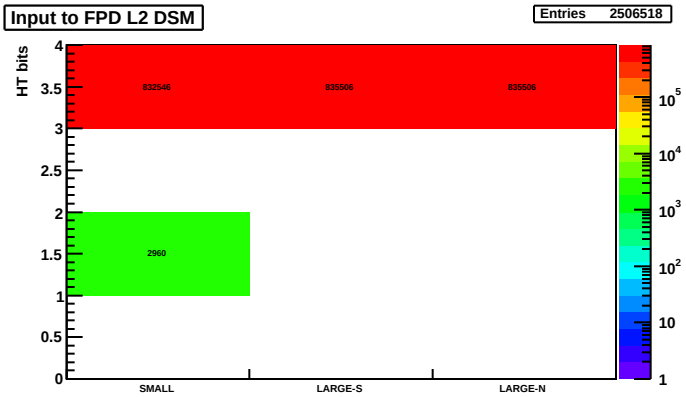

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

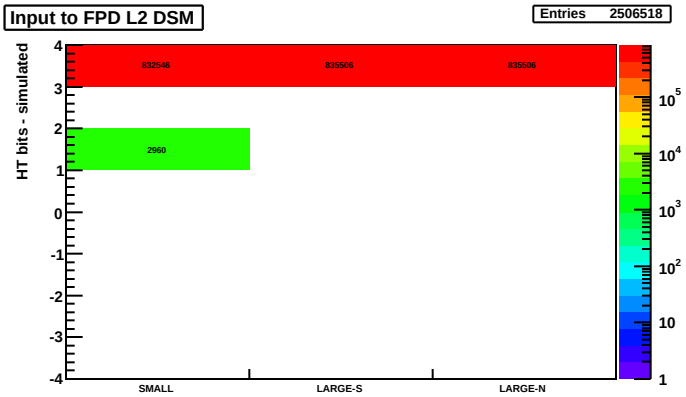

Input to FPD L2 DSM
Entries 6684048
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

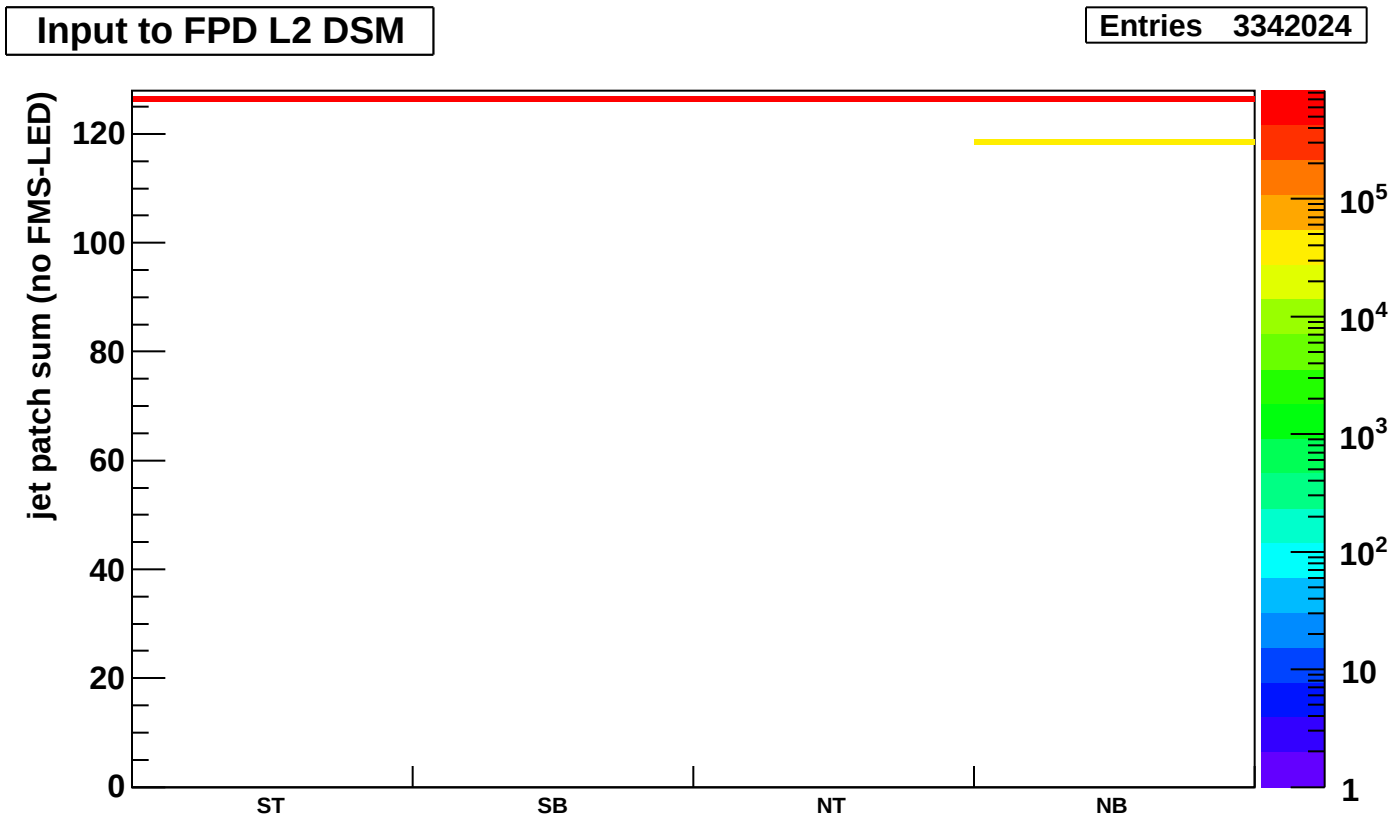

Input to FPD L2 DSM
Entries 6684048
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

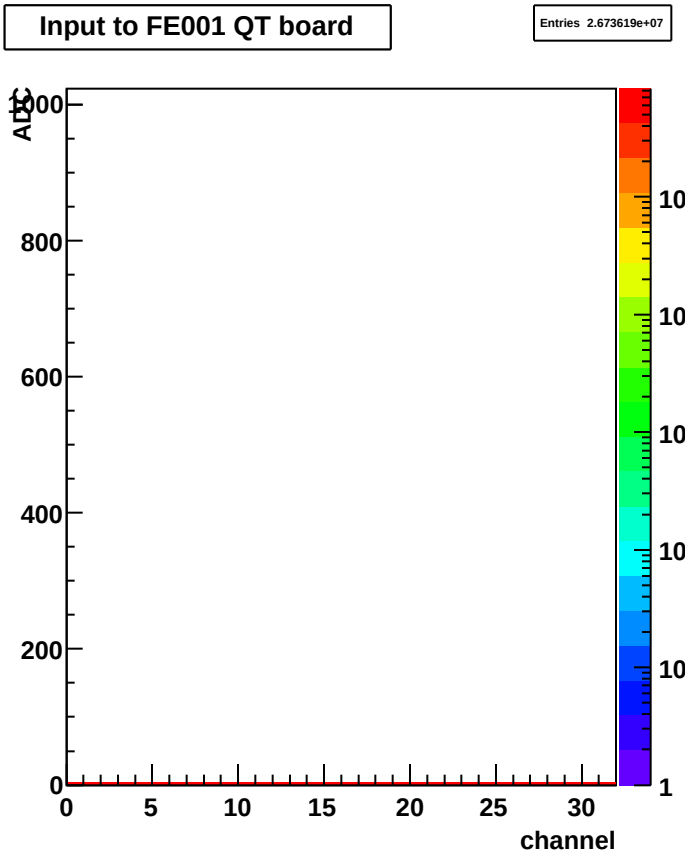

Input to FPD L2 DSM
Entries 2506518
CL bits
835506
835506
835506
SMALL
LARGE-S
LARGE-N

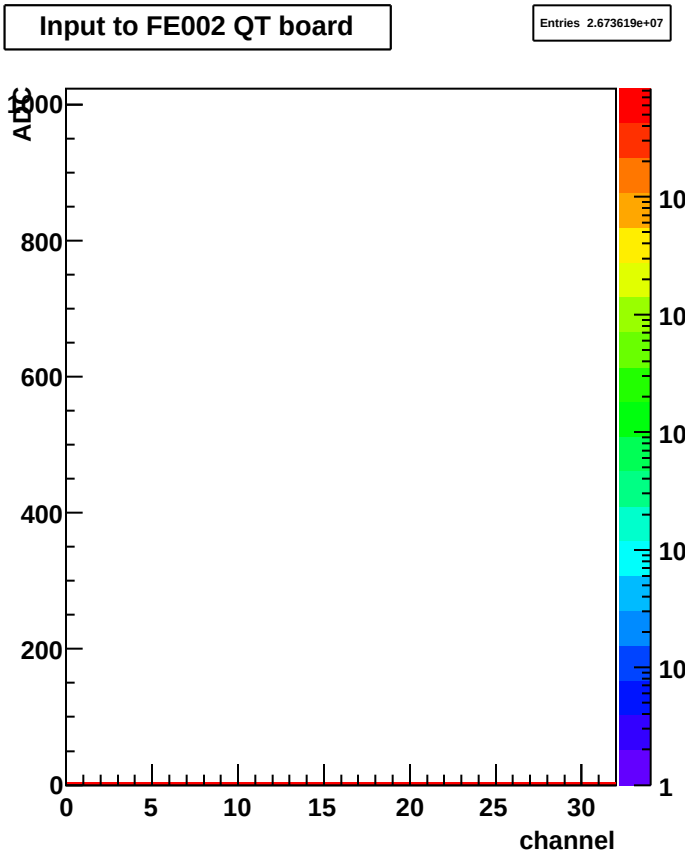

Input to FPD L2 DSM
Entries 2506518
CL bits - simulated
835506
835506
835506
SMALL
LARGE-S
LARGE-N

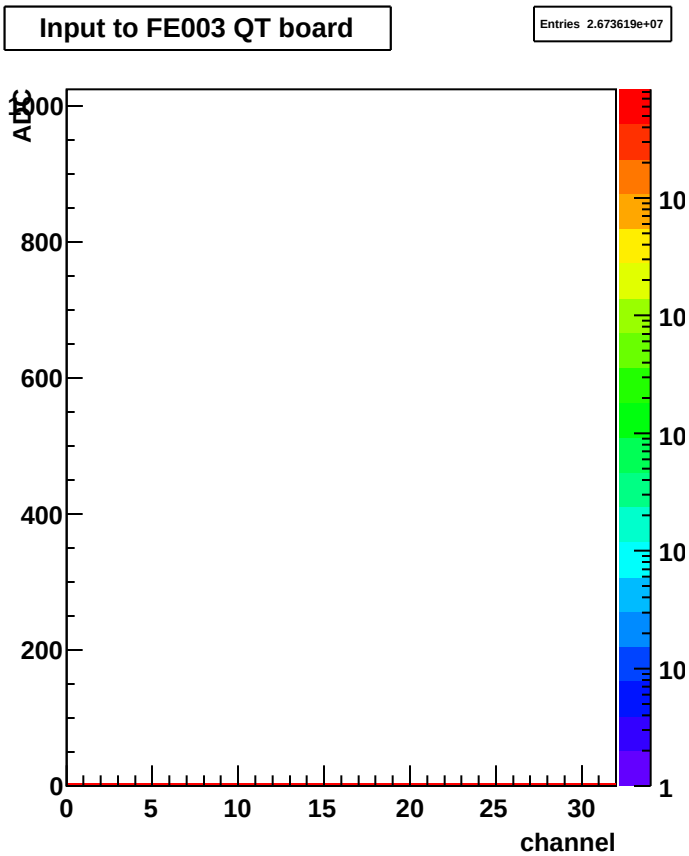

Input to FPD L2 DSM
Entries 2506518
HT bits
832546
835506
835506
2960
SMALL
LARGE-S
LARGE-N

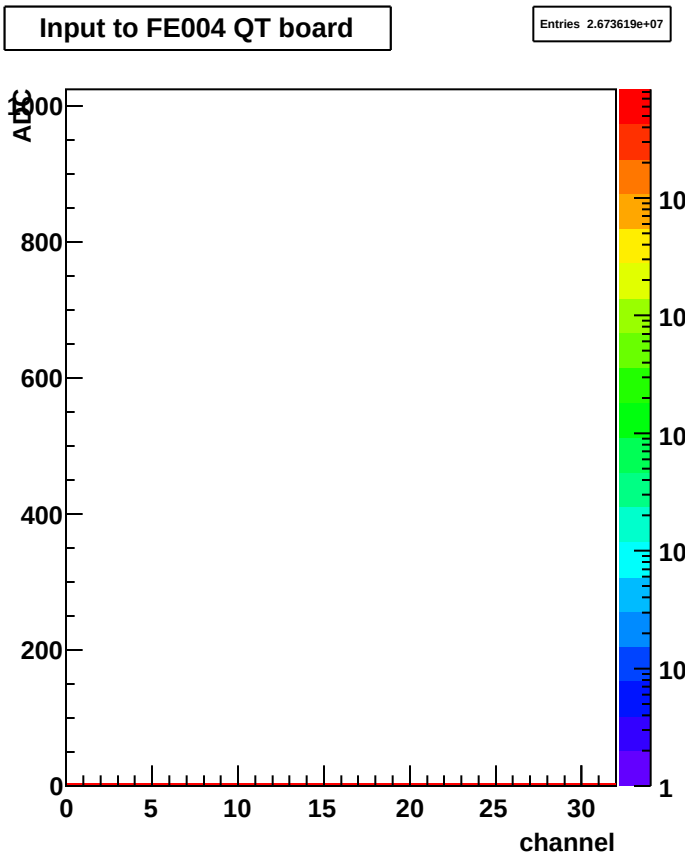

Input to FPD L2 DSM
Entries 2506518
HT bits - simulated
832546
835506
835506
2960
SMALL
LARGE-S
LARGE-N

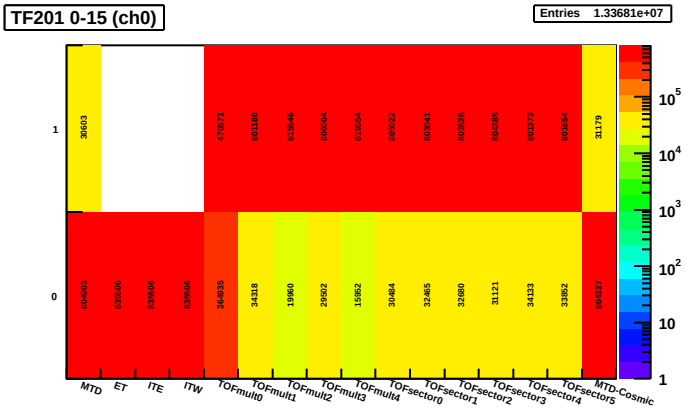

Input to FPD L2 DSM
Entries 3342024
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

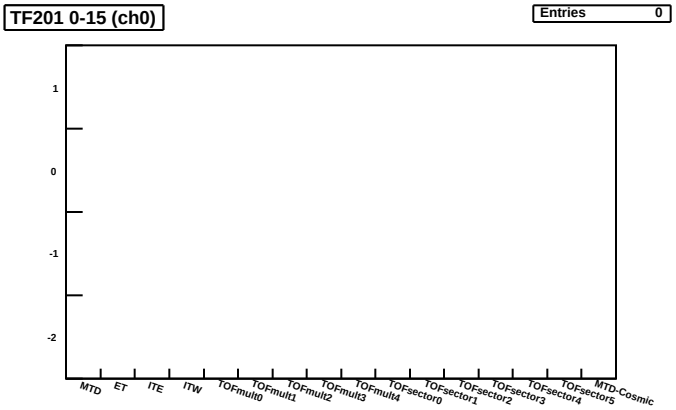

Input to FE001 QT board
Entries 2.673619e+07
ADC
channel

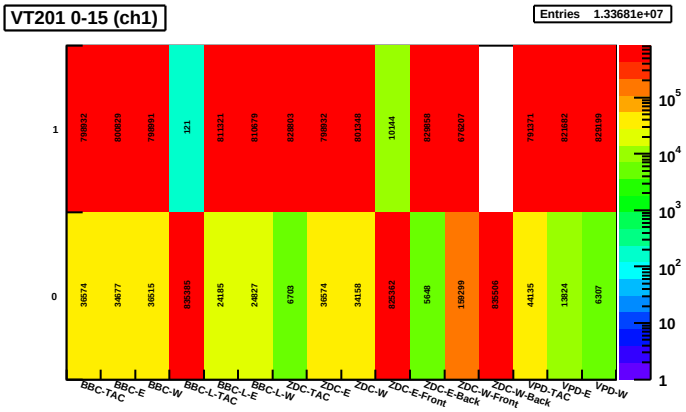

Input to FE002 QT board
Entries 2.673619e+07
ADC
channel

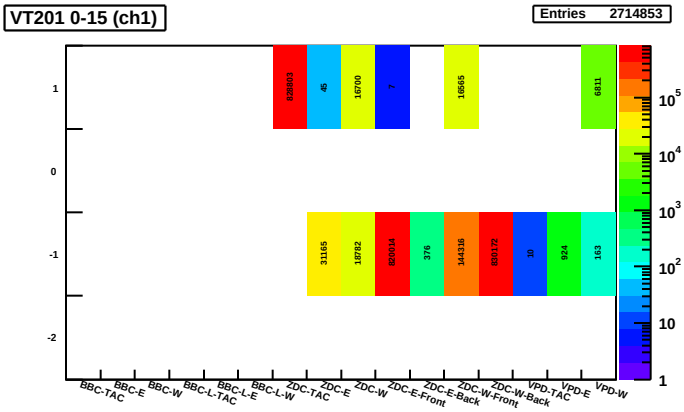

Input to FE003 QT board
Entries 2.673619e+07
ADC
channel

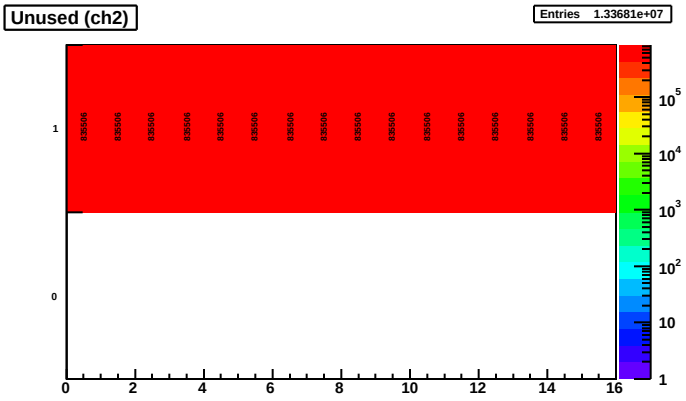

Input to FE004 QT board
Entries 2.673619e+07
ADC
channel

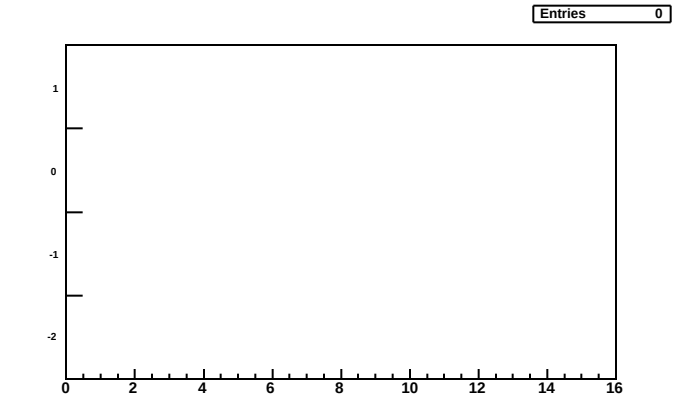

TF201 0-15 (ch0)
Entries 1.33681e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

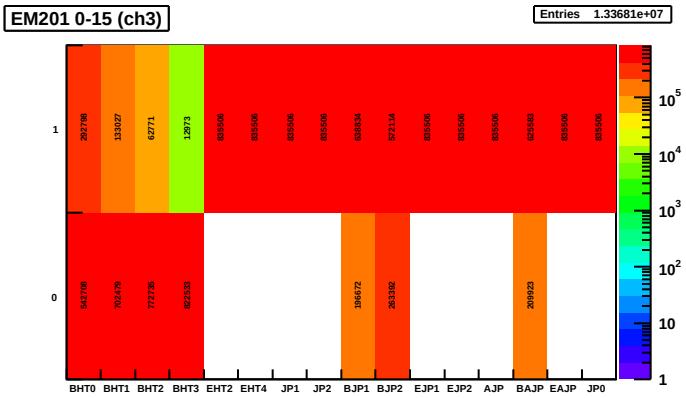

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

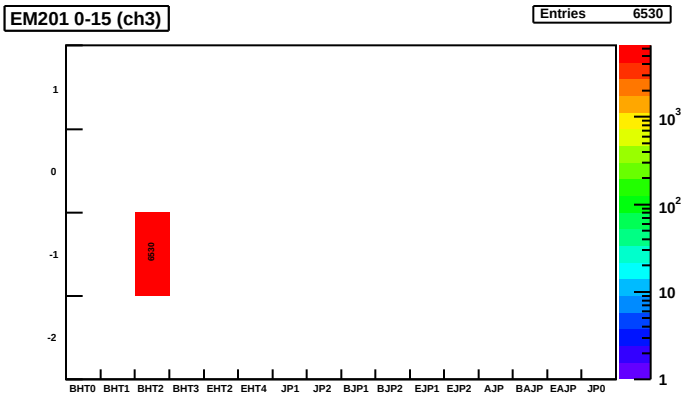

VT201 0-15 (ch1)
Entries 1.33681e+07
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 2714853
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

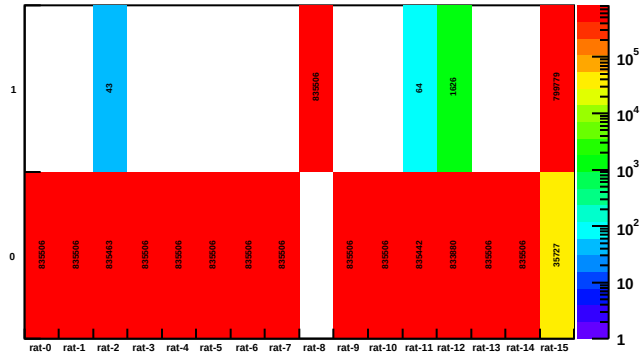

Unused (ch2)
Entries 1.33681e+07

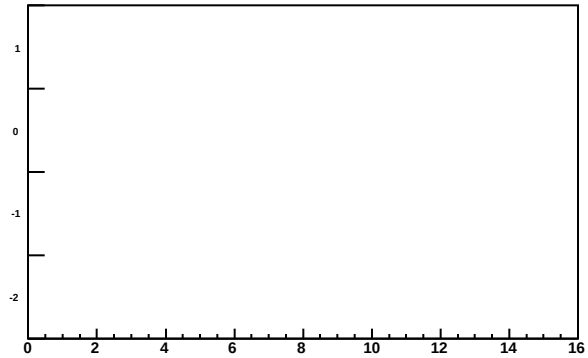

Entries 0


EM201 0-15 (ch3)
Entries 1.33681e+07
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

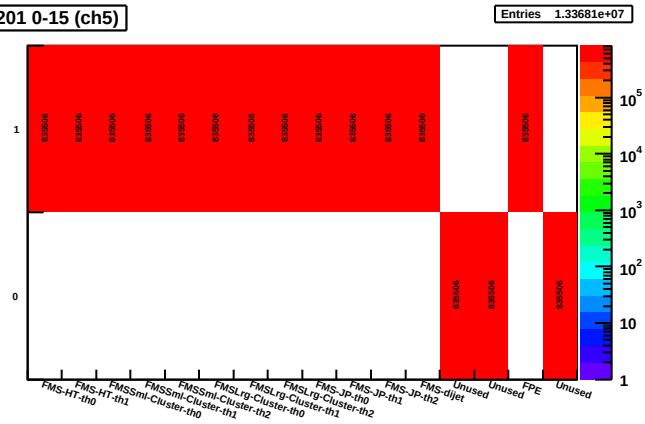

EM201 0-15 (ch3)
Entries 6530
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

RAT board (ch4)

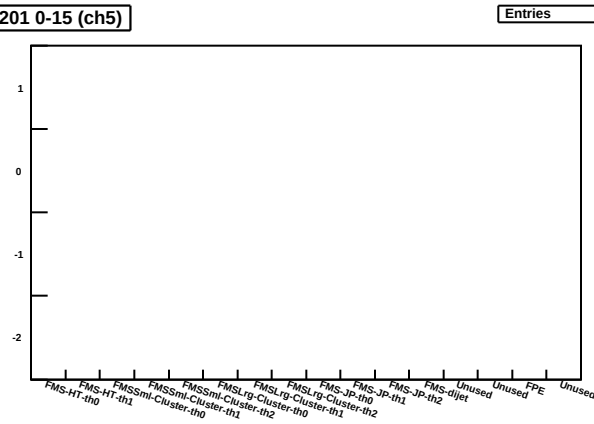




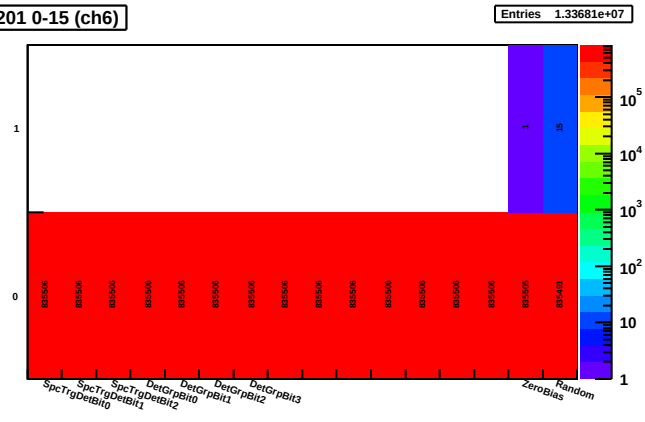
FP201 0-15 (ch5)

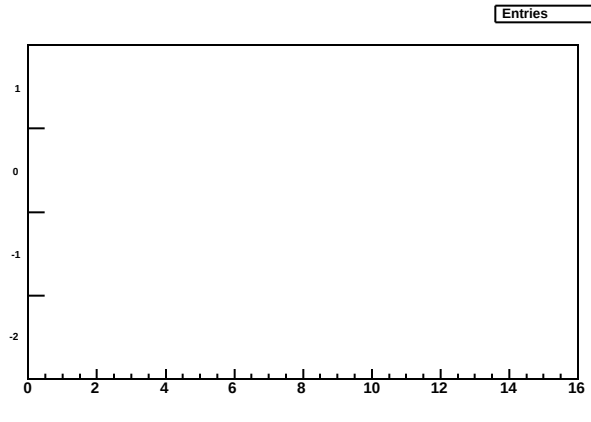
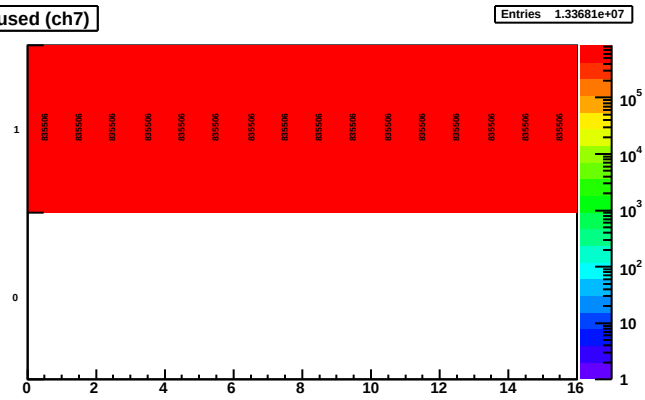
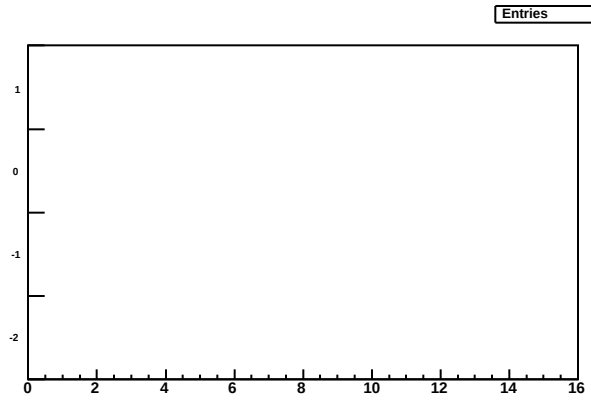


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)