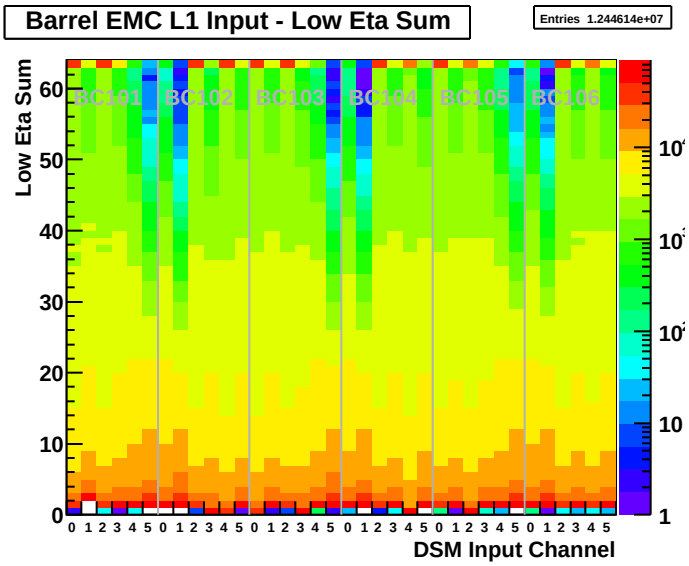
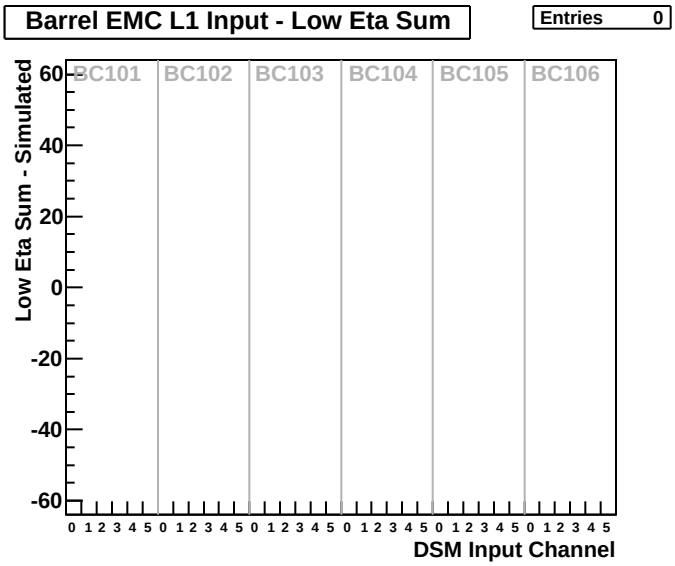
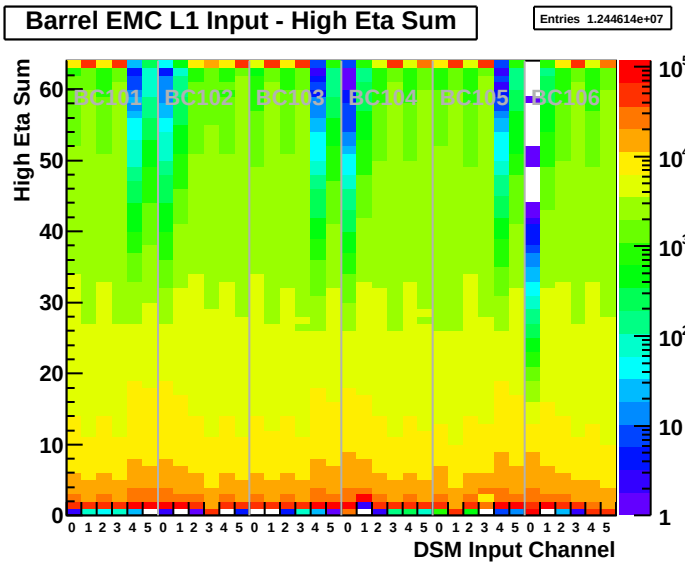
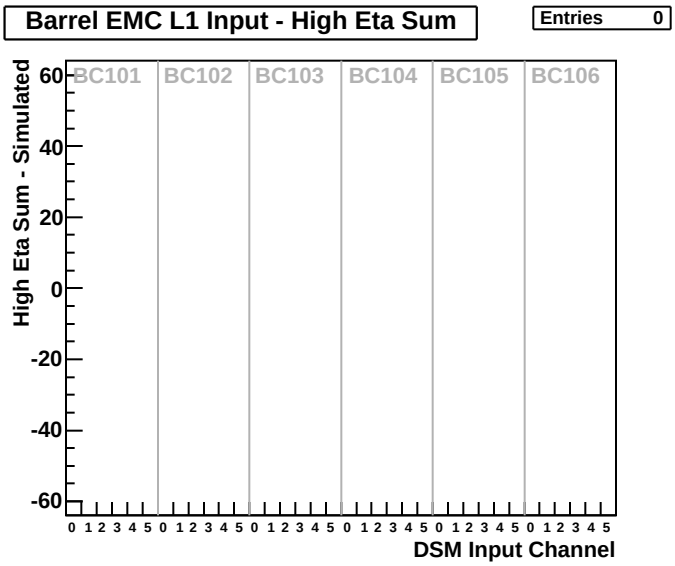
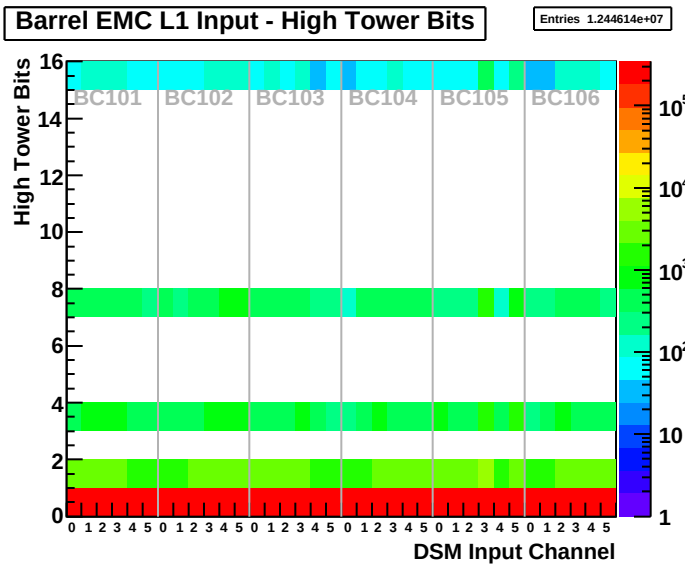
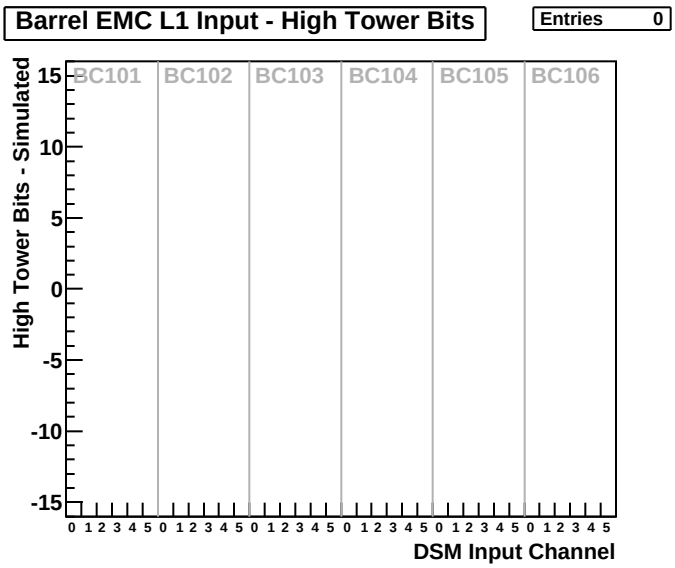

Barrel EMC L1 Input - Low Eta Sum
Entries 1.244614e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

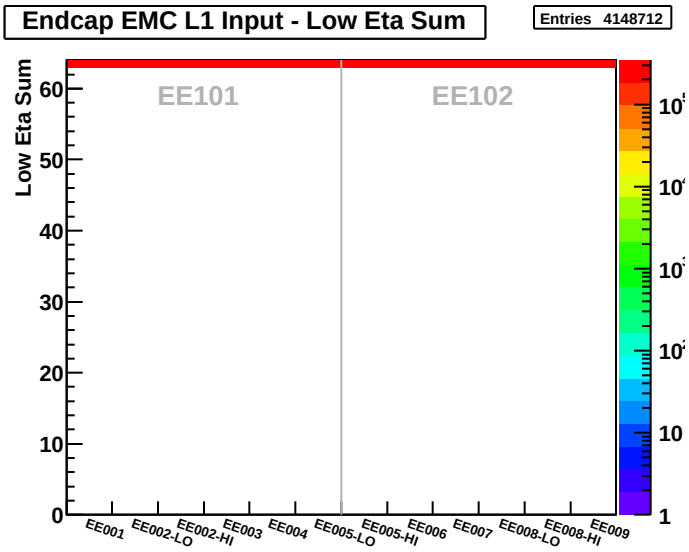

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

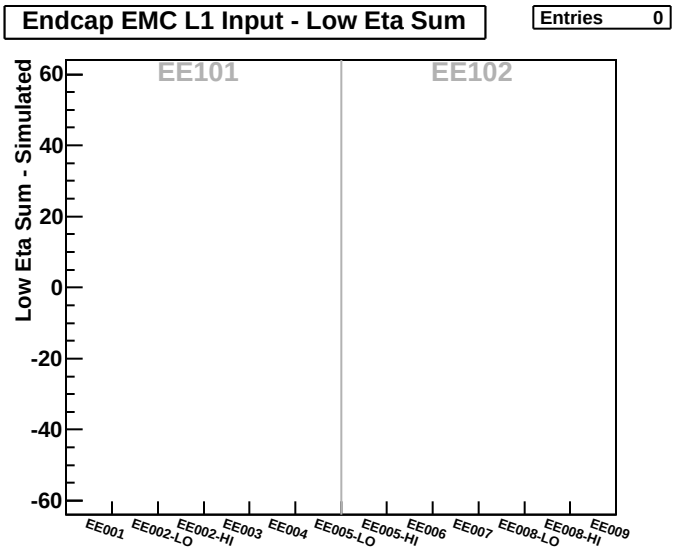

Barrel EMC L1 Input - High Eta Sum
Entries 1.244614e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

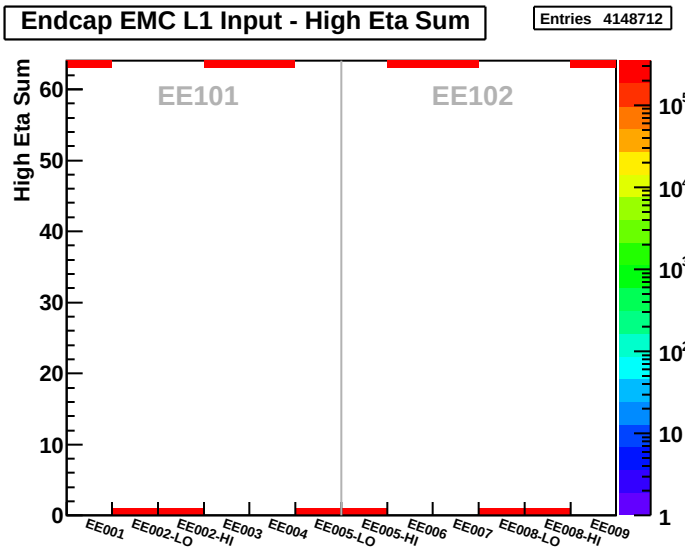

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

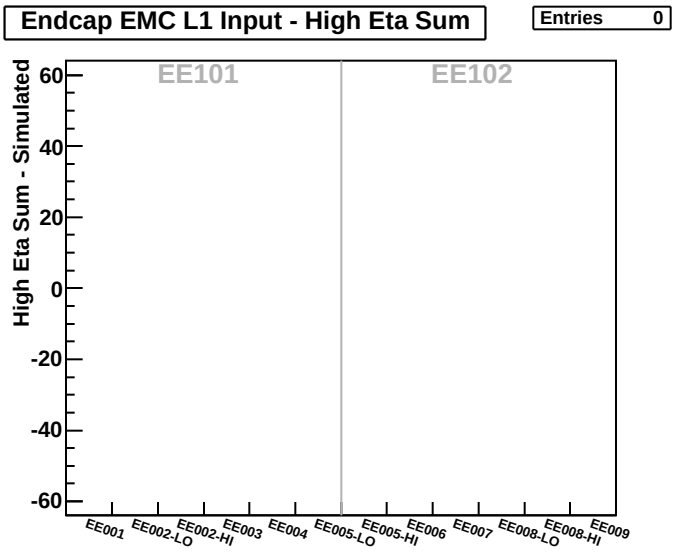

Barrel EMC L1 Input - High Tower Bits
Entries 1.244614e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

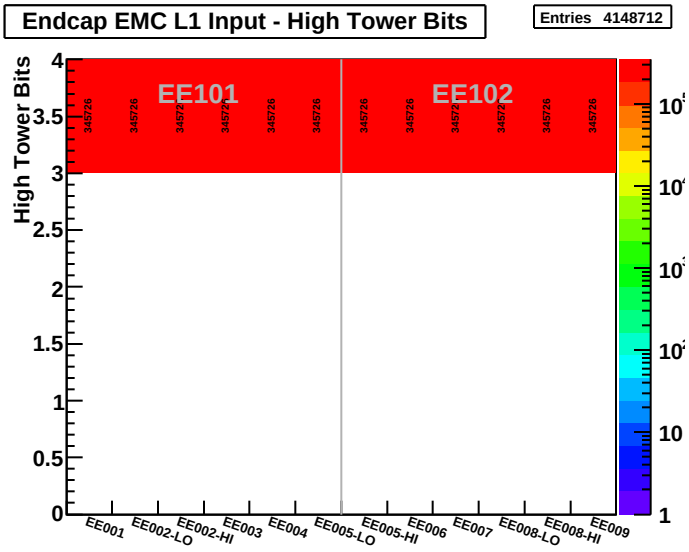

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

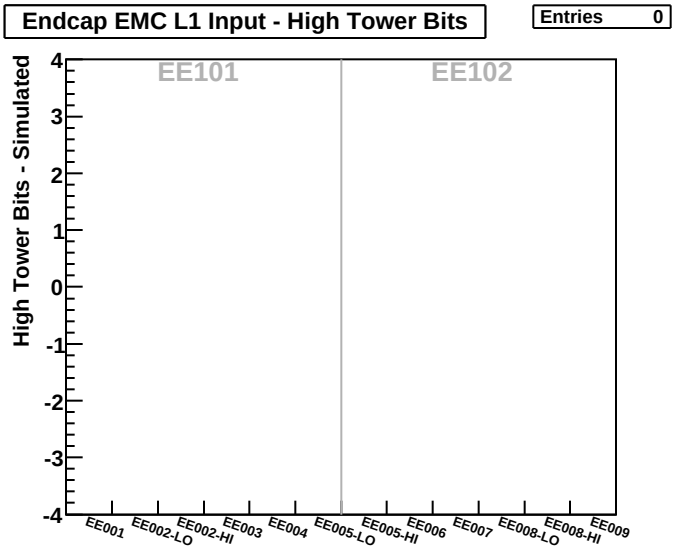

Endcap EMC L1 Input - Low Eta Sum
Entries 4148712
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

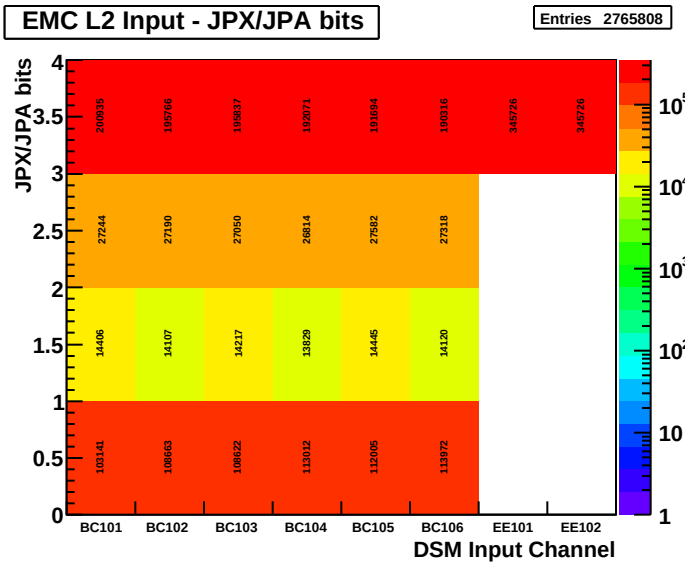

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

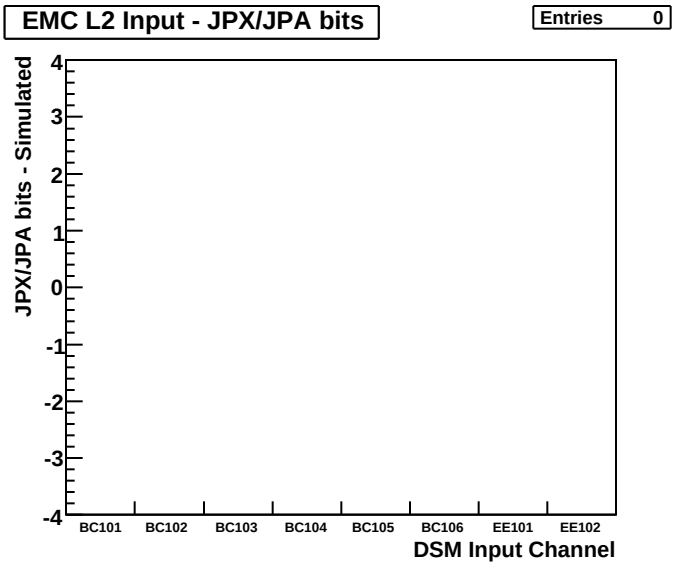

Endcap EMC L1 Input - High Eta Sum
Entries 4148712
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

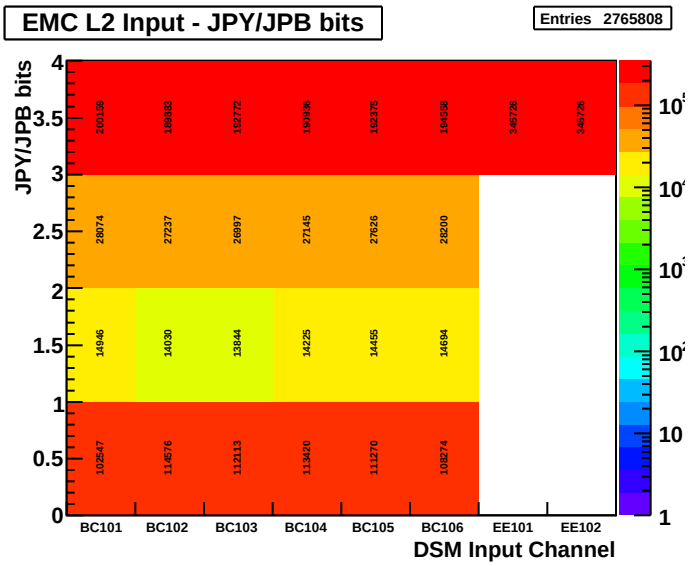

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

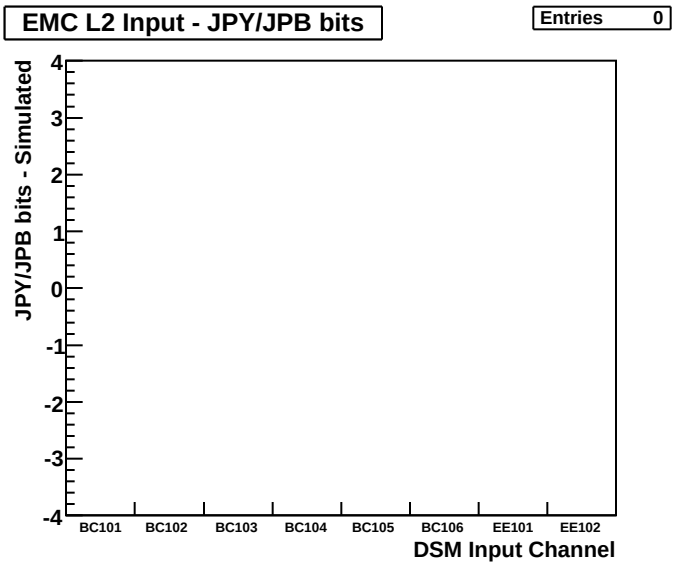

Endcap EMC L1 Input - High Tower Bits
Entries 4148712
High Tower Bits
EE101
EE102
345726
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

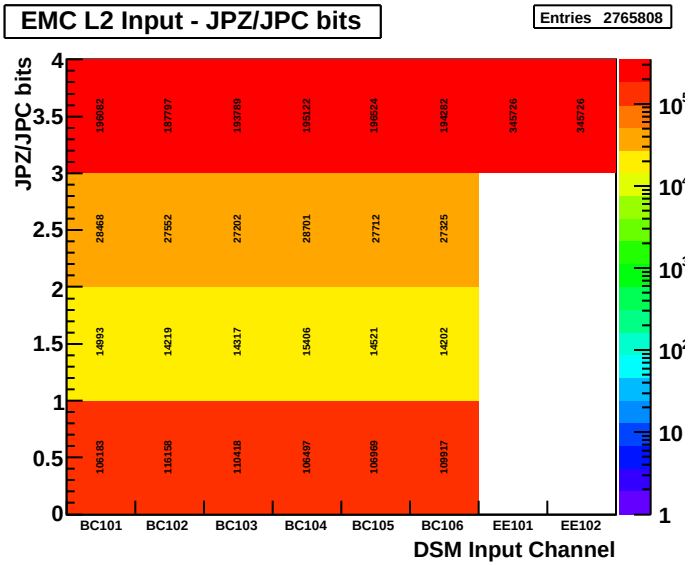

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

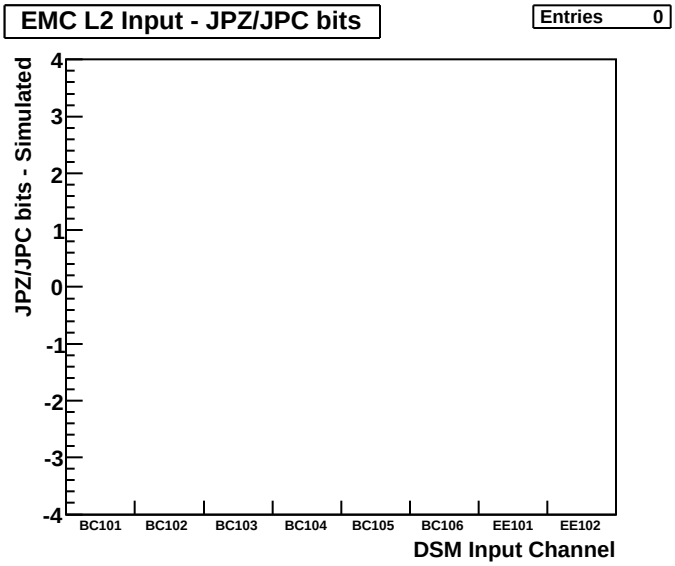

EMC L2 Input - JPX/JPA bits
Entries 2765808
JPX/JPA bits
200935 195766 195837 192071 191694 190316 345726 345726
27244 27190 27050 26814 27582 27318
14406 14107 14217 13829 14445 14120
103141 108663 108622 113012 112005 113972
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
DSM Input Channel

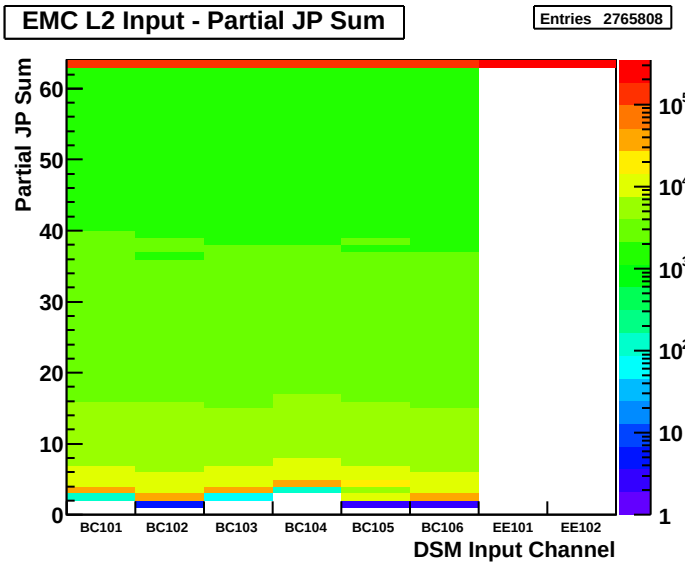

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
DSM Input Channel

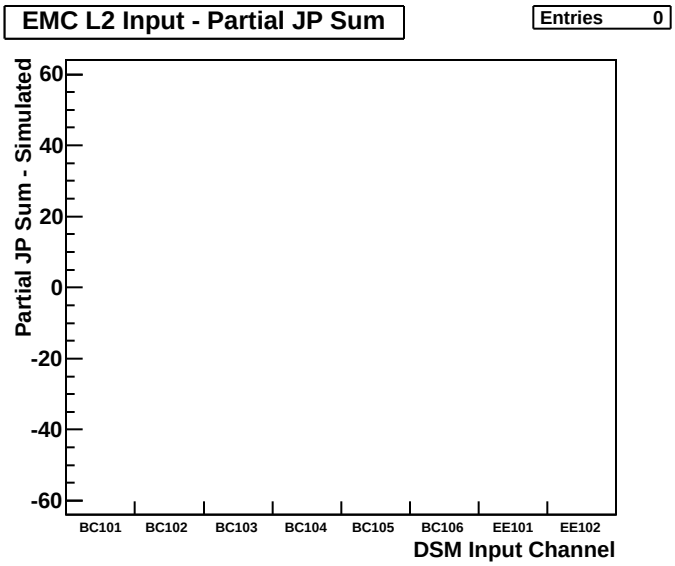

EMC L2 Input - JPY/JPB bits
Entries 2765808
JPY/JPB bits
200159 189883 192772 190936 192375 194558 345726 345726
28074 27237 28997 27145 27626 28200
14946 14030 13844 14225 14455 14694
102547 114576 112113 113420 111270 108274
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
DSM Input Channel

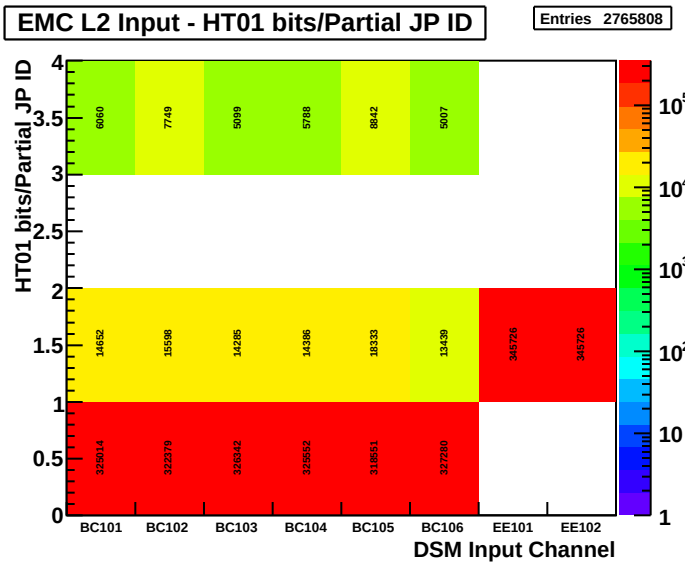

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
DSM Input Channel

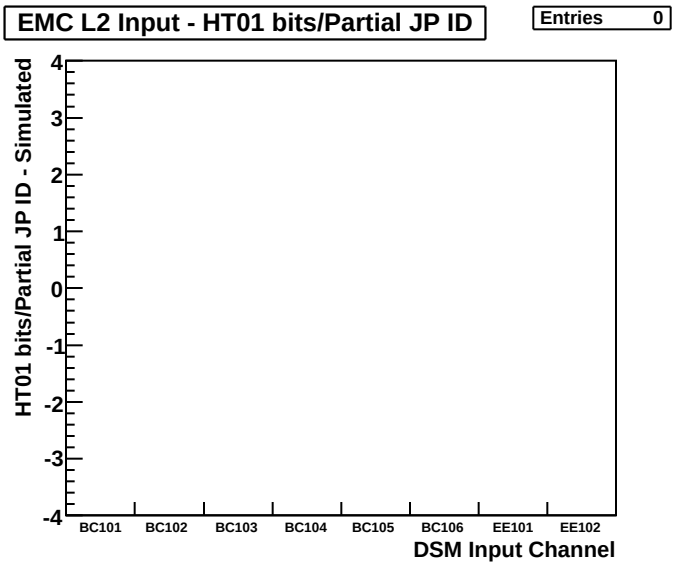

EMC L2 Input - JPZ/JPC bits
Entries 2765808
JPZ/JPC bits
196082 187797 193789 195122 196524 194282 345726 345726
28468 27552 27202 28701 27712 27325
14893 14219 14317 15406 14621 14202
106183 116158 110418 106497 106969 109917
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
DSM Input Channel

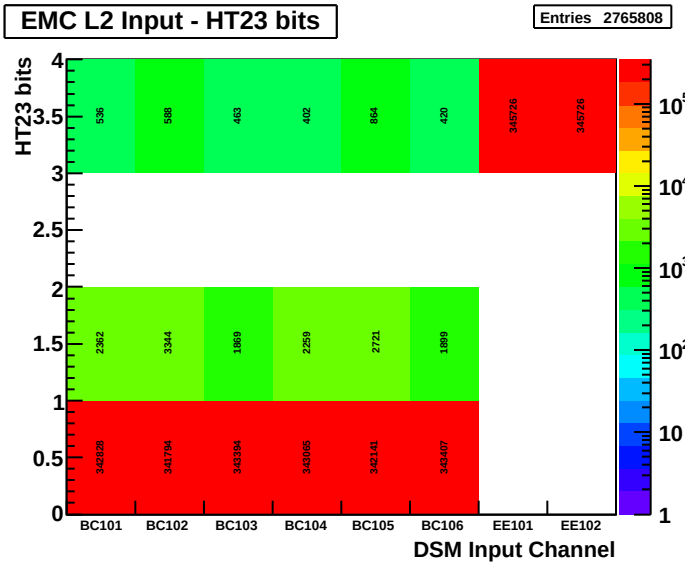

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
DSM Input Channel

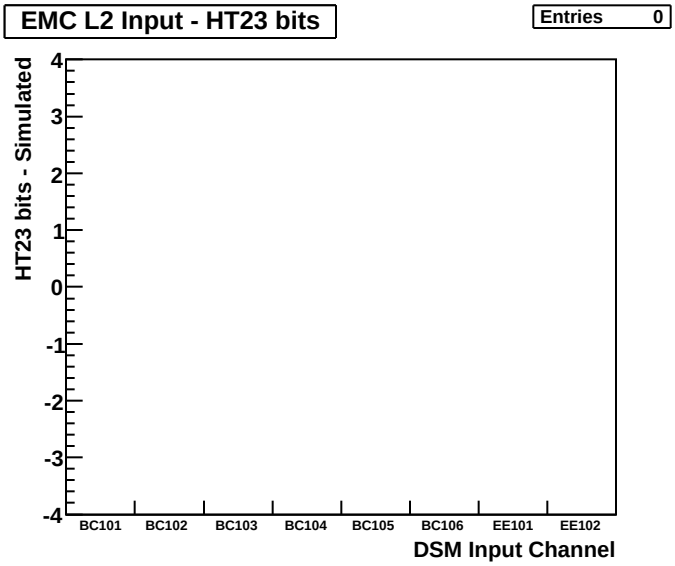

EMC L2 Input - Partial JP Sum
Entries 2765808
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

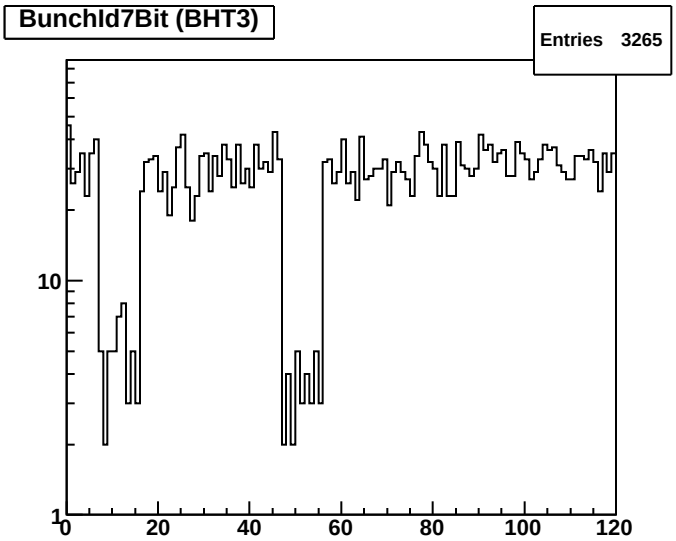

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

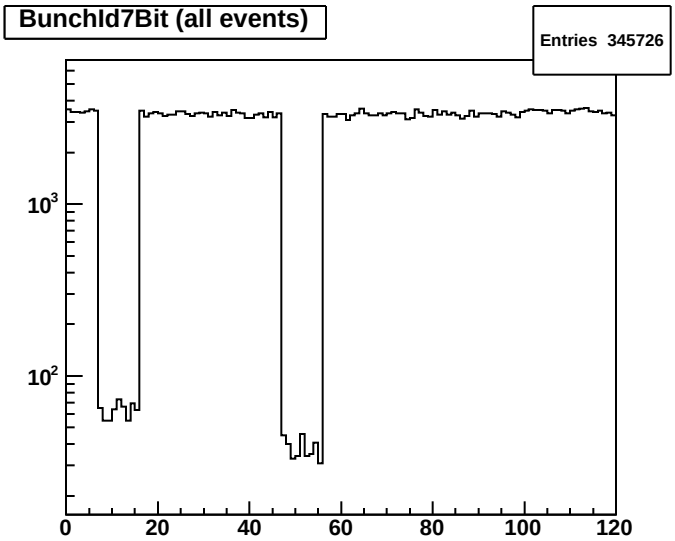

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2765808
HT01 bits/Partial JP ID
6060 7749 5099 5788 8842 5007
14652 15598 14285 14386 18333 13439 345726 345726
323014 322379 326242 325552 318551 327280
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

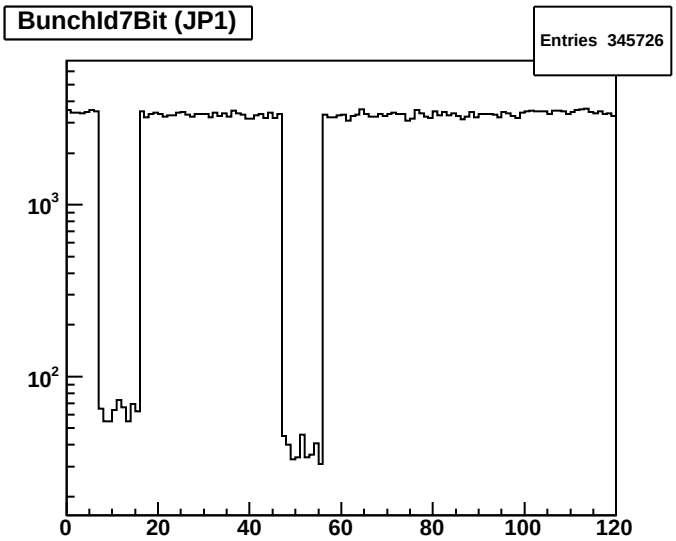

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

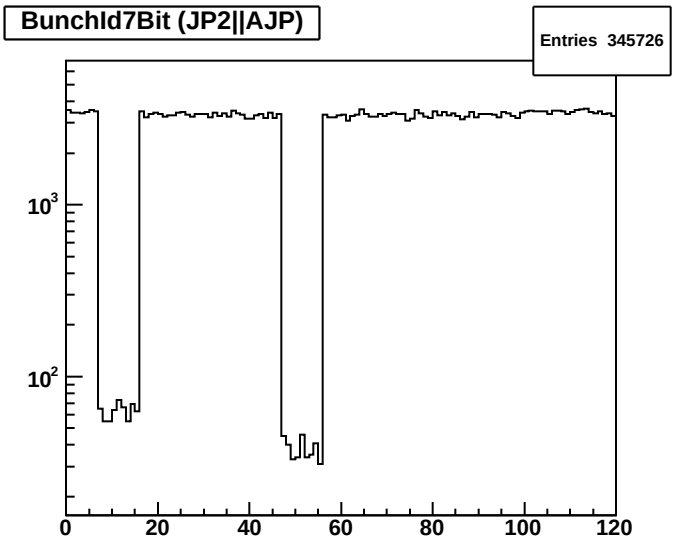

EMC L2 Input - HT23 bits
Entries 2765808
HT23 bits
536 588 463 402 864 420 345726 345726
2362 3344 1869 2259 2721 1899
342328 341794 343394 343065 342141 343407
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

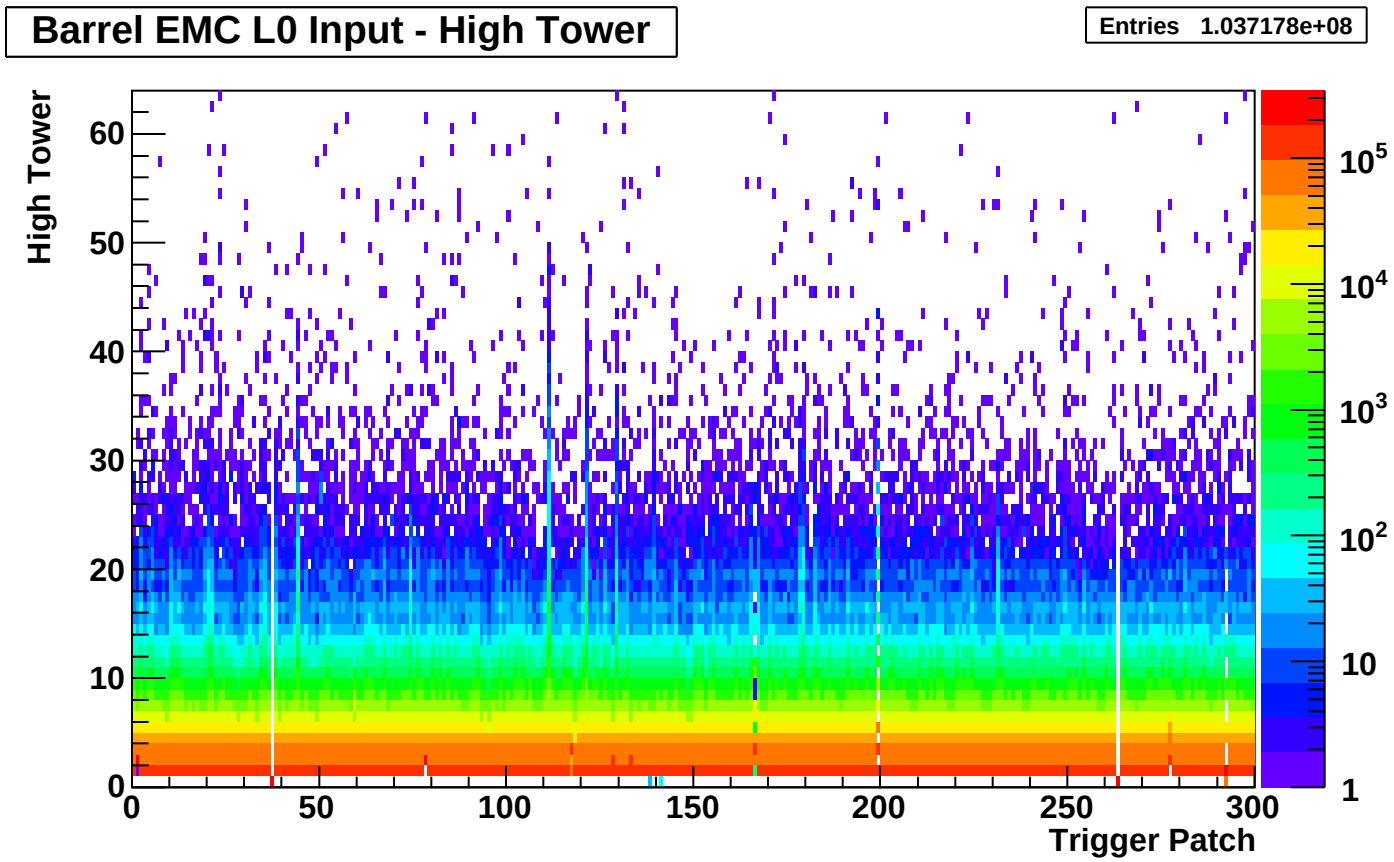

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

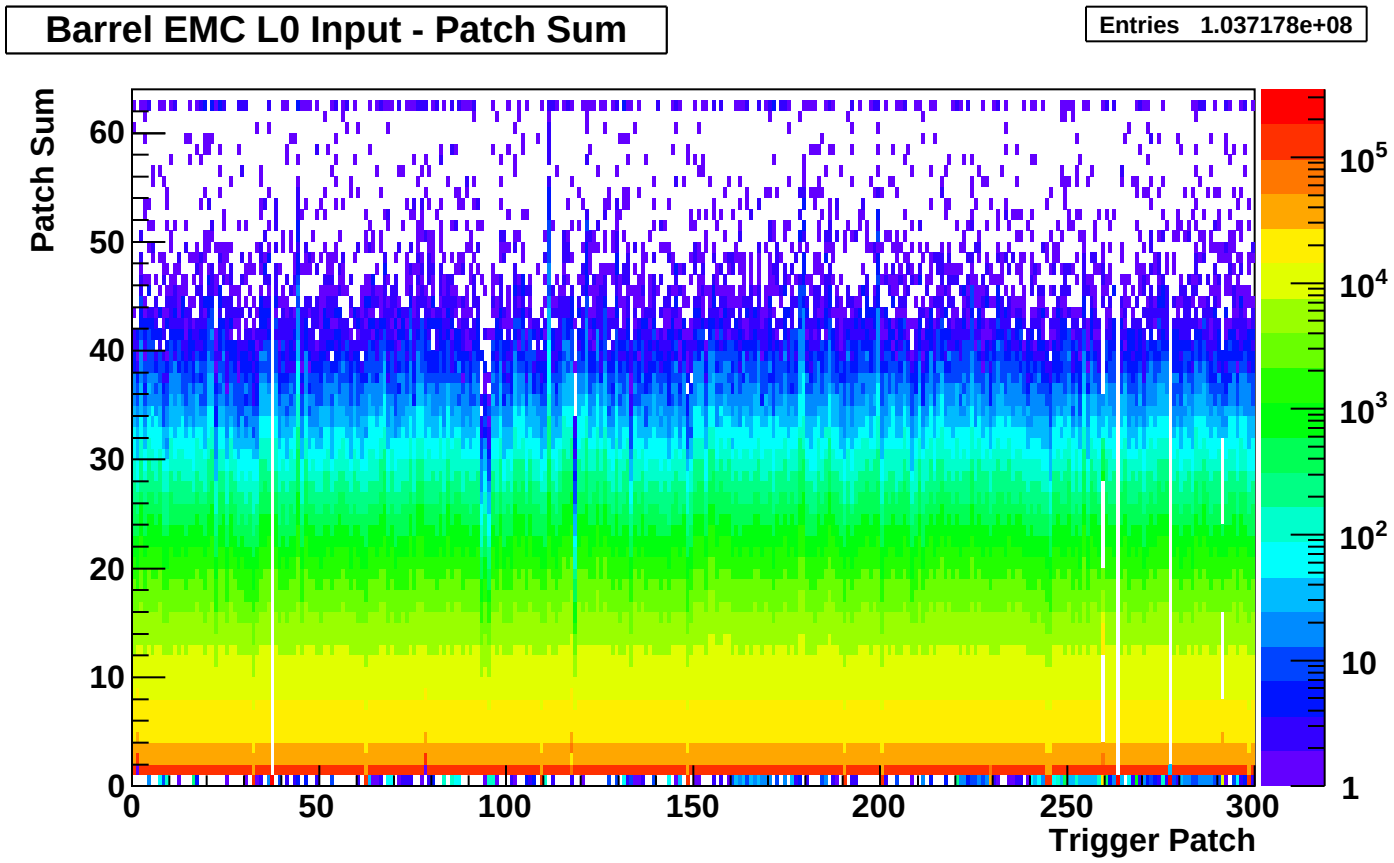

BunchId7Bit (BHT3)
Entries 3265
10
1
0
20
40
60
80
100
120

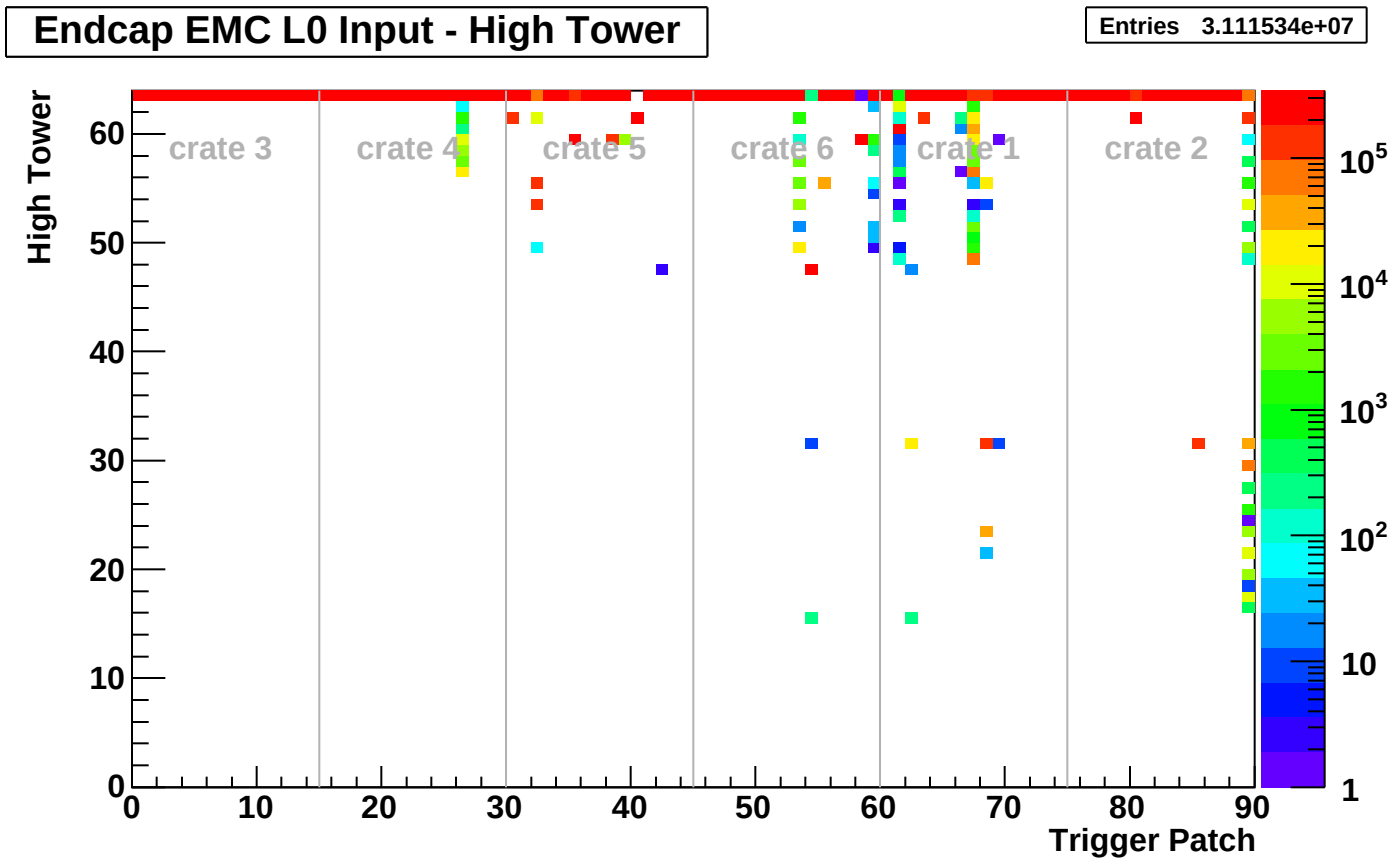

BunchId7Bit (all events)
Entries 345726
$10^3$
$10^2$
0
20
40
60
80
100
120

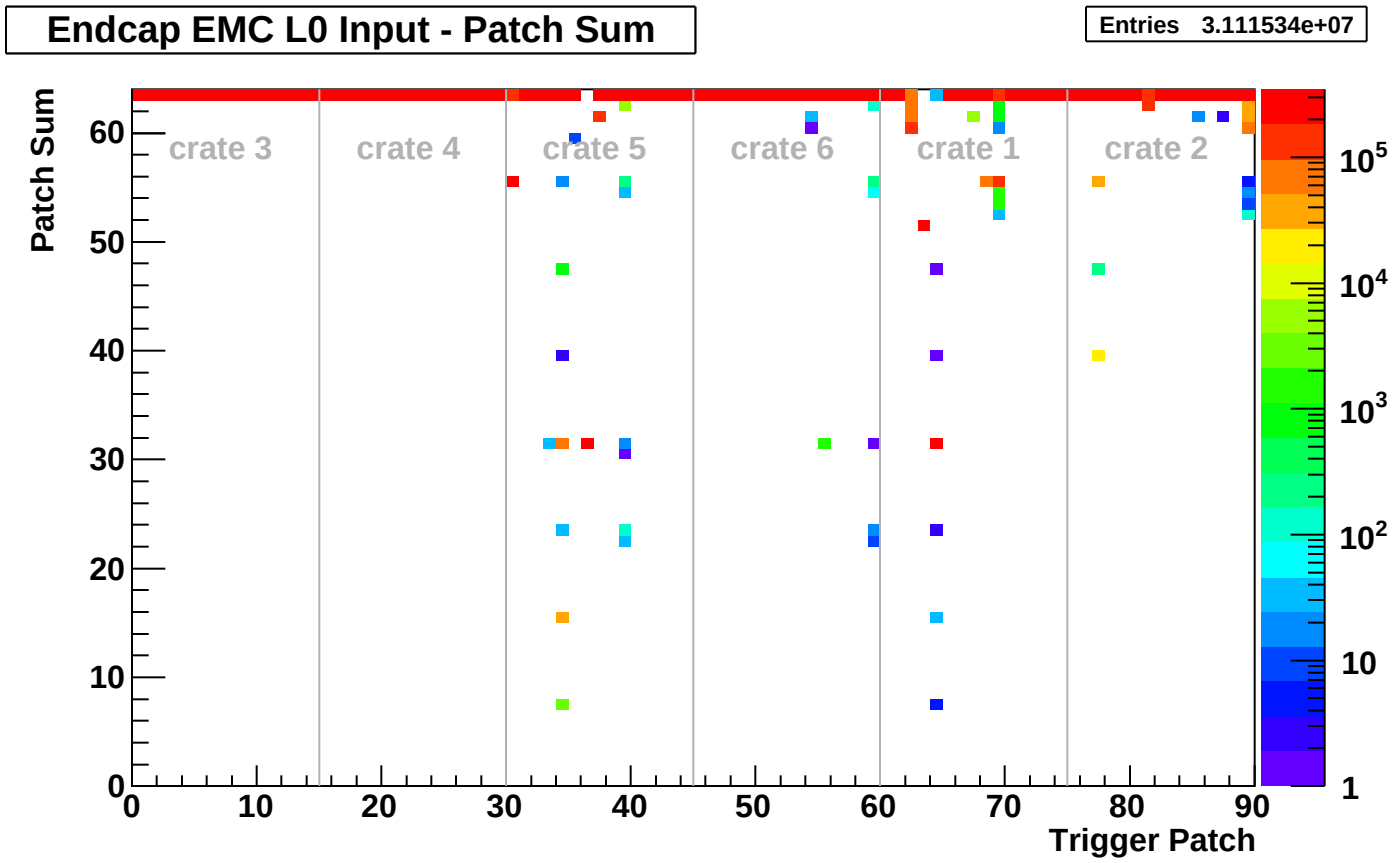

BunchId7Bit (JP1)
Entries 345726
$10^3$
$10^2$
0
20
40
60
80
100
120

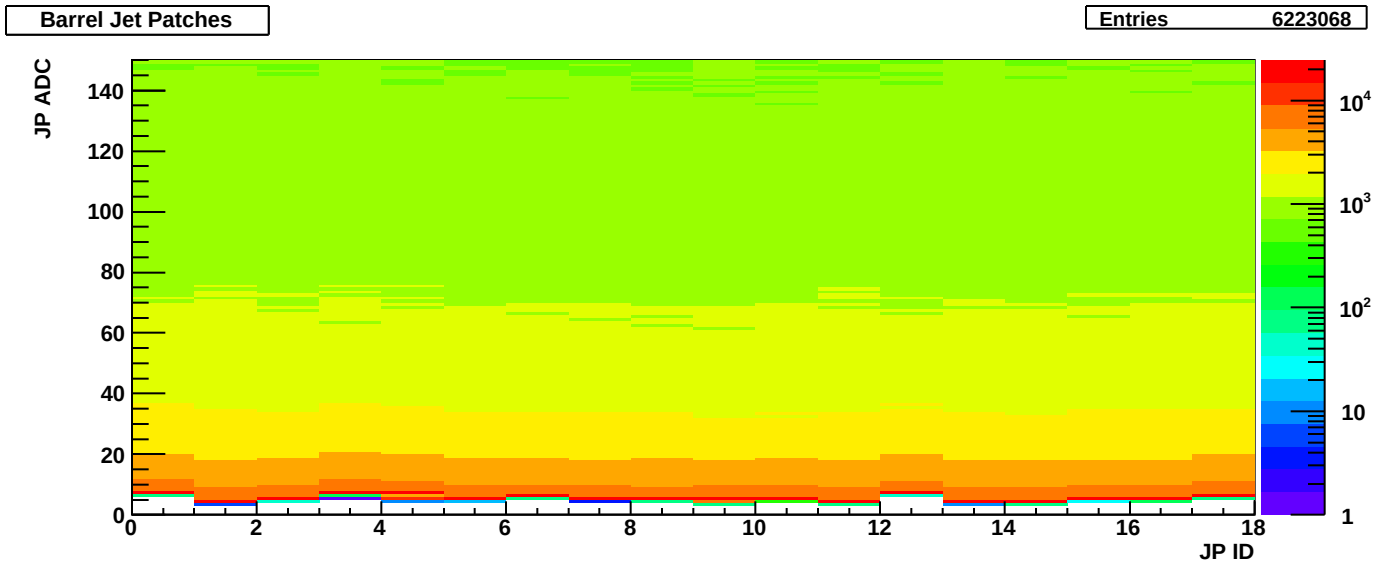

BunchId7Bit (JP2||AJP)
Entries 345726
$10^3$
$10^2$
0
20
40
60
80
100
120

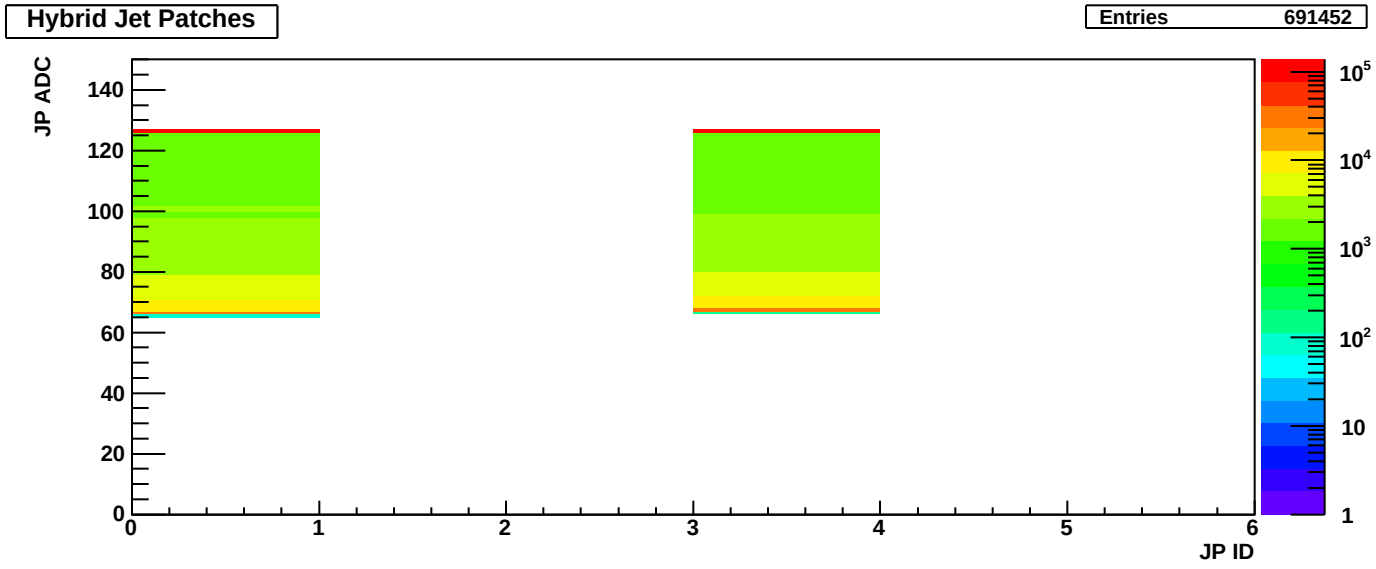

Barrel EMC L0 Input - High Tower
Entries 1.037178e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$
$10^4$
$10^5$

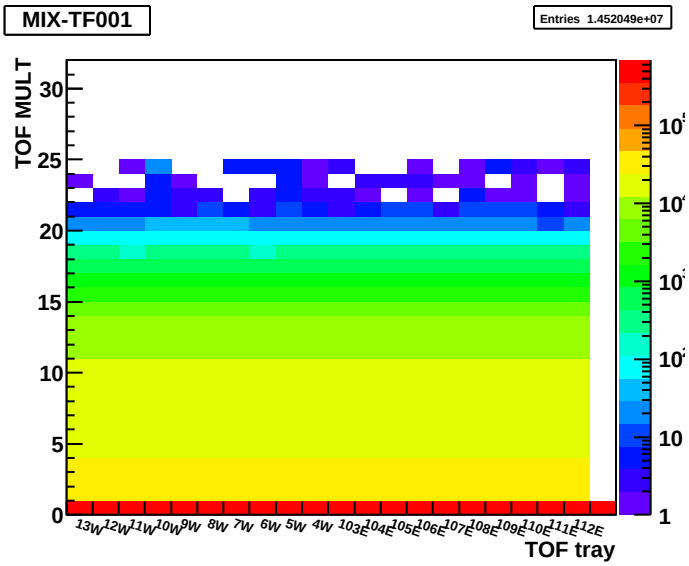

Barrel EMC L0 Input - Patch Sum
Entries 1.037178e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$
$10^4$
$10^5$

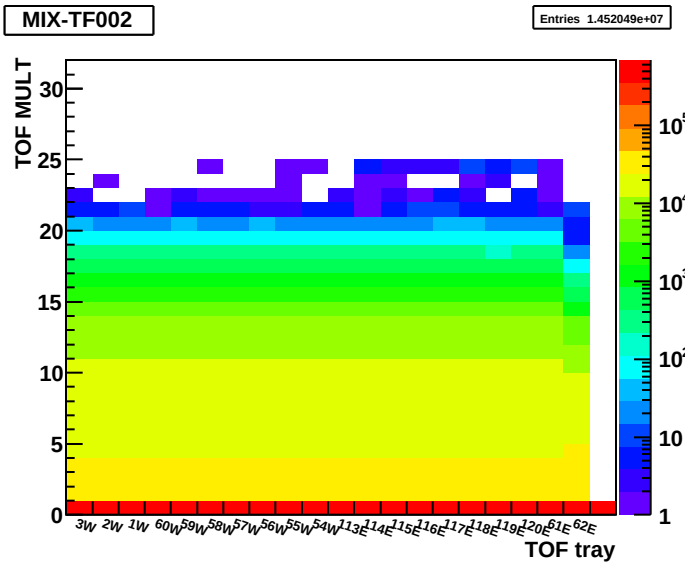

Endcap EMC L0 Input - High Tower
Entries 3.111534e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

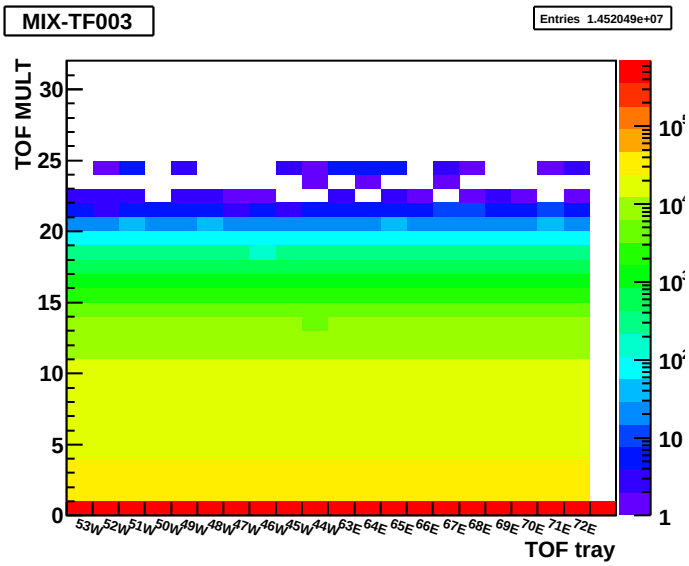

Endcap EMC L0 Input - Patch Sum
Entries 3.111534e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

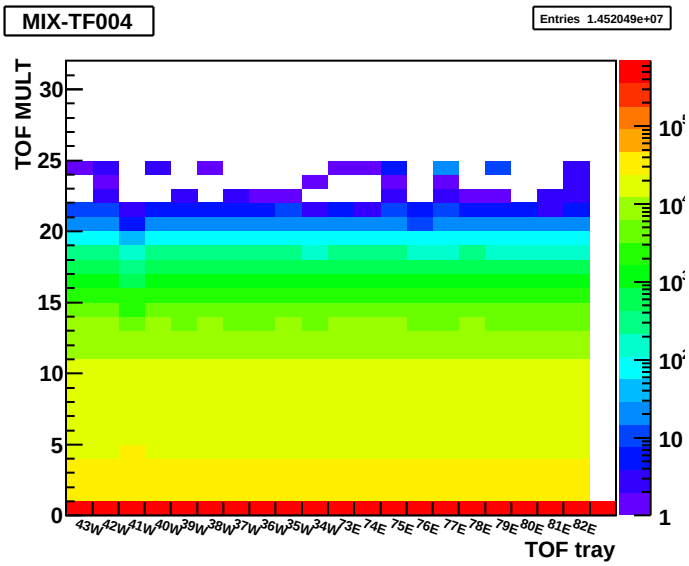

Barrel Jet Patches
Entries 6223068
JP ADC
JP ID

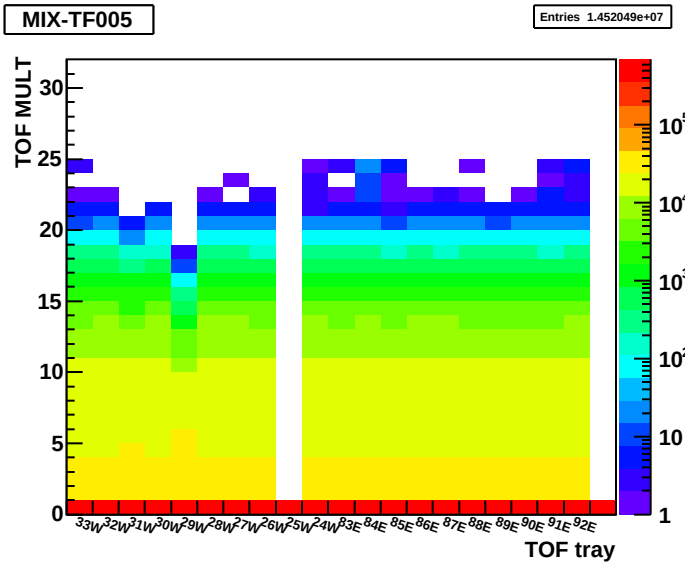

Hybrid Jet Patches
Entries 691452
JP ADC
JP ID

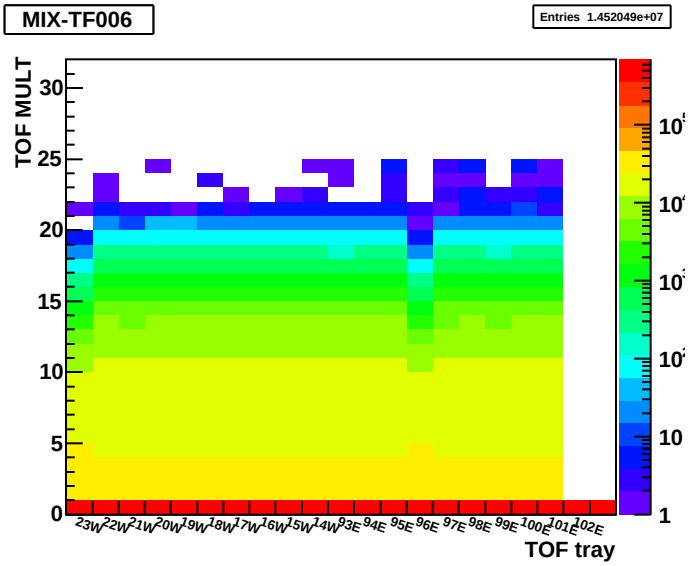

MIX-TF001
Entries 1.452049e+07
TOF MULT
TOF tray

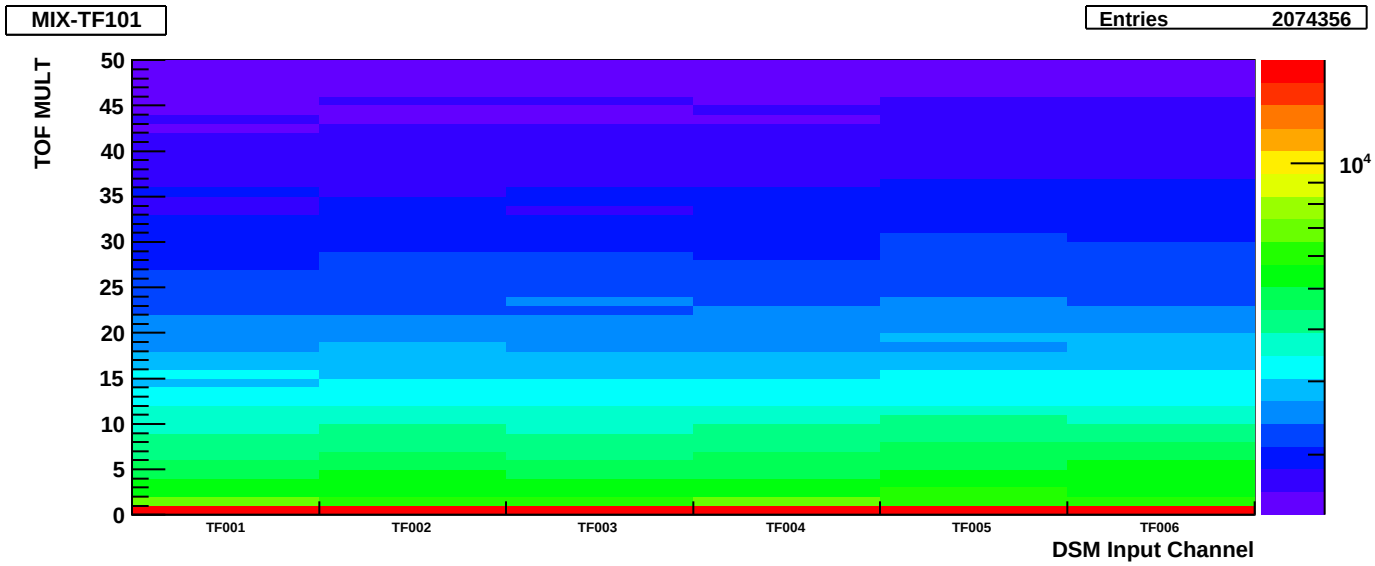

MIX-TF002
Entries 1.452049e+07
TOF MULT
TOF tray

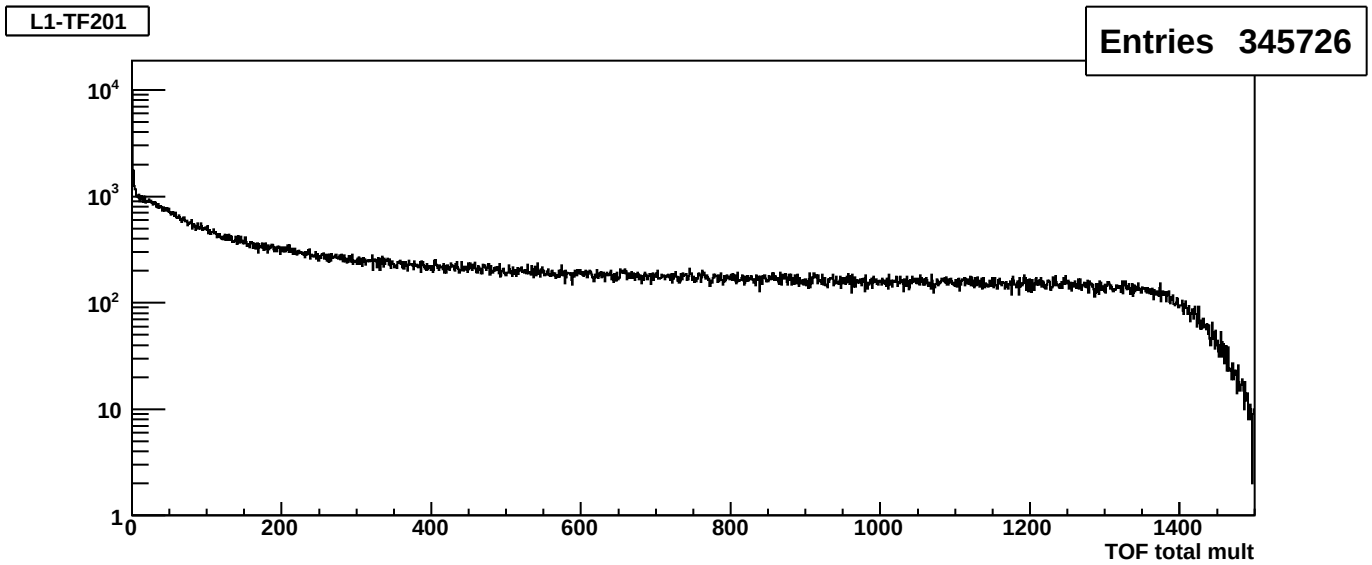

MIX-TF003
Entries 1.452049e+07
TOF MULT
TOF tray

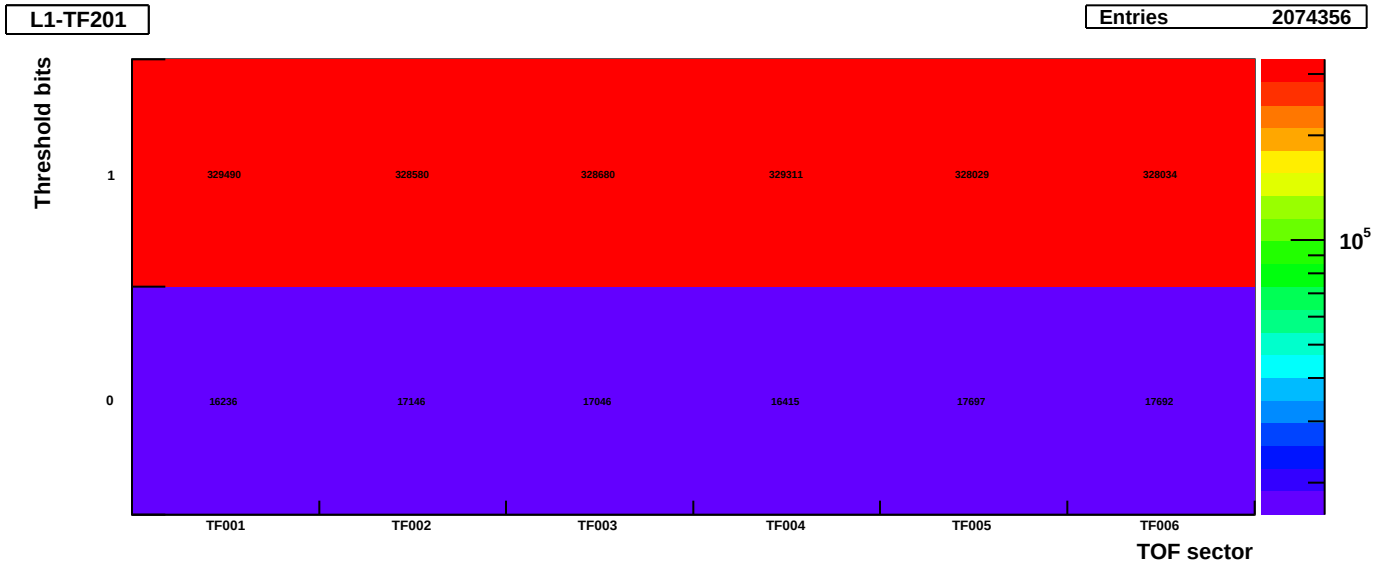

MIX-TF004
Entries 1.452049e+07
TOF MULT
TOF tray

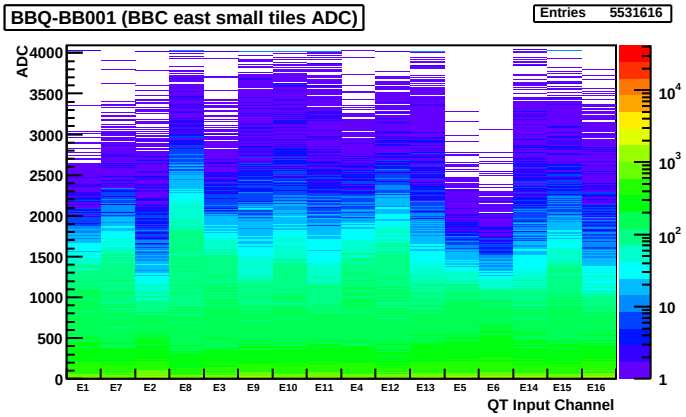

MIX-TF005
Entries 1.452049e+07
TOF MULT
TOF tray

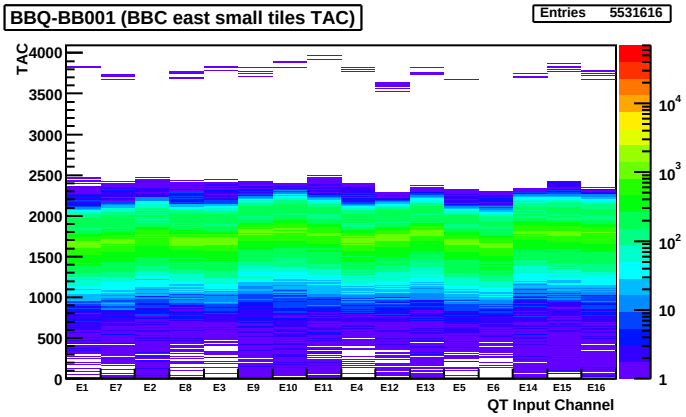

MIX-TF006
Entries 1.452049e+07
TOF MULT
TOF tray

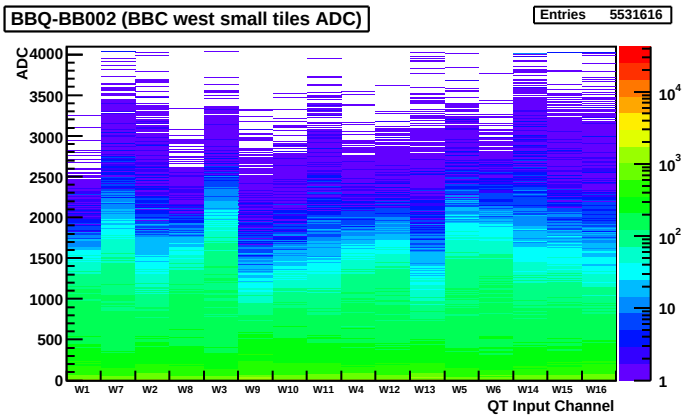

MIX-TF101
Entries 2074356
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
$10^4$

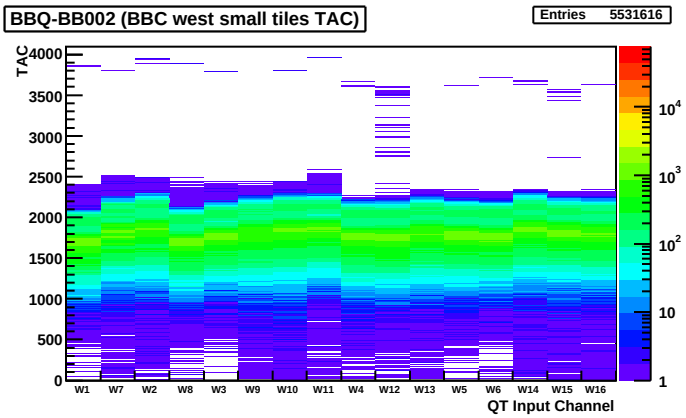

L1-TF201
Entries 345726
$10^4$
$10^3$
$10^2$
10
1
0
200
400
600
800
1000
1200
1400
TOF total mult

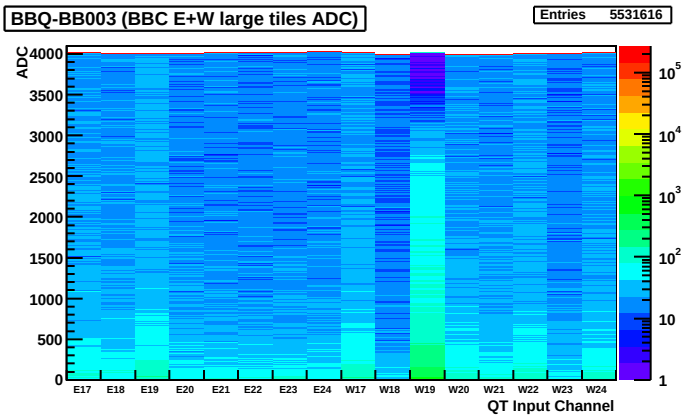

L1-TF201
Entries 2074356
Threshold bits
1
0
329490
328580
328680
329311
328029
328034
16236
17146
17046
16415
17697
17692
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
$10^5$

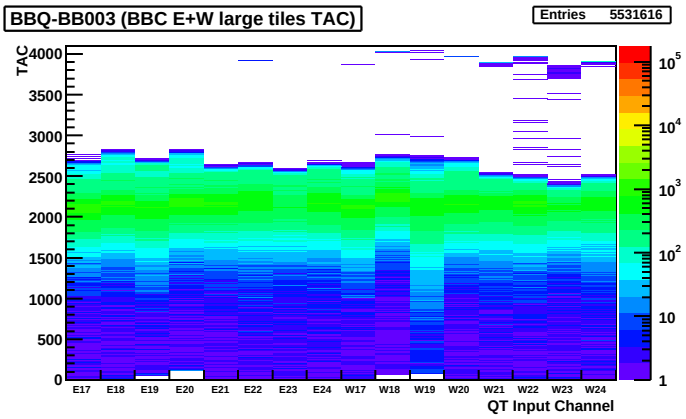

BBQ-BB001 (BBC east small tiles ADC)
Entries 5531616
ADC
QT Input Channel

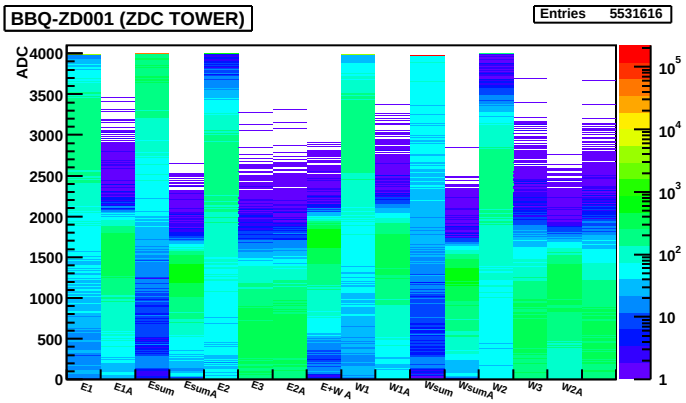

BBQ-BB001 (BBC east small tiles TAC)
Entries 5531616
TAC
QT Input Channel

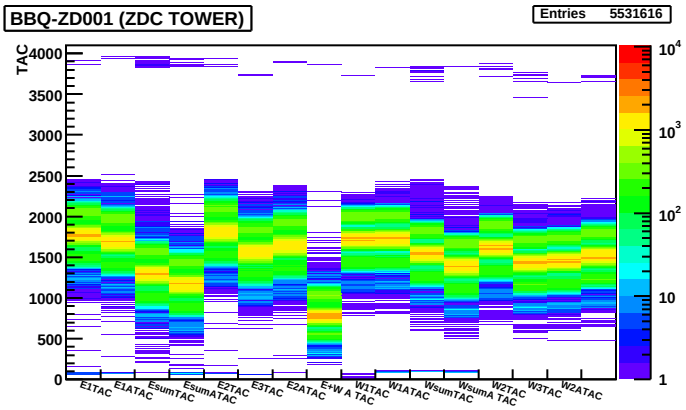

BBQ-BB002 (BBC west small tiles ADC)
Entries 5531616
ADC
QT Input Channel

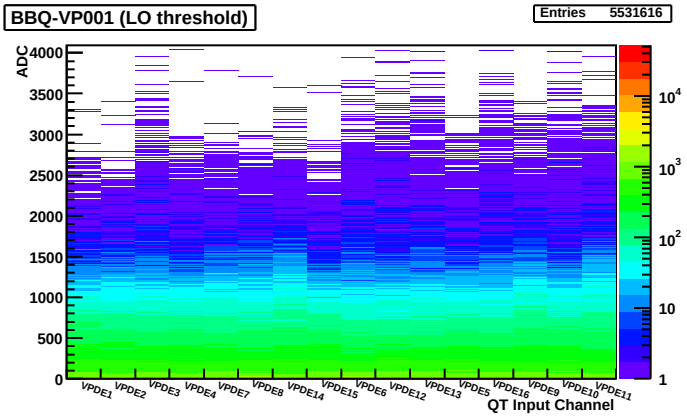

BBQ-BB002 (BBC west small tiles TAC)
Entries 5531616
TAC
QT Input Channel

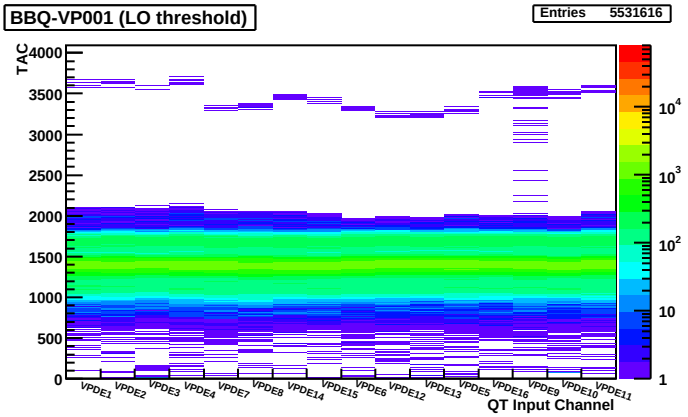

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 5531616
ADC
QT Input Channel

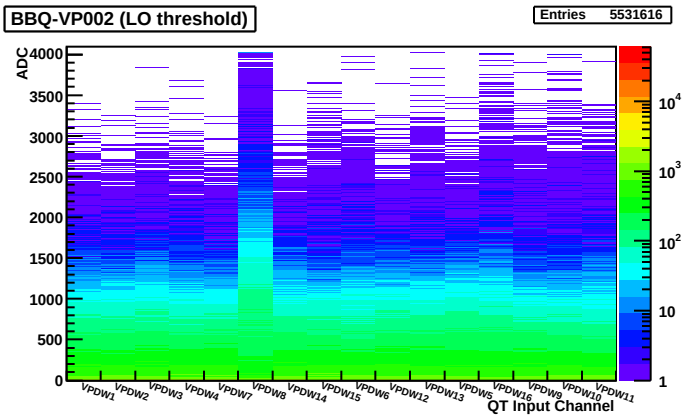

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 5531616
TAC
QT Input Channel

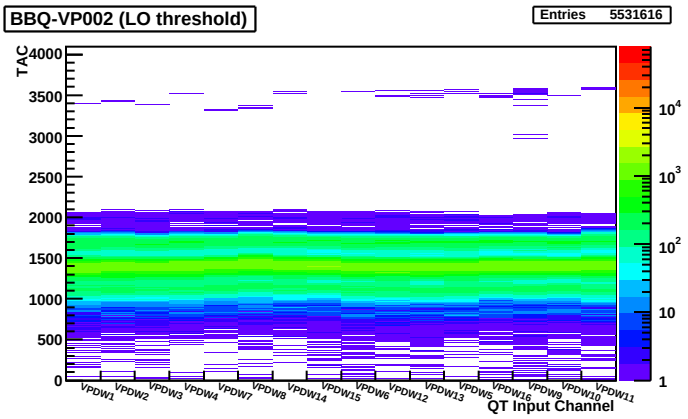

BBQ-ZD001 (ZDC TOWER)
Entries 5531616
ADC

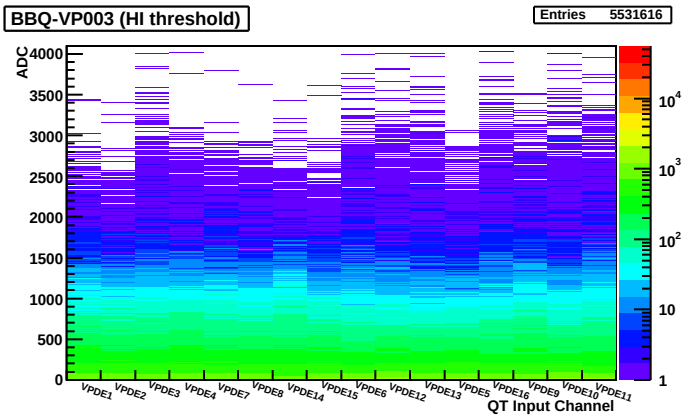

BBQ-ZD001 (ZDC TOWER)
Entries 5531616
TAC

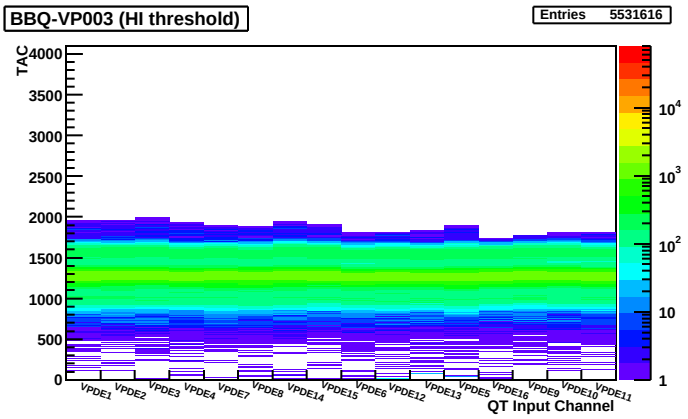

BBQ-VP001 (LO threshold)
Entries 5531616
ADC
QT Input Channel

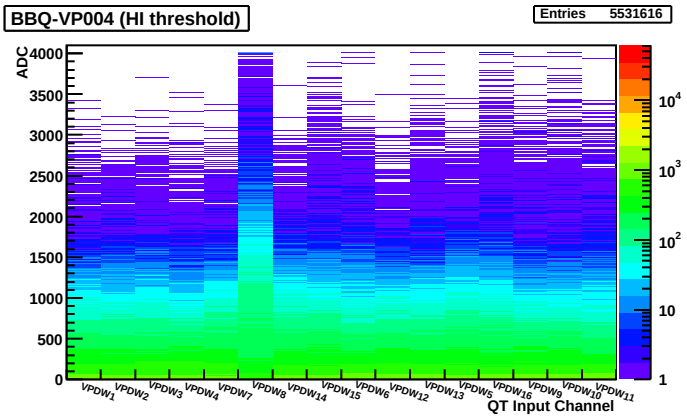

BBQ-VP001 (LO threshold)
Entries 5531616
TAC
QT Input Channel

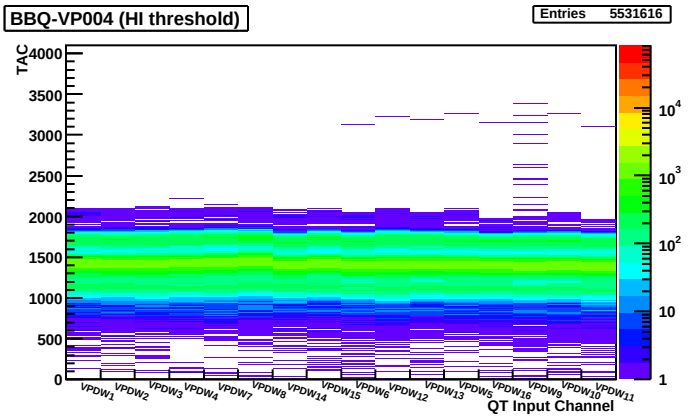

BBQ-VP002 (LO threshold)
Entries 5531616
ADC
QT Input Channel

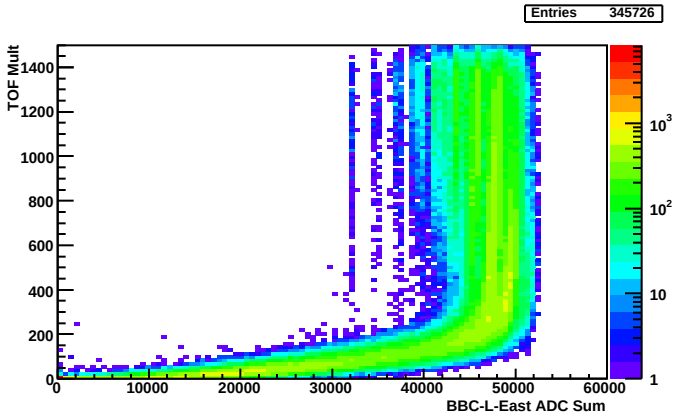

BBQ-VP002 (LO threshold)
Entries 5531616
TAC
QT Input Channel

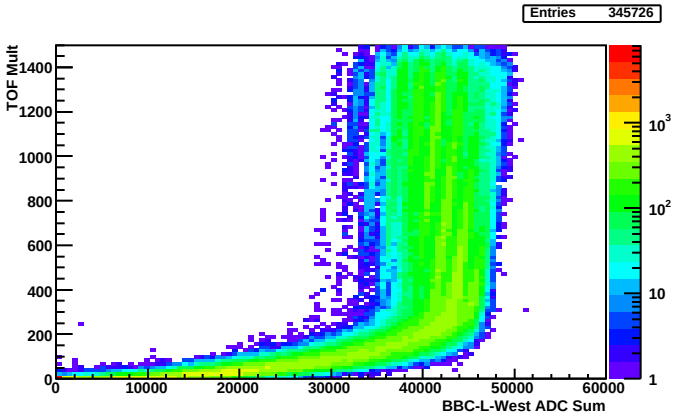

BBQ-VP003 (HI threshold)
Entries 5531616
ADC
QT Input Channel

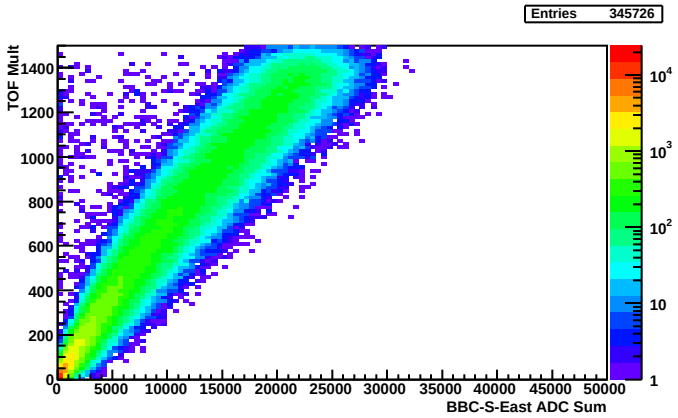

BBQ-VP003 (HI threshold)
Entries 5531616
TAC
QT Input Channel

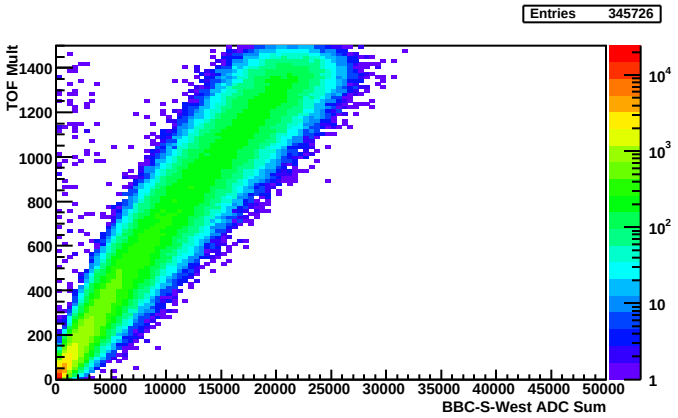

BBQ-VP004 (HI threshold)
Entries 5531616
ADC
QT Input Channel

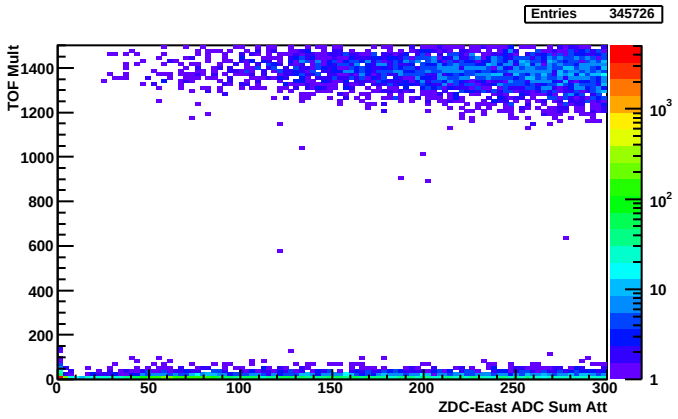

BBQ-VP004 (HI threshold)
Entries 5531616
TAC
QT Input Channel

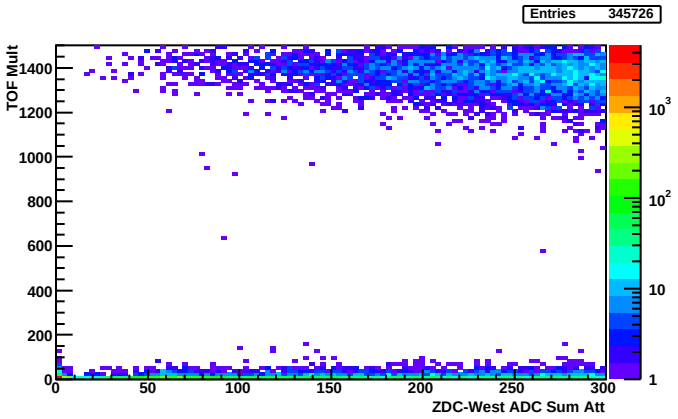

Entries 345726
TOF Mult
BBC-L-East ADC Sum

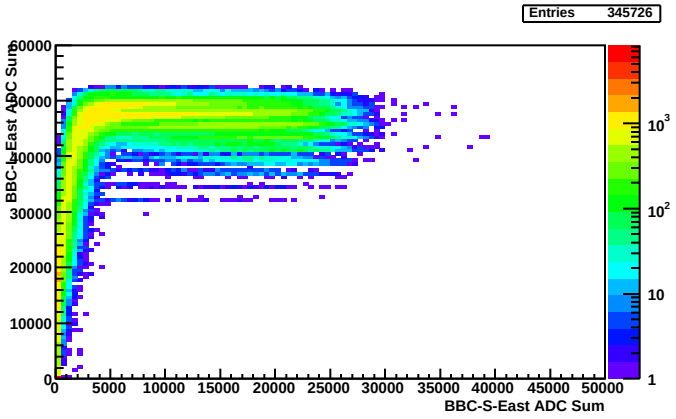

Entries 345726
TOF Mult
BBC-L-West ADC Sum

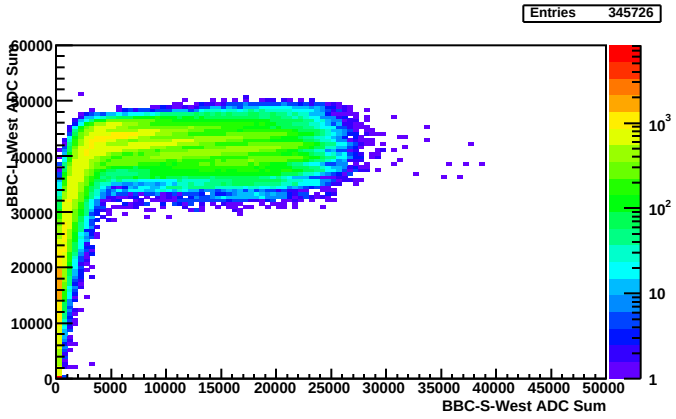

Entries 345726
TOF Mult
BBC-S-East ADC Sum

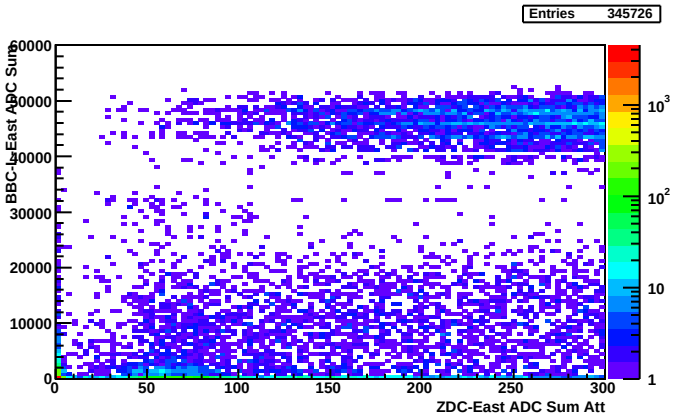

Entries 345726
TOF Mult
BBC-S-West ADC Sum

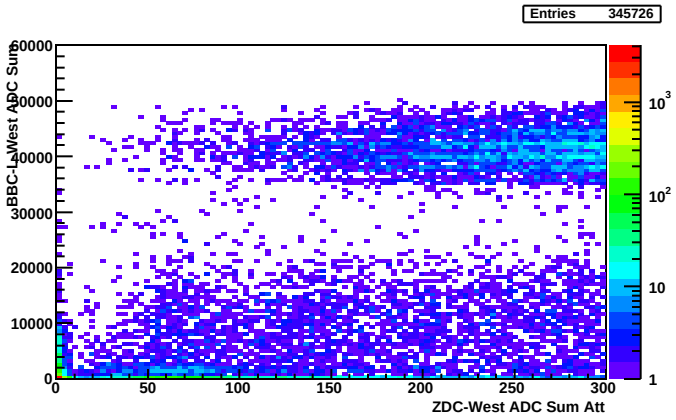

Entries 345726
TOF Mult
ZDC-East ADC Sum Att


Entries 345726
TOF Mult
ZDC-West ADC Sum Att


Entries 345726
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 345726
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 345726
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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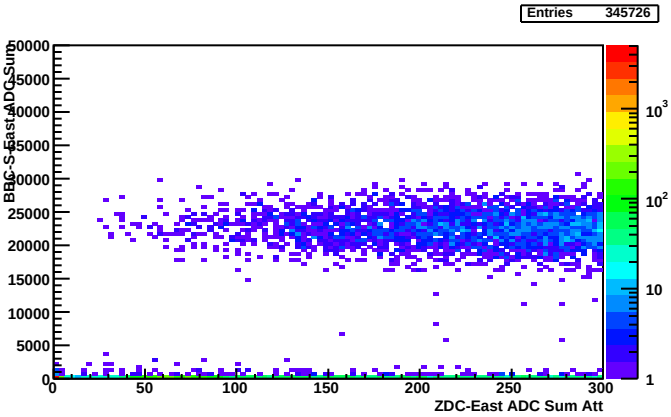
BBC-L-West ADC Sum
ZDC-West ADC Sum Att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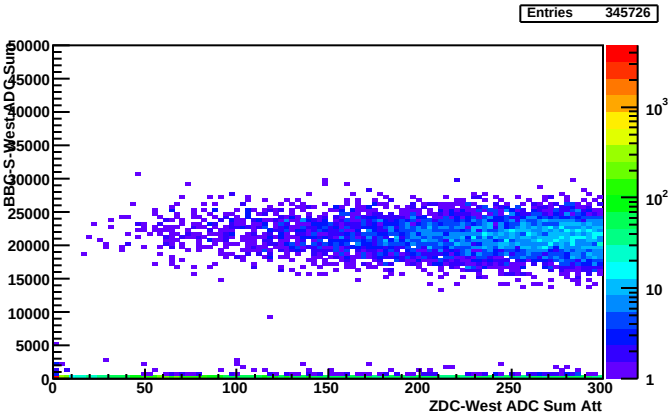
BBC-S-East ADC Sum
ZDC-East ADC Sum Att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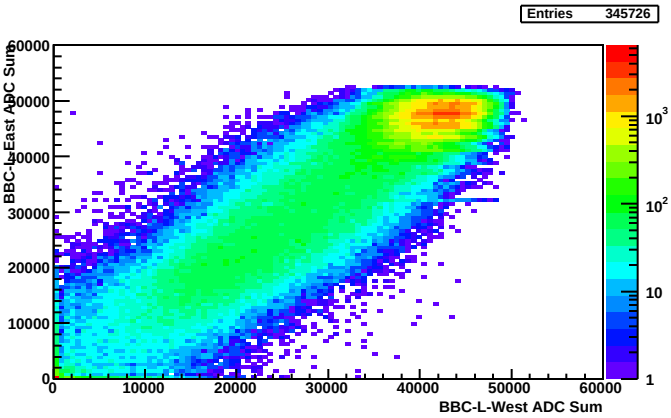
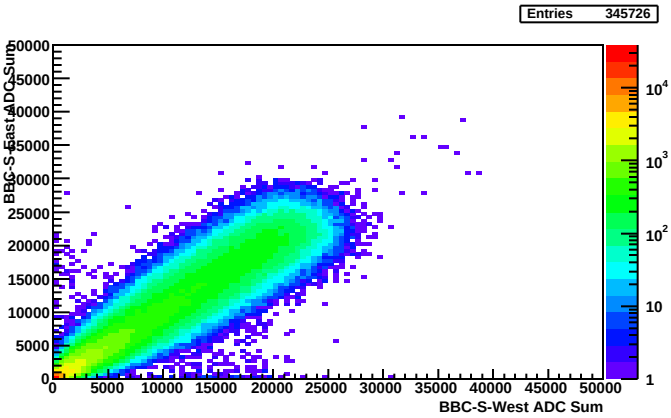
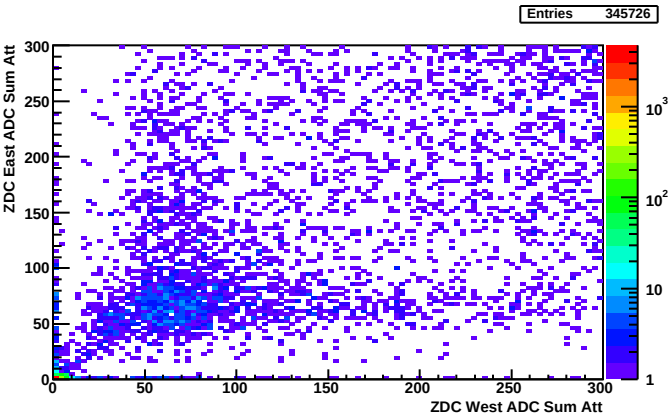
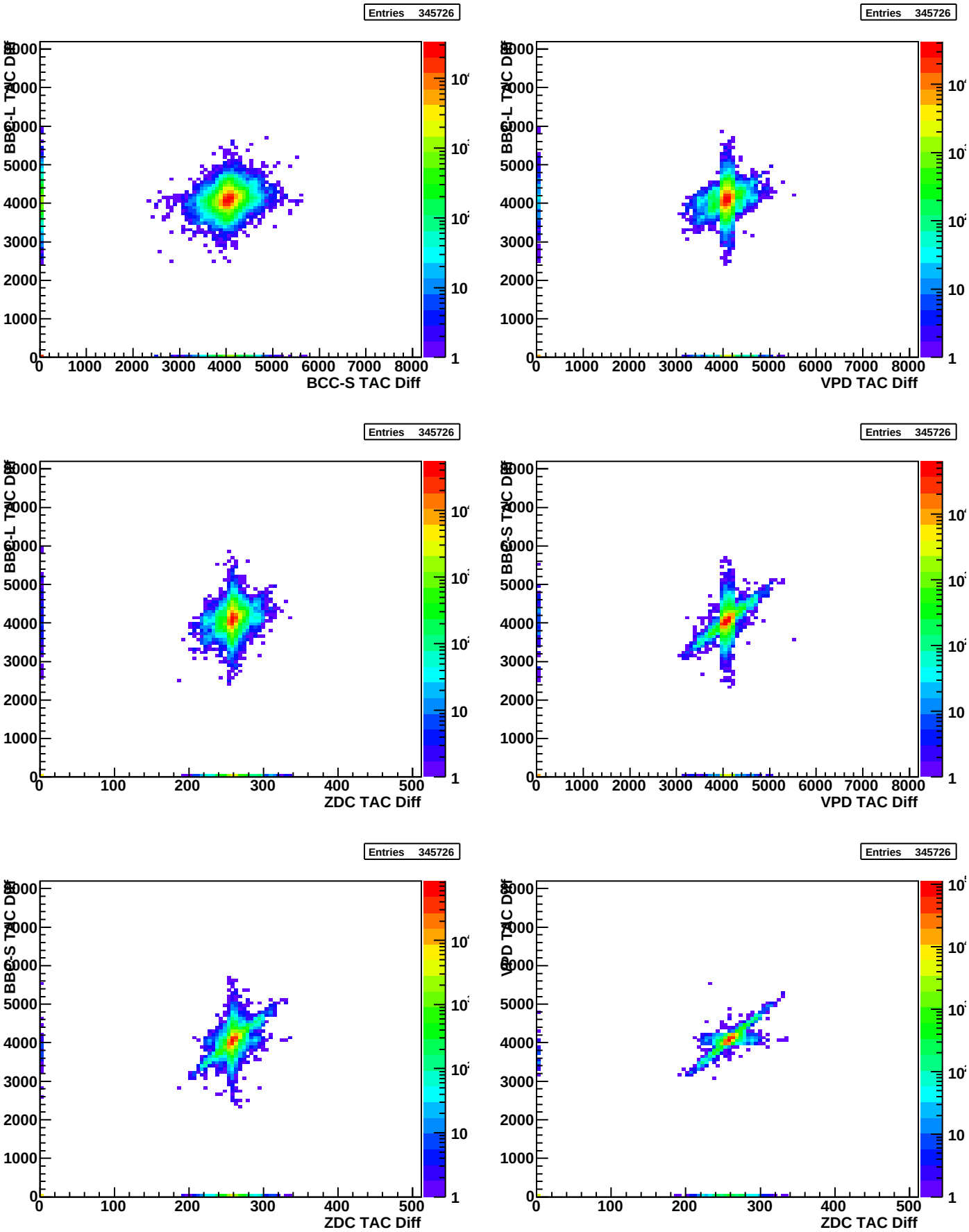
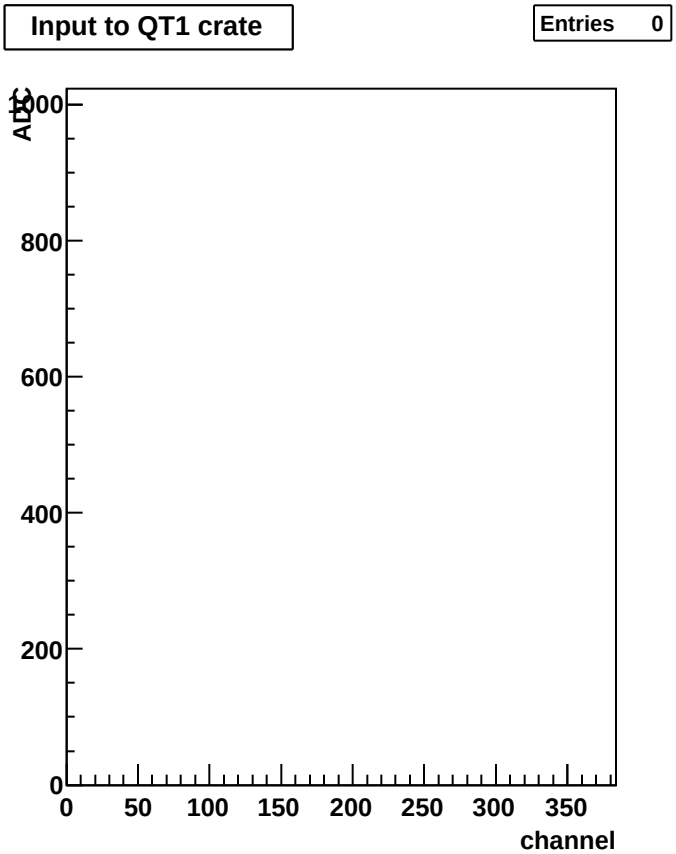
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

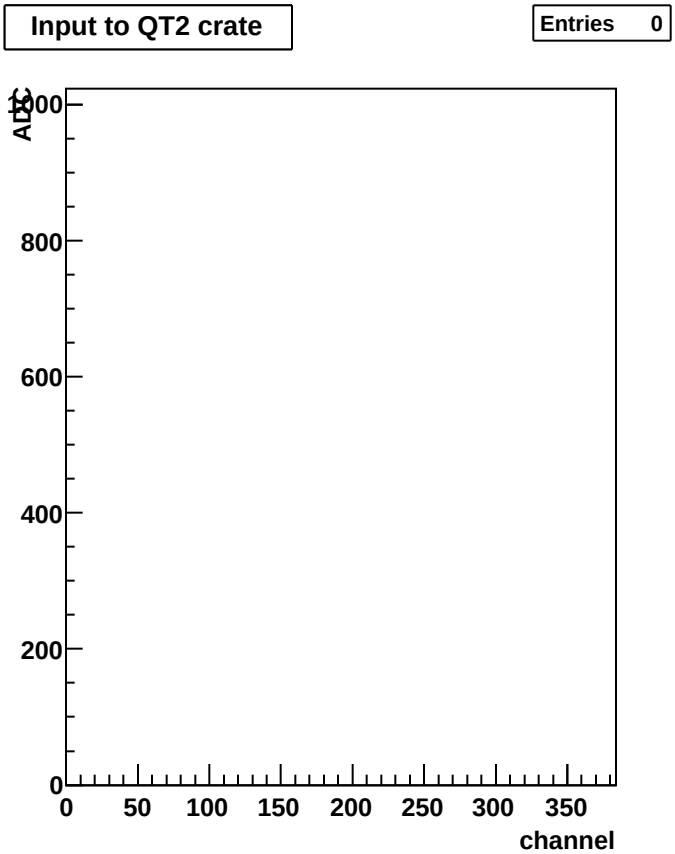

Entries 345726
BBC-L-East ADC Sum
BBC-L-West ADC Sum

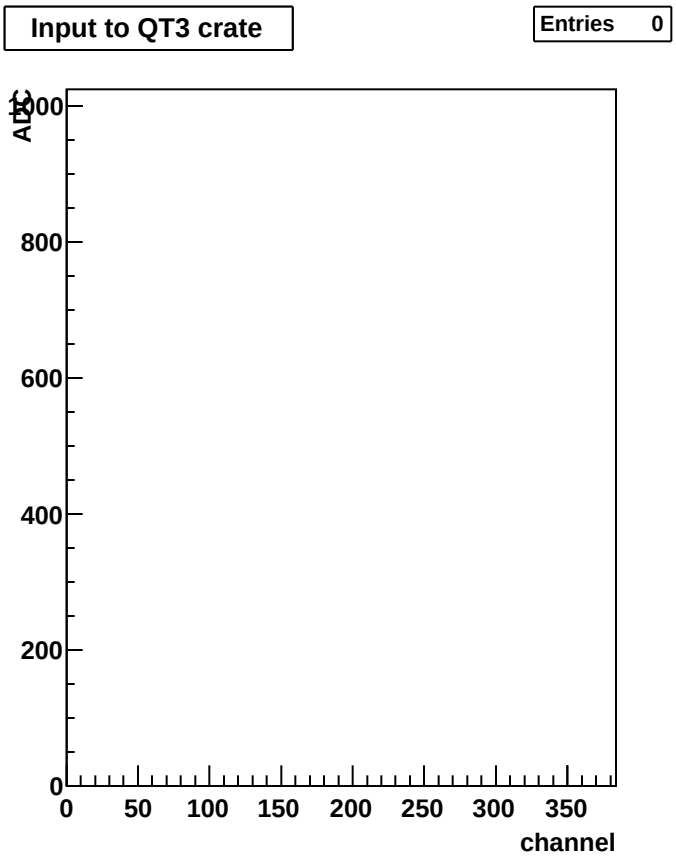

Entries 345726
BBC-S-East ADC Sum
BBC-S-West ADC Sum

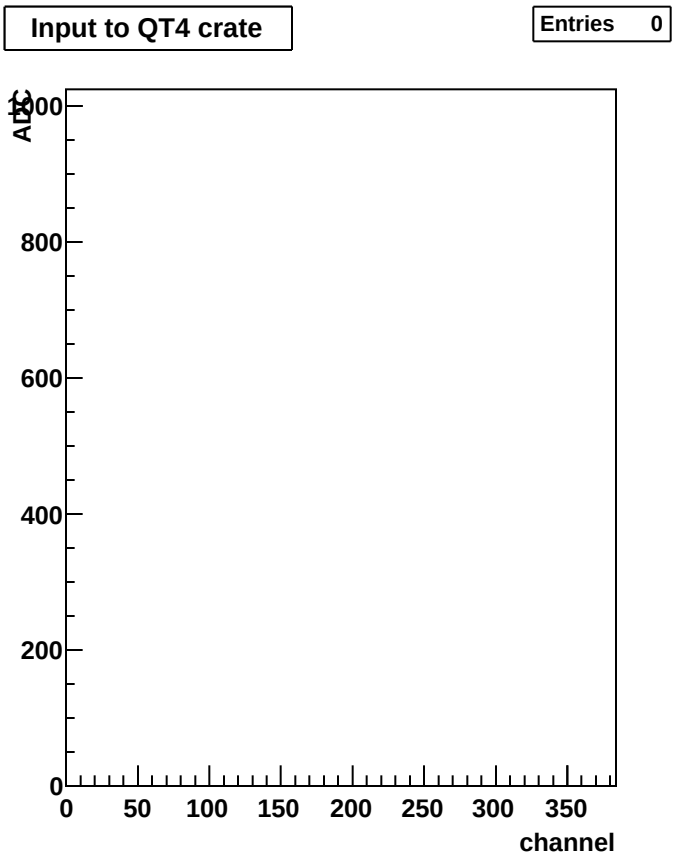

Entries 345726
ZDC East ADC Sum Att
ZDC West ADC Sum Att

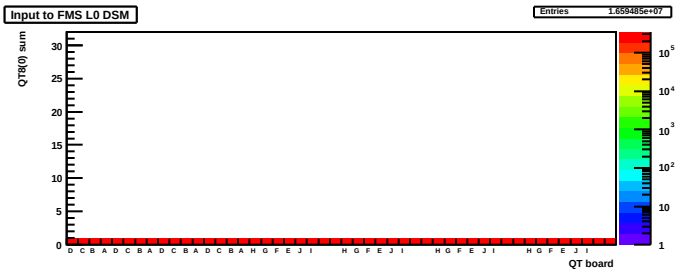

Entries 345726
BBC-L TAC Diff
BCC-S TAC Diff
Entries 345726
BBC-L TAC Diff
VPD TAC Diff
Entries 345726
BBC-L TAC Diff
ZDC TAC Diff
Entries 345726
BBC-S TAC Diff
VPD TAC Diff
Entries 345726
BBC-S TAC Diff
ZDC TAC Diff
Entries 345726
VPD TAC Diff
ZDC TAC Diff

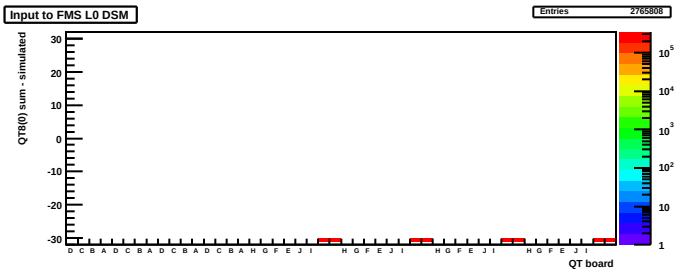

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

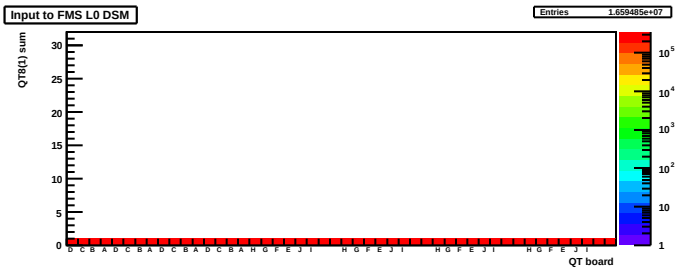

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

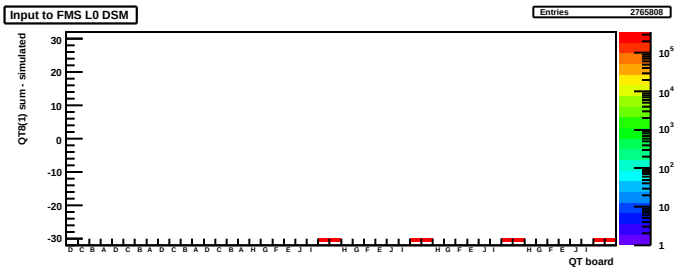

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

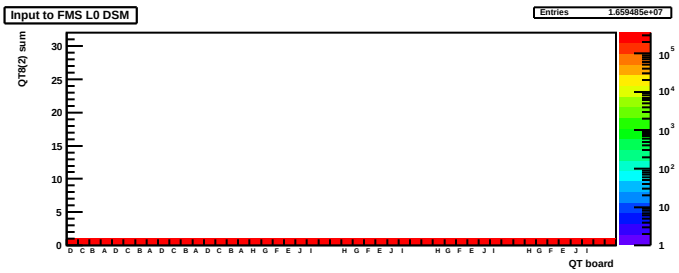

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

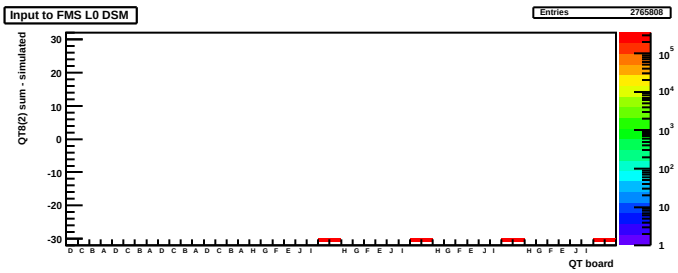

Input to FMS L0 DSM
Entries 1.659485e+07
QT8(0) sum
QT board

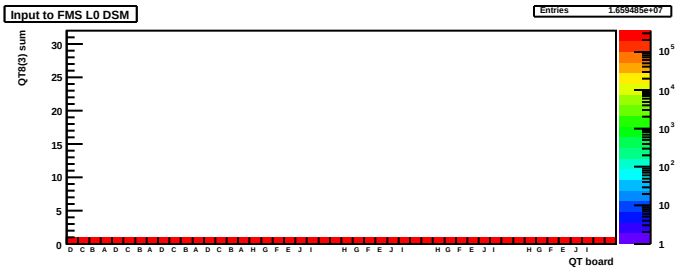

Input to FMS L0 DSM
Entries 2765808
QT8(0) sum - simulated
QT board

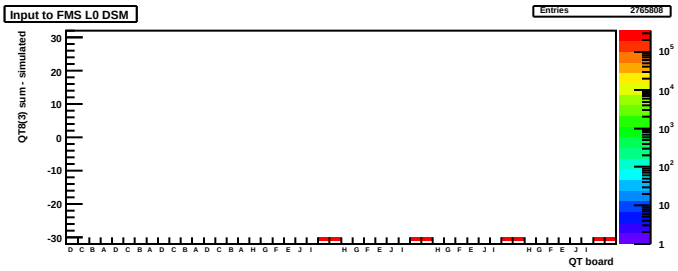

Input to FMS L0 DSM
Entries 1.659485e+07
QT8(1) sum
QT board

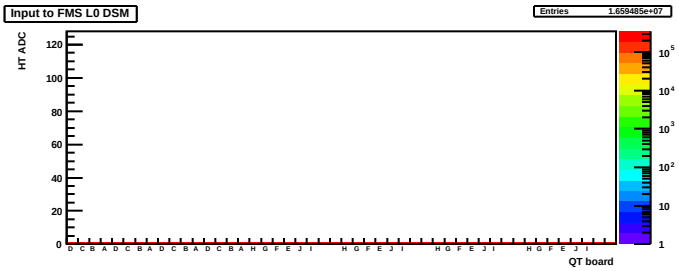

Input to FMS L0 DSM
Entries 2765808
QT8(1) sum - simulated
QT board

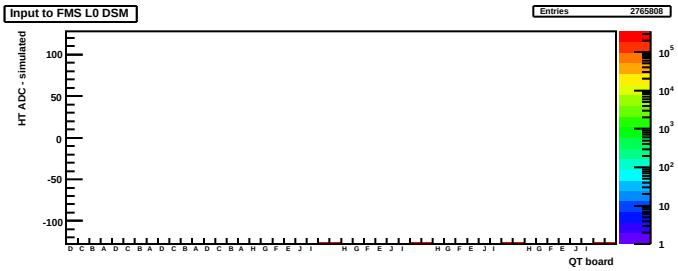

Input to FMS L0 DSM
Entries 1.659485e+07
QT8(2) sum
QT board

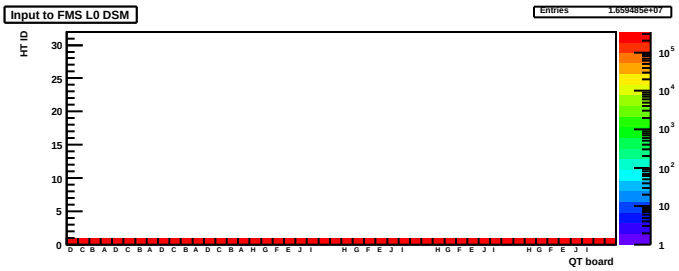

Input to FMS L0 DSM
Entries 2765808
QT8(2) sum - simulated
QT board

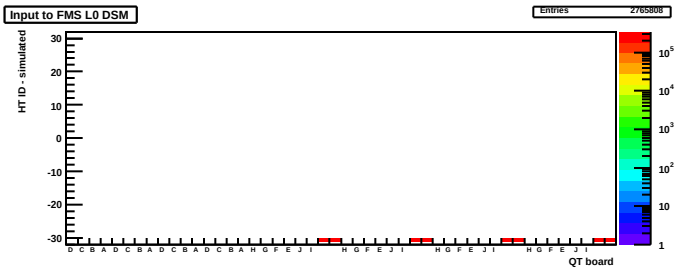

Input to FMS L0 DSM
Entries 1.659485e+07
QT8(3) sum
QT board

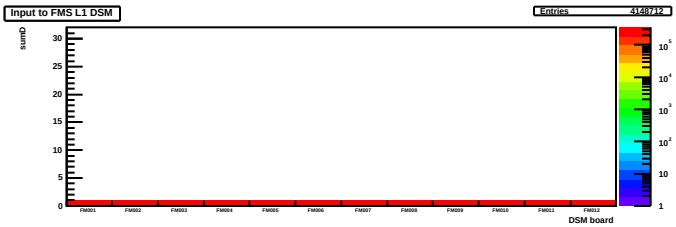

Input to FMS L0 DSM
Entries 2765808
QT8(3) sum - simulated
QT board

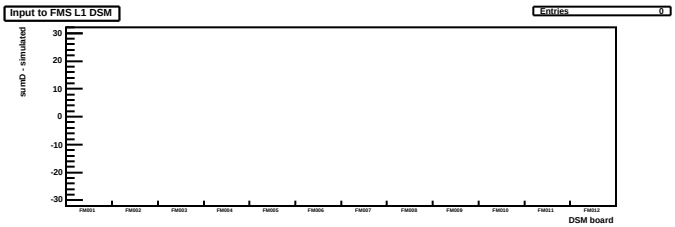

Input to FMS L0 DSM
Entries 1.659485e+07
HT ADC
QT board

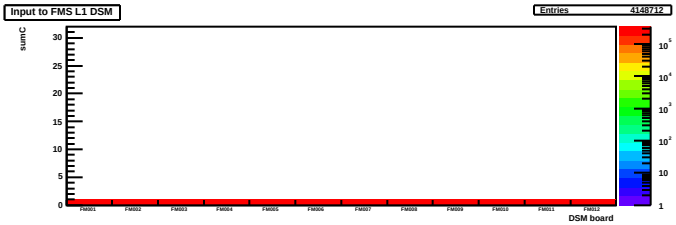

Input to FMS L0 DSM
Entries 2765808
HT ADC - simulated
QT board

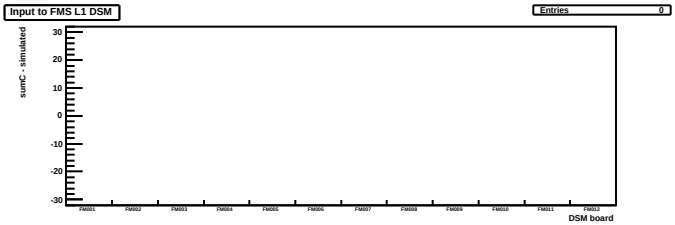

Input to FMS L0 DSM
Entries 1.659485e+07
HT ID
QT board

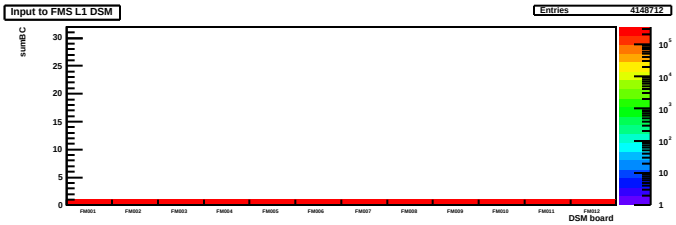

Input to FMS L0 DSM
Entries 2765808
HT ID - simulated
QT board

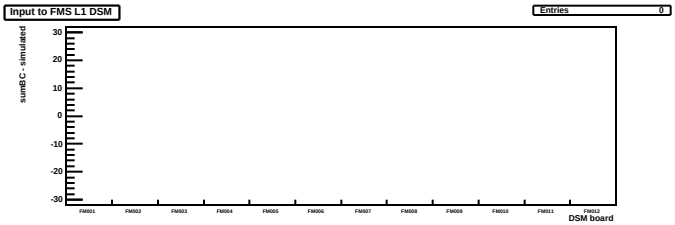

Input to FMS L1 DSM
Entries 4148712
sumD
DSM board

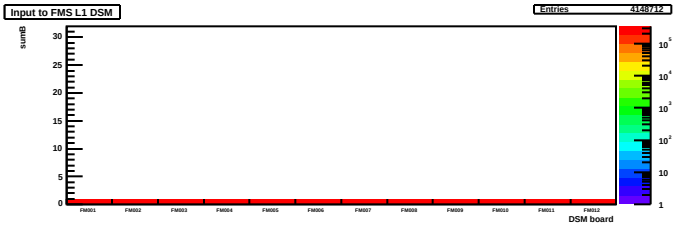

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

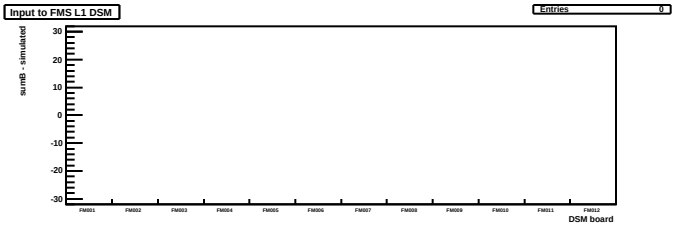

Input to FMS L1 DSM
Entries 4148712
sumC
DSM board

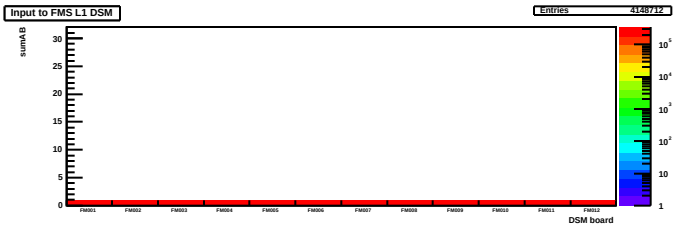

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

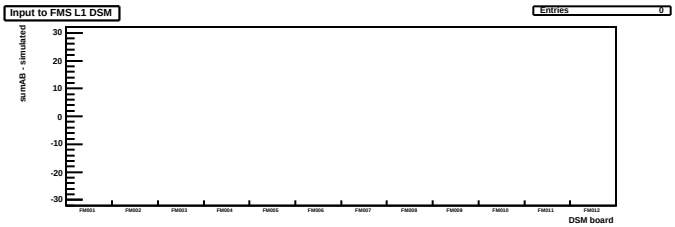

Input to FMS L1 DSM
Entries 4148712
sumBC
DSM board

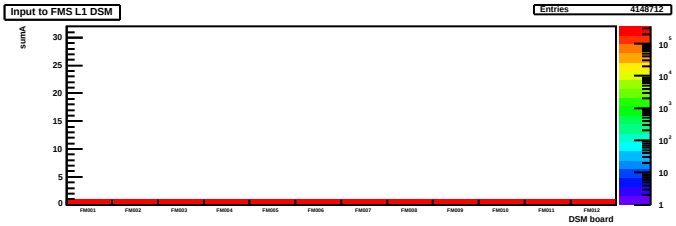

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

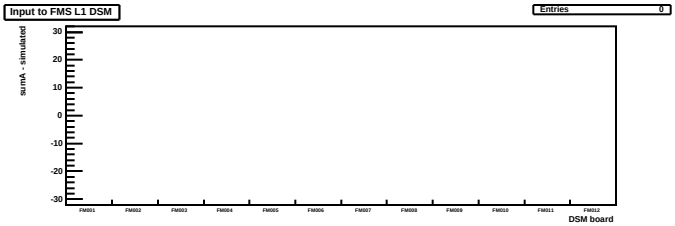

Input to FMS L1 DSM
Entries 4148712
sumB
DSM board

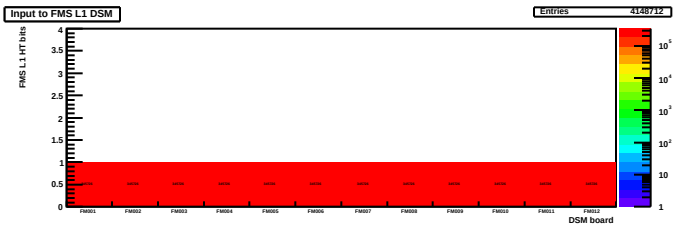

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

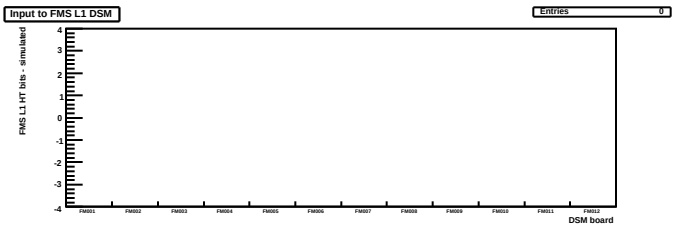

Input to FMS L1 DSM
Entries 4148712
sumAB
DSM board

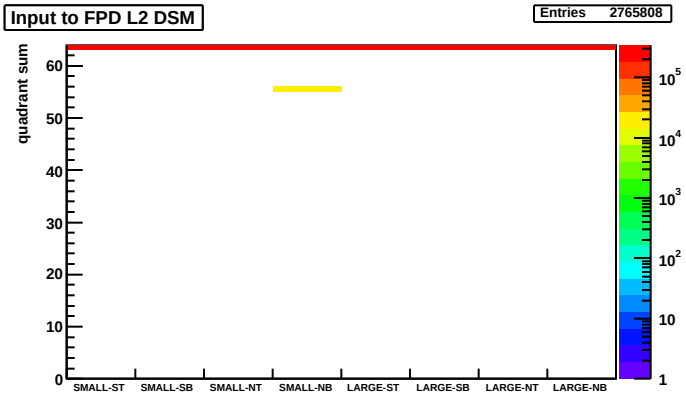

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

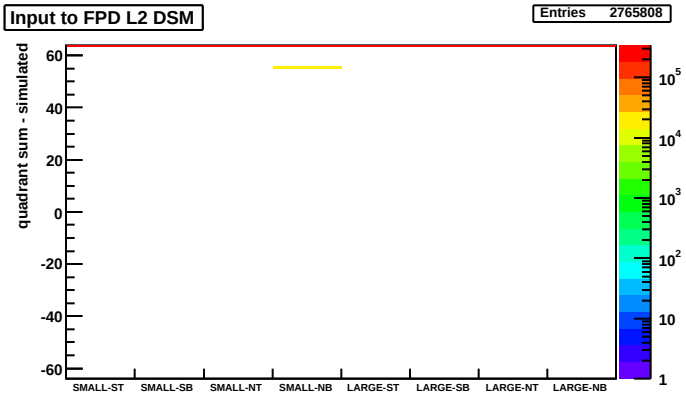

Input to FMS L1 DSM
Entries 4148712
sumA
DSM board

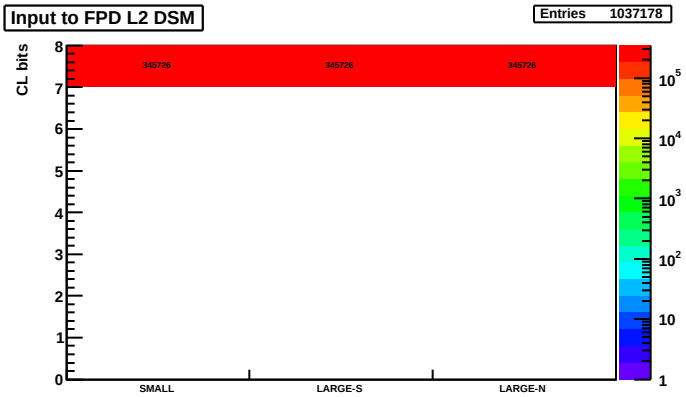

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

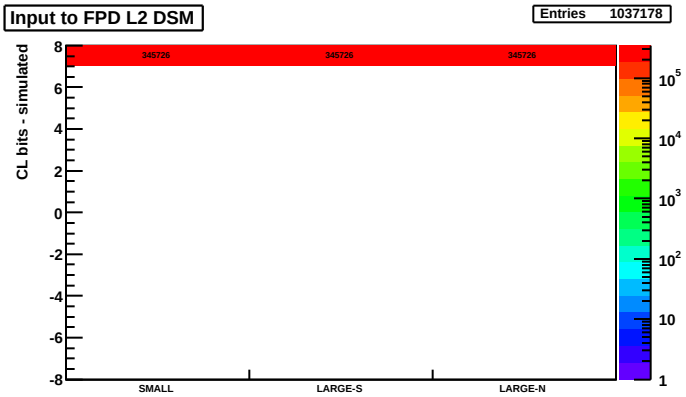

Input to FMS L1 DSM
Entries 4148712
FMS L1 HT bits
DSM board

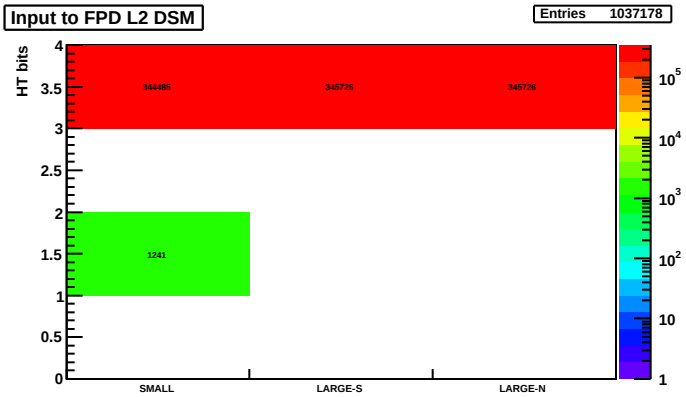

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

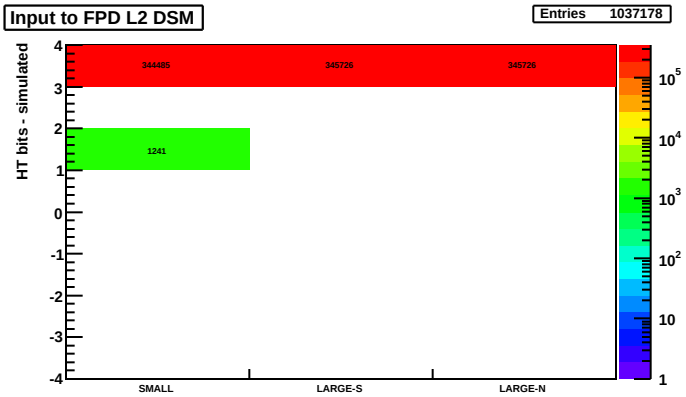

Input to FPD L2 DSM
Entries 2765808
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

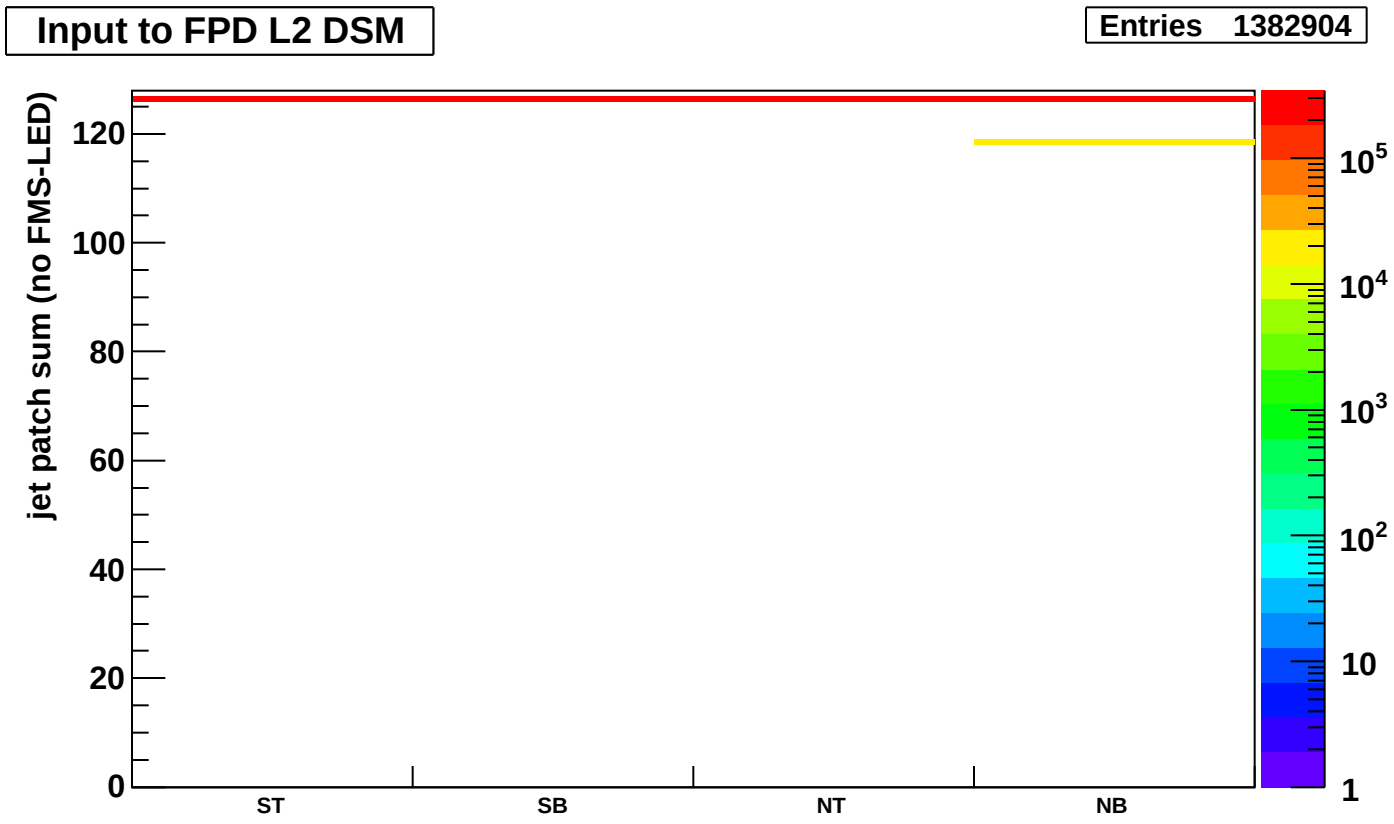

Input to FPD L2 DSM
Entries 2765808
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

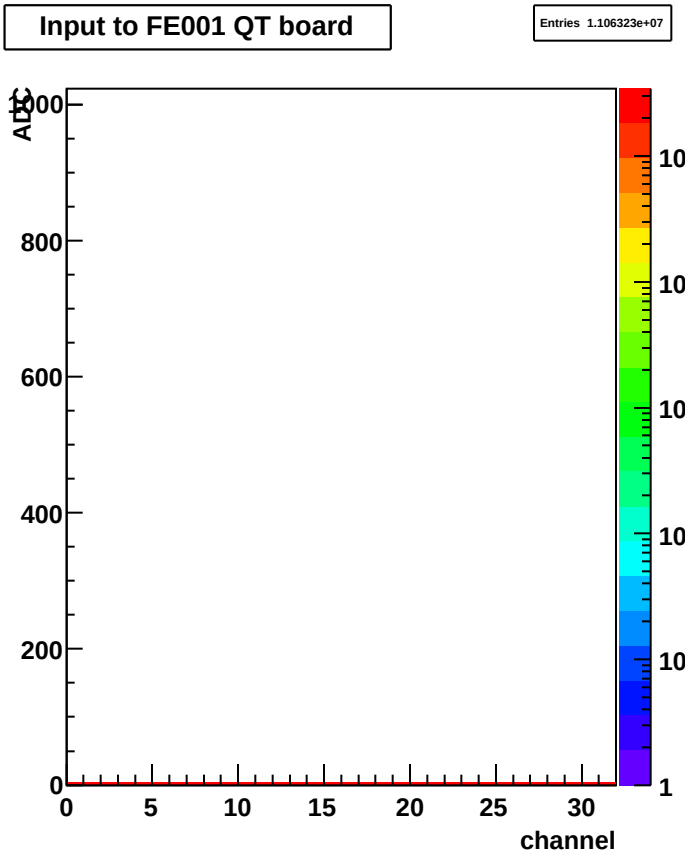

Input to FPD L2 DSM
Entries 1037178
CL bits
345726
345726
345726
SMALL
LARGE-S
LARGE-N

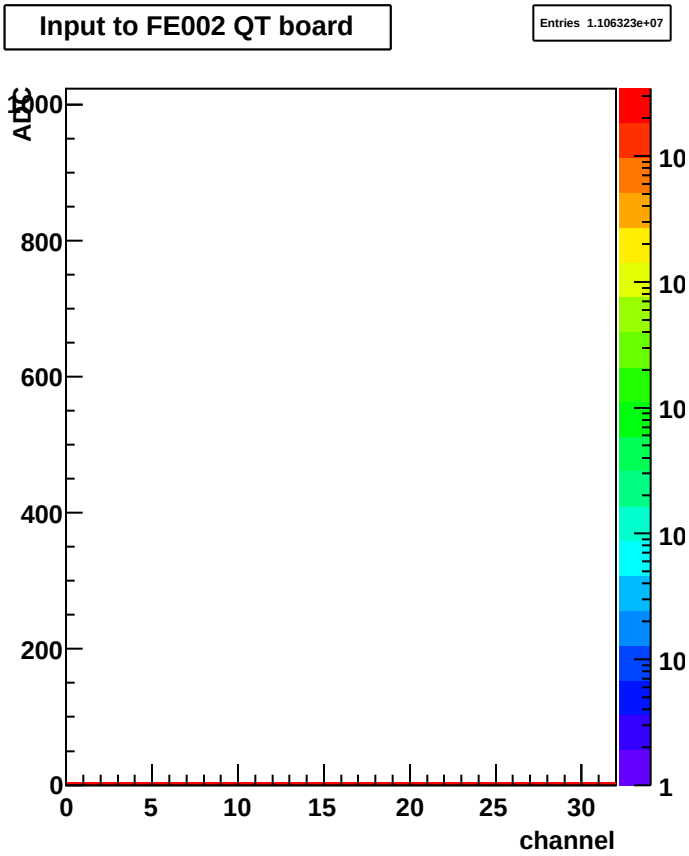

Input to FPD L2 DSM
Entries 1037178
CL bits - simulated
345726
345726
345726
SMALL
LARGE-S
LARGE-N

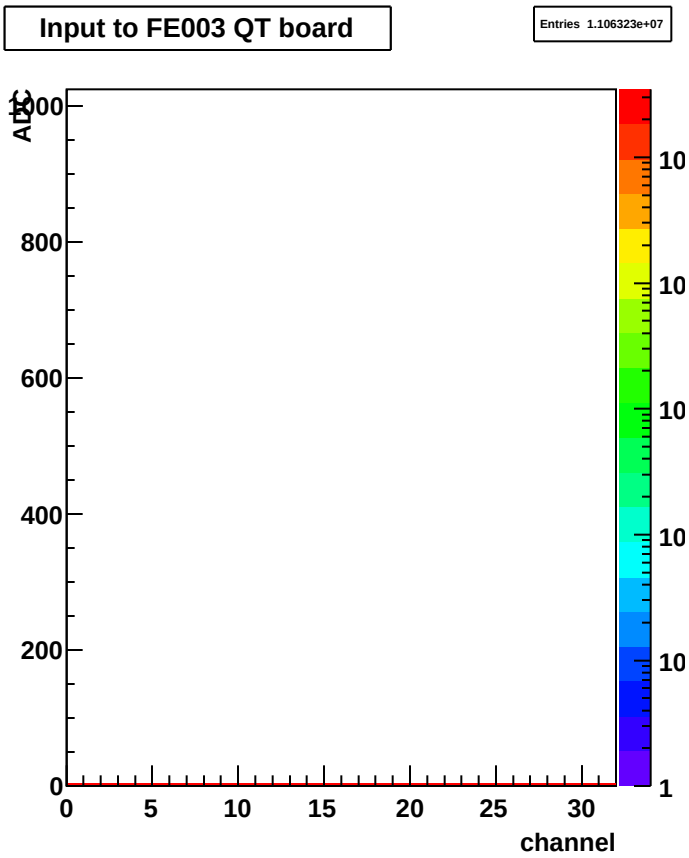

Input to FPD L2 DSM
Entries 1037178
HT bits
344485
345726
345726
1241
SMALL
LARGE-S
LARGE-N

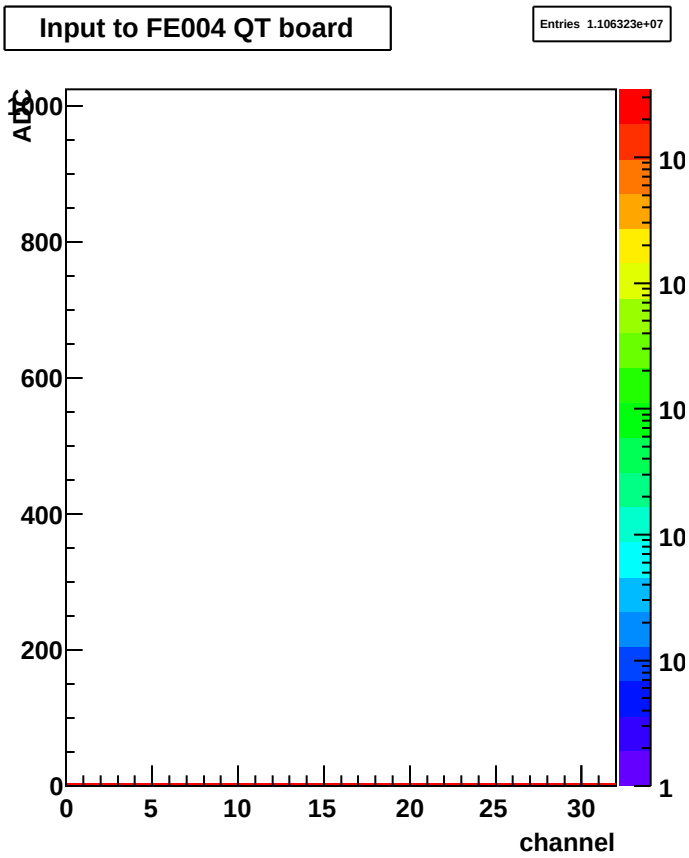

Input to FPD L2 DSM
Entries 1037178
HT bits - simulated
344485
345726
345726
1241
SMALL
LARGE-S
LARGE-N

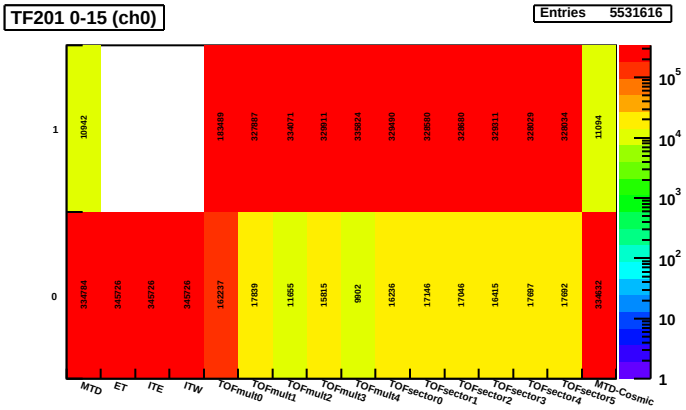

Input to FPD L2 DSM
Entries 1382904
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
$10^2$
$10^3$
$10^4$
$10^5$

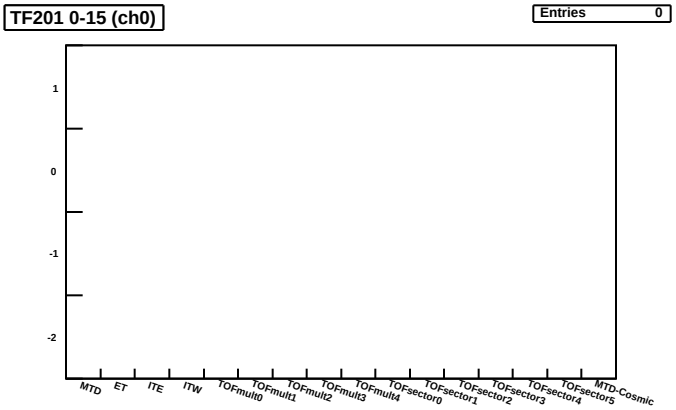

Input to FE001 QT board
Entries 1.106323e+07
ADC
channel

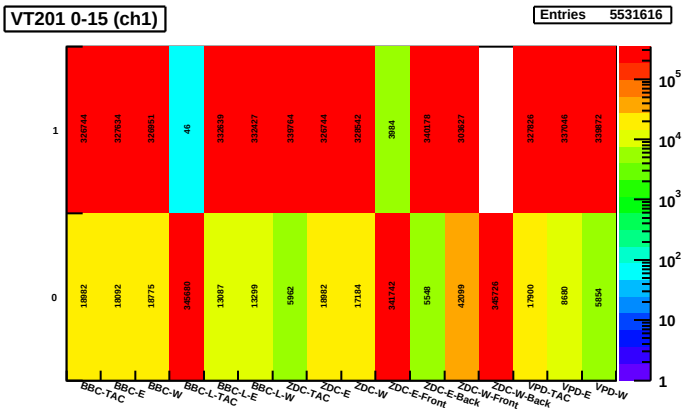

Input to FE002 QT board
Entries 1.106323e+07
ADC
channel

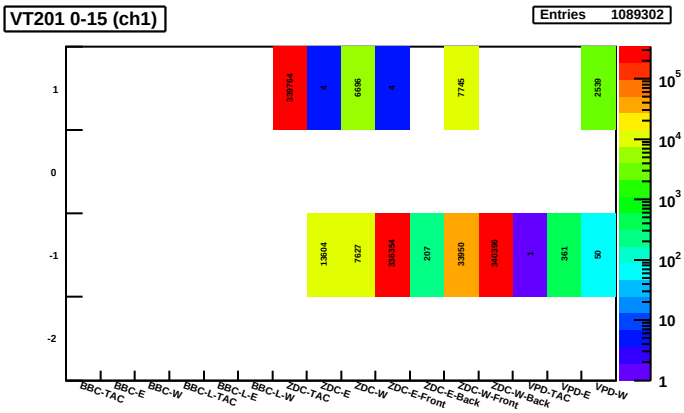

Input to FE003 QT board
Entries 1.106323e+07
ADC
channel

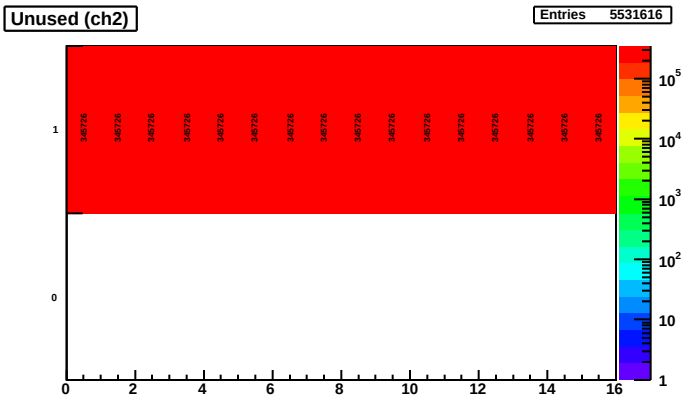

Input to FE004 QT board
Entries 1.106323e+07
ADC
channel

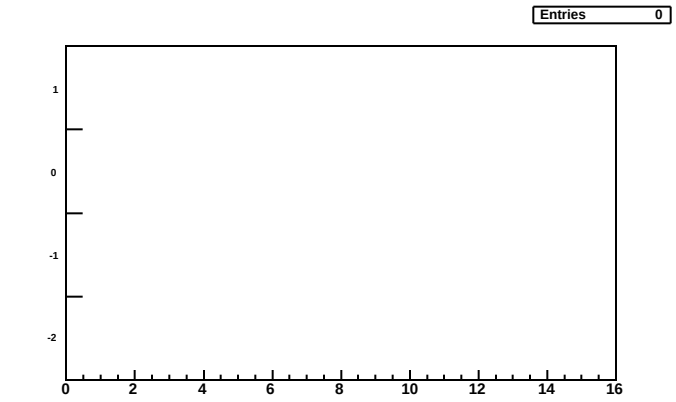

TF201 0-15 (ch0)
Entries 5531616
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

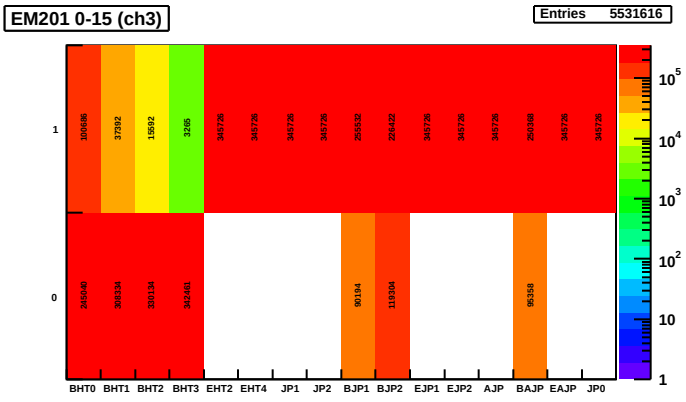

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

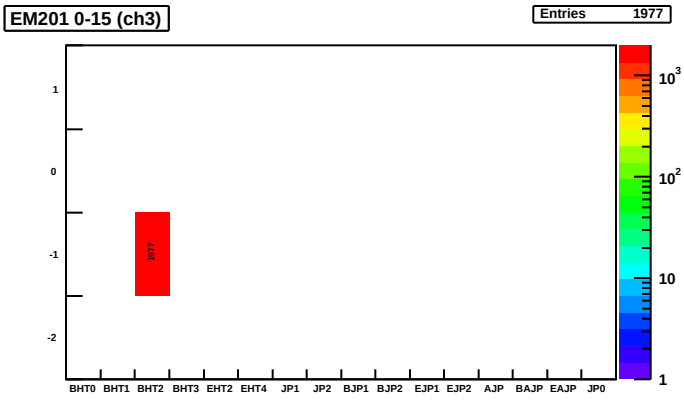

VT201 0-15 (ch1)
Entries 5531616
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 1089302
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

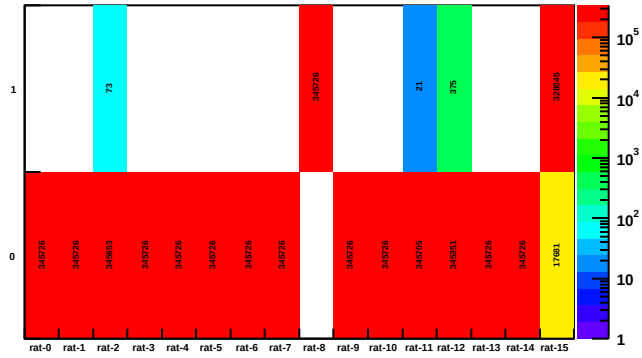

Unused (ch2)
Entries 5531616

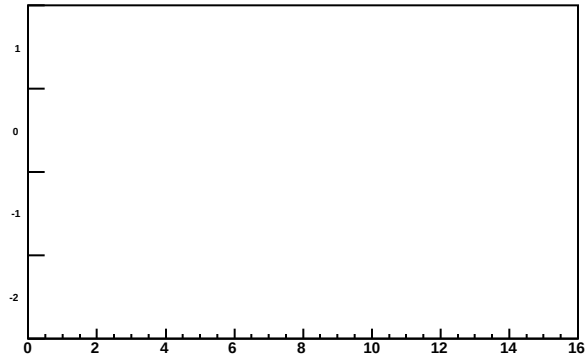

Entries 0


EM201 0-15 (ch3)
Entries 5531616
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

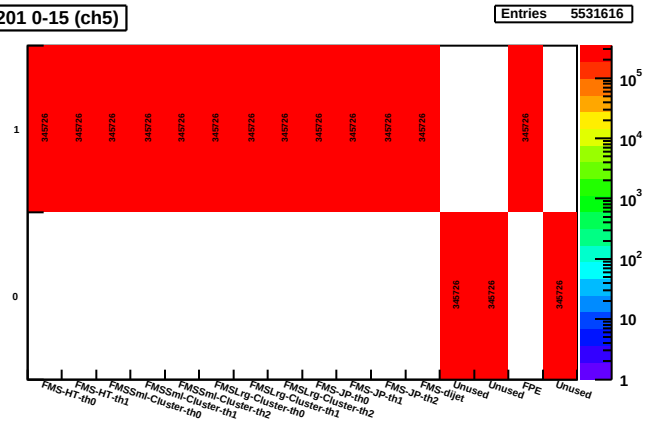

EM201 0-15 (ch3)
Entries 1977
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

RAT board (ch4)

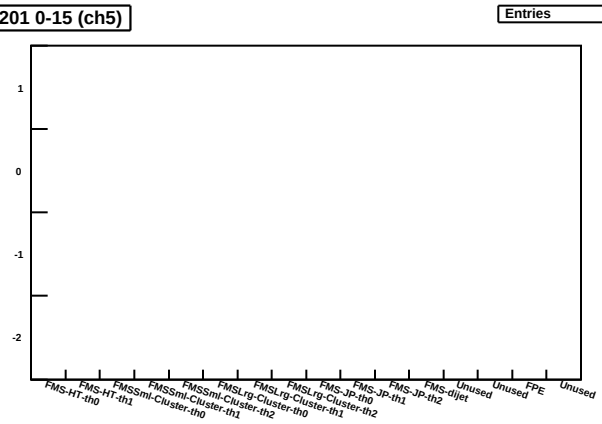


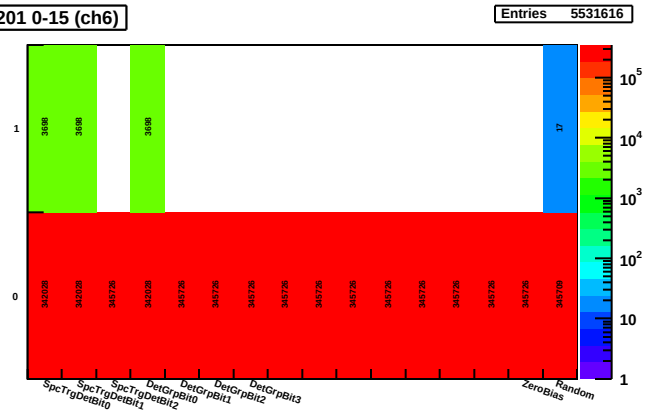

FP201 0-15 (ch5)

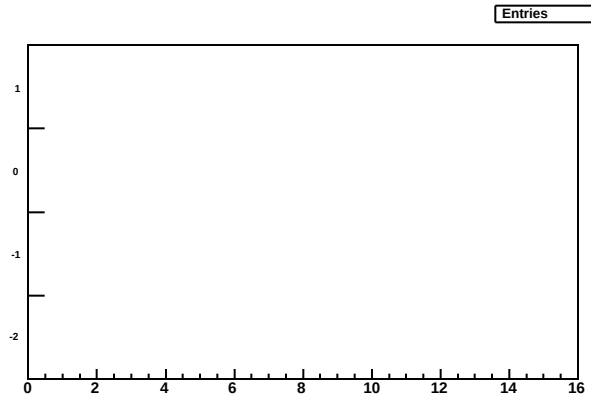


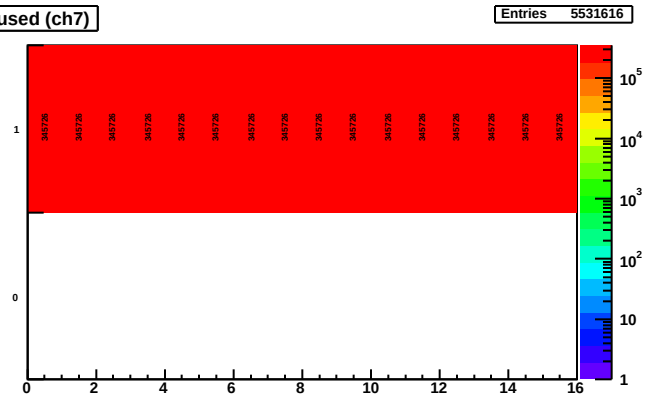
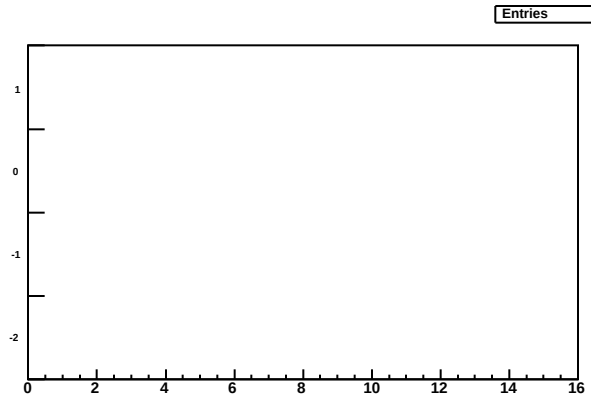

FP201 0-15 (ch5)




ST201 0-15 (ch6)




Unused (ch7)