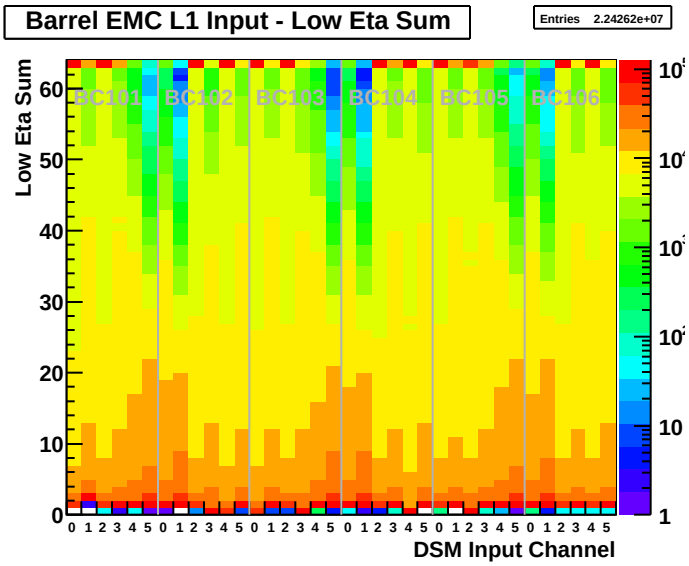
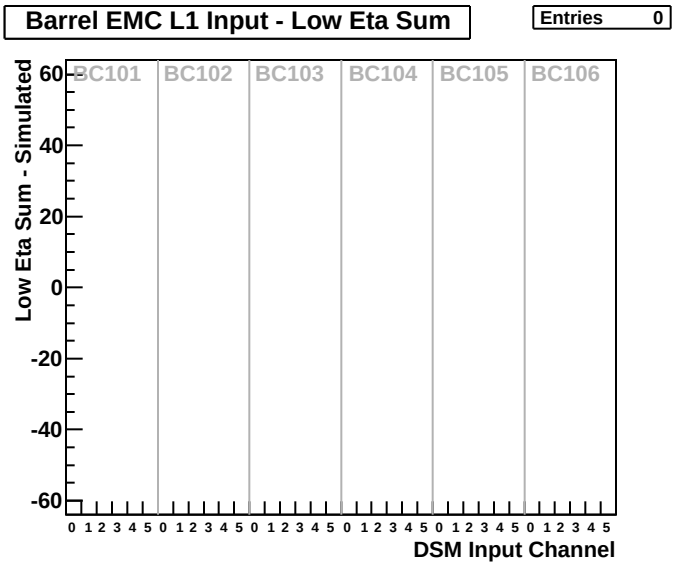
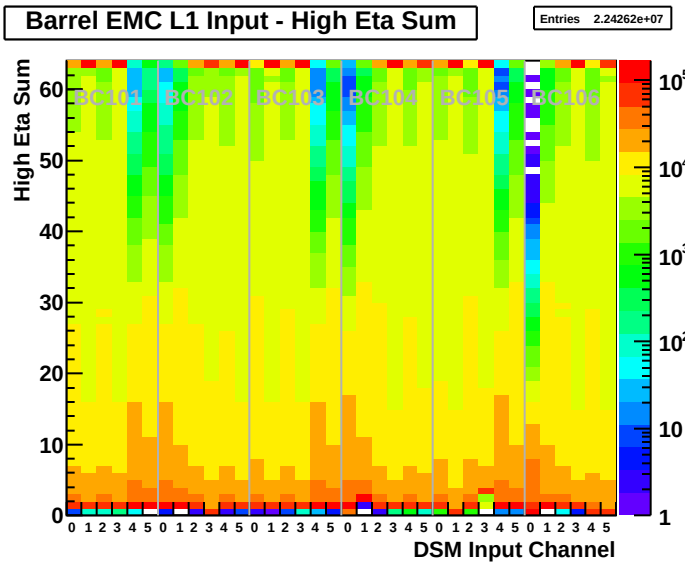
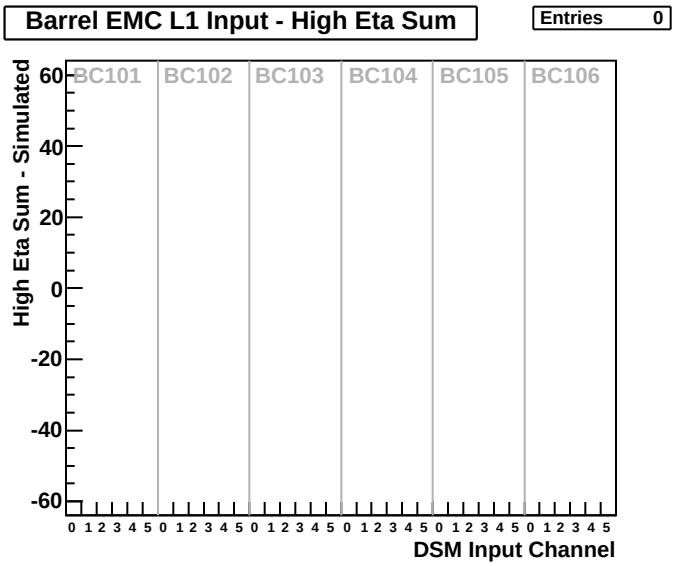
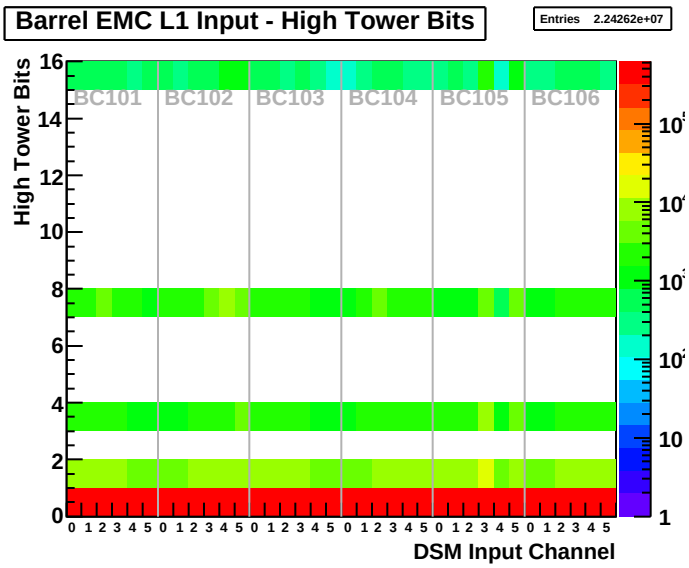
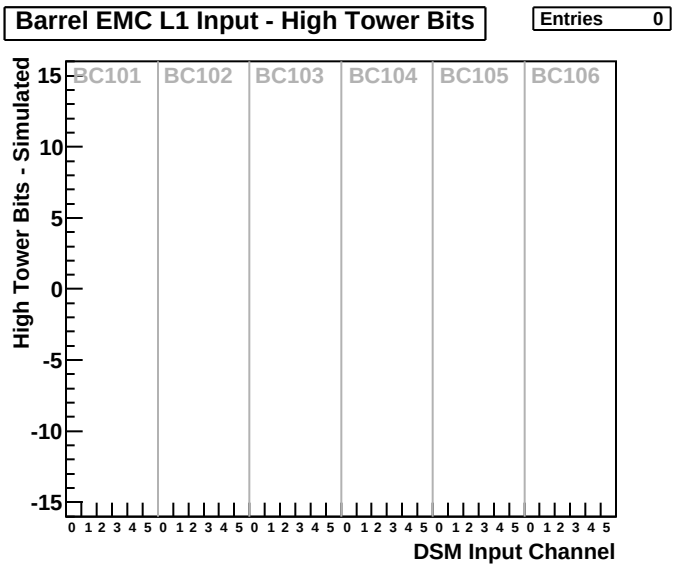

Barrel EMC L1 Input - Low Eta Sum
Entries 2.24262e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

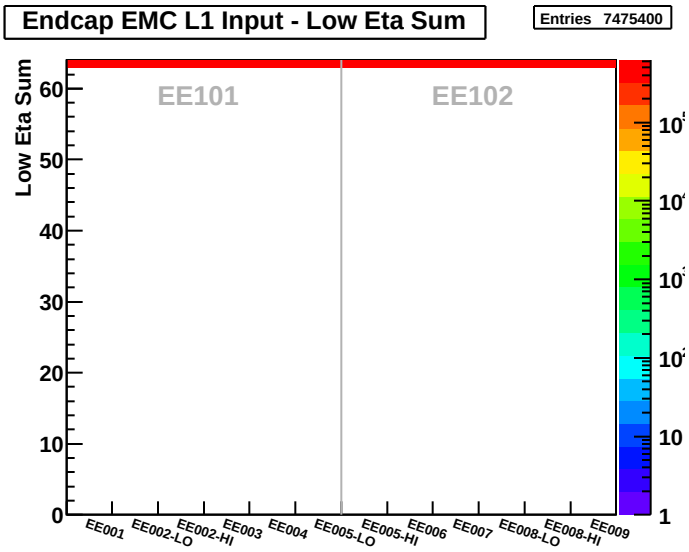

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

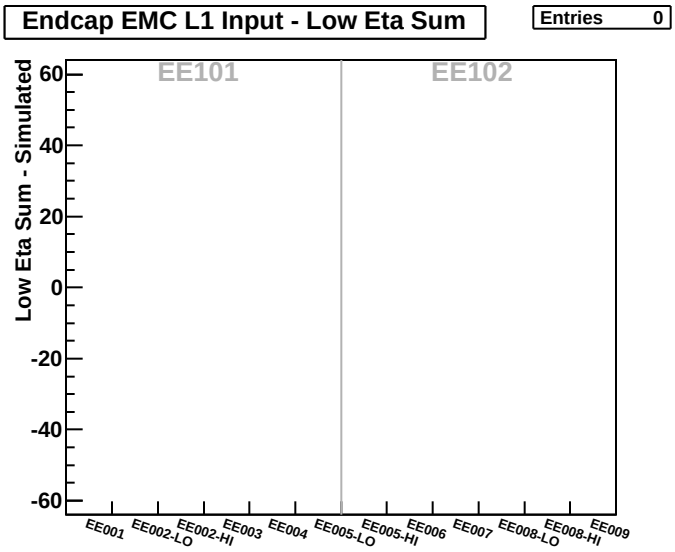

Barrel EMC L1 Input - High Eta Sum
Entries 2.24262e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

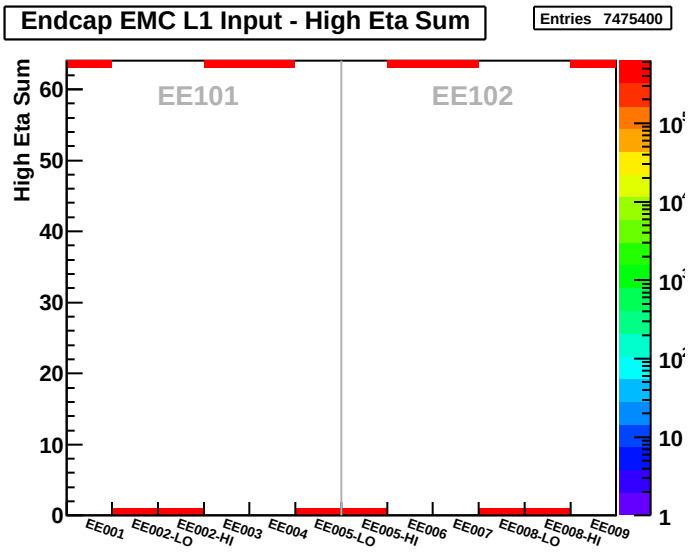

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

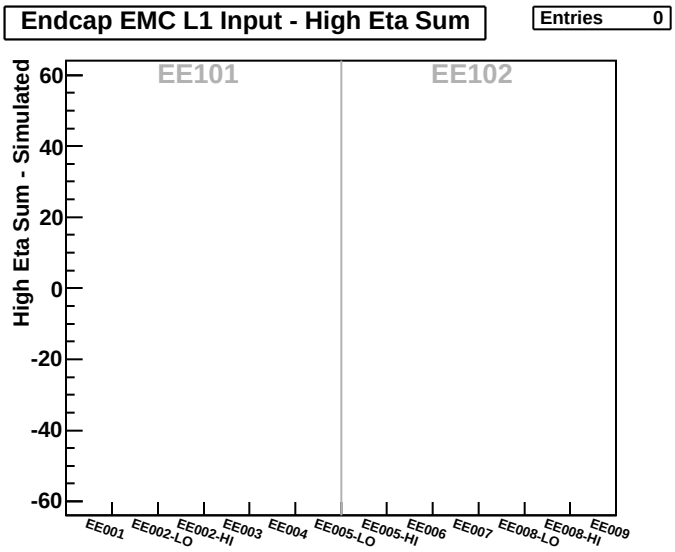

Barrel EMC L1 Input - High Tower Bits
Entries 2.24262e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

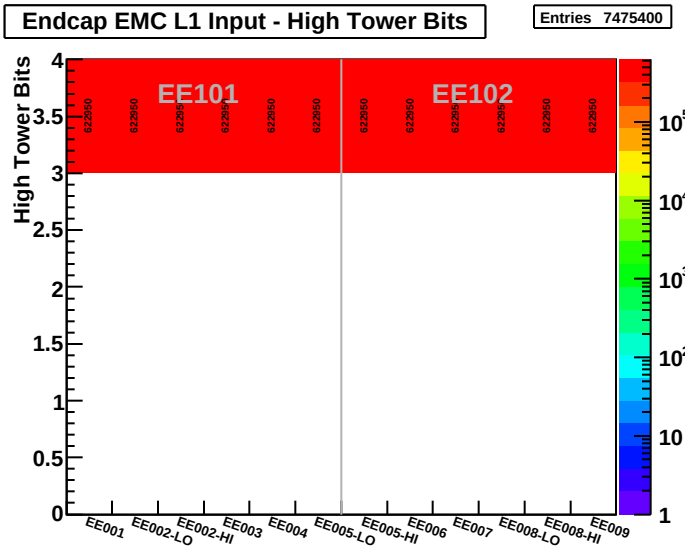

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

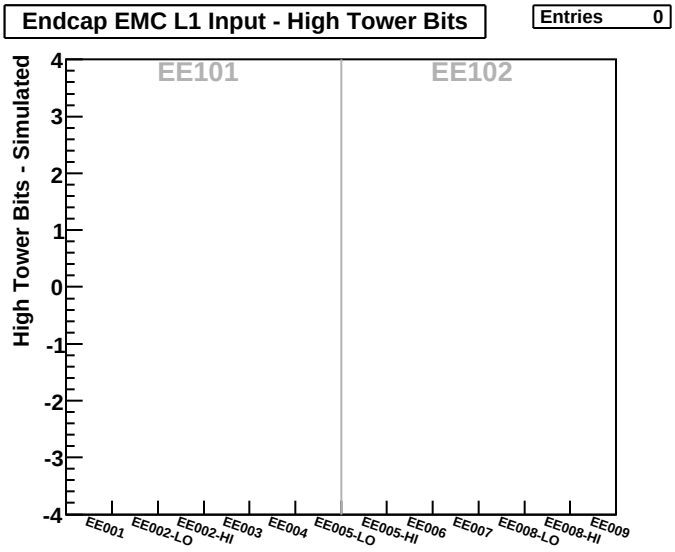

Endcap EMC L1 Input - Low Eta Sum
Entries 7475400
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

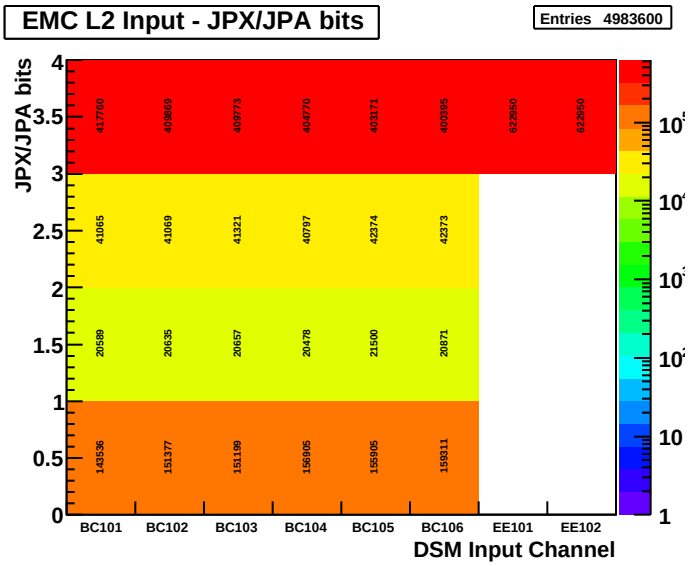

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

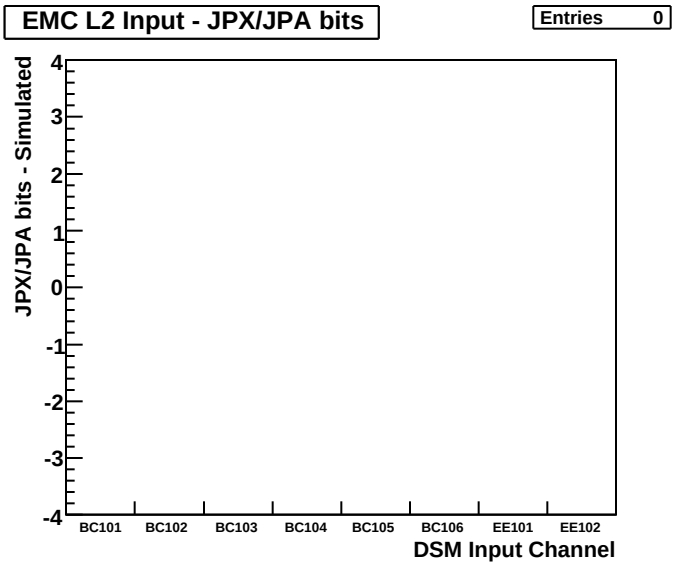

Endcap EMC L1 Input - High Eta Sum
Entries 7475400
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

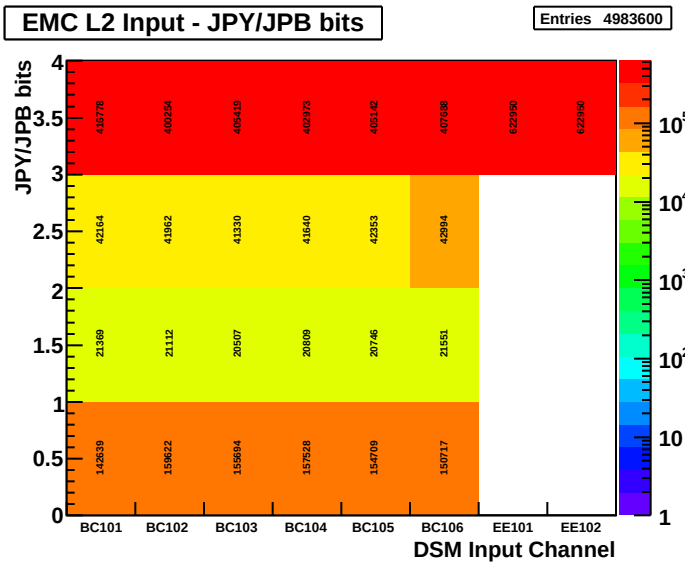

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

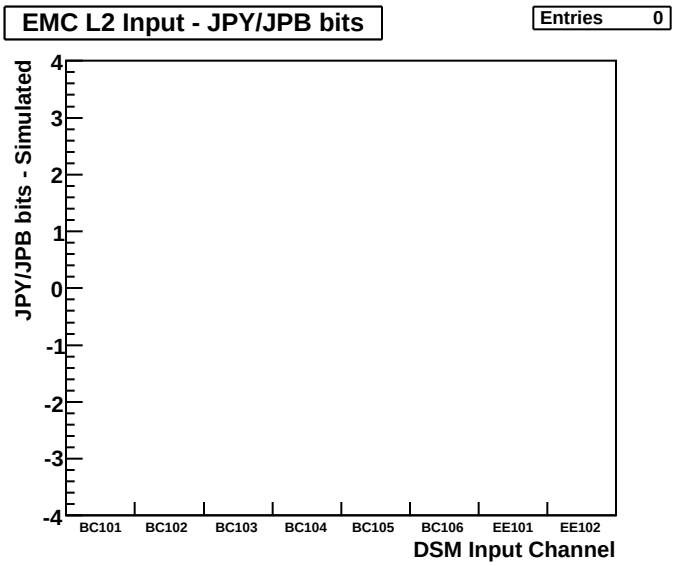

Endcap EMC L1 Input - High Tower Bits
Entries 7475400
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

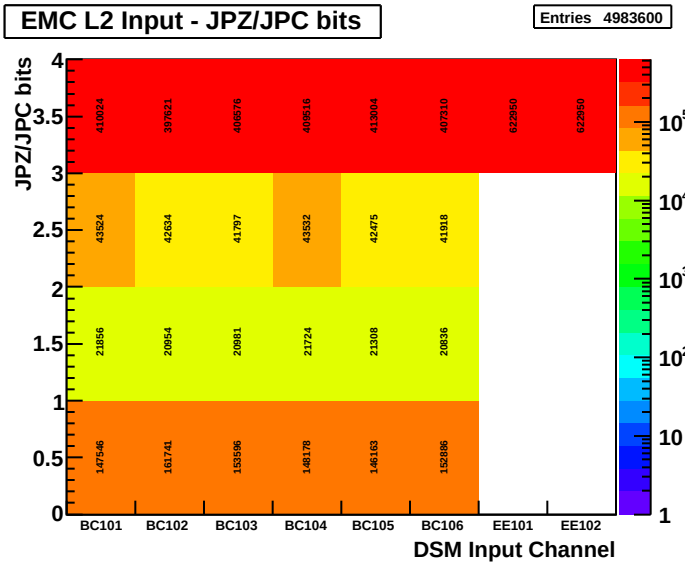

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

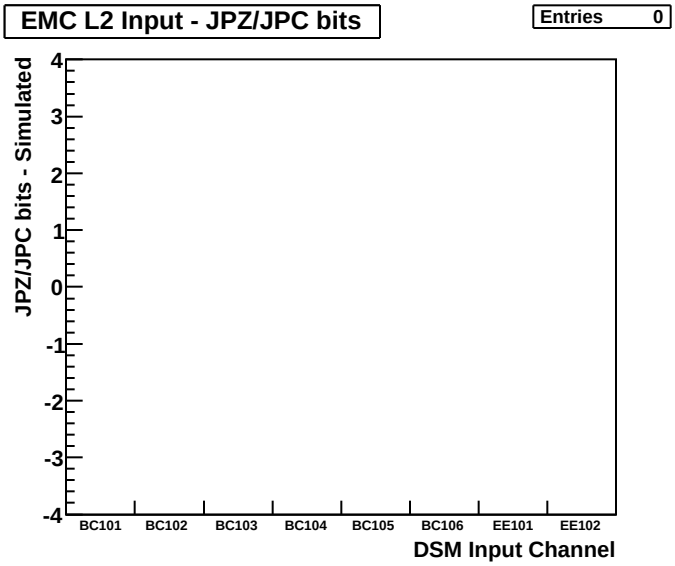

EMC L2 Input - JPX/JPA bits
Entries 4983600
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
417760 409869 409773 404770 403171 400395 622950 622950
41065 41069 41321 40797 42374 42373
20589 20635 20657 20478 21500 20871
149536 151377 151199 156905 155905 159311

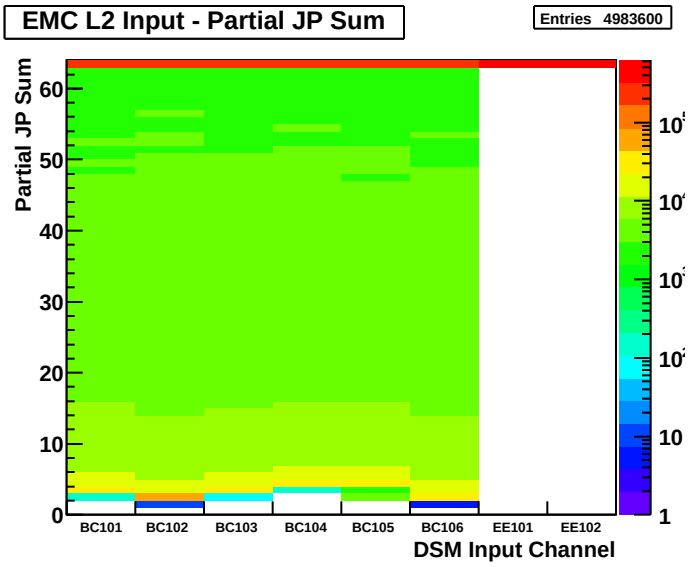

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

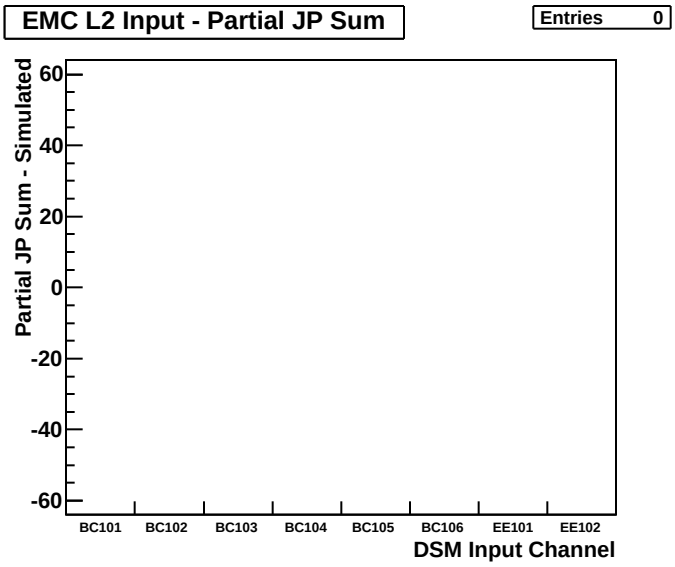

EMC L2 Input - JPY/JPB bits
Entries 4983600
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
416778 400254 405419 402973 405142 407688 622950 622950
42164 41962 41330 41640 42353 42994
21369 21112 20507 20809 20746 21551
149539 159622 155694 157528 154709 150717

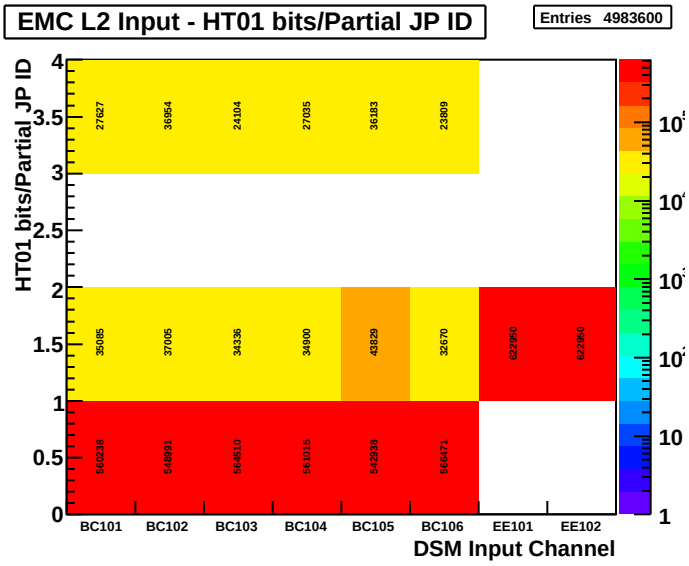

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

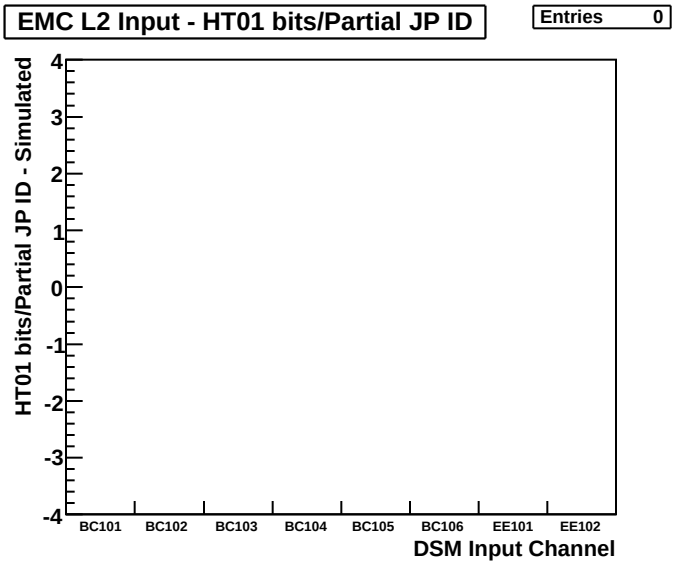

EMC L2 Input - JPZ/JPC bits
Entries 4983600
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
410024 397621 406576 409516 413004 407310 622950 622950
43524 42634 41797 43532 42475 41918
21856 20954 20981 21724 21308 20836
147546 161741 153596 148178 146163 152886

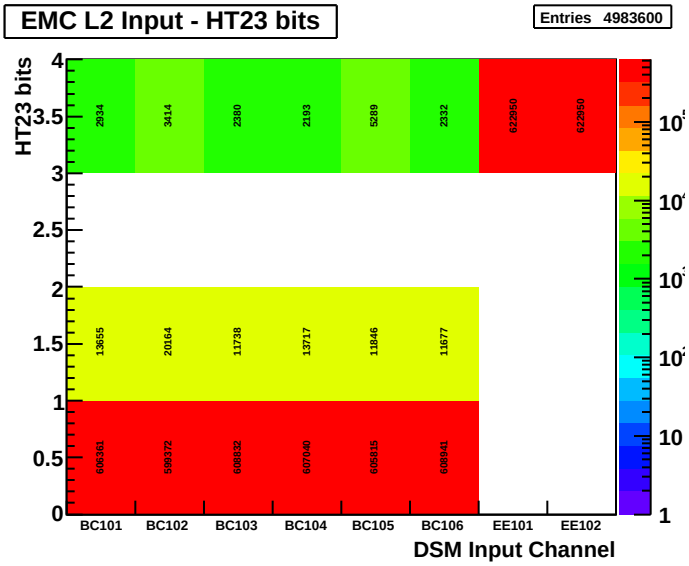

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

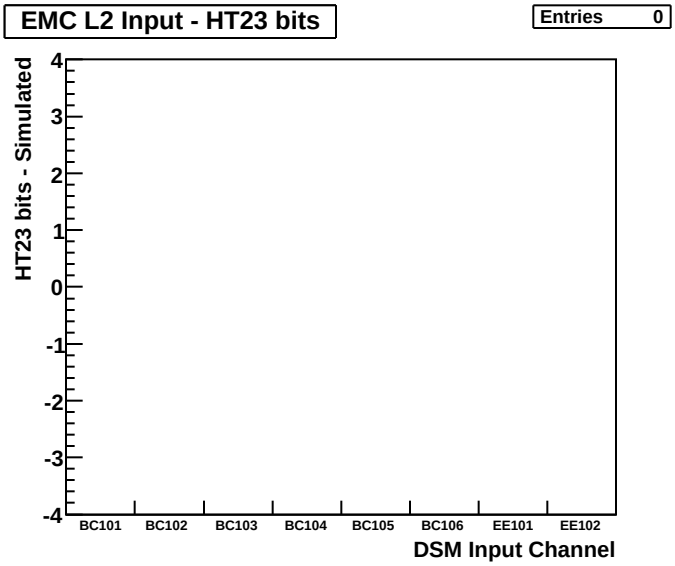

EMC L2 Input - Partial JP Sum
Entries 4983600
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

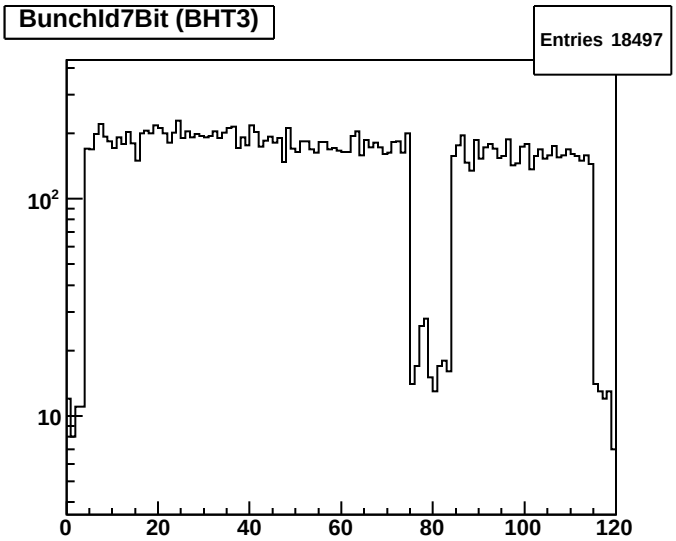

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

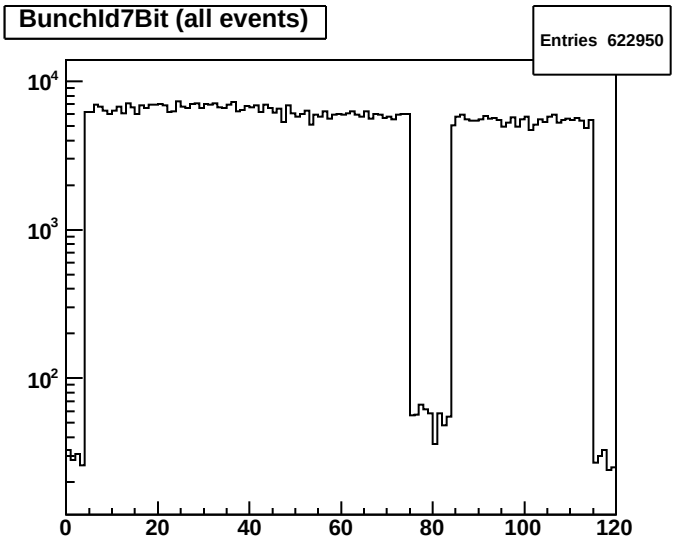

EMC L2 Input - HT01 bits/Partial JP ID
Entries 4983600
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

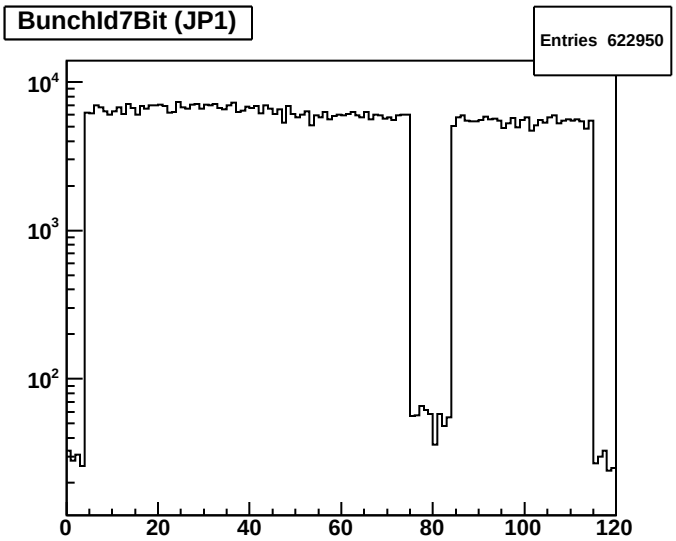

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

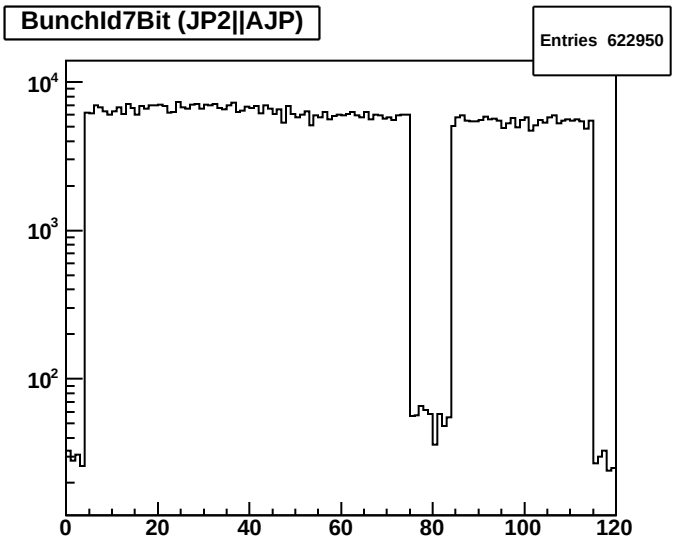

EMC L2 Input - HT23 bits
Entries 4983600
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

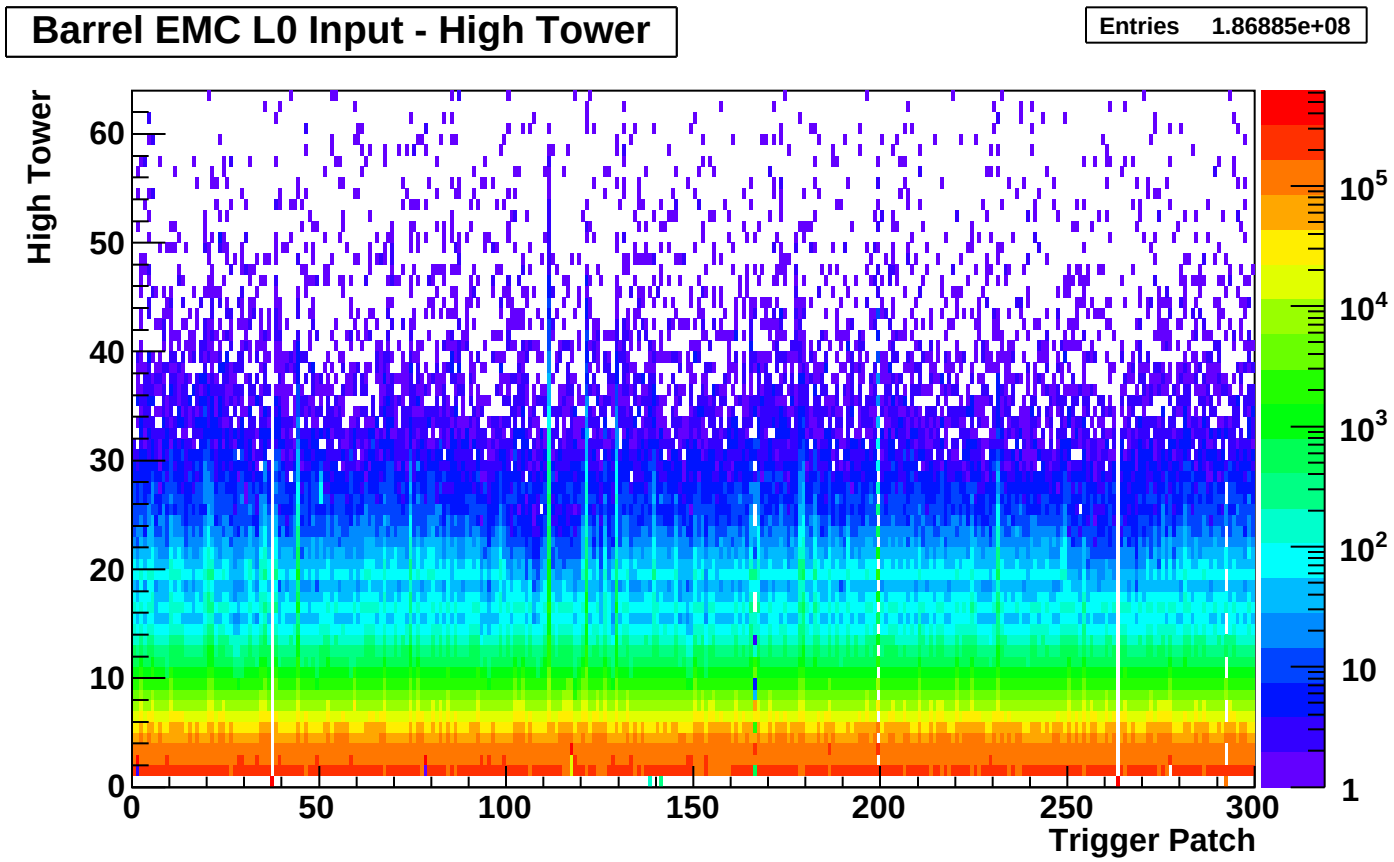

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

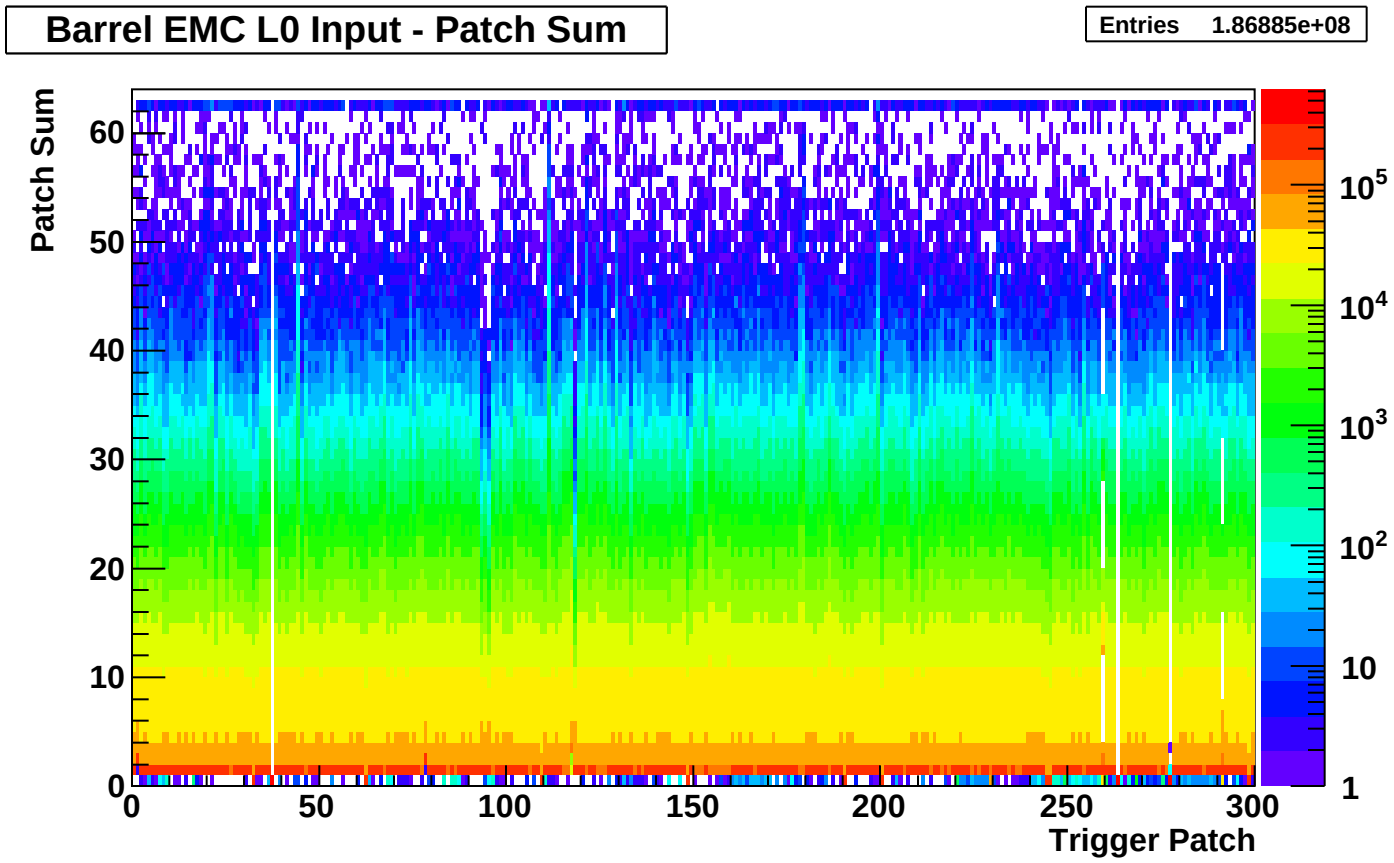

BunchId7Bit (BHT3)
Entries 18497
10^2
10
0
20
40
60
80
100
120

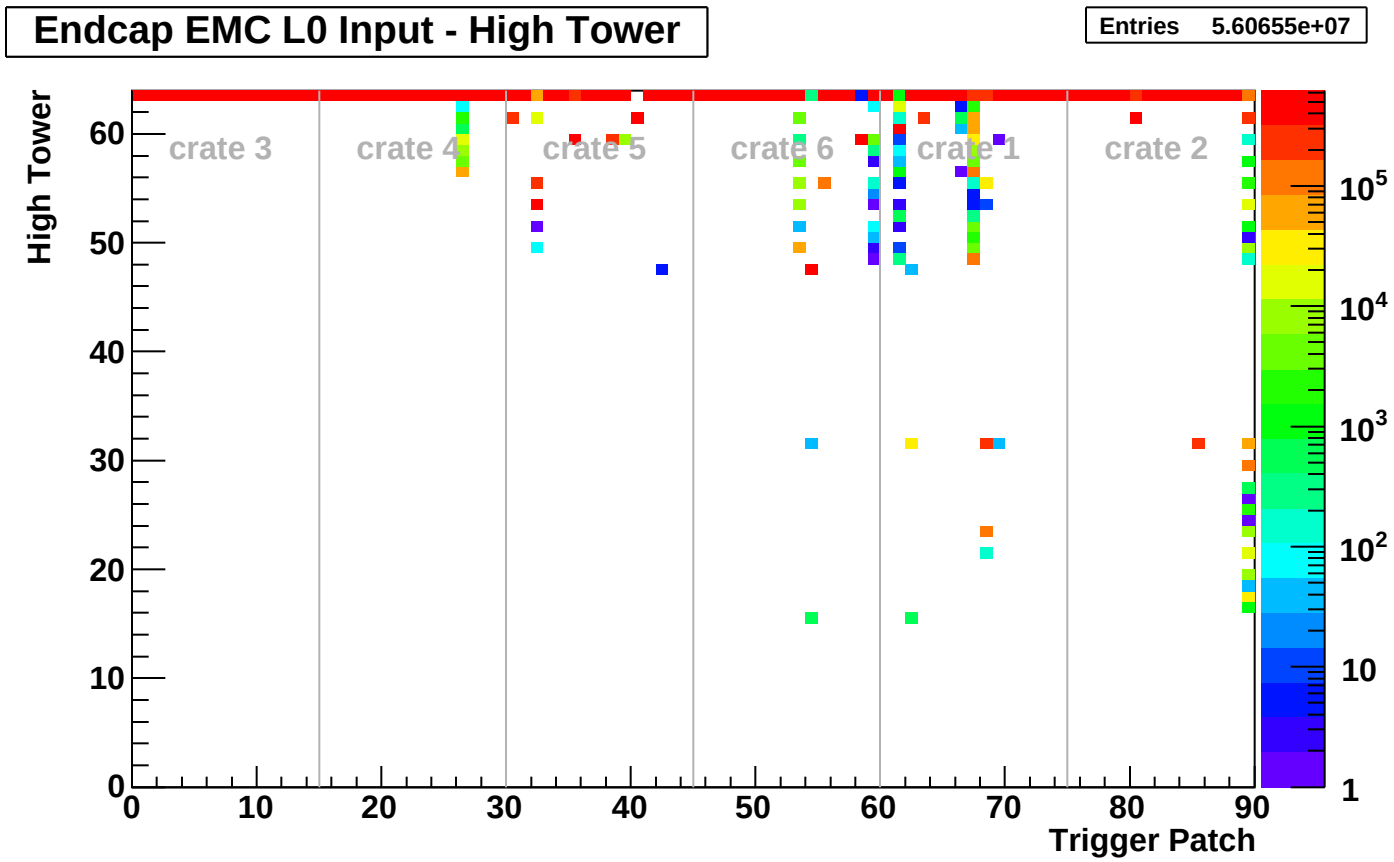

BunchId7Bit (all events)
Entries 622950
10^4
10^3
10^2
0
20
40
60
80
100
120

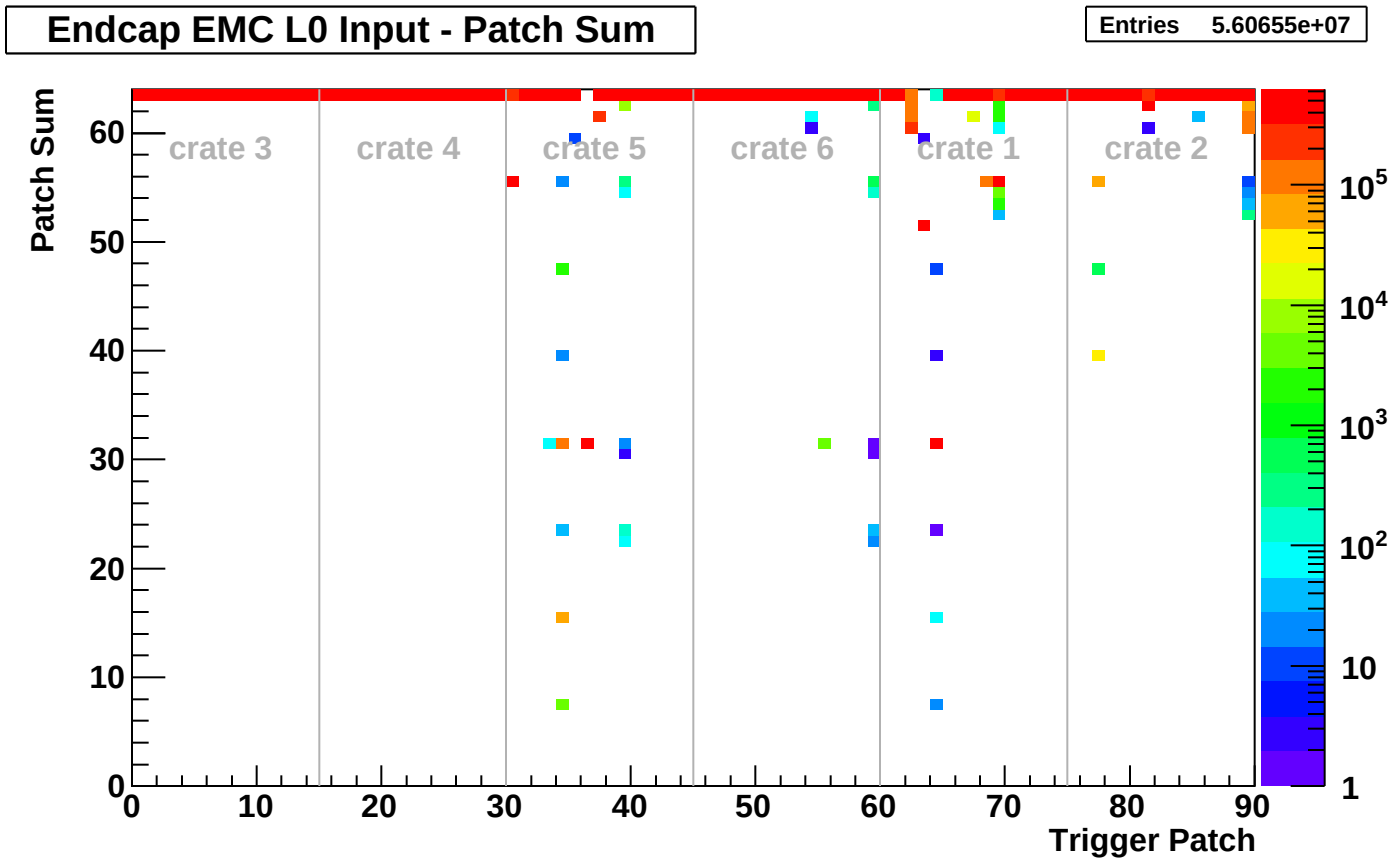

BunchId7Bit (JP1)
Entries 622950
10^4
10^3
10^2
0
20
40
60
80
100
120

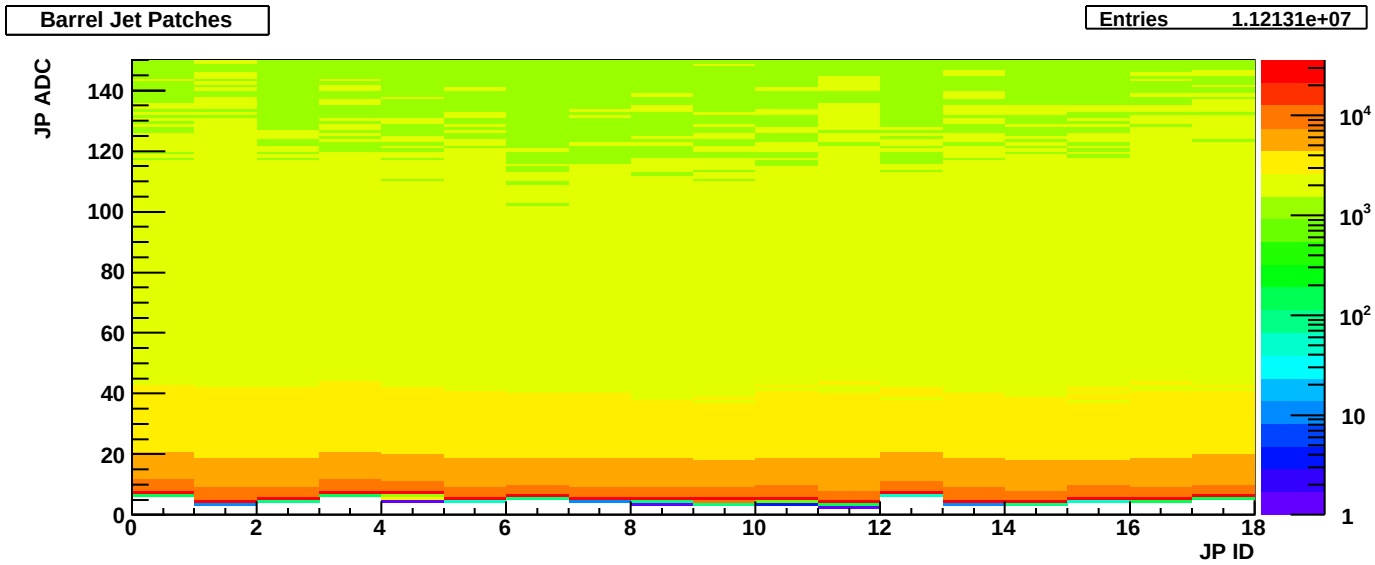

BunchId7Bit (JP2||AJP)
Entries 622950
10^4
10^3
10^2
0
20
40
60
80
100
120

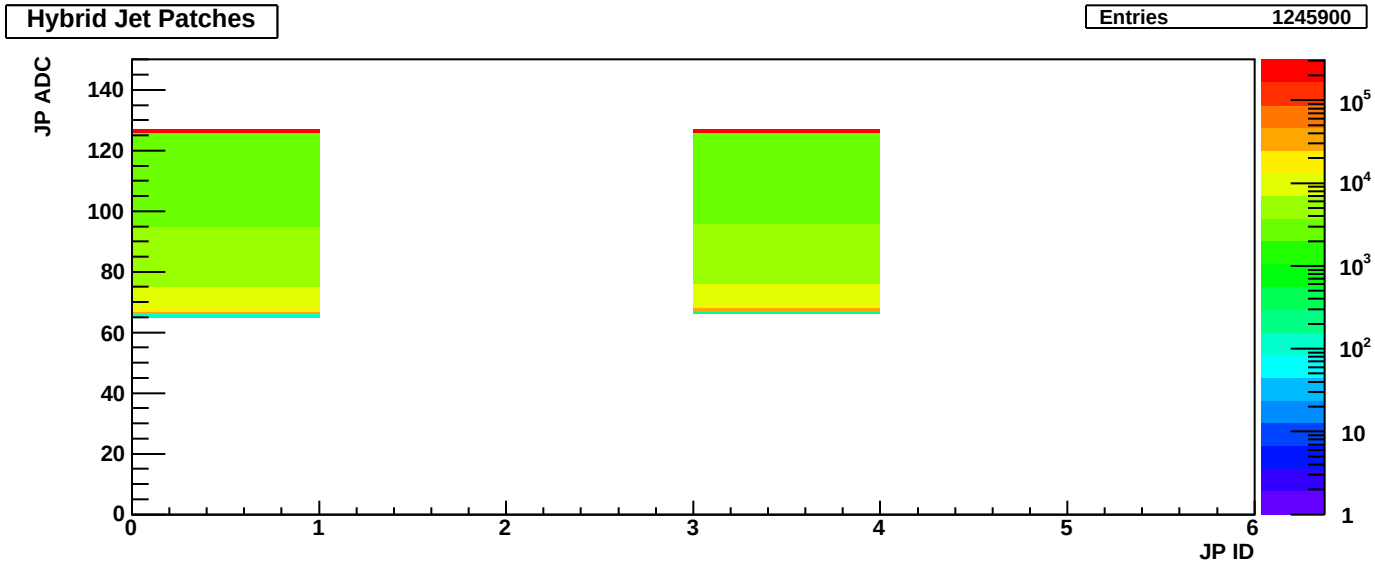

Barrel EMC L0 Input - High Tower
Entries 1.86885e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

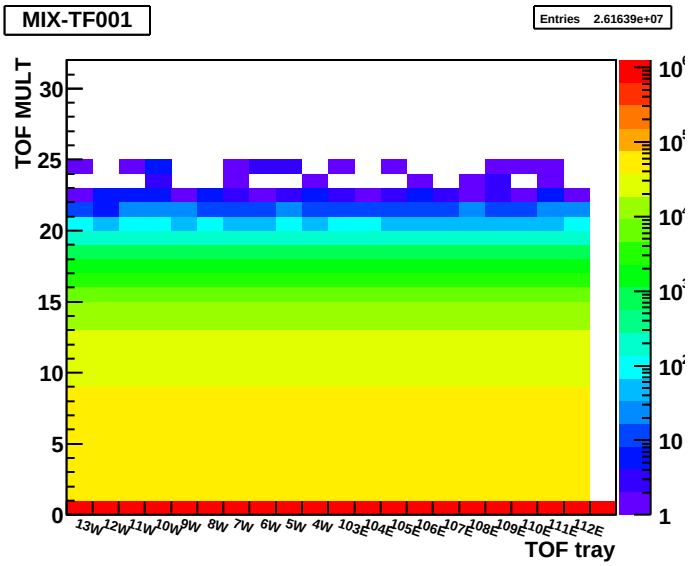

Barrel EMC L0 Input - Patch Sum
Entries 1.86885e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

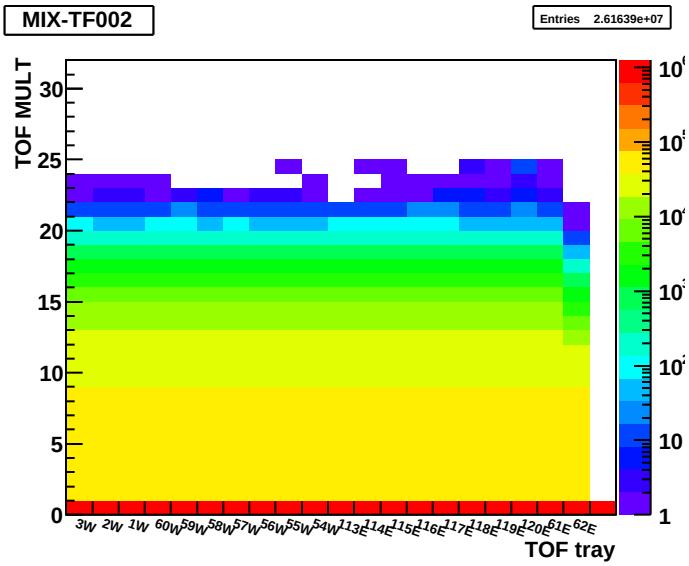

Endcap EMC L0 Input - High Tower
Entries 5.60655e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

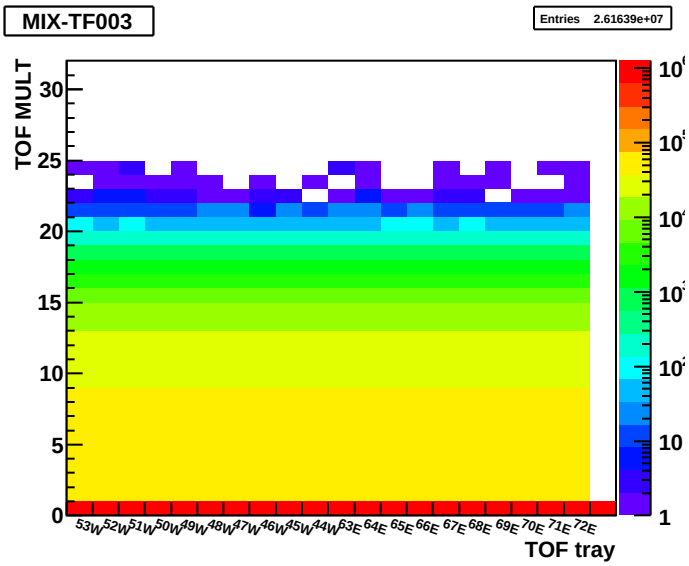

Endcap EMC L0 Input - Patch Sum
Entries 5.60655e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

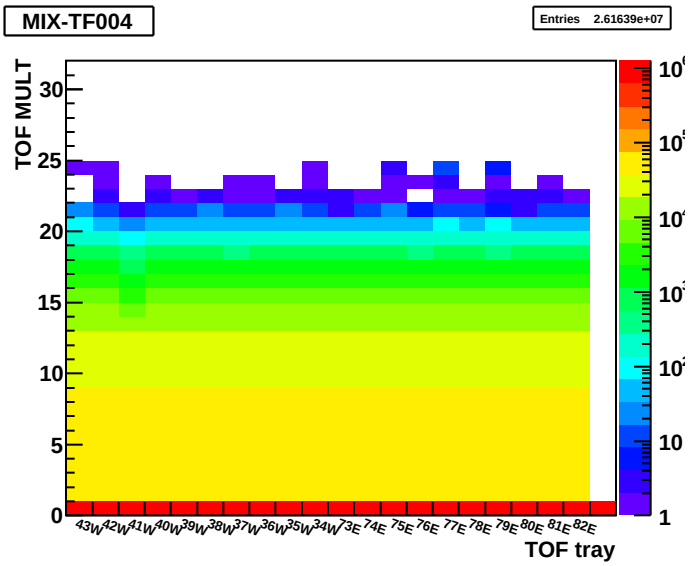

Barrel Jet Patches
Entries 1.12131e+07
JP ADC
JP ID

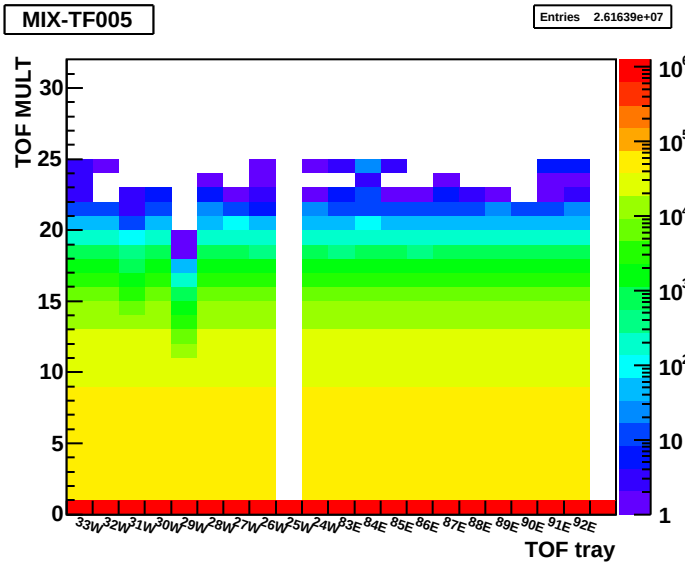

Hybrid Jet Patches
Entries 1245900
JP ADC
JP ID

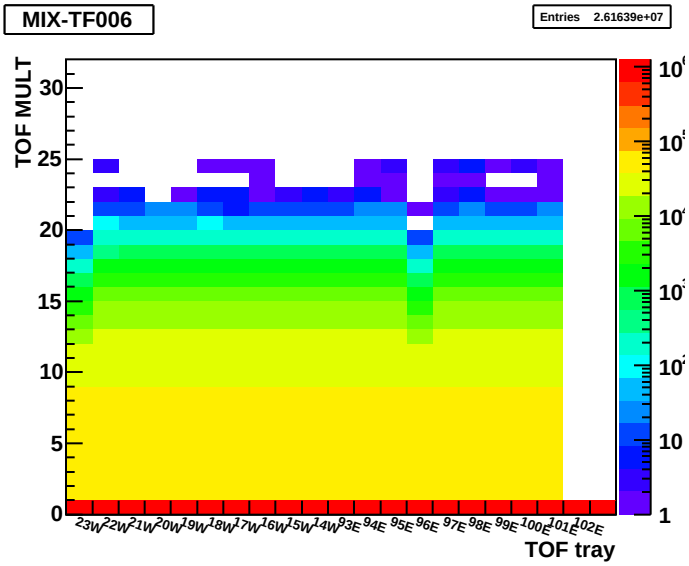

MIX-TF001
Entries 2.61639e+07
TOF MULT
TOF tray

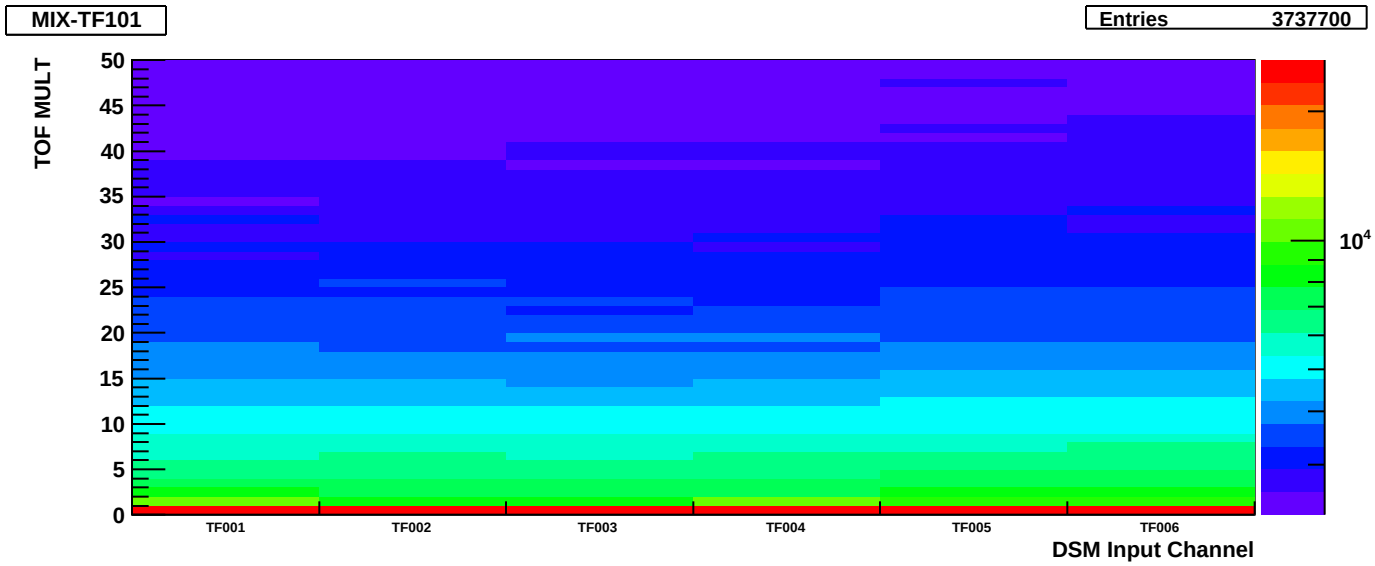

MIX-TF002
Entries 2.61639e+07
TOF MULT
TOF tray

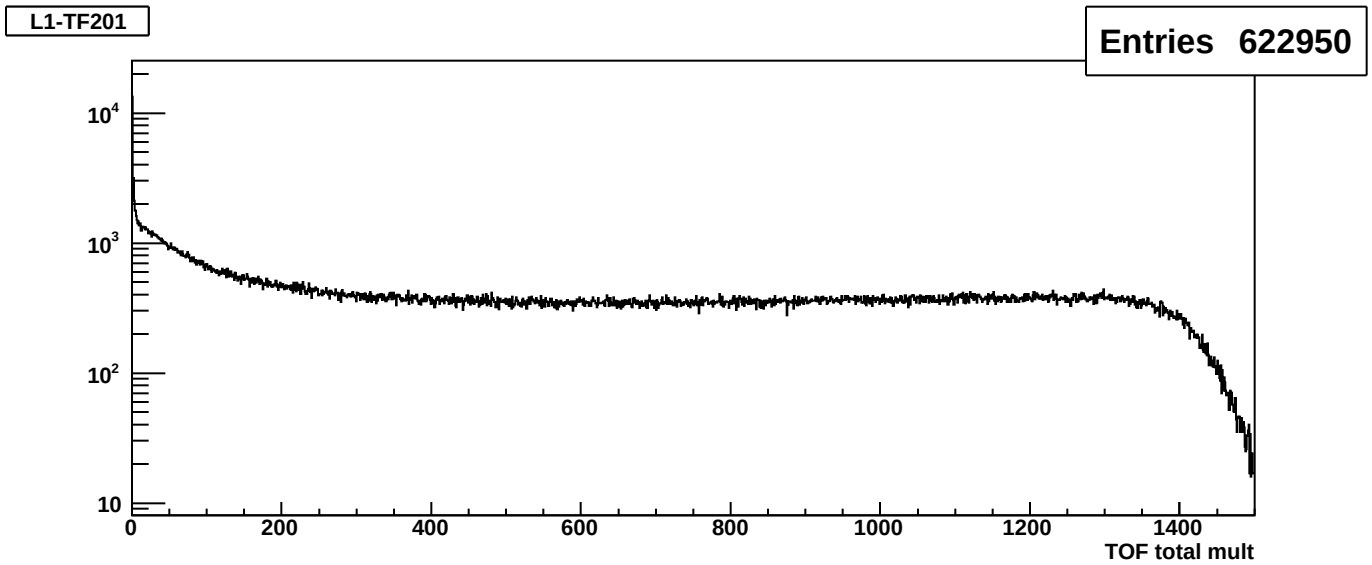

MIX-TF003
Entries 2.61639e+07
TOF MULT
TOF tray

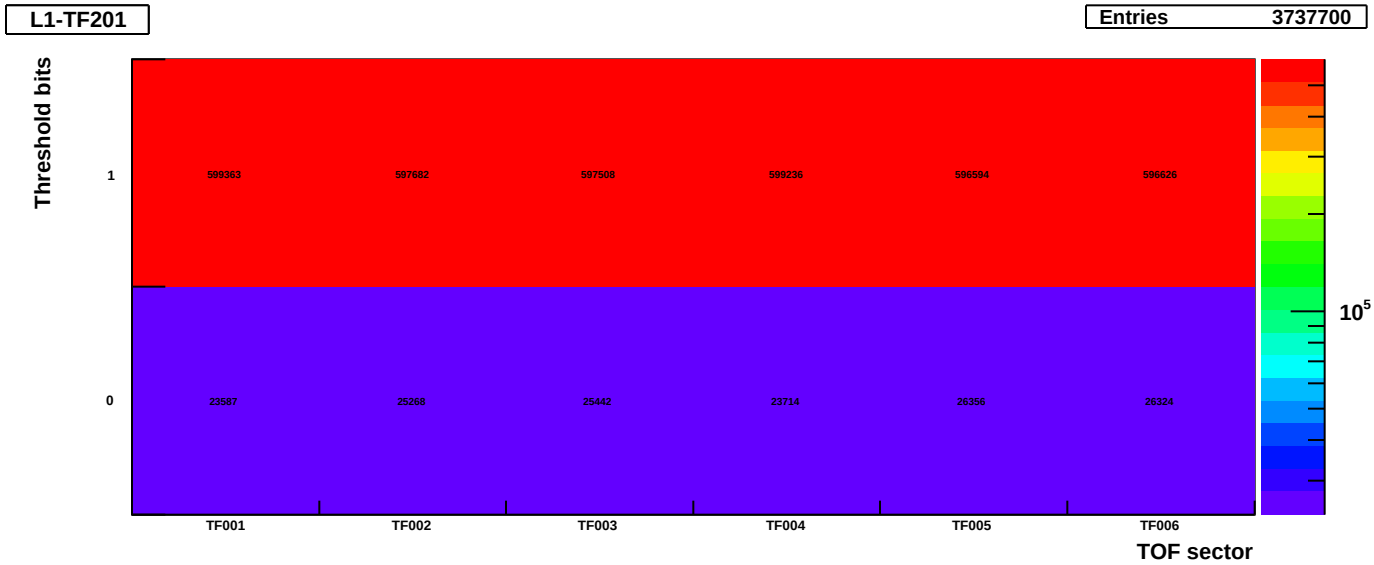

MIX-TF004
Entries 2.61639e+07
TOF MULT
TOF tray

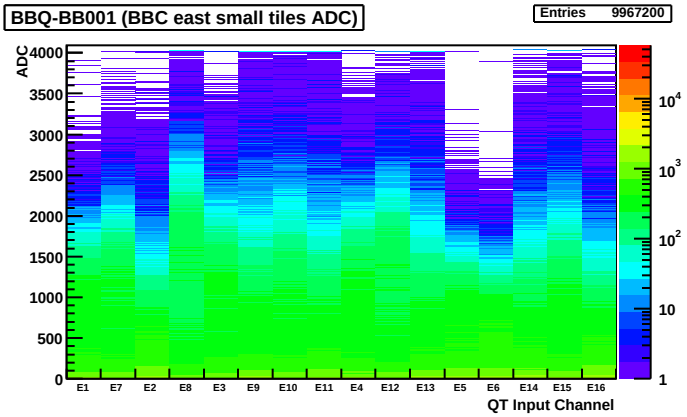

MIX-TF005
Entries 2.61639e+07
TOF MULT
TOF tray

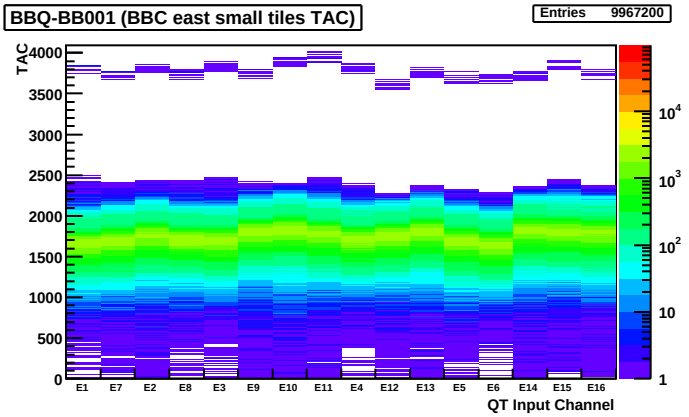

MIX-TF006
Entries 2.61639e+07
TOF MULT
TOF tray

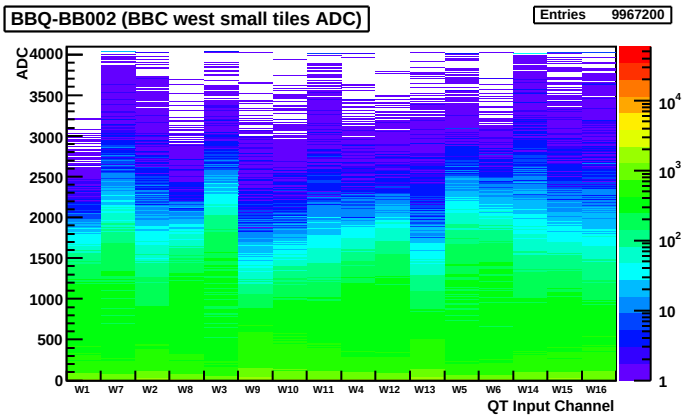

MIX-TF101
Entries 3737700
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

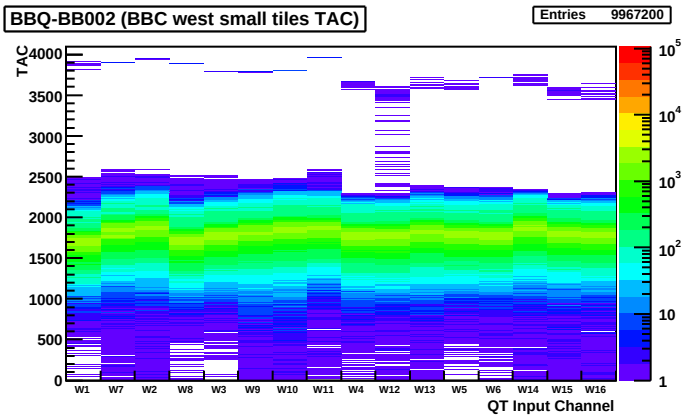

L1-TF201
Entries 622950
10^4
10^3
10^2
10
0
200
400
600
800
1000
1200
1400
TOF total mult

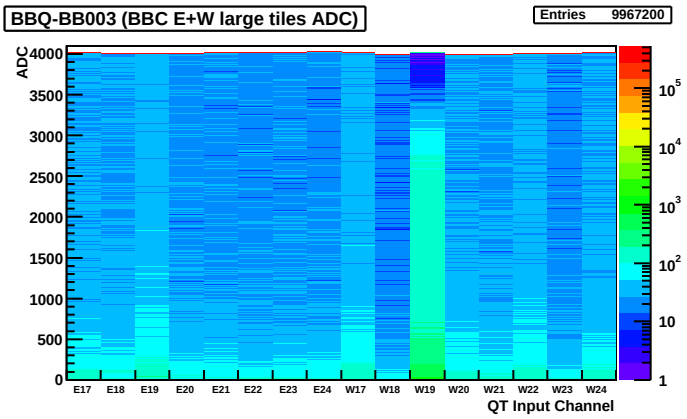

L1-TF201
Entries 3737700
Threshold bits
1
0
599363
597682
597508
599236
596594
596626
23587
25268
25442
23714
26356
26324
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

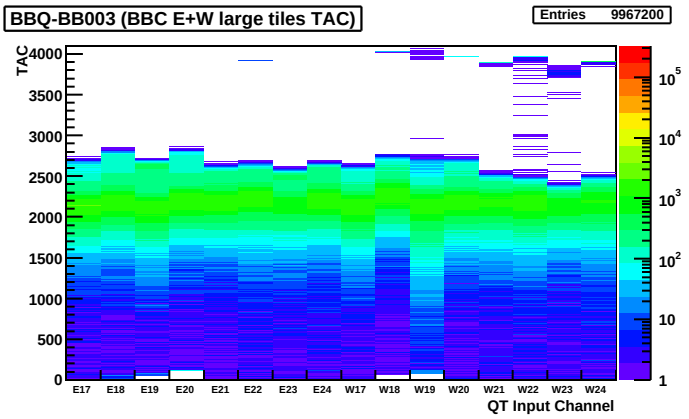

BBQ-BB001 (BBC east small tiles ADC)
Entries 9967200
ADC
QT Input Channel

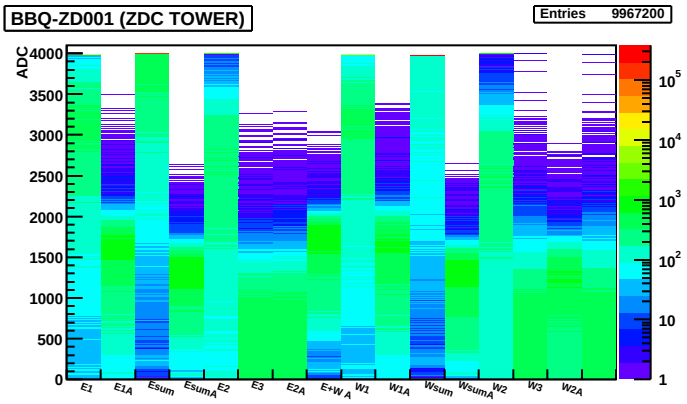

BBQ-BB001 (BBC east small tiles TAC)
Entries 9967200
TAC
QT Input Channel

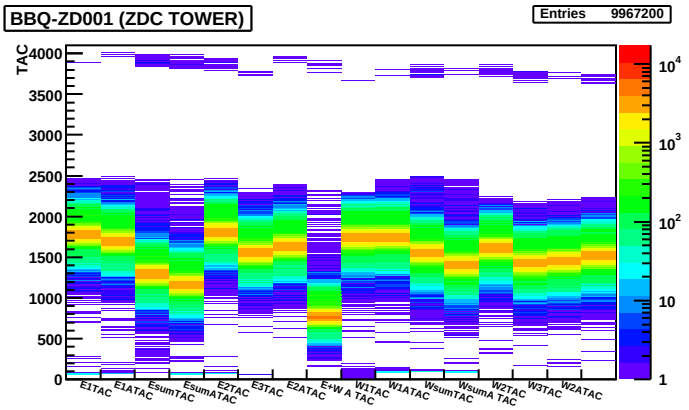

BBQ-BB002 (BBC west small tiles ADC)
Entries 9967200
ADC
QT Input Channel

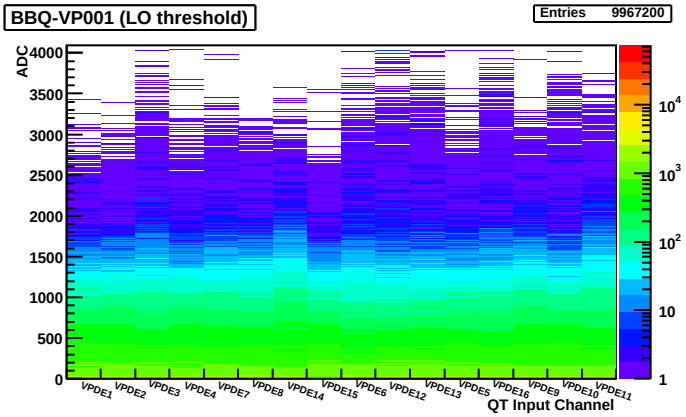

BBQ-BB002 (BBC west small tiles TAC)
Entries 9967200
TAC
QT Input Channel

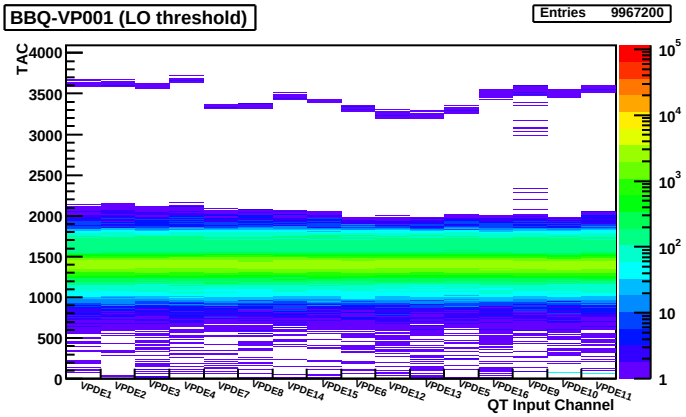

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 9967200
ADC
QT Input Channel

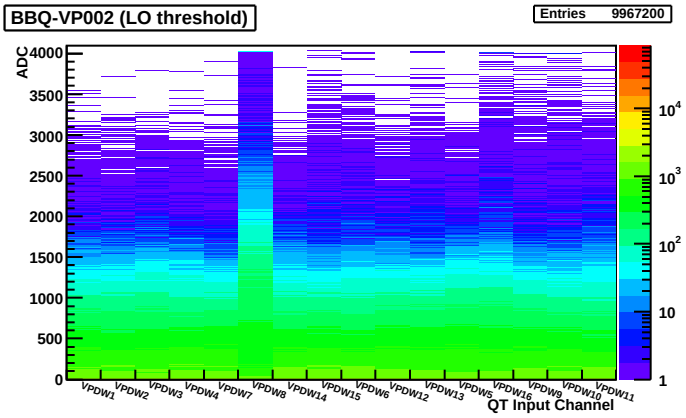

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 9967200
TAC
QT Input Channel

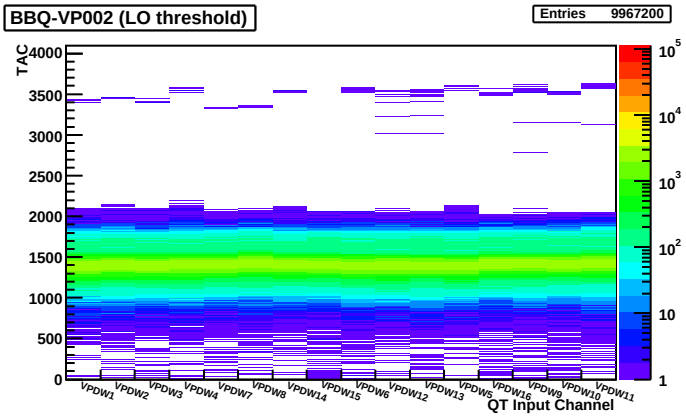

BBQ-ZD001 (ZDC TOWER)
Entries 9967200
ADC

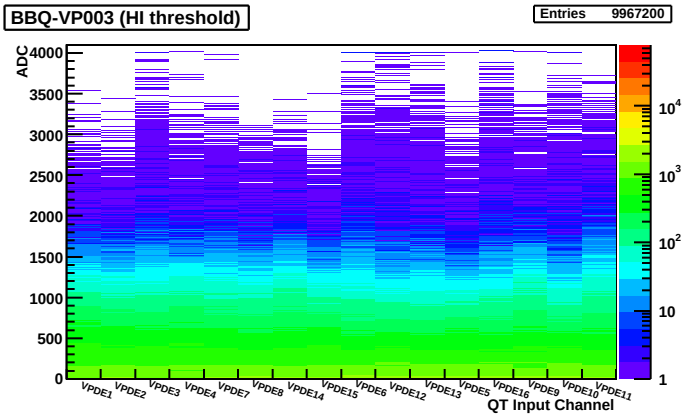

BBQ-ZD001 (ZDC TOWER)
Entries 9967200
TAC

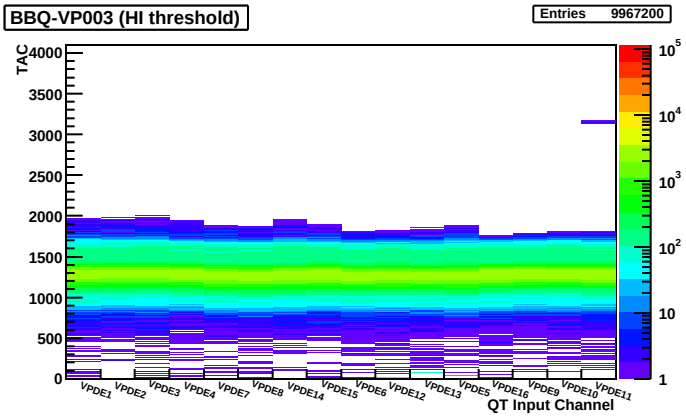

BBQ-VP001 (LO threshold)
Entries 9967200
ADC
QT Input Channel

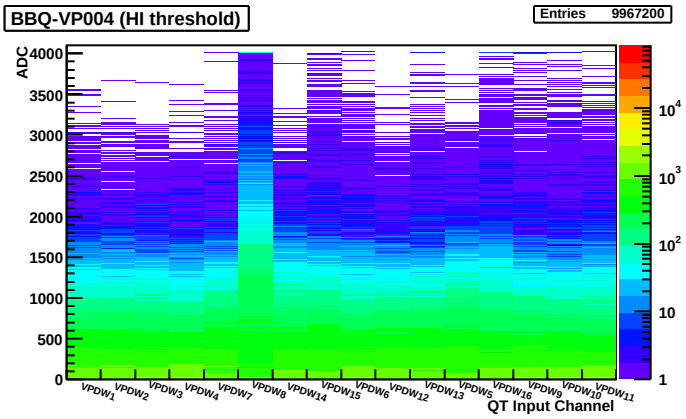

BBQ-VP001 (LO threshold)
Entries 9967200
TAC
QT Input Channel

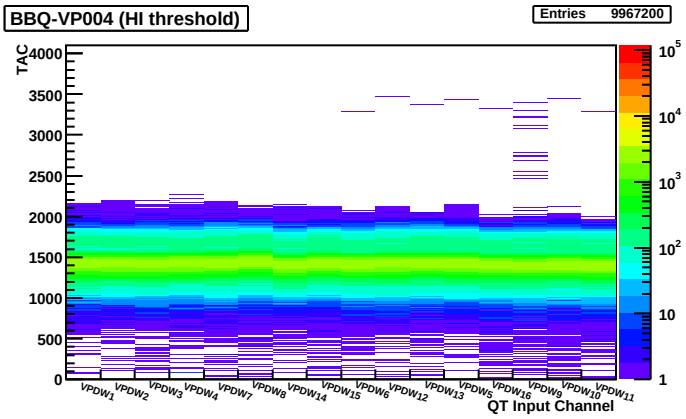

BBQ-VP002 (LO threshold)
Entries 9967200
ADC
QT Input Channel

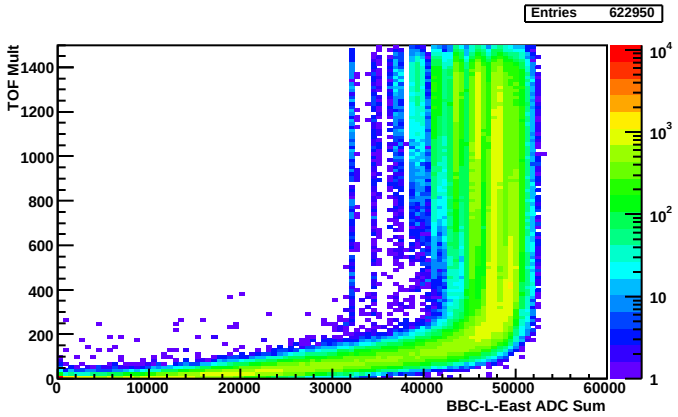

BBQ-VP002 (LO threshold)
Entries 9967200
TAC
QT Input Channel

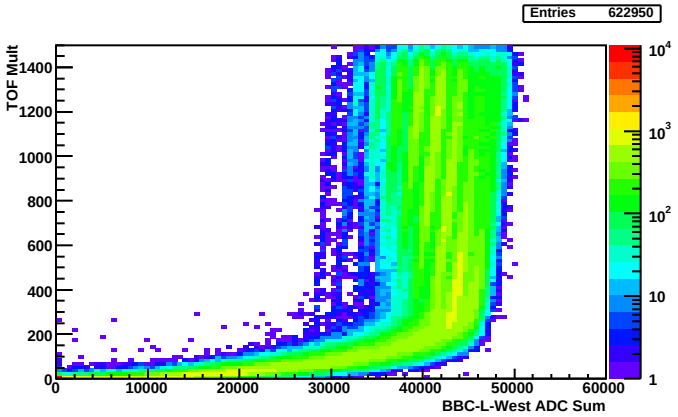

BBQ-VP003 (HI threshold)
Entries 9967200
ADC
QT Input Channel

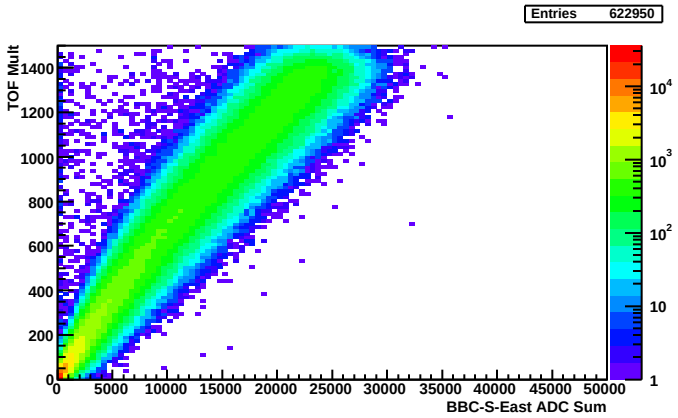

BBQ-VP003 (HI threshold)
Entries 9967200
TAC
QT Input Channel

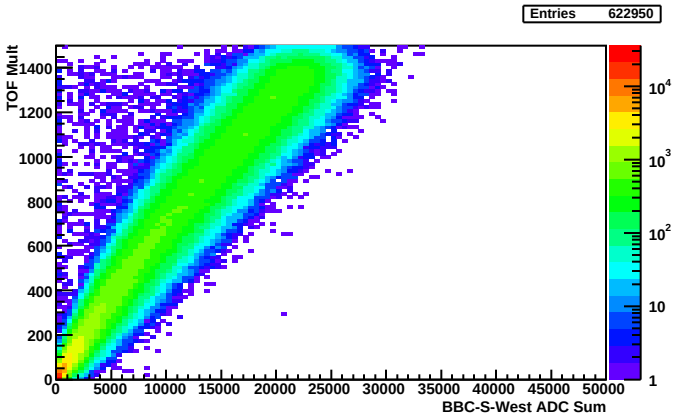

BBQ-VP004 (HI threshold)
Entries 9967200
ADC
QT Input Channel

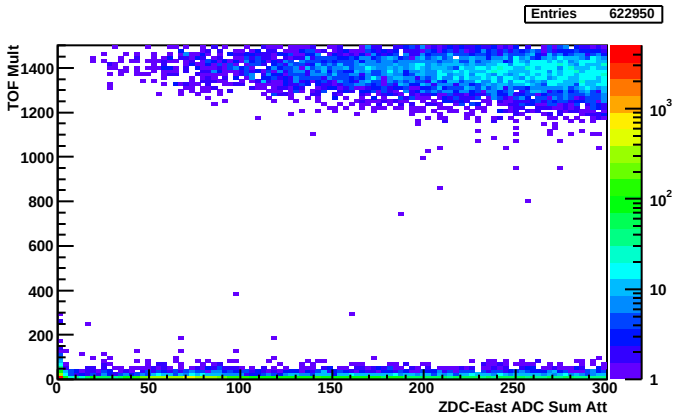

BBQ-VP004 (HI threshold)
Entries 9967200
TAC
QT Input Channel

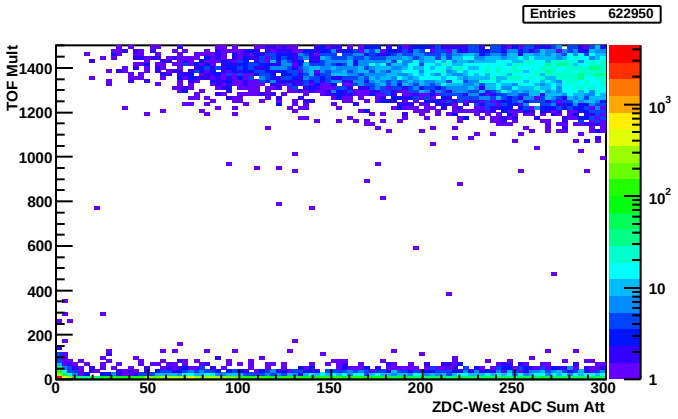

Entries 622950
TOF Mult
BBC-L-East ADC Sum

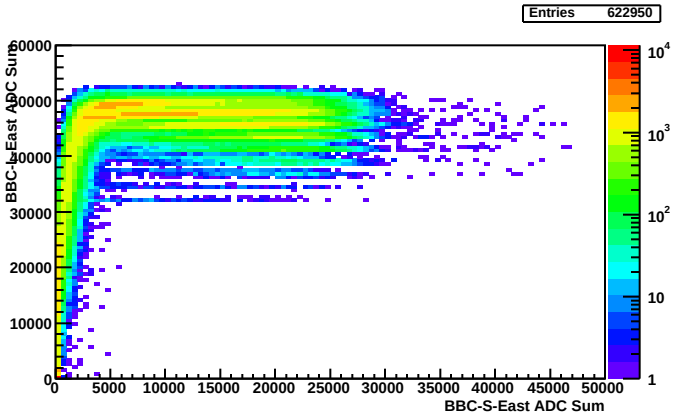

Entries 622950
TOF Mult
BBC-L-West ADC Sum

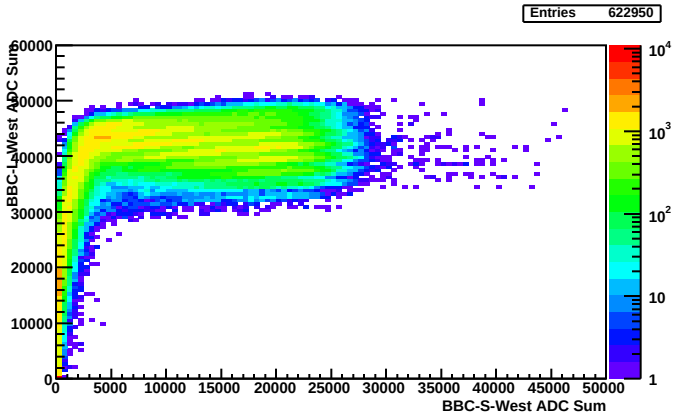

Entries 622950
TOF Mult
BBC-S-East ADC Sum

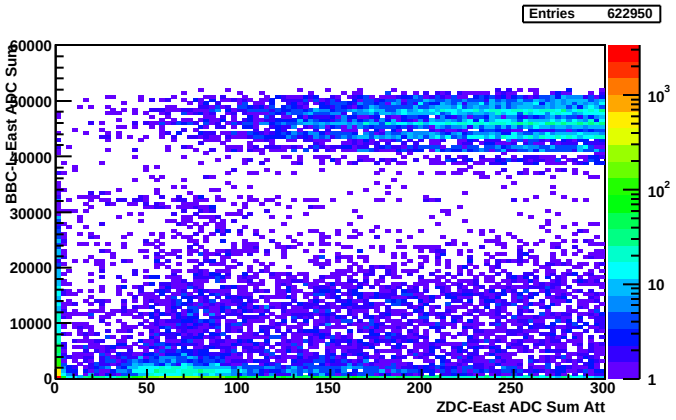

Entries 622950
TOF Mult
BBC-S-West ADC Sum


Entries 622950
TOF Mult
ZDC-East ADC Sum Att


Entries 622950
TOF Mult
ZDC-West ADC Sum Att


Entries 622950
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 622950
BBC-L-West ADC Sum
BBC-S-West ADC Sum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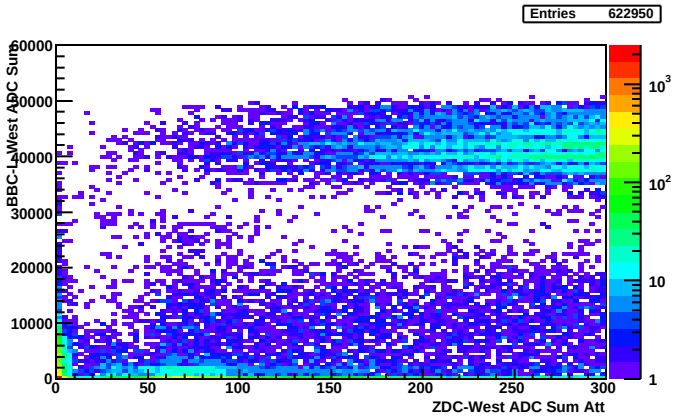
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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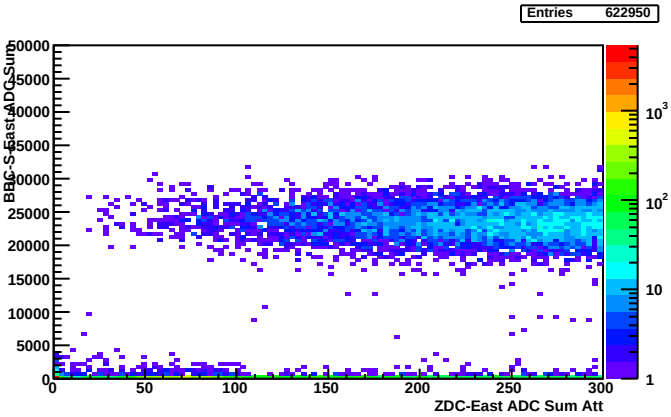
BBC-L-West ADC Sum
ZDC-West ADC Sum Att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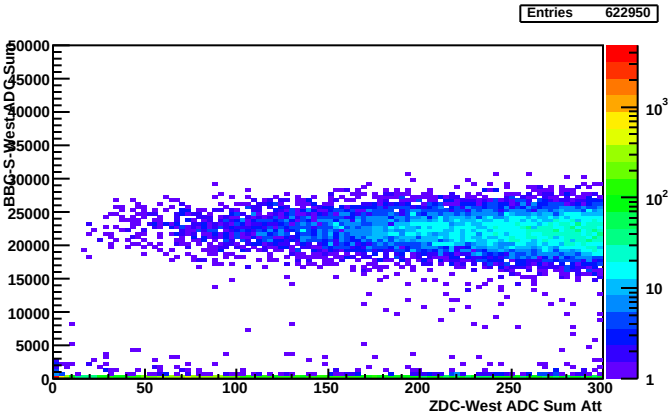
BBC-S-East ADC Sum
ZDC-East ADC Sum Att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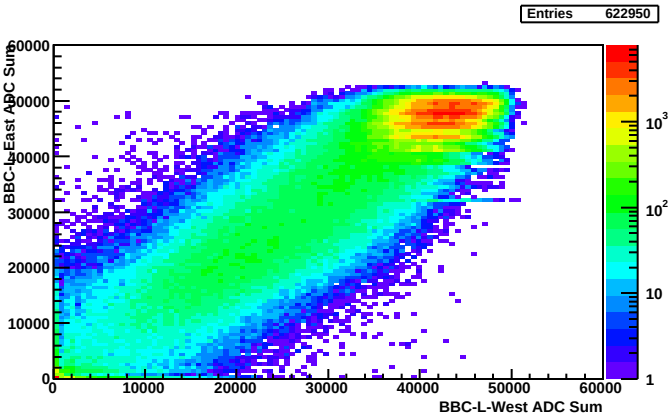
BBC-S-West ADC Sum
ZDC-West ADC Sum Att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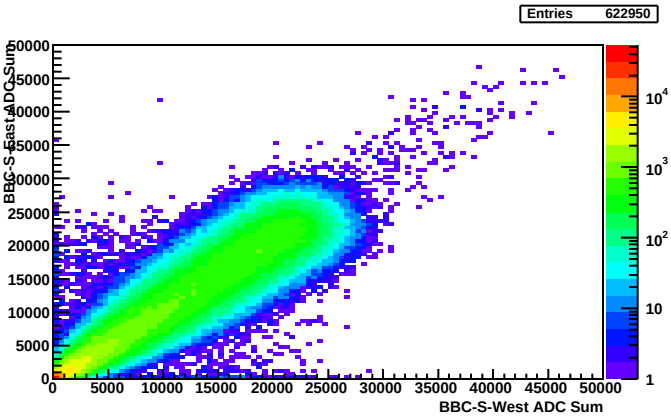
BBC-L-East ADC Sum
BBC-L-West ADC Sum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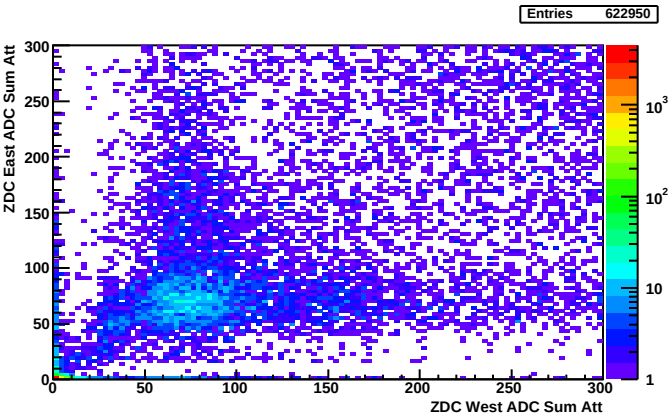
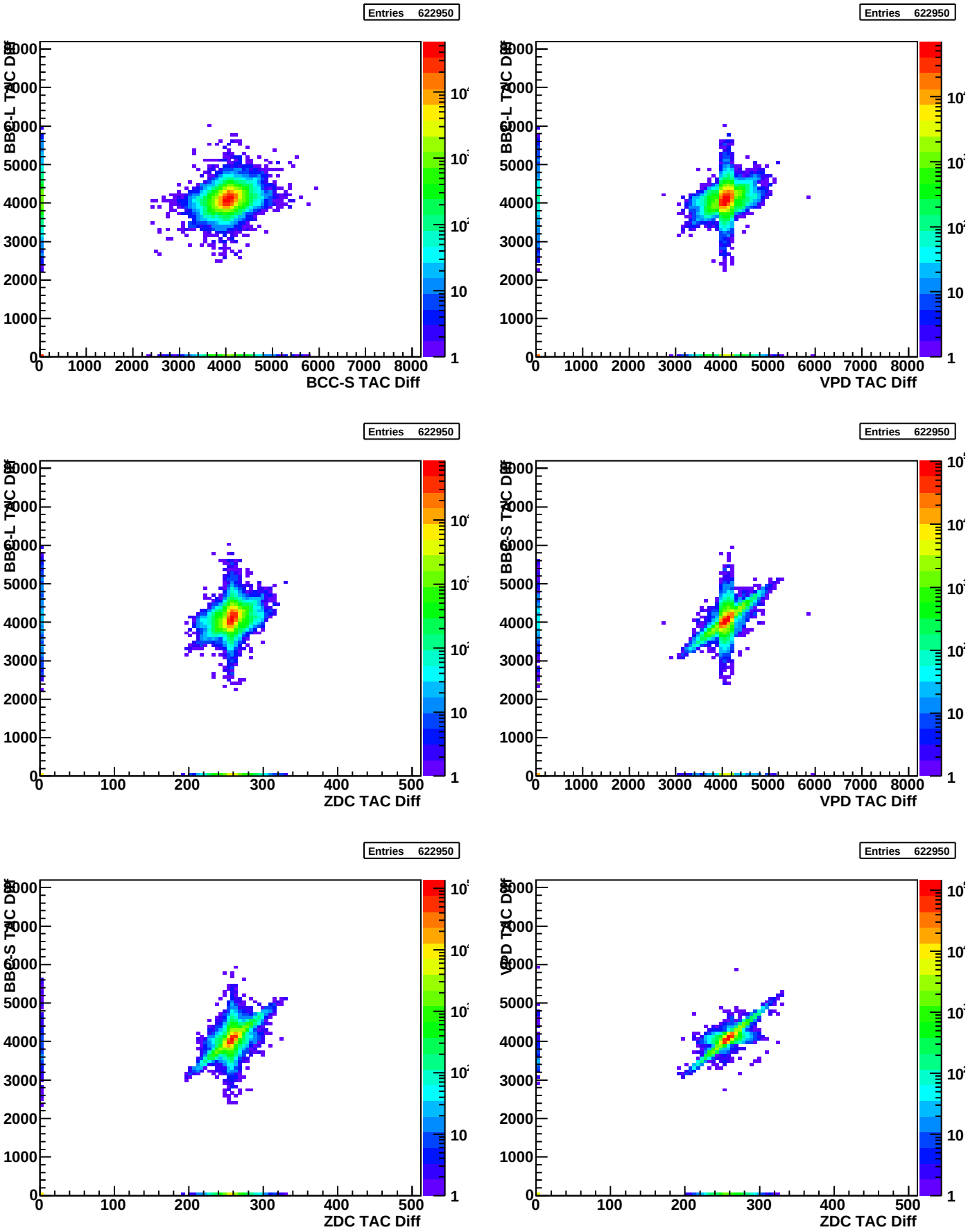
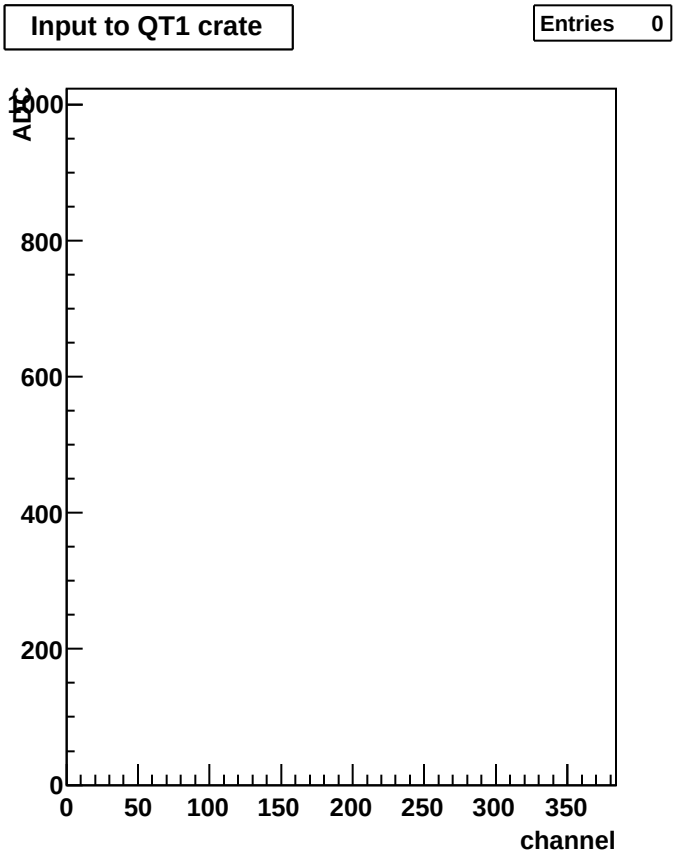
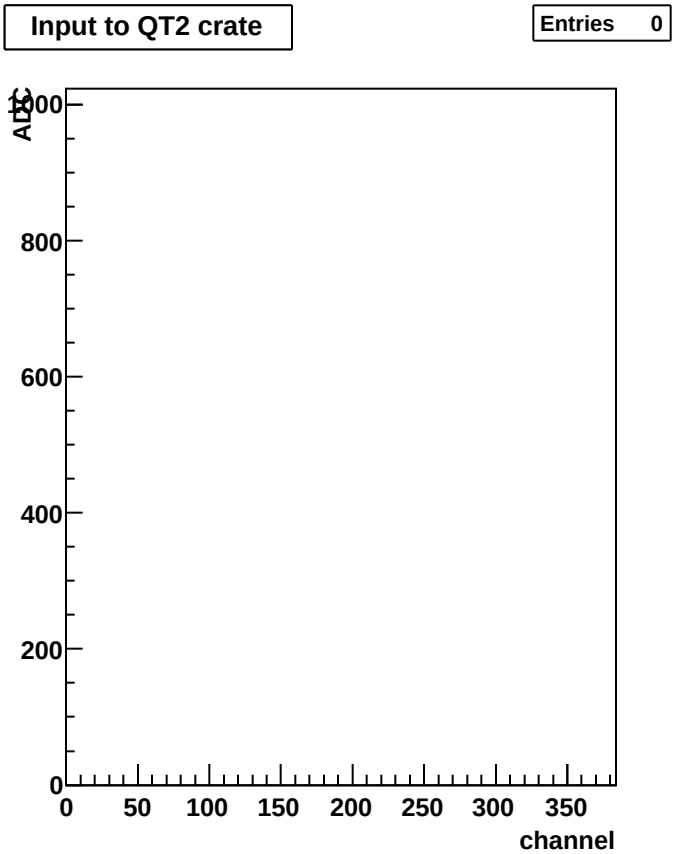
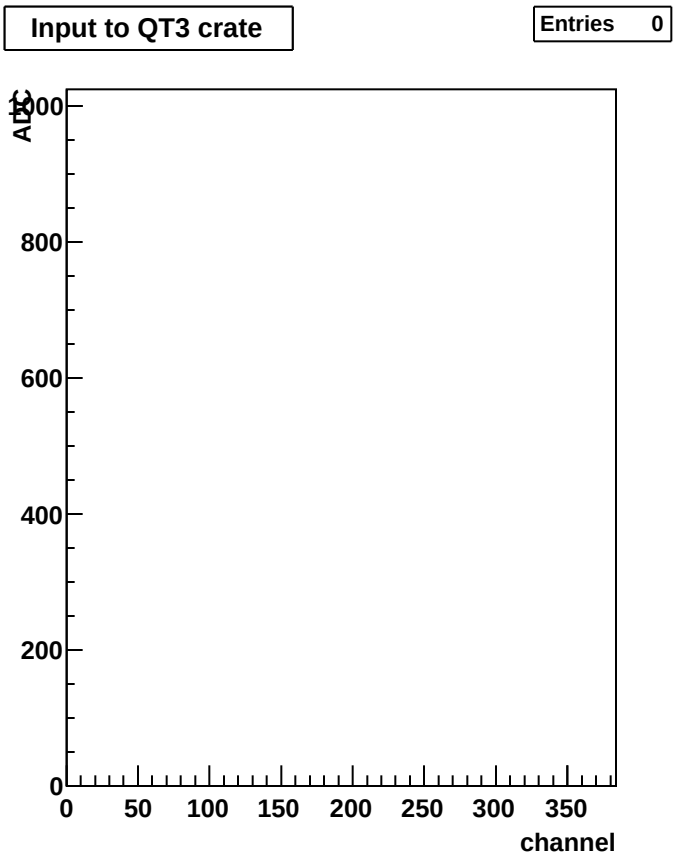
BBC-S-East ADC Sum
BBC-S-West ADC Sum

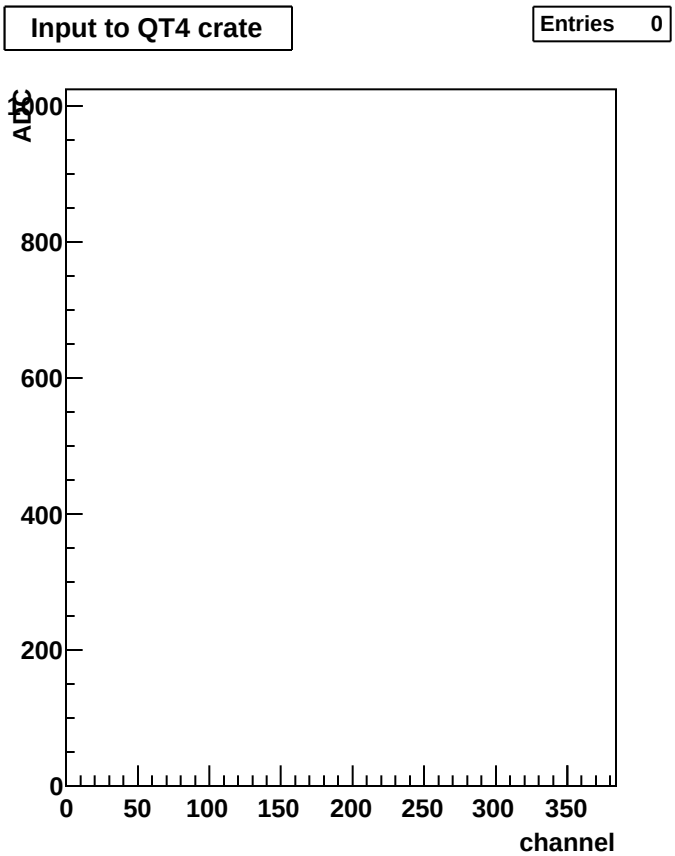

Entries 622950
ZDC East ADC Sum Att
ZDC West ADC Sum Att

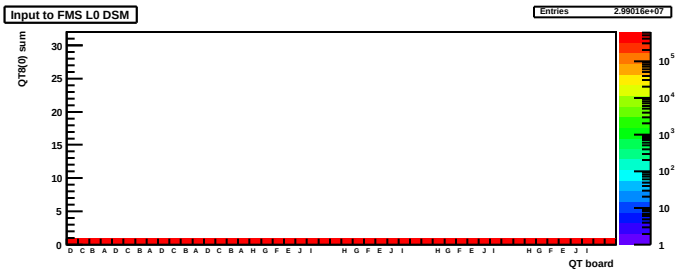

Entries 622950
BBC-L TAC Diff
BCC-S TAC Diff
Entries 622950
BBC-L TAC Diff
VPD TAC Diff
Entries 622950
BBC-L TAC Diff
ZDC TAC Diff
Entries 622950
BBC-S TAC Diff
VPD TAC Diff
Entries 622950
BBC-S TAC Diff
ZDC TAC Diff
Entries 622950
VPD TAC Diff
ZDC TAC Diff

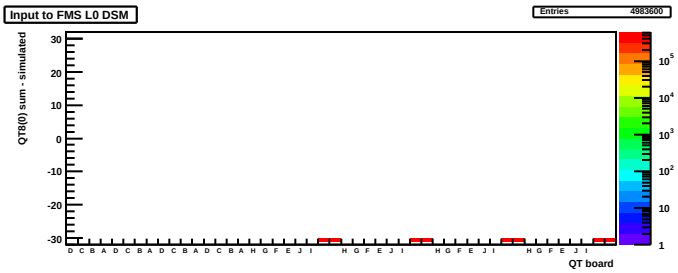

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

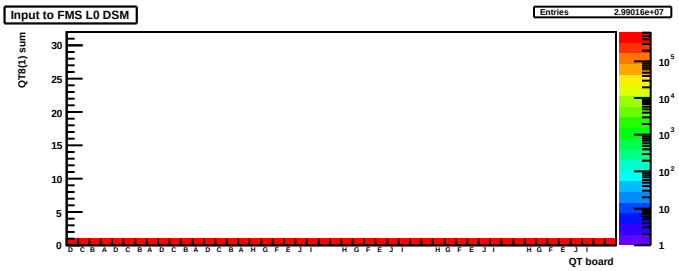

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

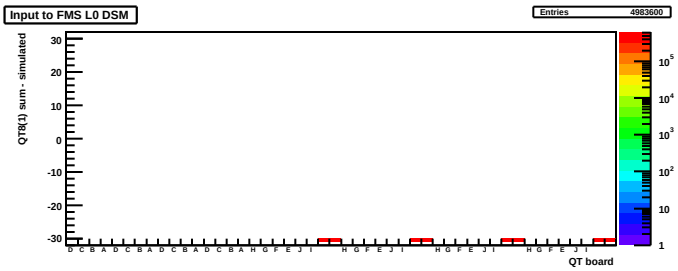

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

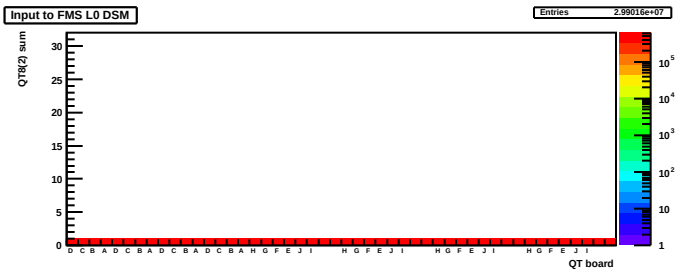

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

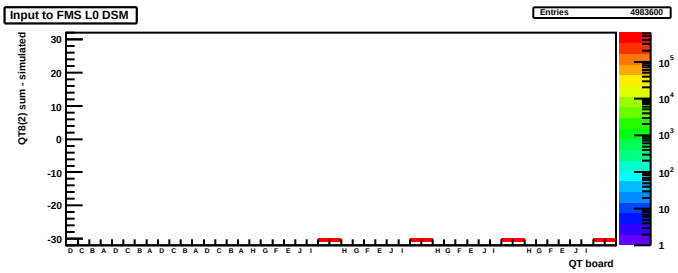

Input to FMS L0 DSM
Entries 2.99016e+07
QT8(0) sum
QT board

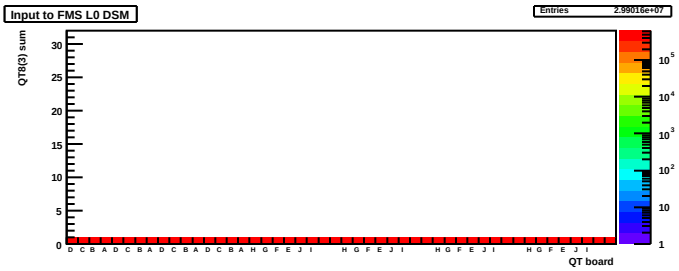

Input to FMS L0 DSM
Entries 4983600
QT8(0) sum - simulated
QT board

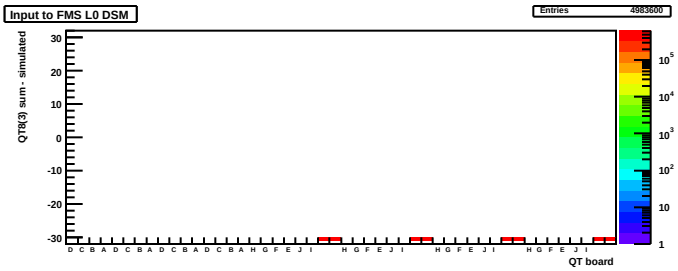

Input to FMS L0 DSM
Entries 2.99016e+07
QT8(1) sum
QT board

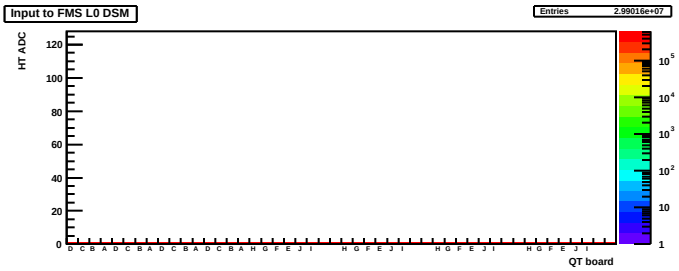

Input to FMS L0 DSM
Entries 4983600
QT8(1) sum - simulated
QT board

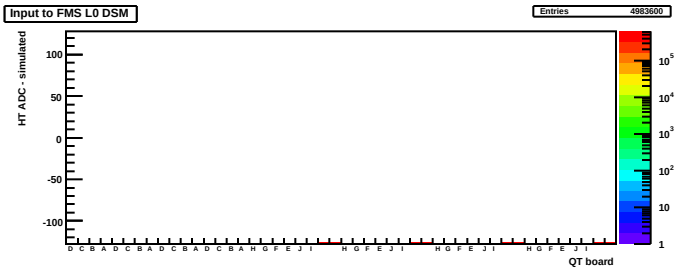

Input to FMS L0 DSM
Entries 2.99016e+07
QT8(2) sum
QT board

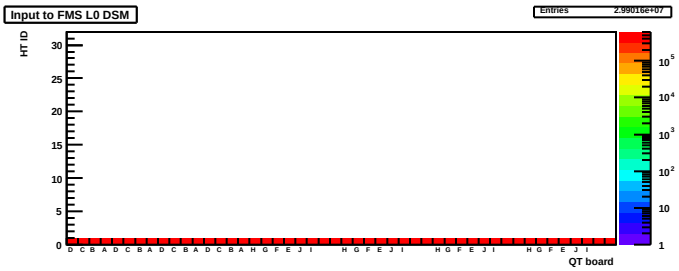

Input to FMS L0 DSM
Entries 4983600
QT8(2) sum - simulated
QT board

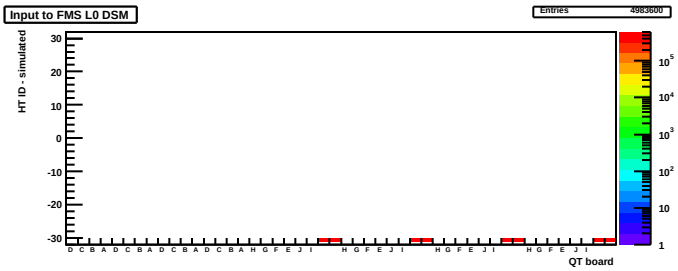

Input to FMS L0 DSM
Entries 2.99016e+07
QT8(3) sum
QT board

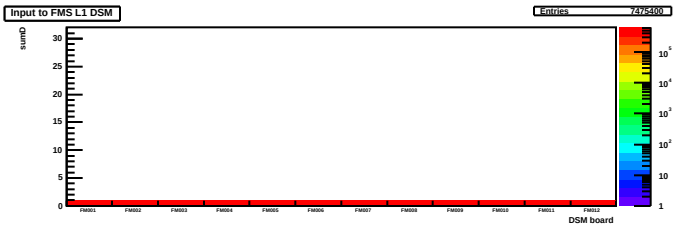

Input to FMS L0 DSM
Entries 4983600
QT8(3) sum - simulated
QT board

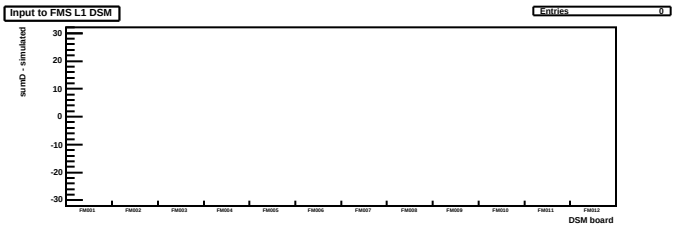

Input to FMS L0 DSM
Entries 2.99016e+07
HT ADC
QT board

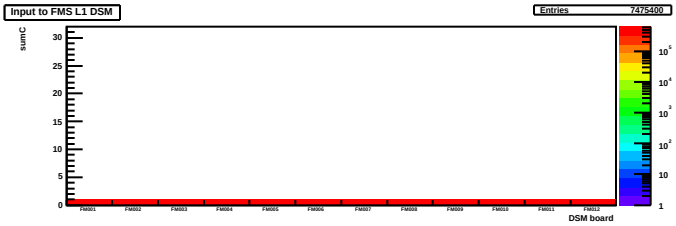

Input to FMS L0 DSM
Entries 4983600
HT ADC - simulated
QT board

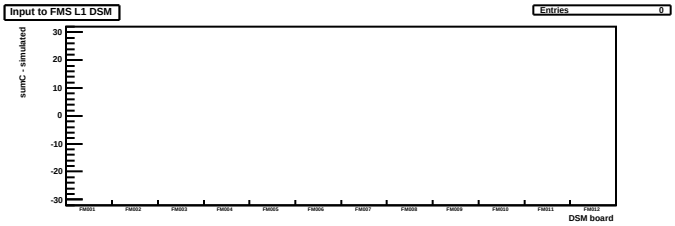

Input to FMS L0 DSM
Entries 2.99016e+07
HT ID
QT board

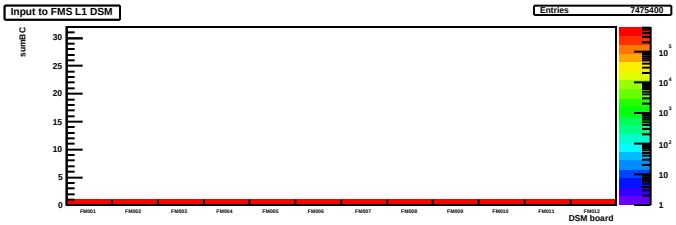

Input to FMS L0 DSM
Entries 4983600
HT ID - simulated
QT board

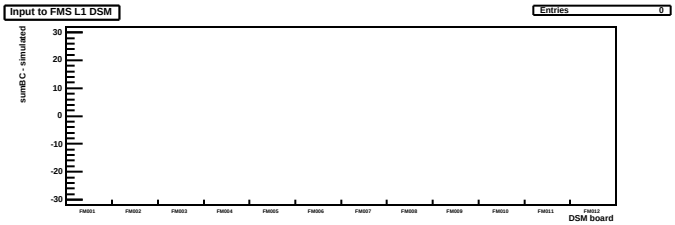

Input to FMS L1 DSM
Entries 7475400
sumD
DSM board

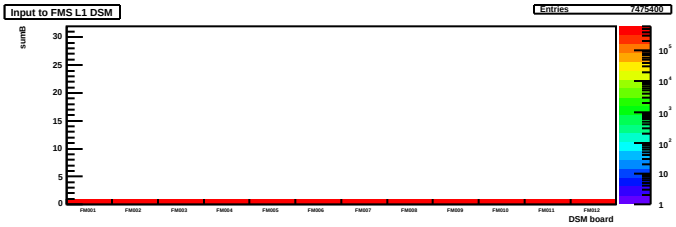

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

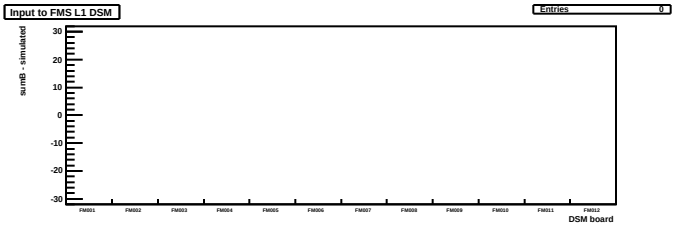

Input to FMS L1 DSM
Entries 7475400
sumC
DSM board

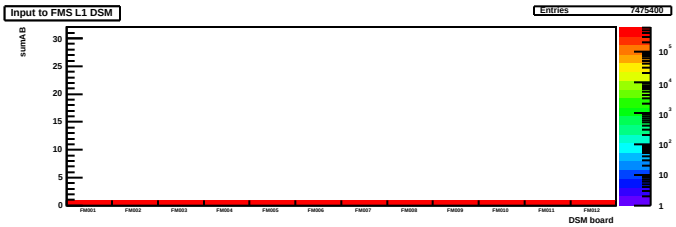

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

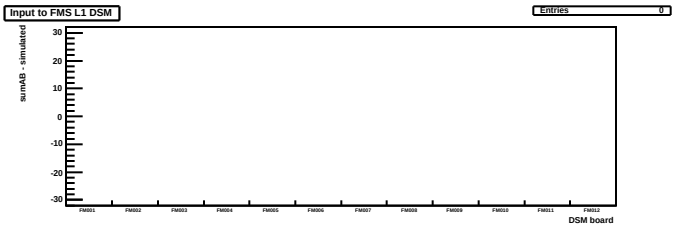

Input to FMS L1 DSM
Entries 7475400
sumBC
DSM board

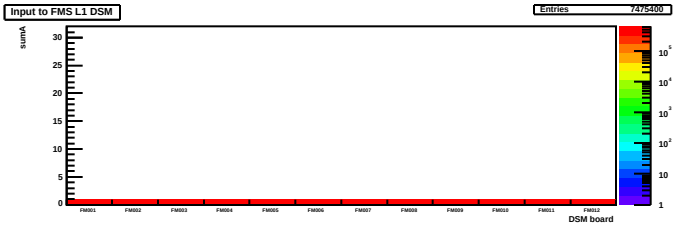

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

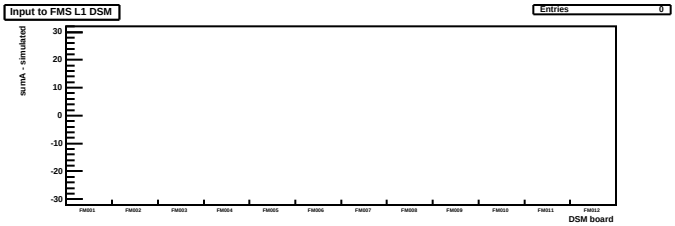

Input to FMS L1 DSM
Entries 7475400
sumB
DSM board

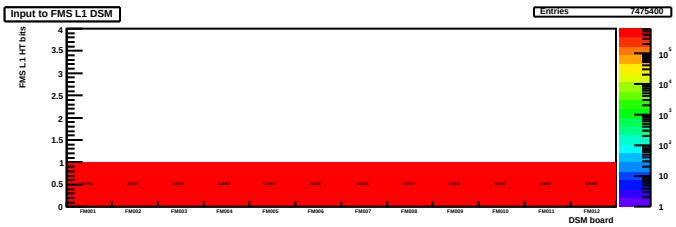

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

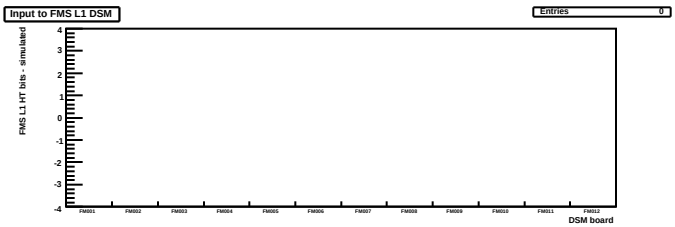

Input to FMS L1 DSM
Entries 7475400
sumAB
DSM board

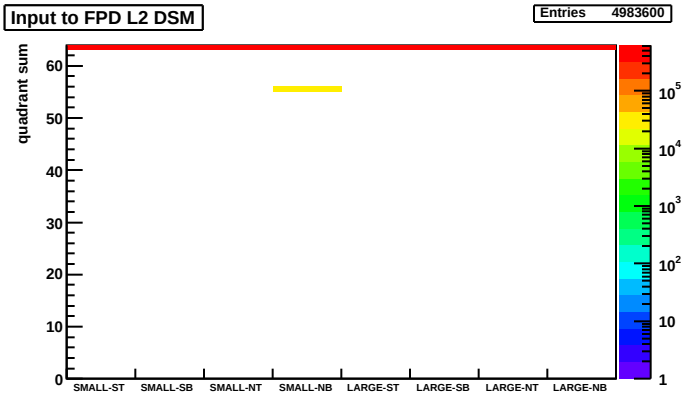

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

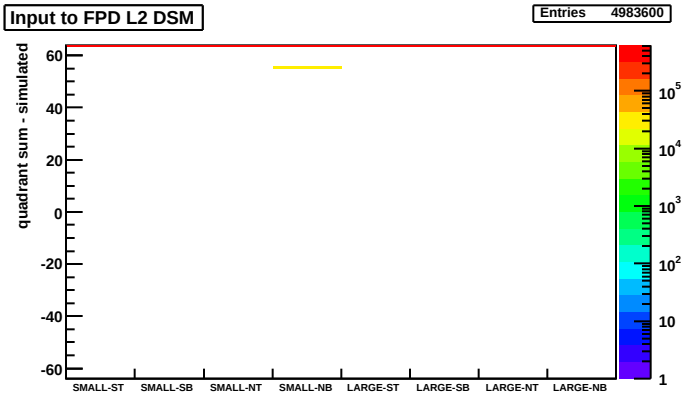

Input to FMS L1 DSM
Entries 7475400
sumA
DSM board

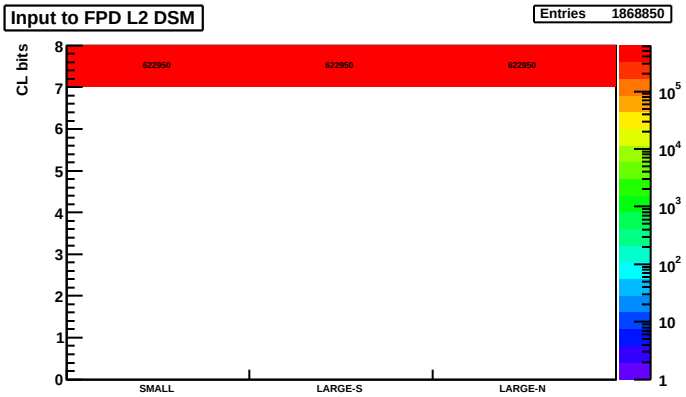

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

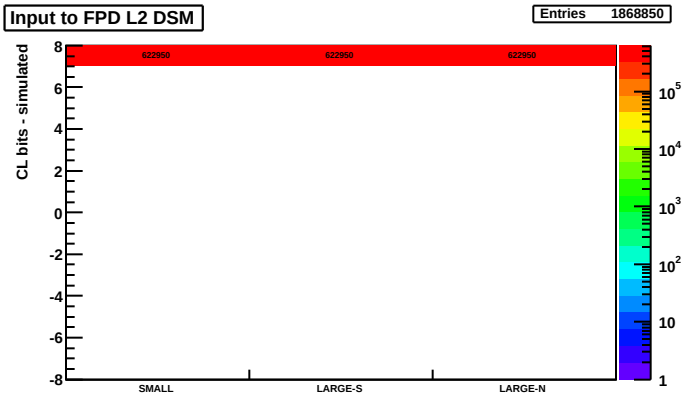

Input to FMS L1 DSM
Entries 7475400
FMS L1 HT bits
DSM board

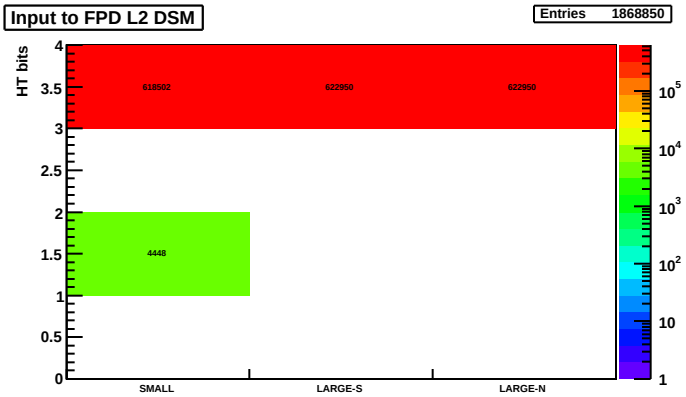

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

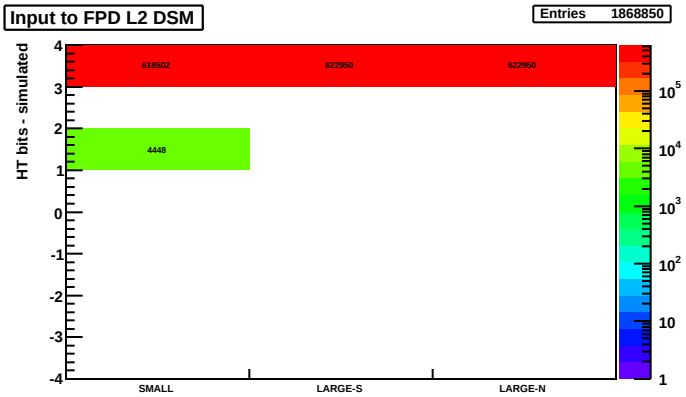

Input to FPD L2 DSM
Entries 4983600
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

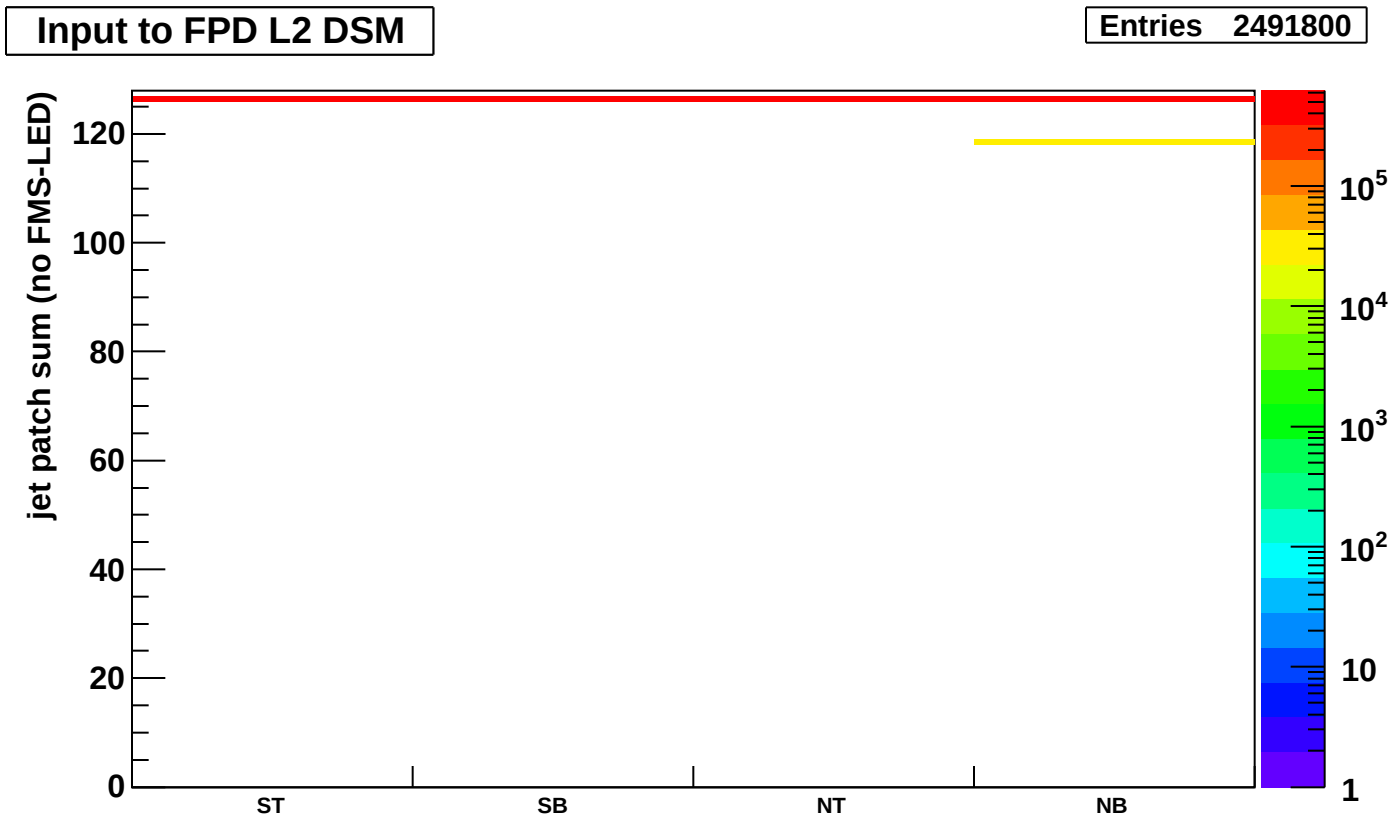

Input to FPD L2 DSM
Entries 4983600
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

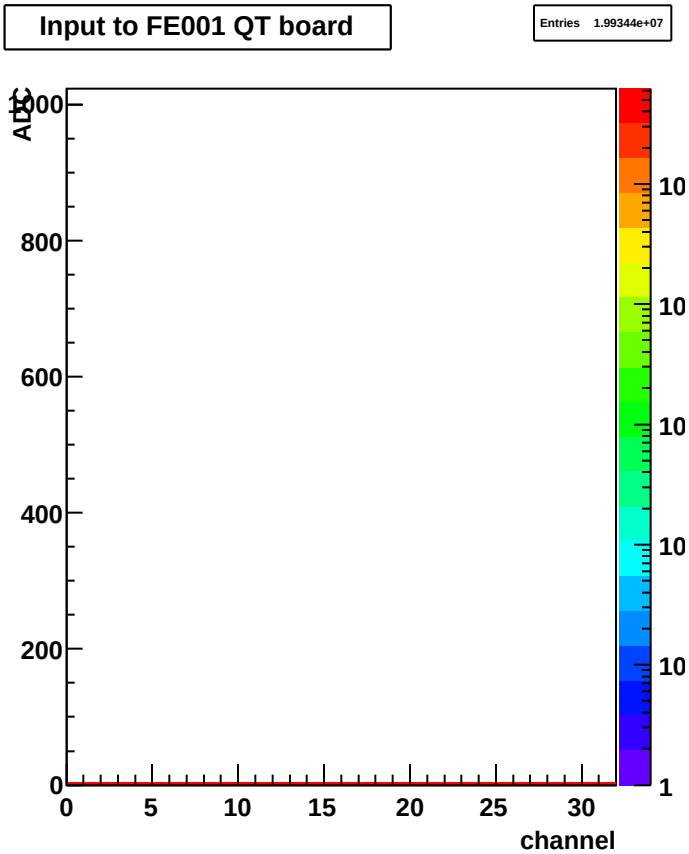

Input to FPD L2 DSM
Entries 1868850
CL bits
622950
622950
622950
SMALL
LARGE-S
LARGE-N

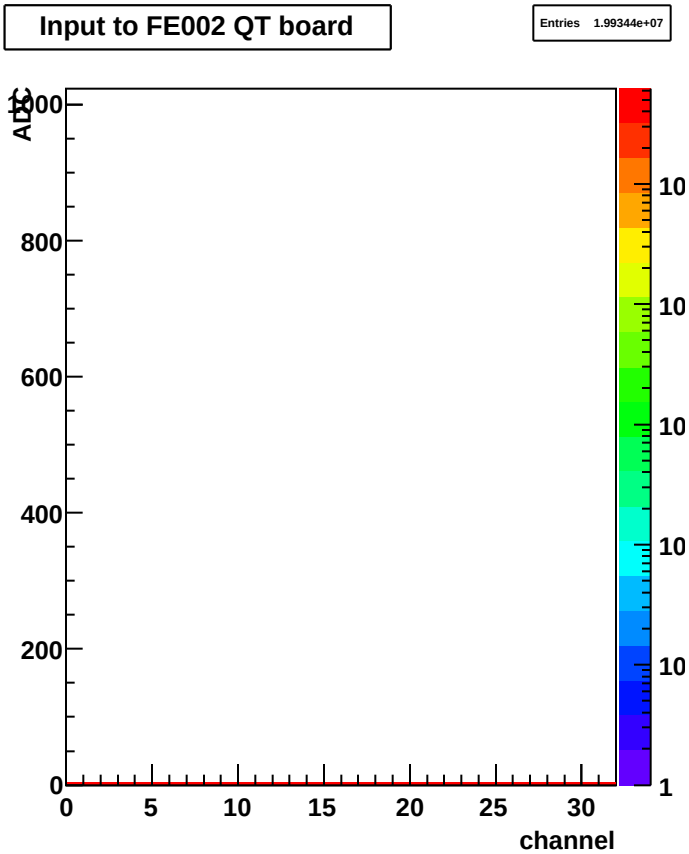

Input to FPD L2 DSM
Entries 1868850
CL bits - simulated
622950
622950
622950
SMALL
LARGE-S
LARGE-N

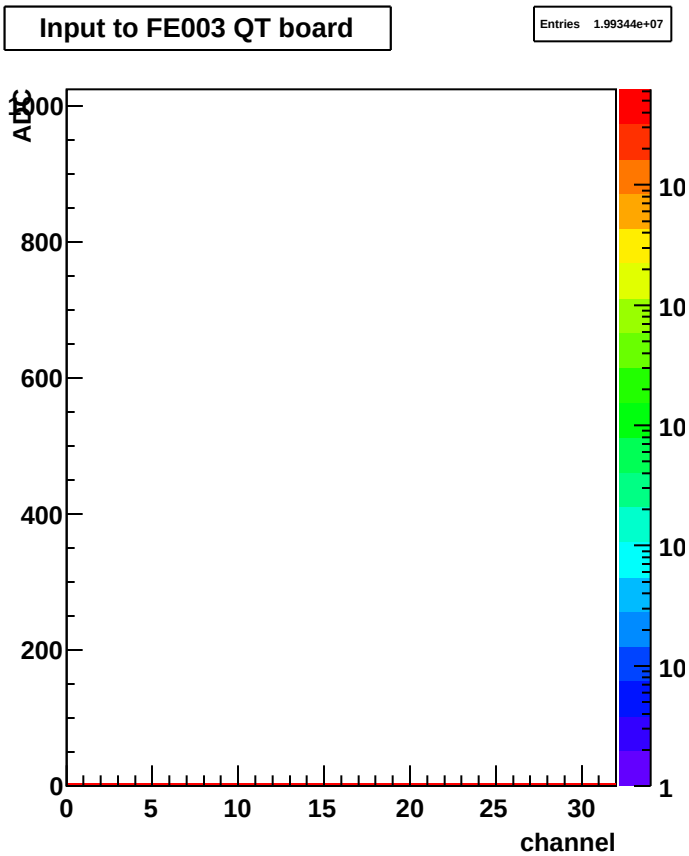

Input to FPD L2 DSM
Entries 1868850
HT bits
618502
622950
622950
4448
SMALL
LARGE-S
LARGE-N

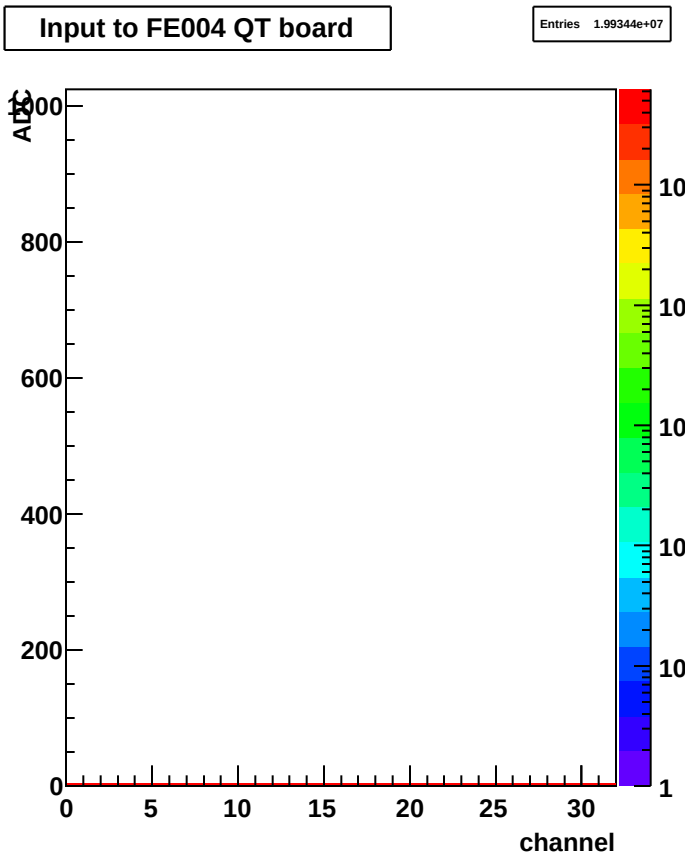

Input to FPD L2 DSM
Entries 1868850
HT bits - simulated
618502
622950
622950
4448
SMALL
LARGE-S
LARGE-N

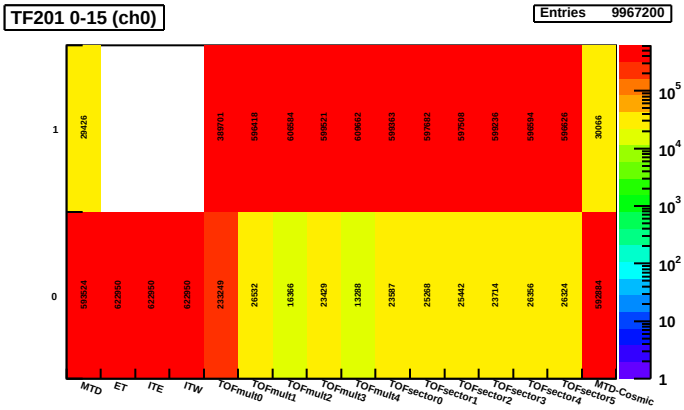

Input to FPD L2 DSM
Entries 2491800
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

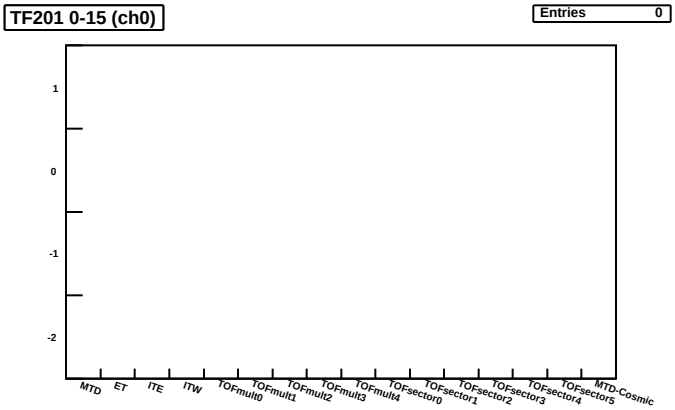

Input to FE001 QT board
Entries 1.99344e+07
ADC
channel

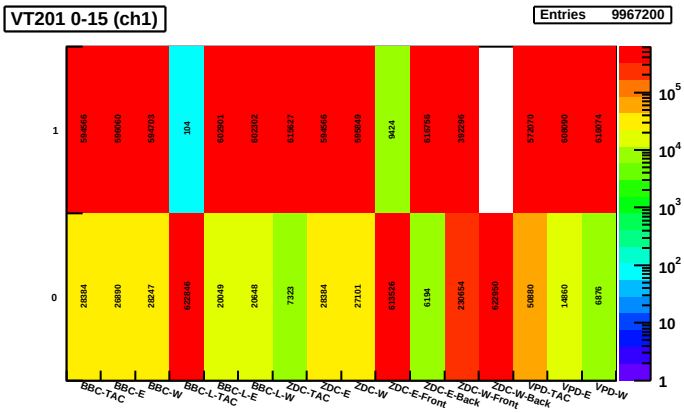

Input to FE002 QT board
Entries 1.99344e+07
ADC
channel

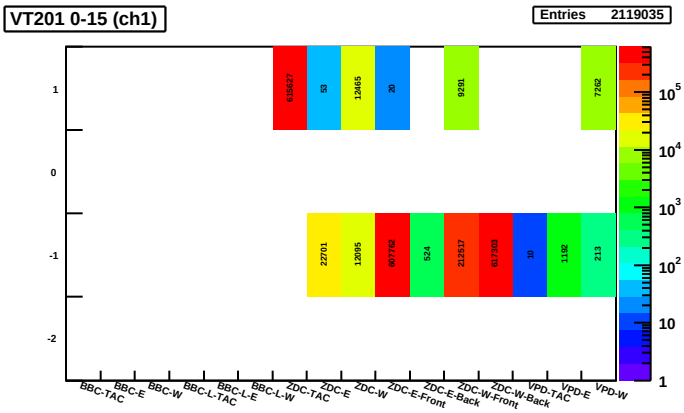

Input to FE003 QT board
Entries 1.99344e+07
ADC
channel

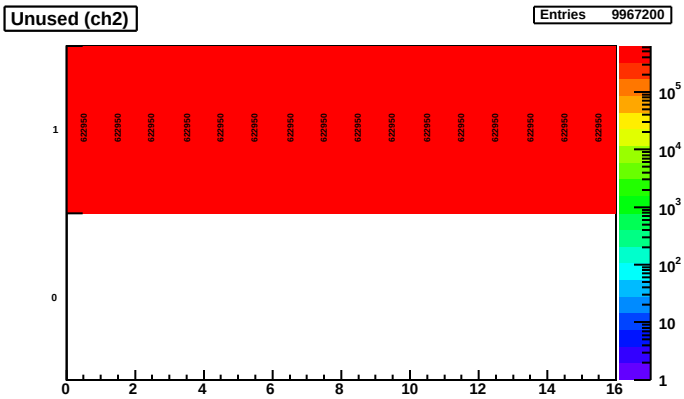

Input to FE004 QT board
Entries 1.99344e+07
ADC
channel

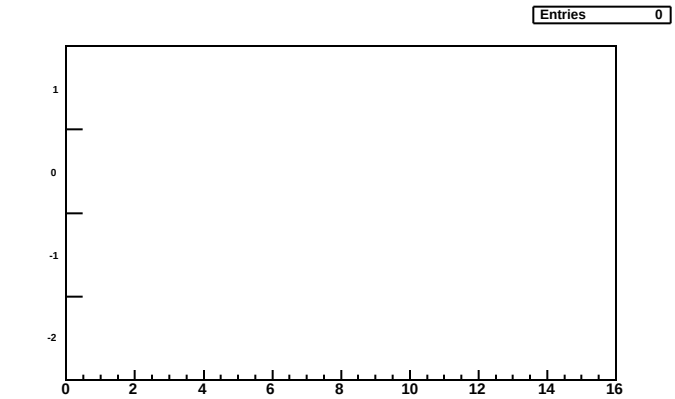

TF201 0-15 (ch0)
Entries 9967200
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

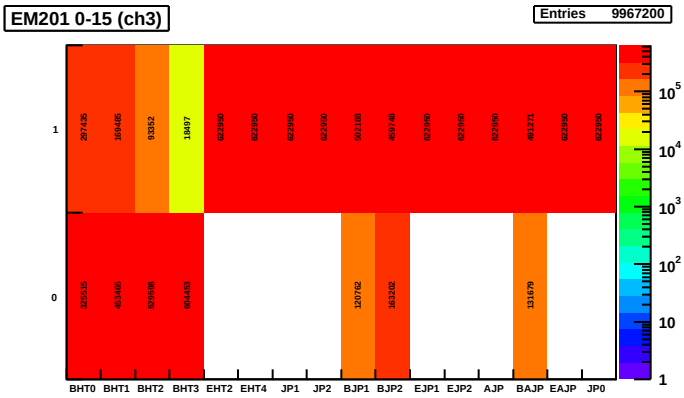

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

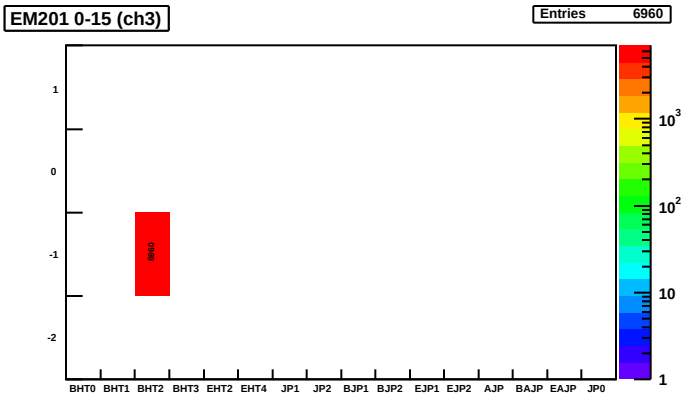

VT201 0-15 (ch1)
Entries 9967200
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 2119035
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

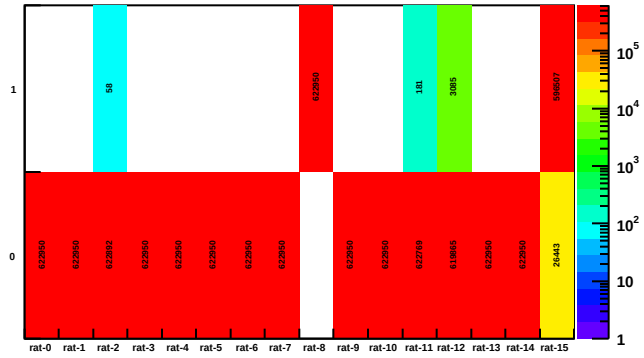

Unused (ch2)
Entries 9967200

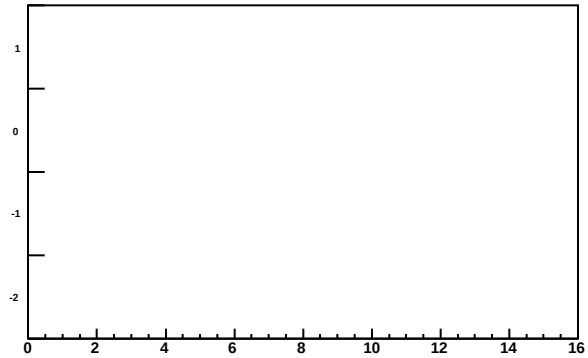

Entries 0


EM201 0-15 (ch3)
Entries 9967200
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

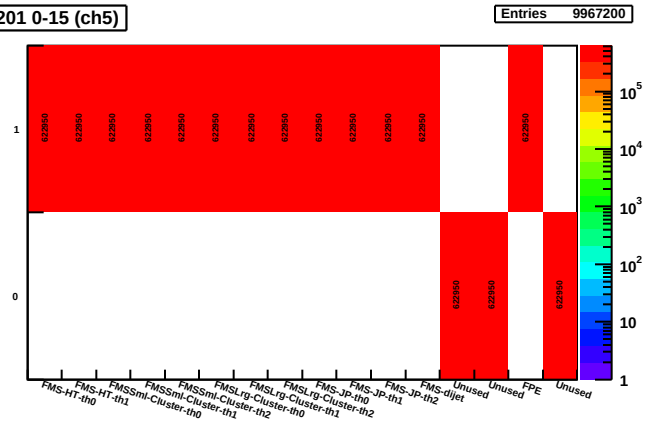

EM201 0-15 (ch3)
Entries 6960
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

RAT board (ch4)

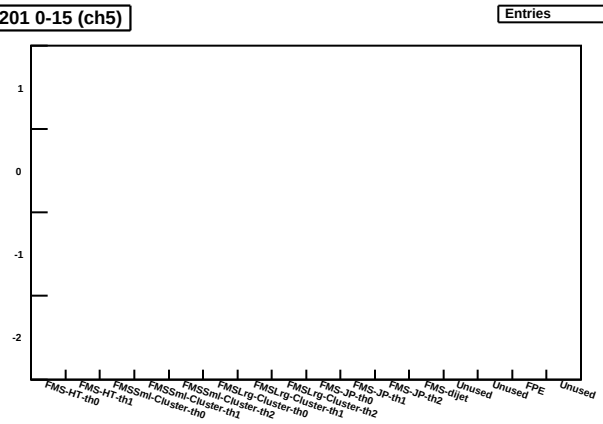




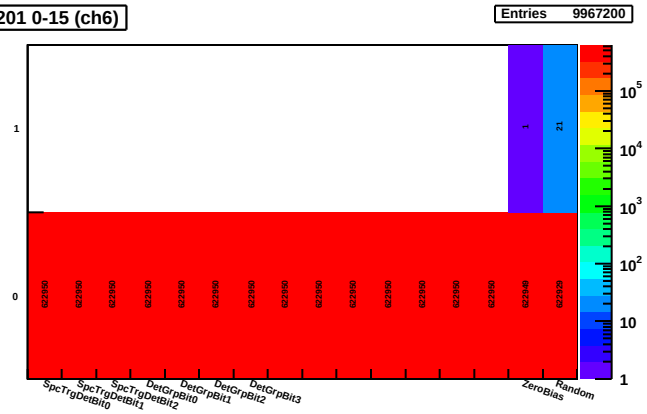
FP201 0-15 (ch5)

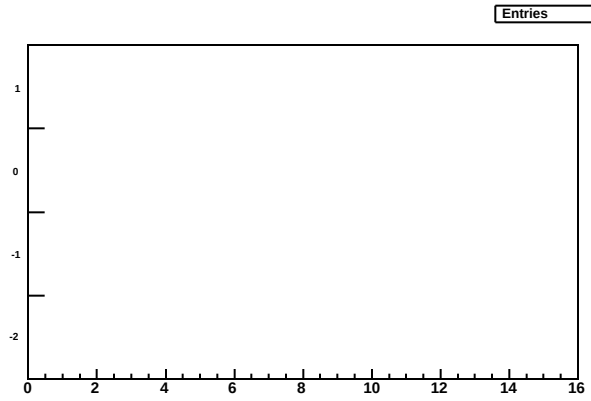
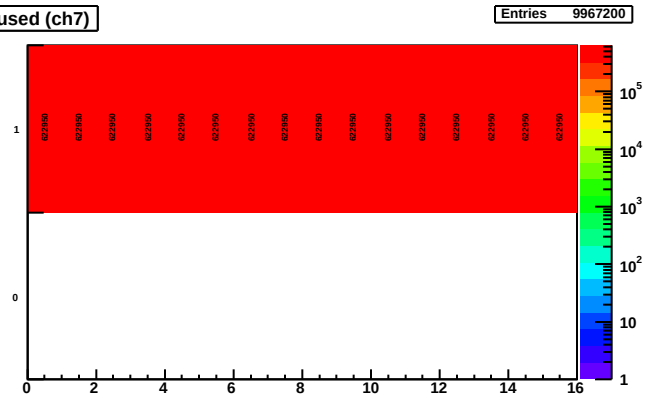
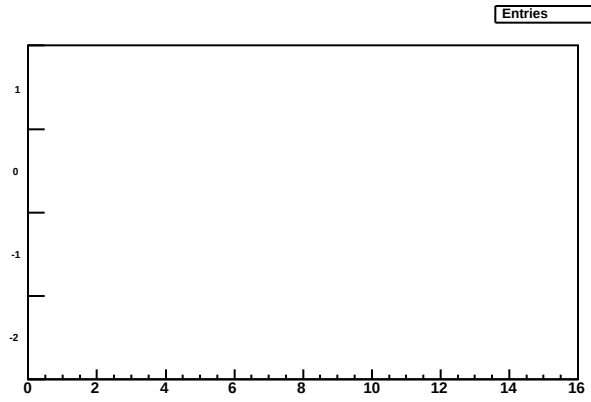


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)