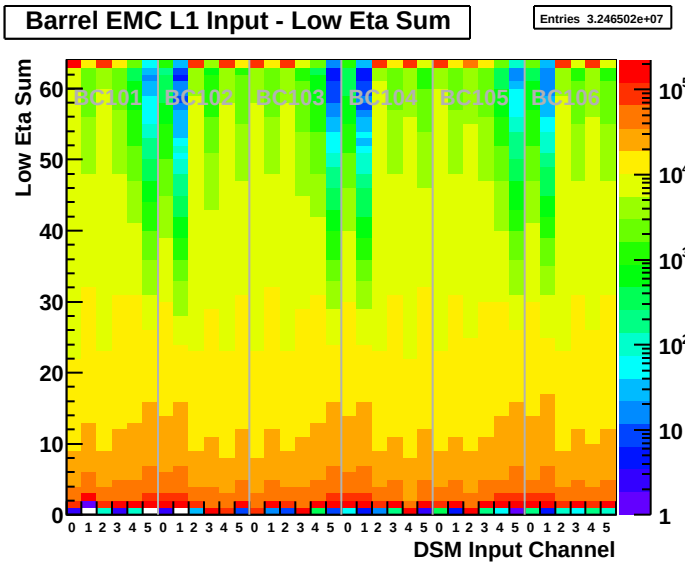
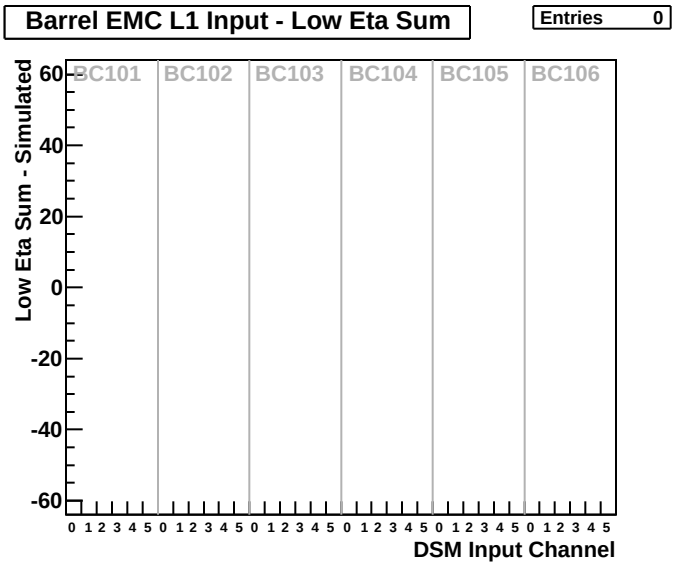
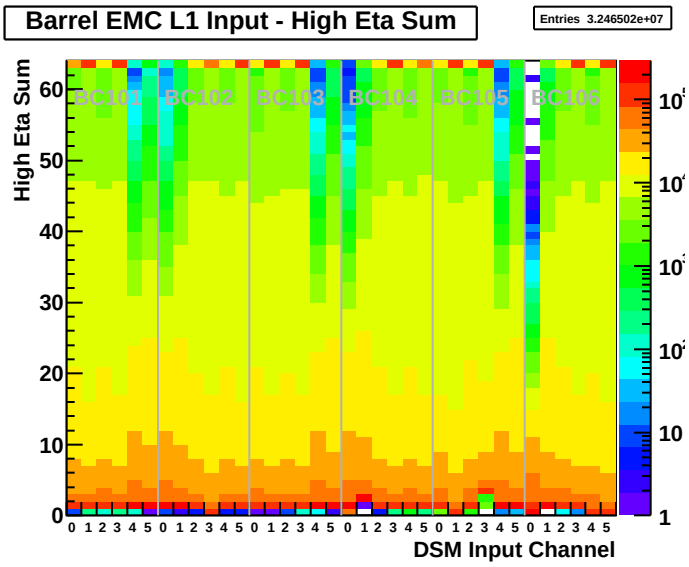
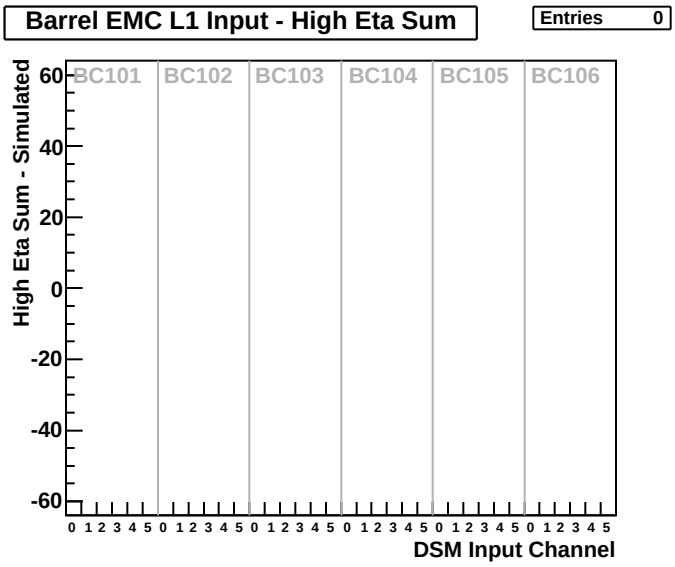
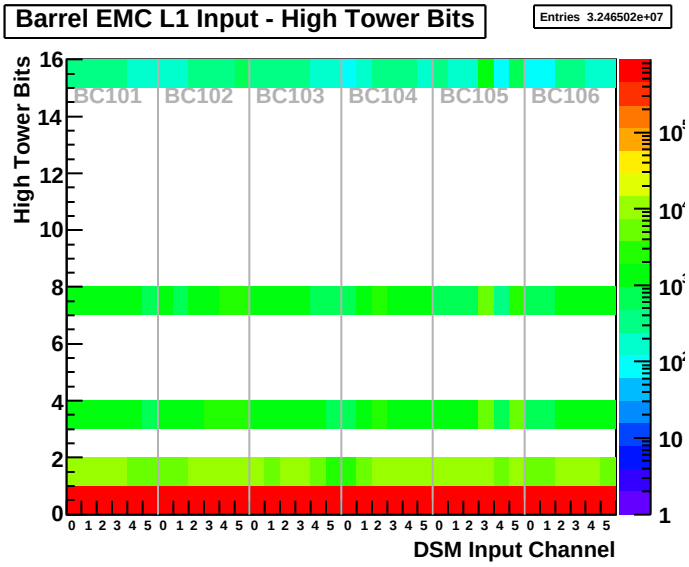
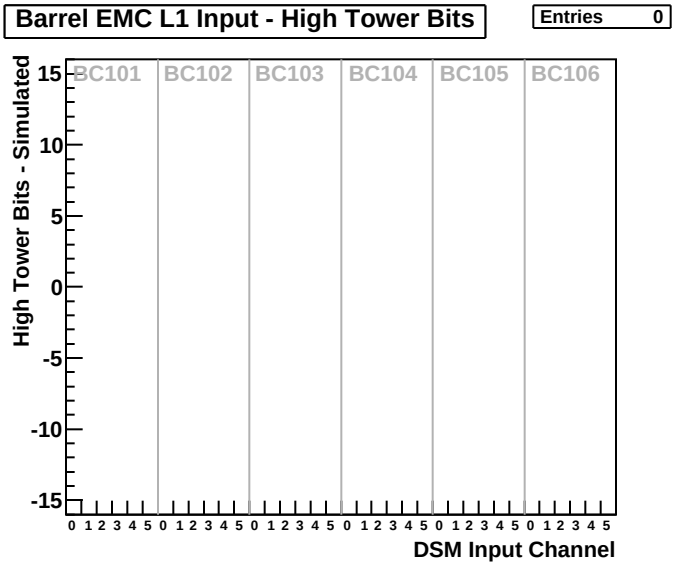

Barrel EMC L1 Input - Low Eta Sum
Entries 3.246502e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

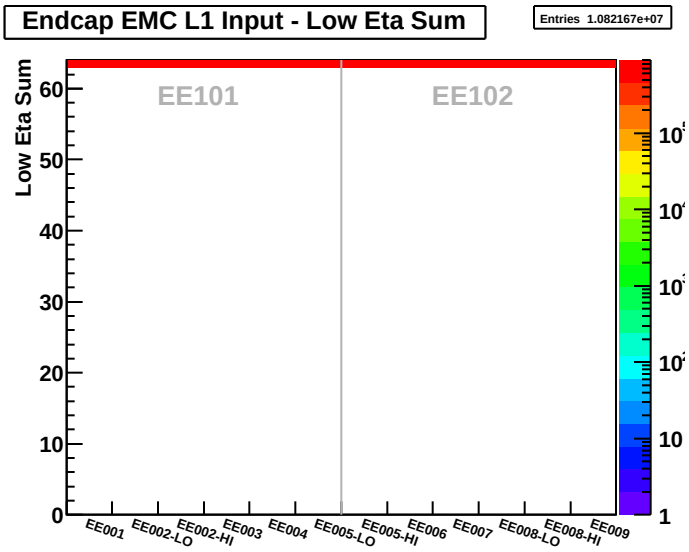

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

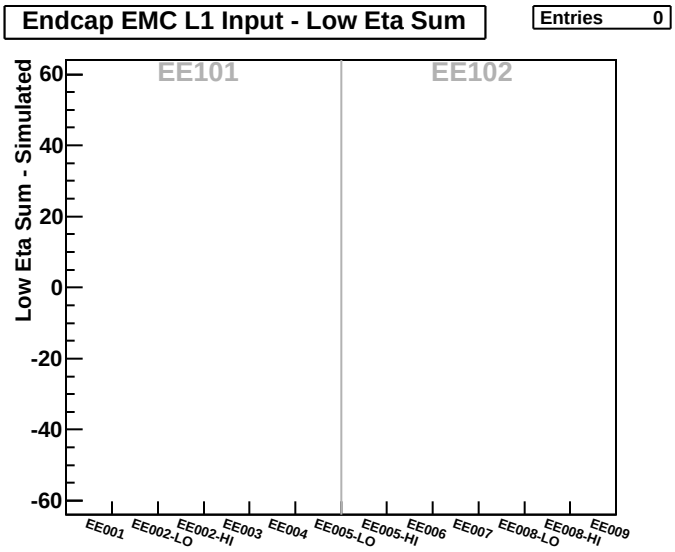

Barrel EMC L1 Input - High Eta Sum
Entries 3.246502e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

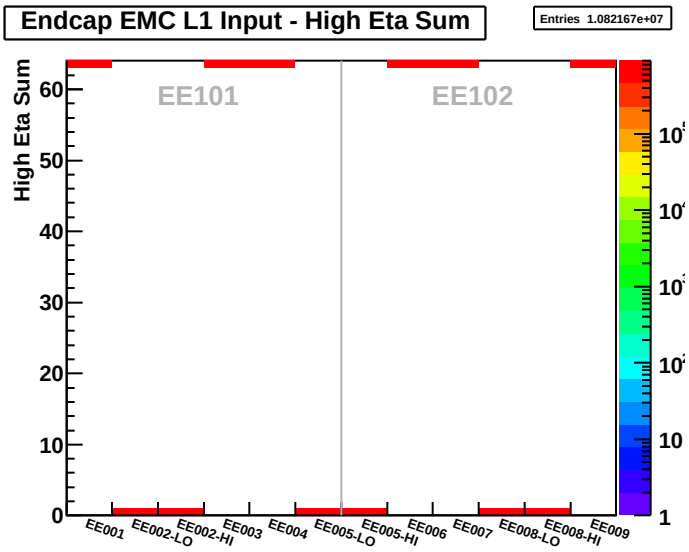

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

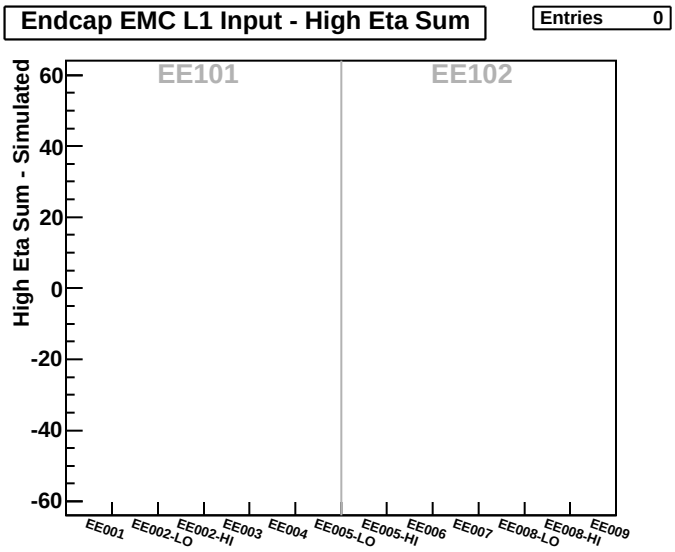

Barrel EMC L1 Input - High Tower Bits
Entries 3.246502e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

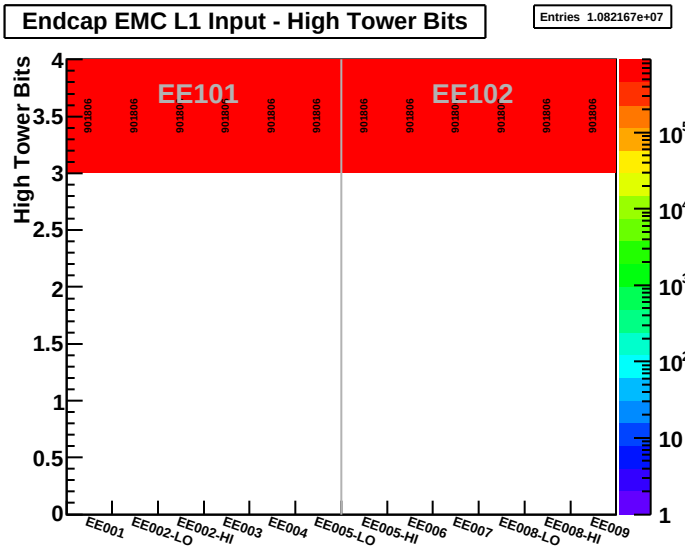

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

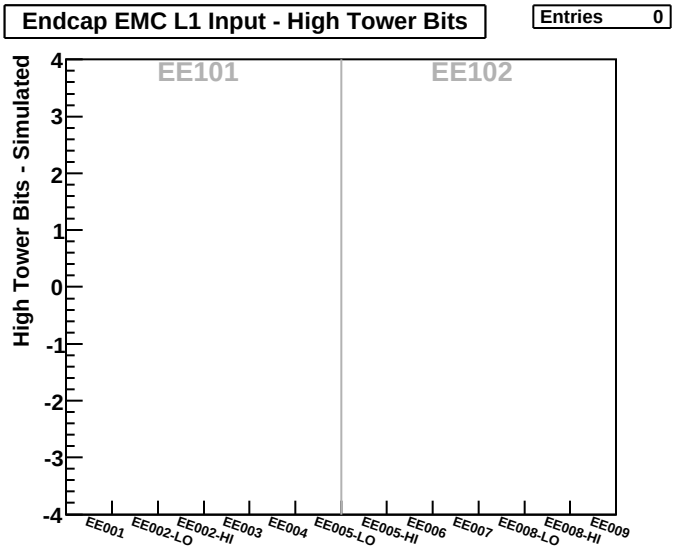

Endcap EMC L1 Input - Low Eta Sum
Entries 1.082167e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

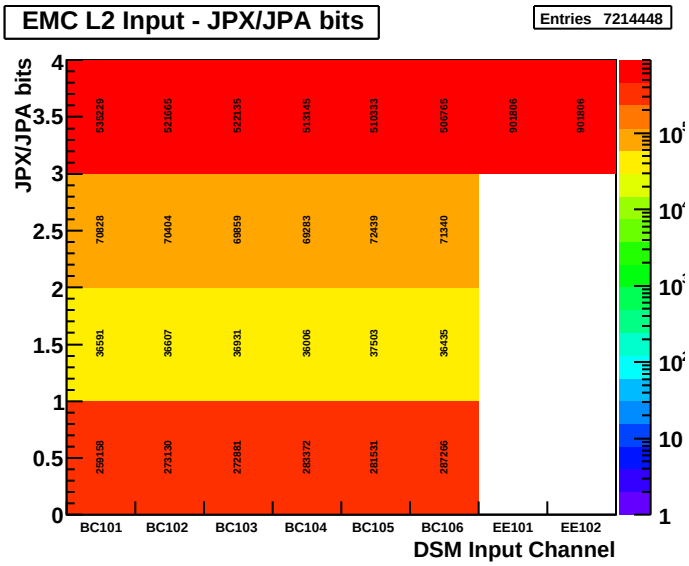

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

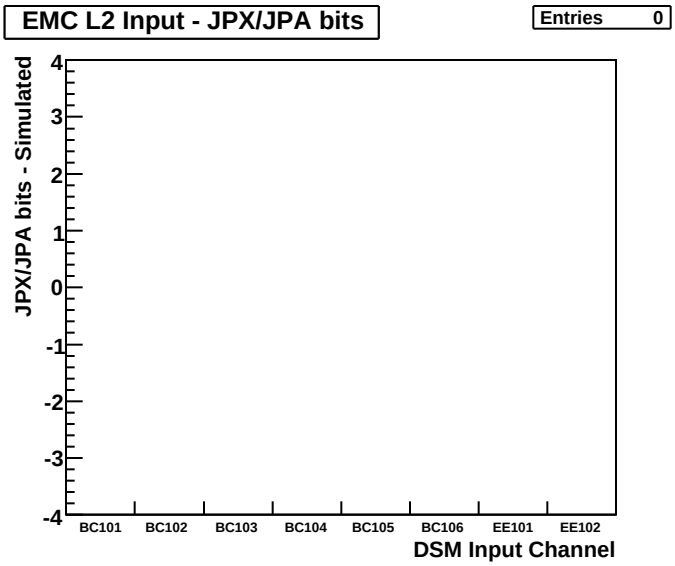

Endcap EMC L1 Input - High Eta Sum
Entries 1.082167e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

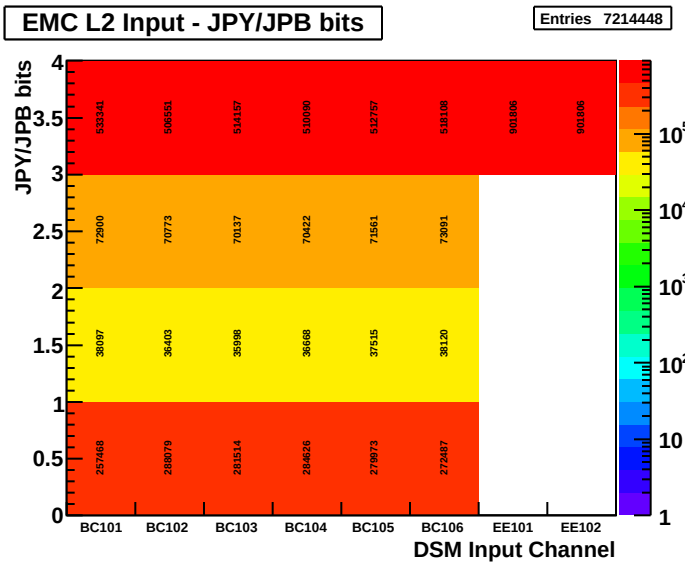

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

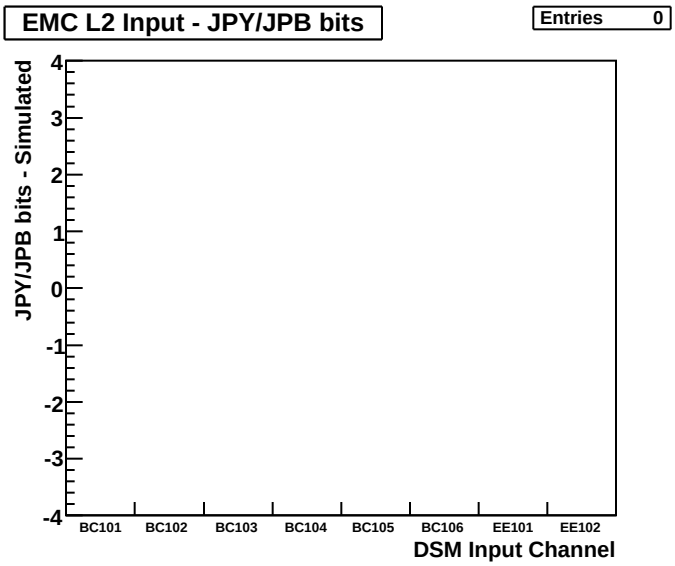

Endcap EMC L1 Input - High Tower Bits
Entries 1.082167e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

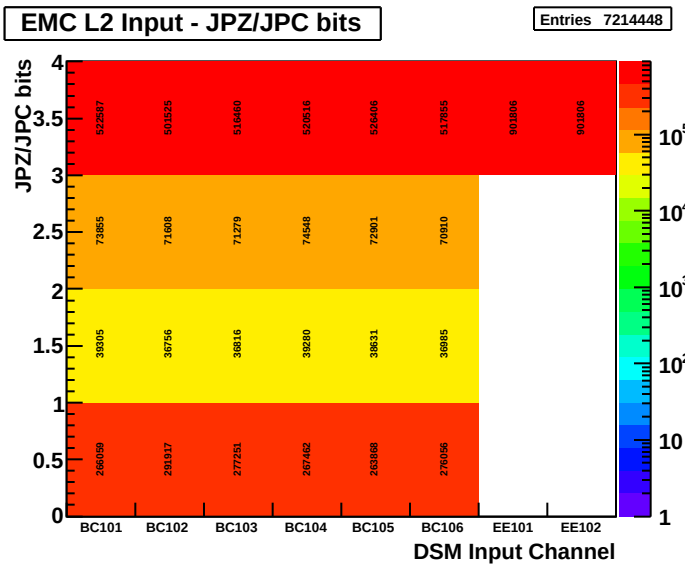

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

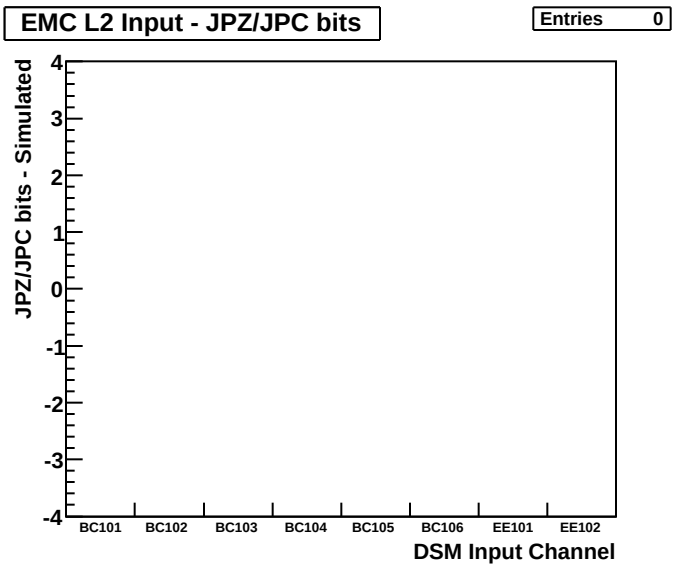

EMC L2 Input - JPX/JPA bits
Entries 7214448
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
535229 521665 522135 513145 510333 506765 901806 901806
70828 70404 69859 69283 72439 71340
36591 36607 36931 36006 37503 36435
259158 273130 272881 283372 281531 287266

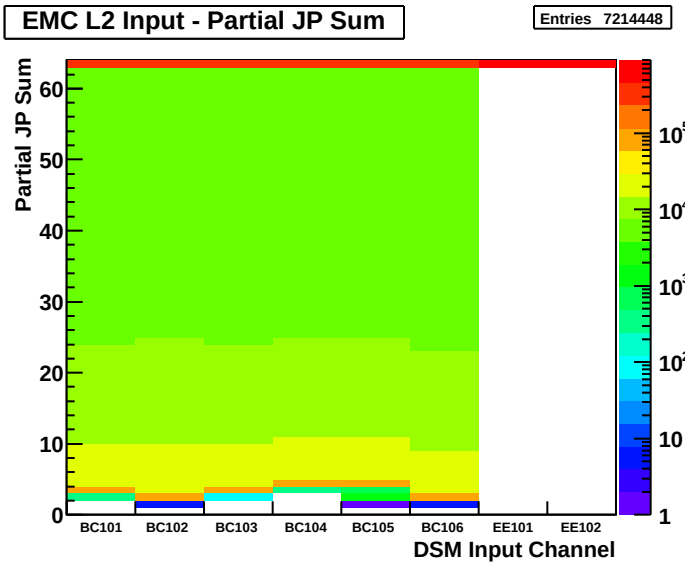

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

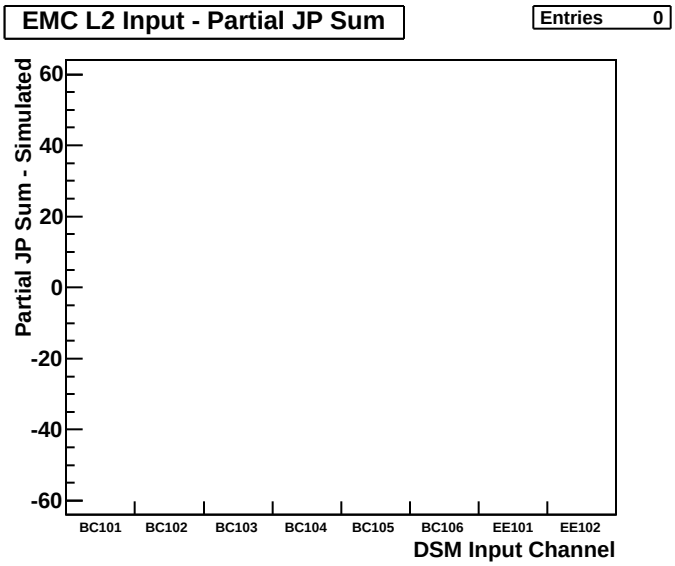

EMC L2 Input - JPY/JPB bits
Entries 7214448
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
533341 506551 514157 510000 512757 518108 901806 901806
72900 70773 70137 70422 71561 73091
38097 36403 35998 36668 37515 38120
257468 288079 281514 284626 279973 272487

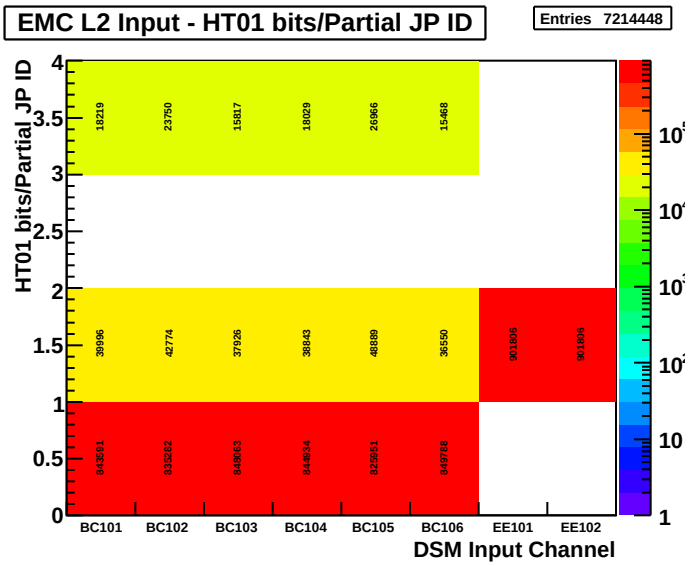

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

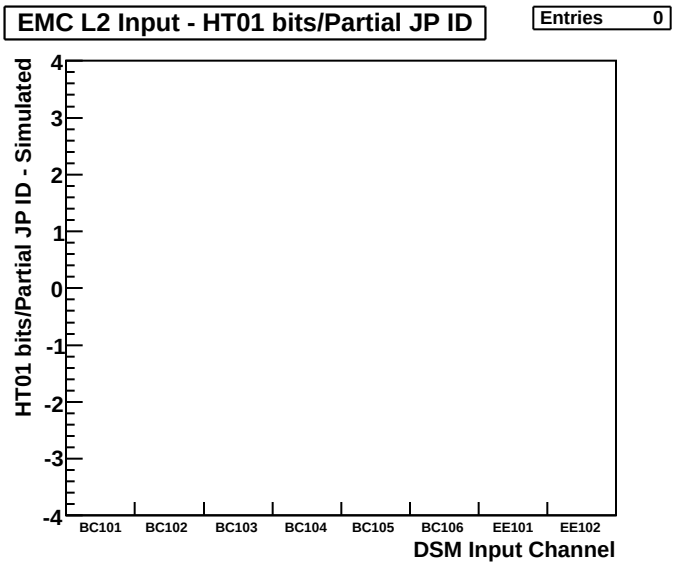

EMC L2 Input - JPZ/JPC bits
Entries 7214448
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
522587 501525 516460 520516 526406 517055 901806 901806
73855 71608 71279 74548 72901 70910
39305 36756 36816 39280 38631 36985
266099 291937 277251 267462 263868 276056

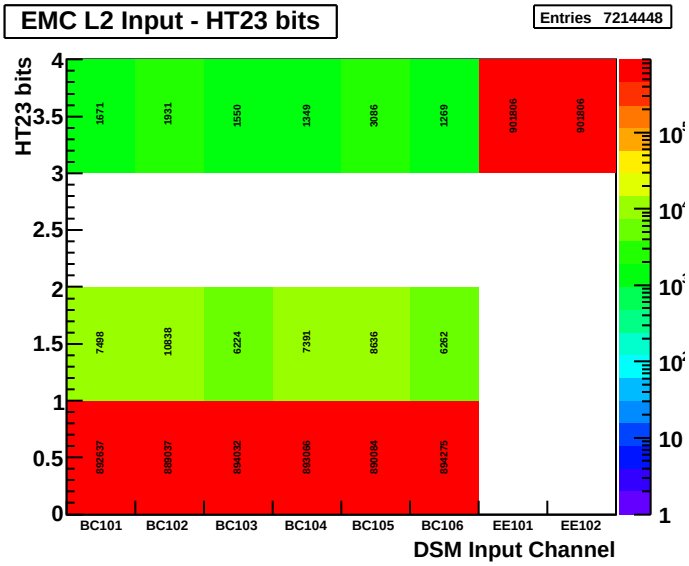

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

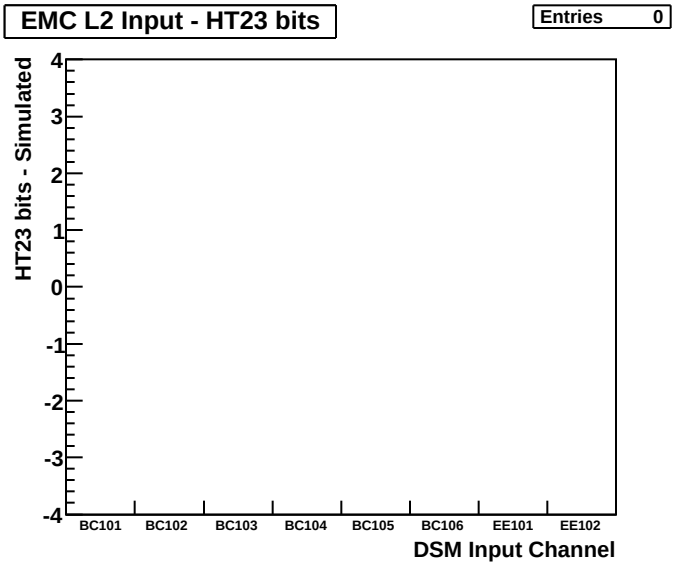

EMC L2 Input - Partial JP Sum
Entries 7214448
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

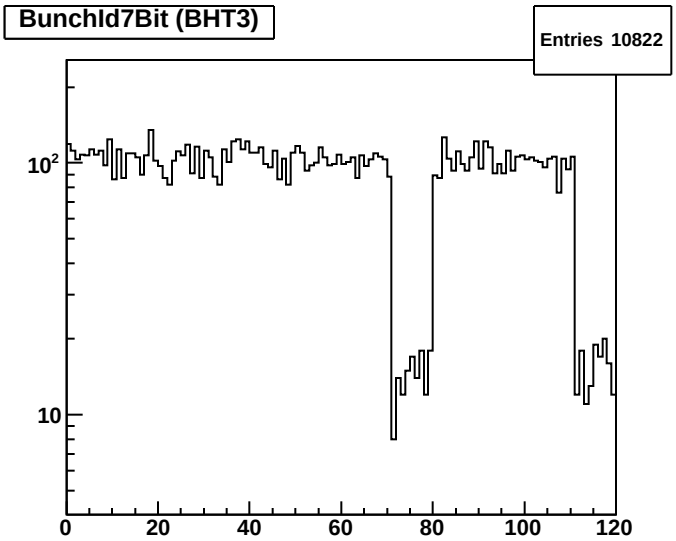

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

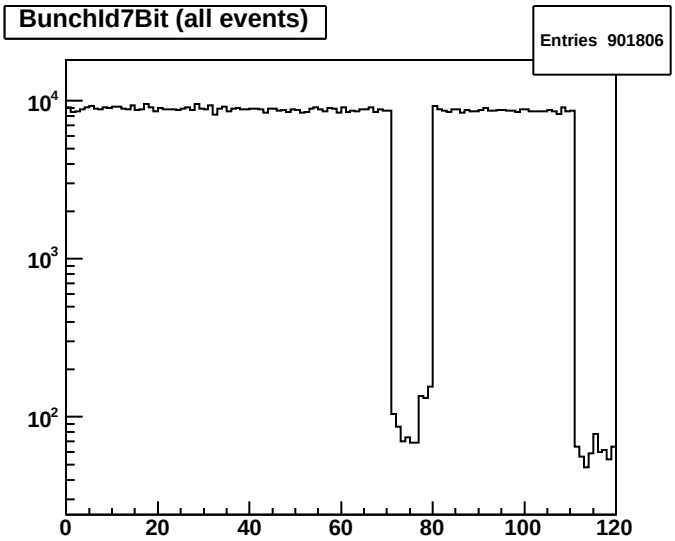

EMC L2 Input - HT01 bits/Partial JP ID
Entries 7214448
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

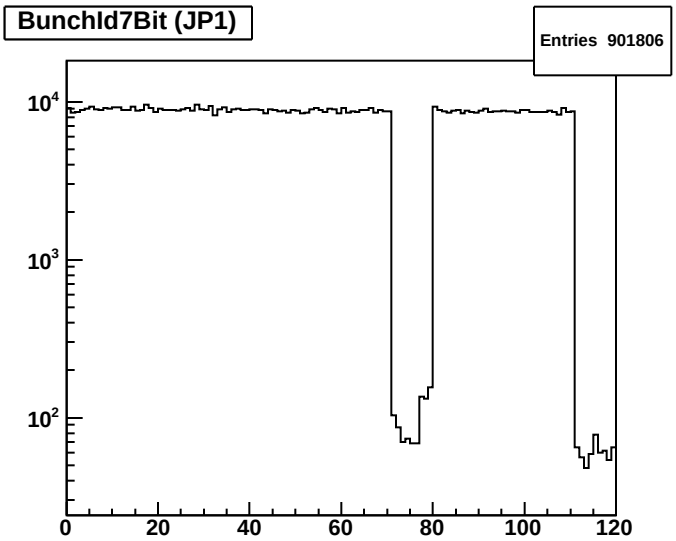

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

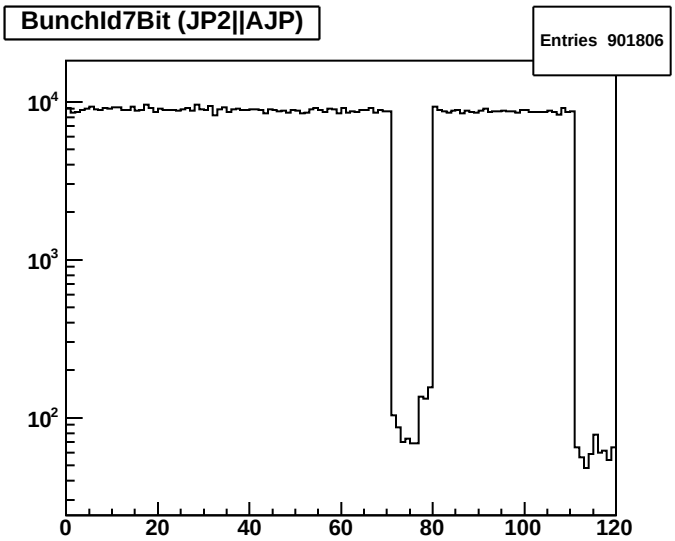

EMC L2 Input - HT23 bits
Entries 7214448
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

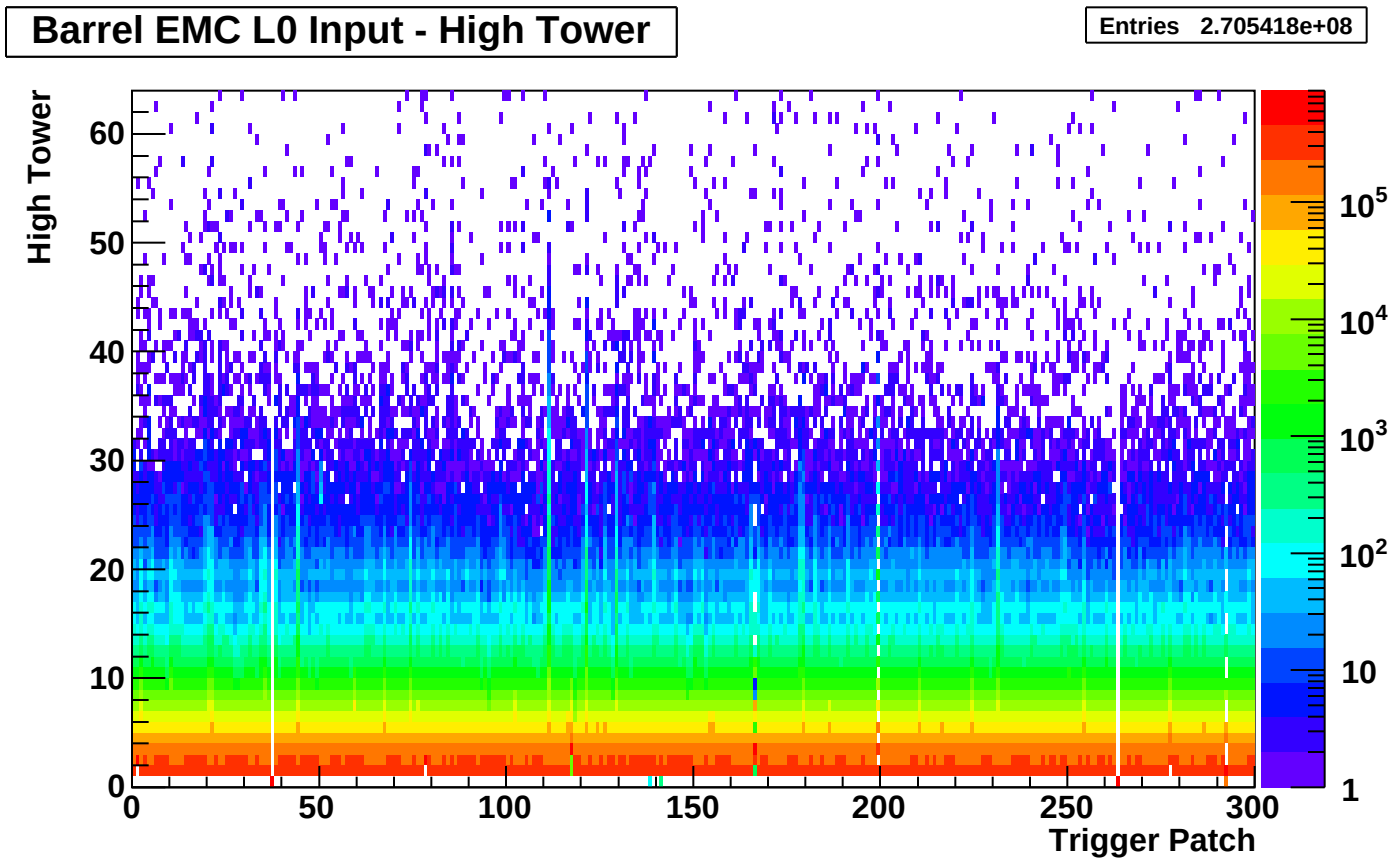

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

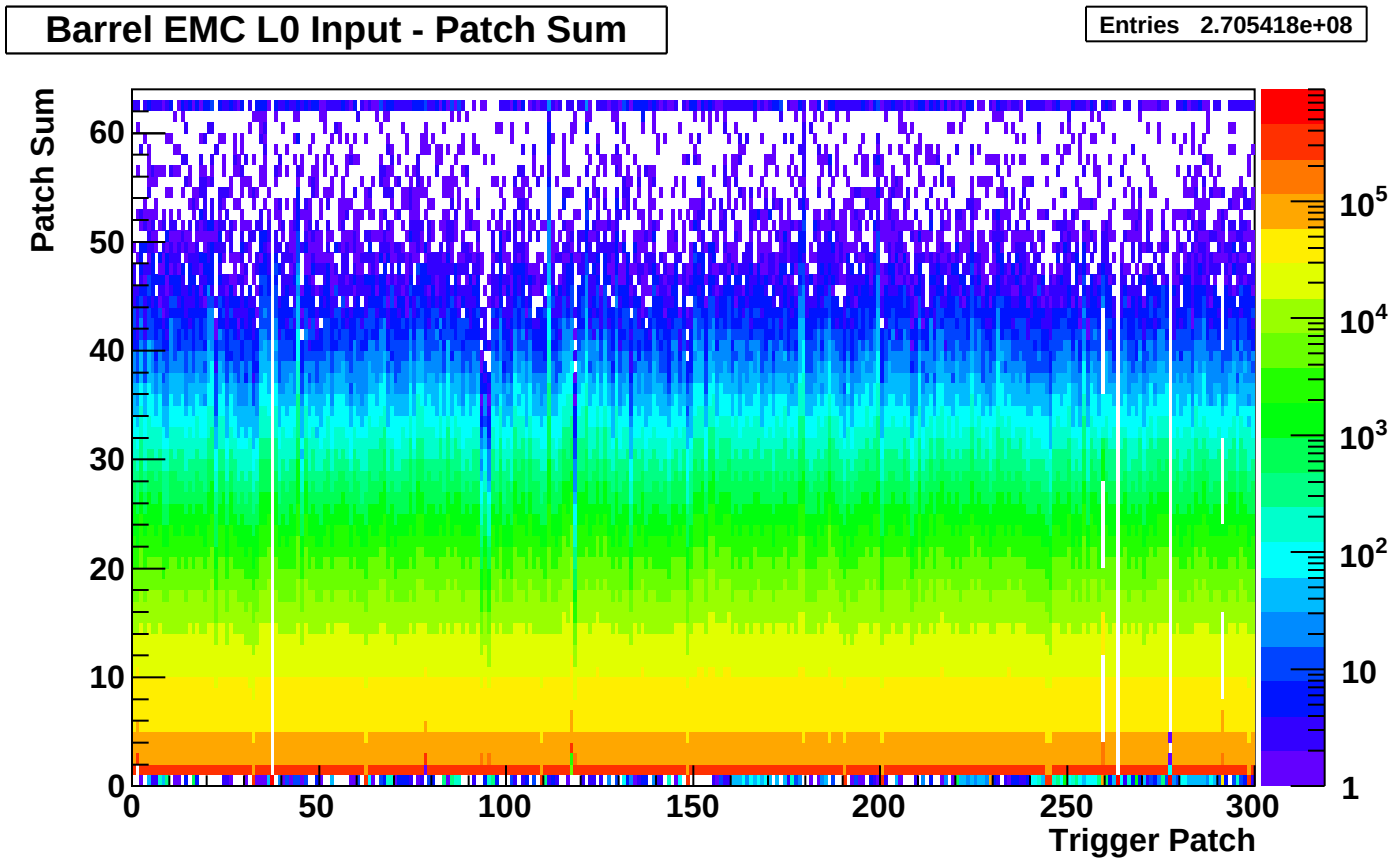

BunchId7Bit (BHT3)
Entries 10822
10^2
10
0
20
40
60
80
100
120

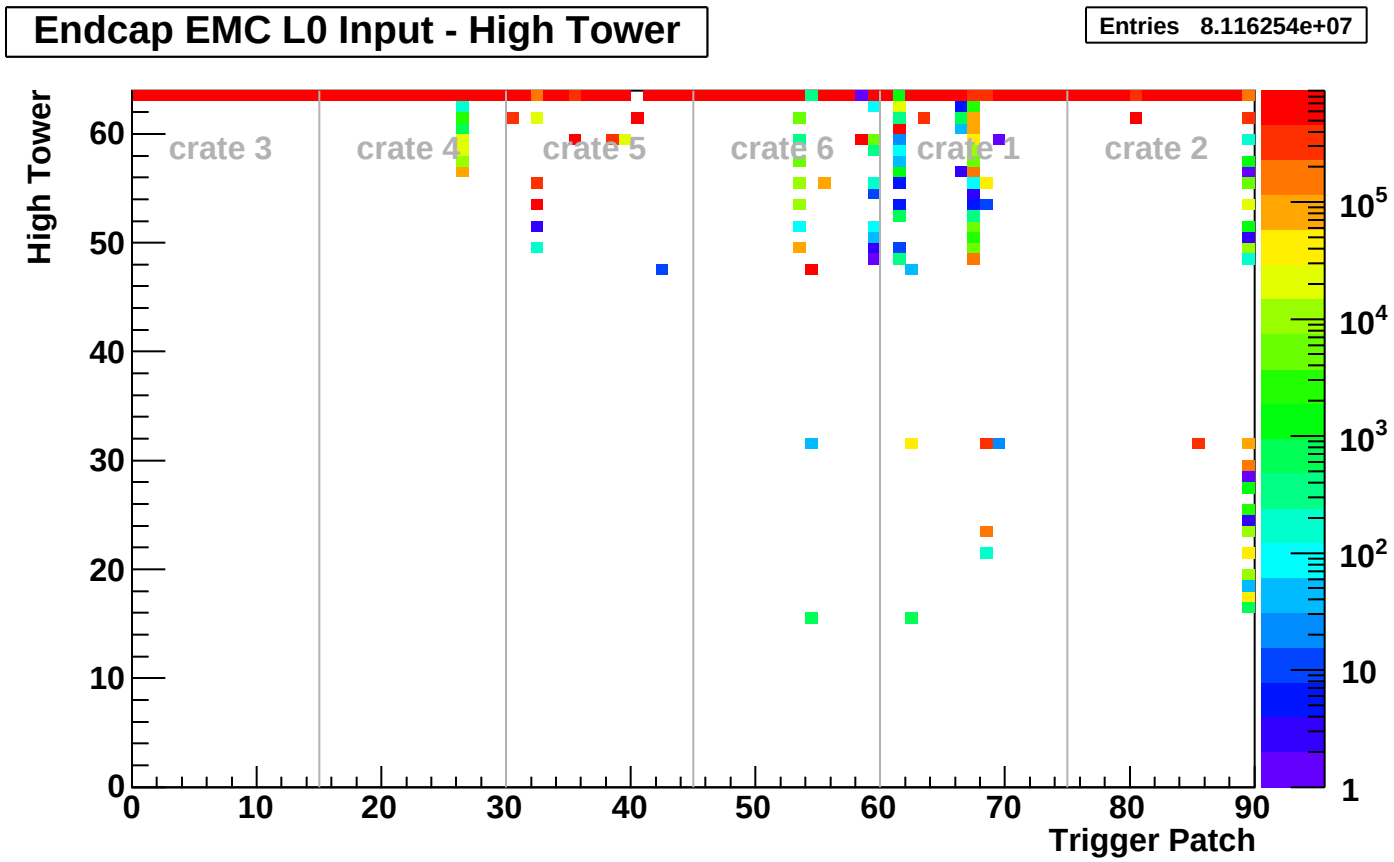

BunchId7Bit (all events)
Entries 901806
10^4
10^3
10^2
0
20
40
60
80
100
120

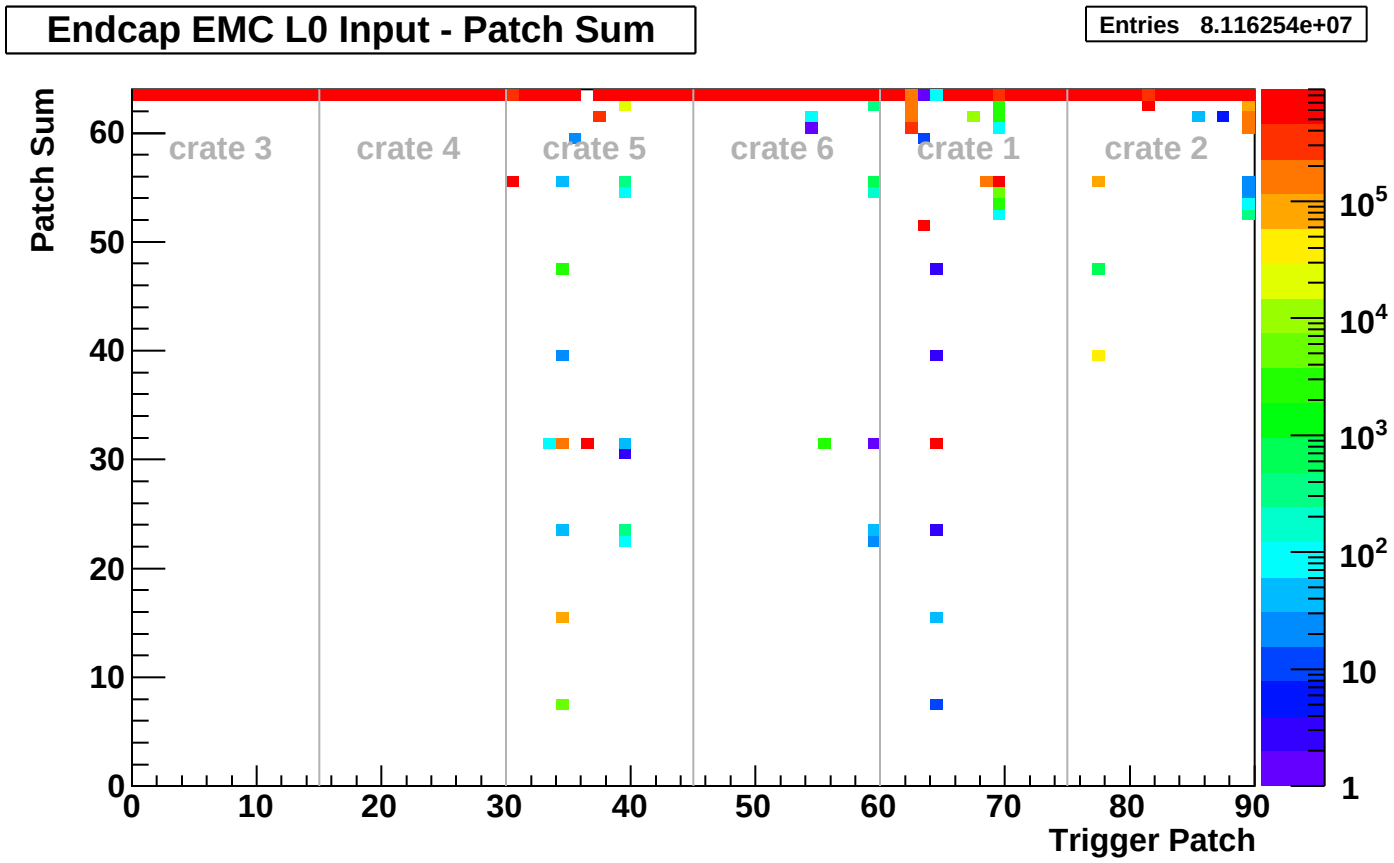

BunchId7Bit (JP1)
Entries 901806
10^4
10^3
10^2
0
20
40
60
80
100
120

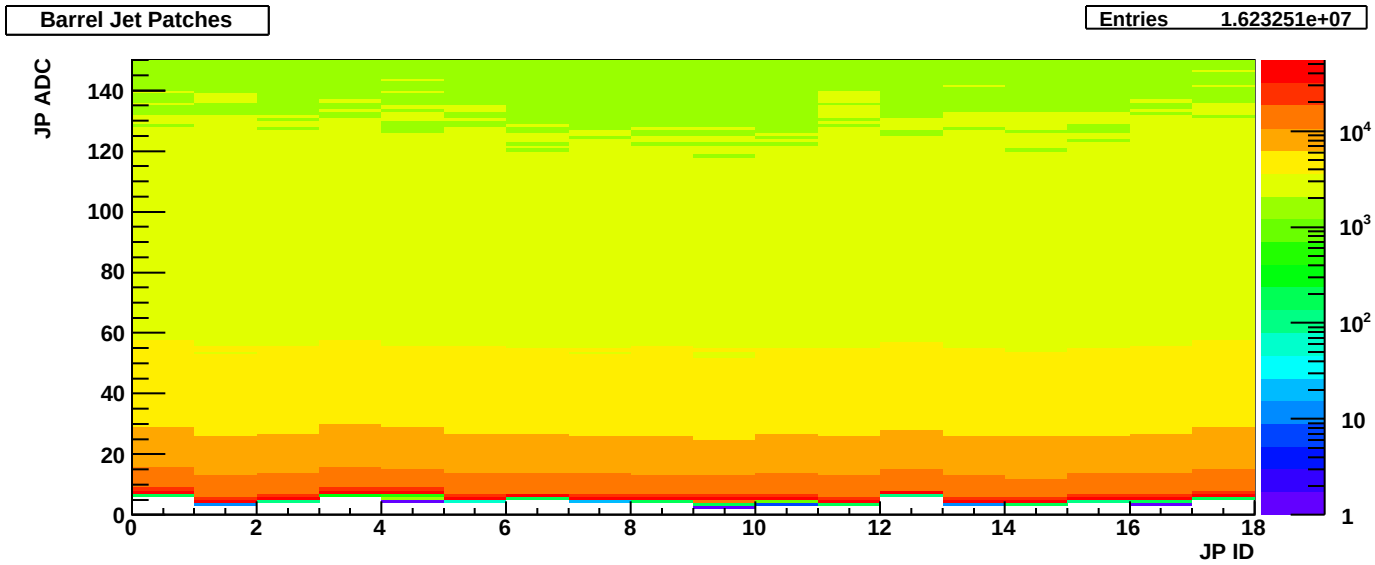

BunchId7Bit (JP2||AJP)
Entries 901806
10^4
10^3
10^2
0
20
40
60
80
100
120

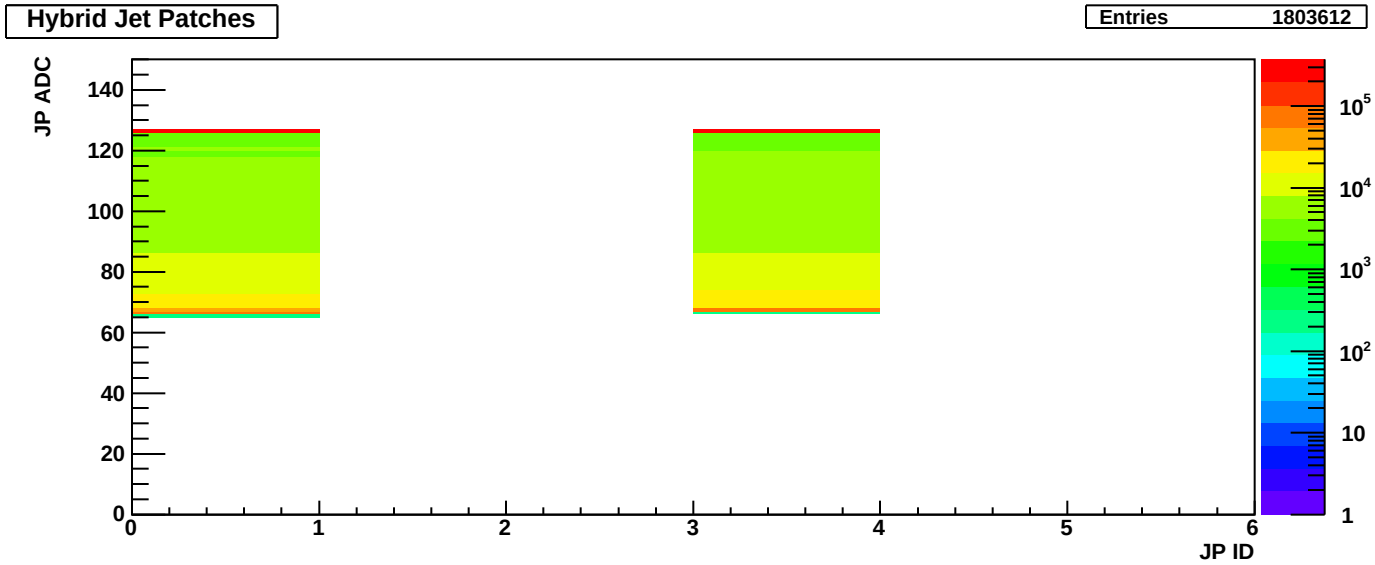

Barrel EMC L0 Input - High Tower
Entries 2.705418e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

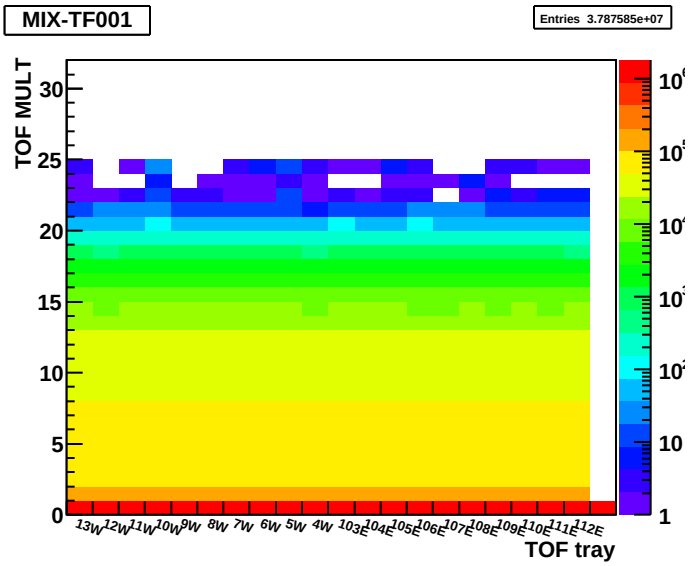

Barrel EMC L0 Input - Patch Sum
Entries 2.705418e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

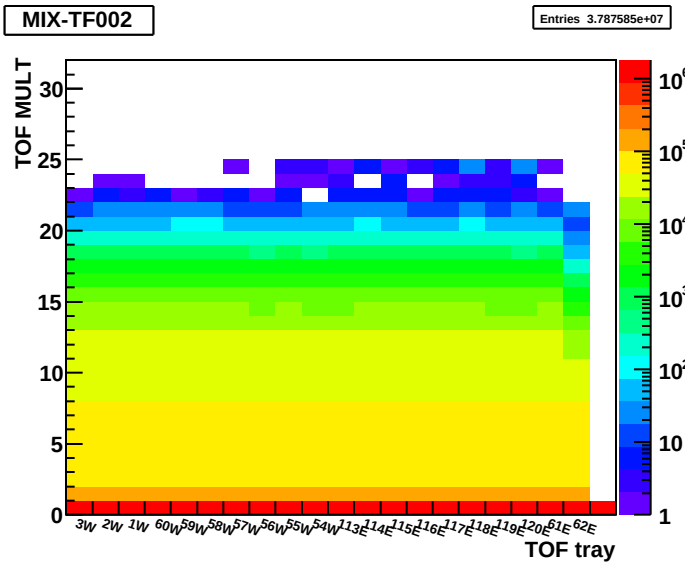

Endcap EMC L0 Input - High Tower
Entries 8.116254e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

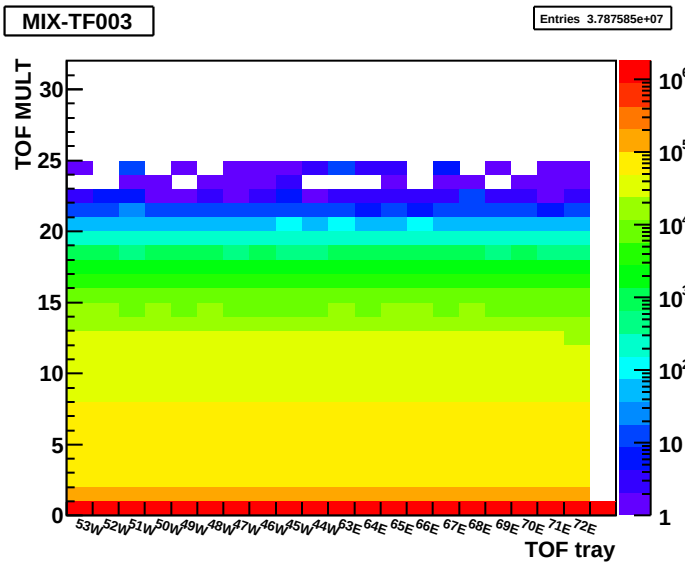

Endcap EMC L0 Input - Patch Sum
Entries 8.116254e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

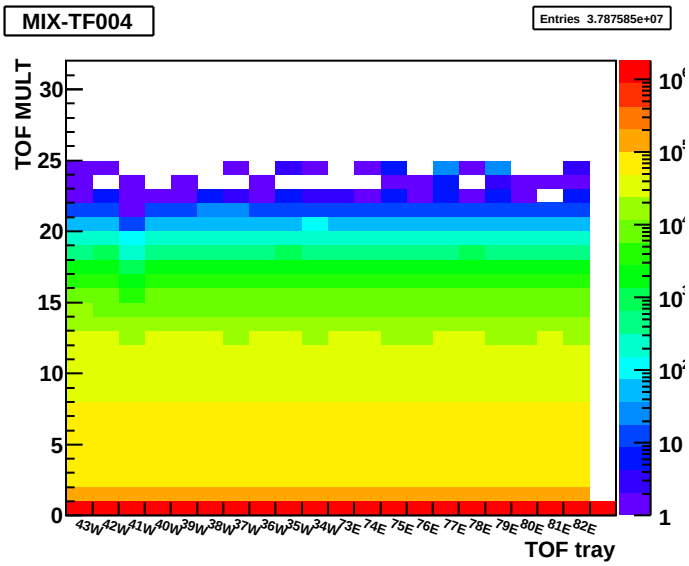

Barrel Jet Patches
Entries 1.623251e+07
JP ADC
JP ID

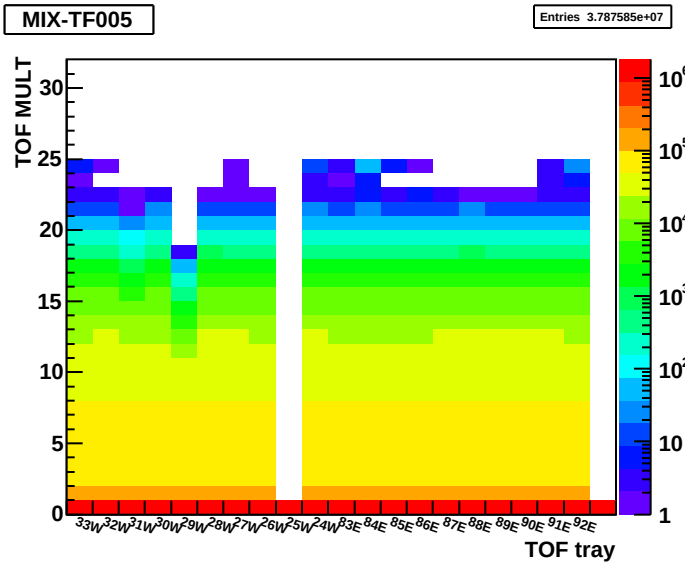

Hybrid Jet Patches
Entries 1803612
JP ADC
JP ID

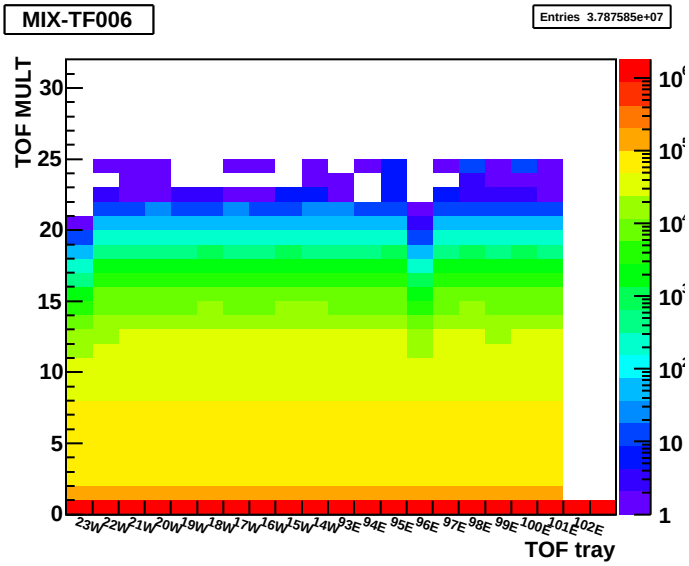

MIX-TF001
Entries 3.787585e+07
TOF MULT
TOF tray

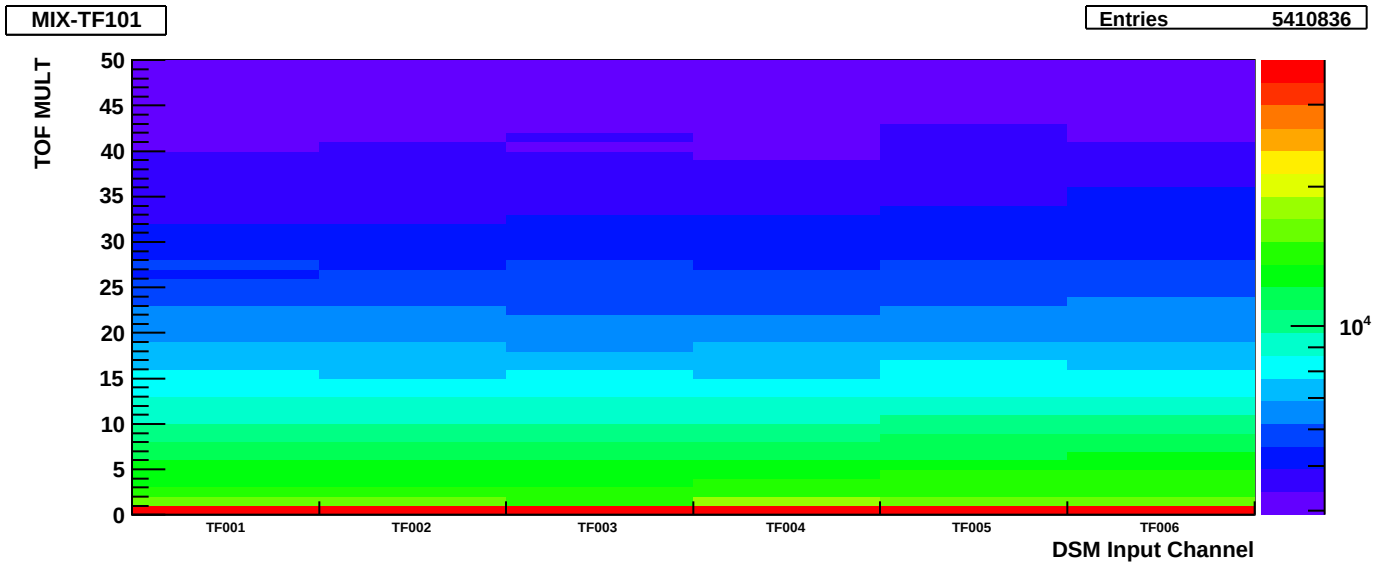

MIX-TF002
Entries 3.787585e+07
TOF MULT
TOF tray

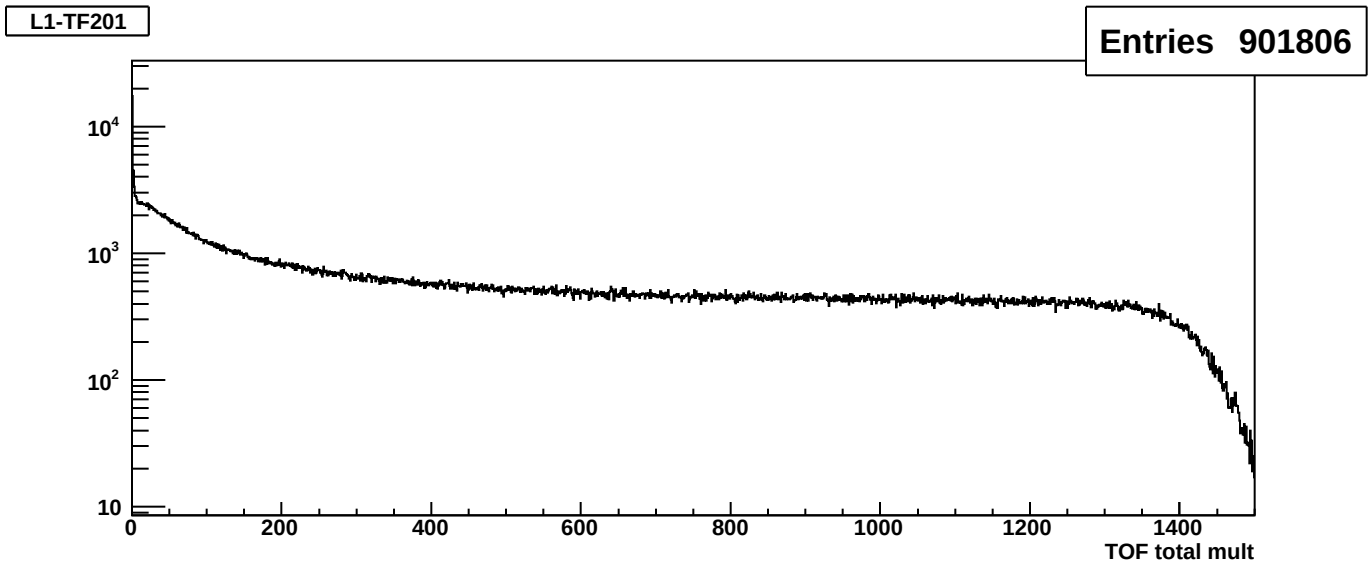

MIX-TF003
Entries 3.787585e+07
TOF MULT
TOF tray

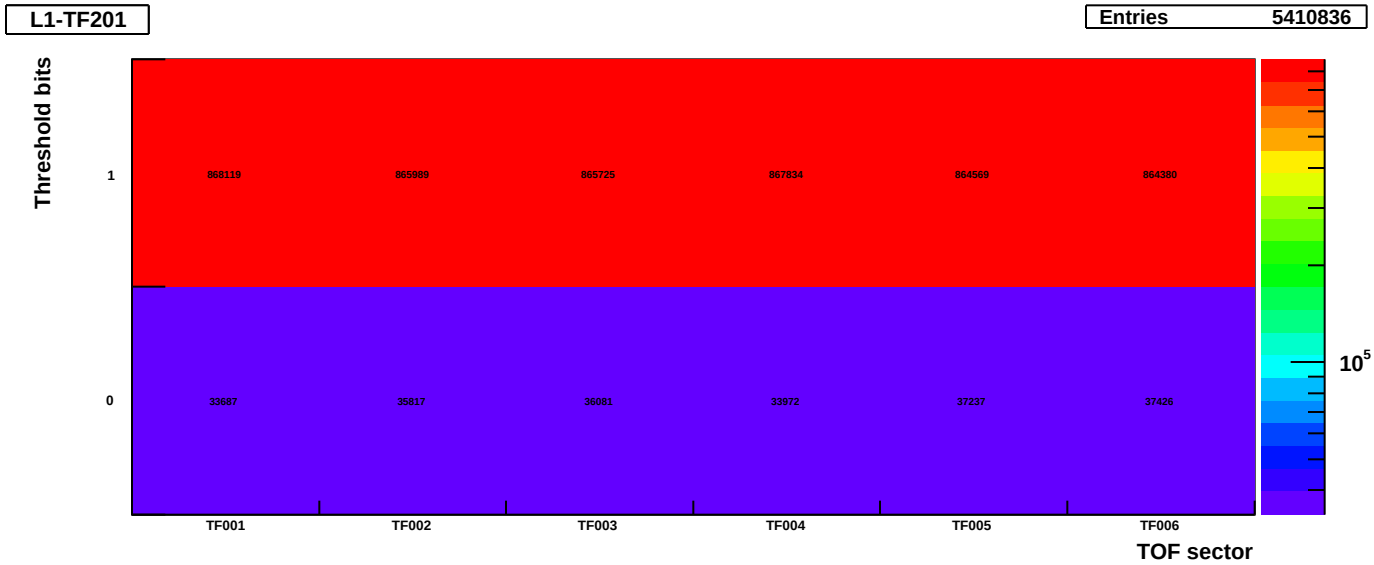

MIX-TF004
Entries 3.787585e+07
TOF MULT
TOF tray

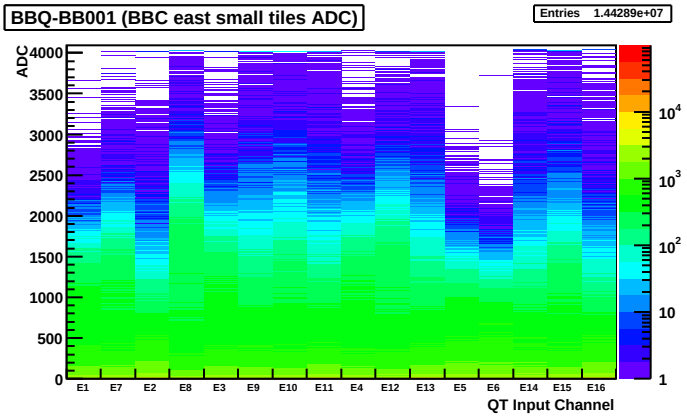

MIX-TF005
Entries 3.787585e+07
TOF MULT
TOF tray

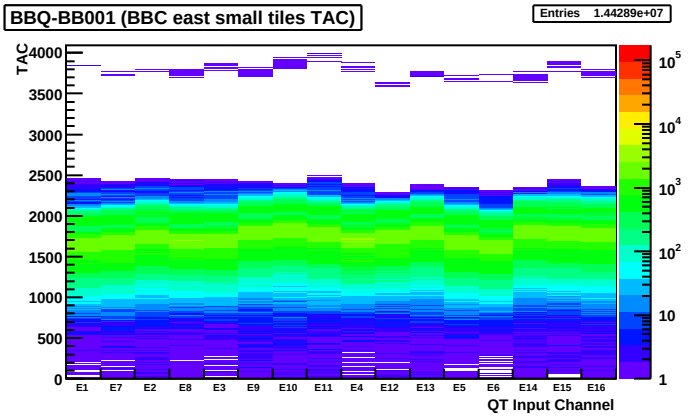

MIX-TF006
Entries 3.787585e+07
TOF MULT
TOF tray

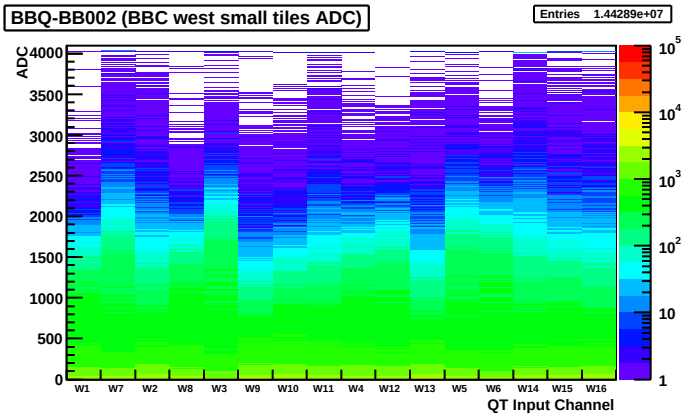

MIX-TF101
Entries 5410836
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

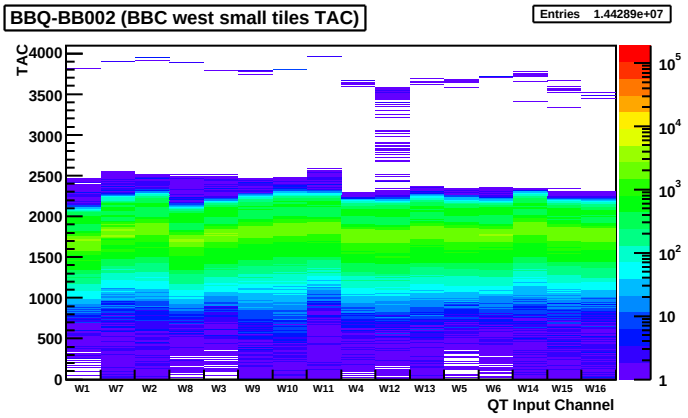

L1-TF201
Entries 901806
10^4
10^3
10^2
10
0
200
400
600
800
1000
1200
1400
TOF total mult

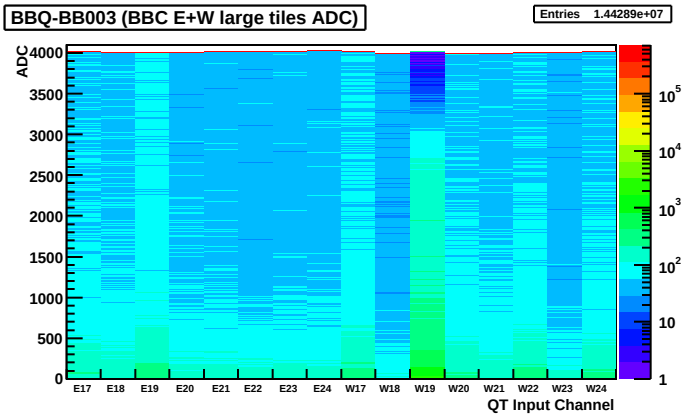

L1-TF201
Entries 5410836
Threshold bits
1
0
868119
865989
865725
867834
864569
864380
33687
35817
36081
33972
37237
37426
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

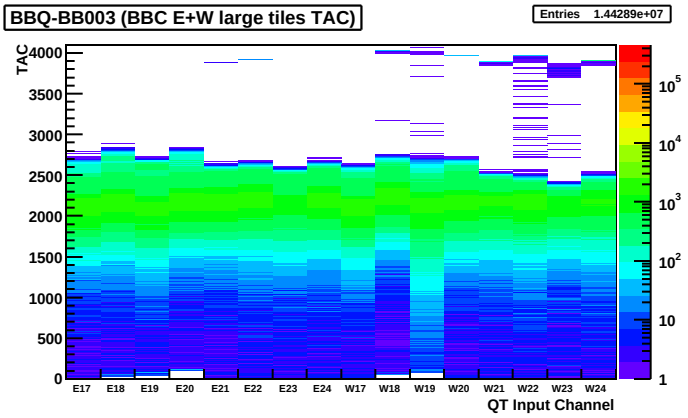

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.44289e+07
ADC
QT Input Channel

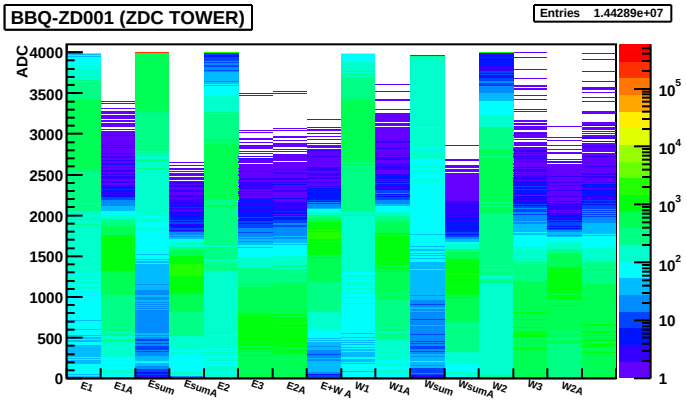

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.44289e+07
TAC
QT Input Channel

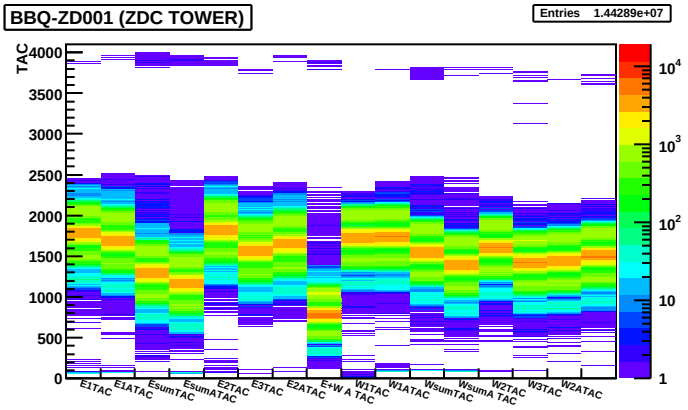

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.44289e+07
ADC
QT Input Channel

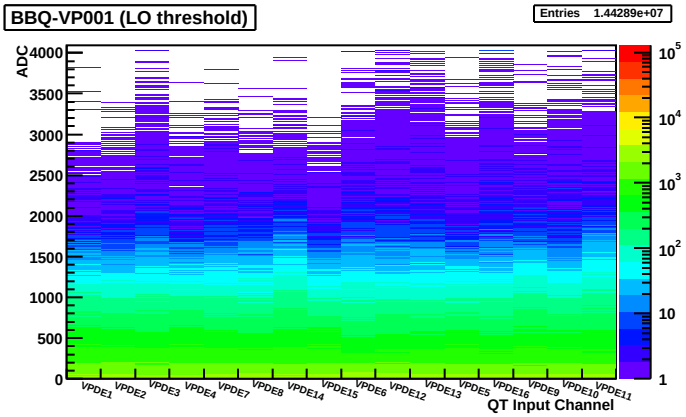

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.44289e+07
TAC
QT Input Channel

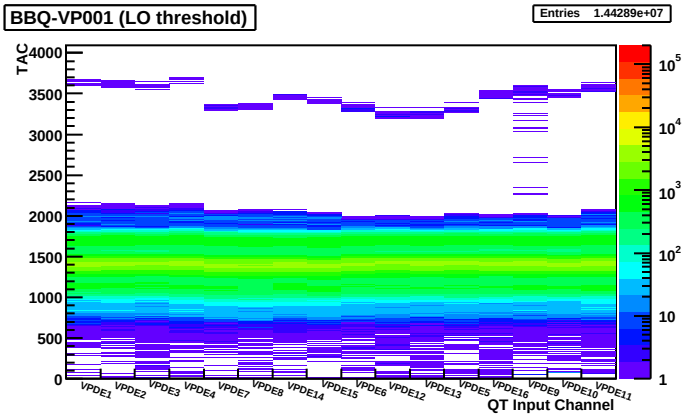

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.44289e+07
ADC
QT Input Channel

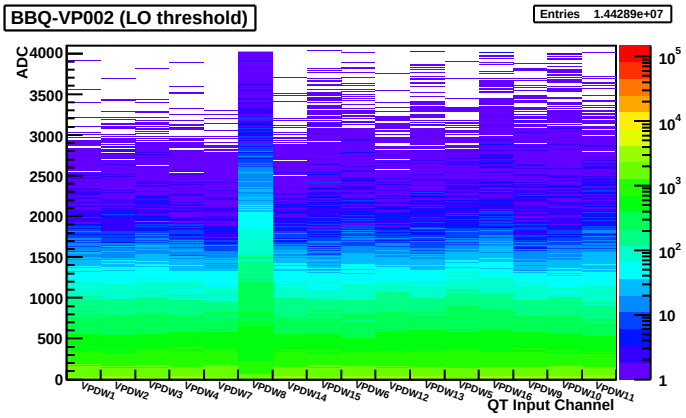

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.44289e+07
TAC
QT Input Channel

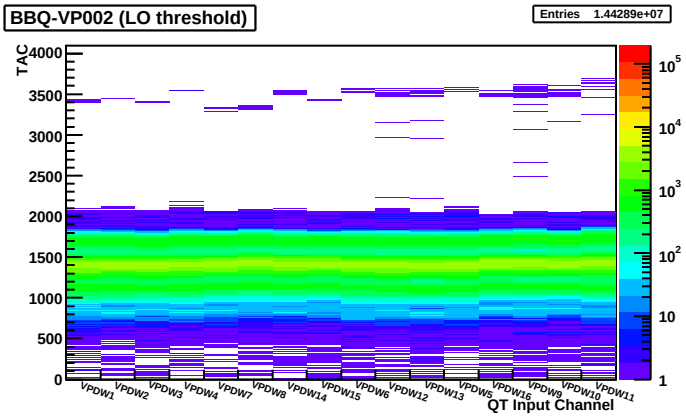

BBQ-ZD001 (ZDC TOWER)
Entries 1.44289e+07
ADC

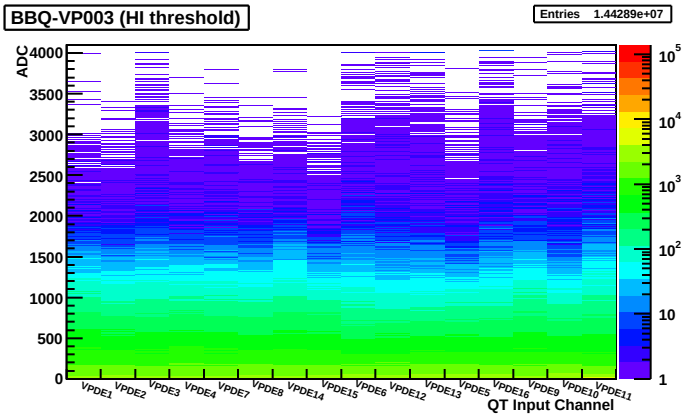

BBQ-ZD001 (ZDC TOWER)
Entries 1.44289e+07
TAC

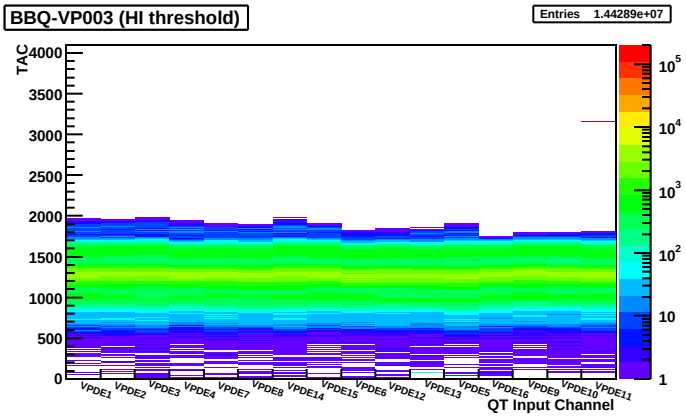

BBQ-VP001 (LO threshold)
Entries 1.44289e+07
ADC
QT Input Channel

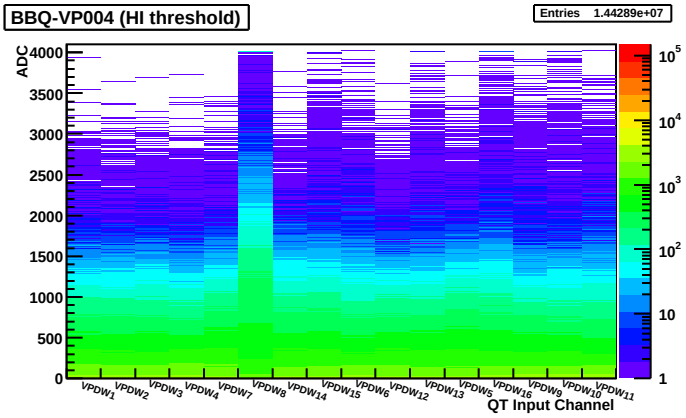

BBQ-VP001 (LO threshold)
Entries 1.44289e+07
TAC
QT Input Channel

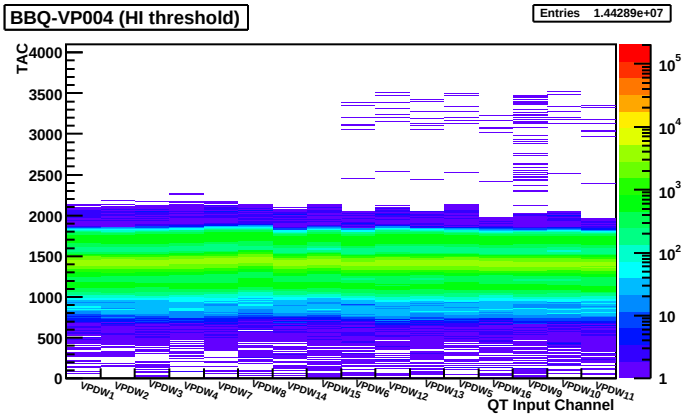

BBQ-VP002 (LO threshold)
Entries 1.44289e+07
ADC
QT Input Channel

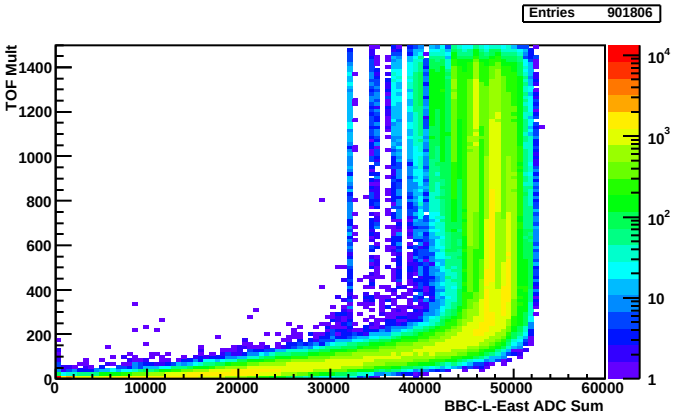

BBQ-VP002 (LO threshold)
Entries 1.44289e+07
TAC
QT Input Channel

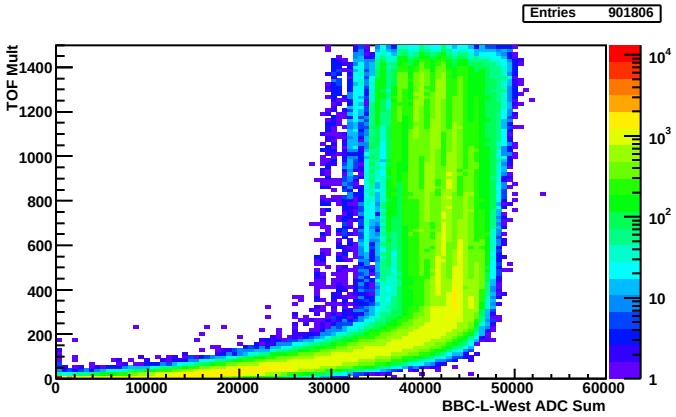

BBQ-VP003 (HI threshold)
Entries 1.44289e+07
ADC
QT Input Channel

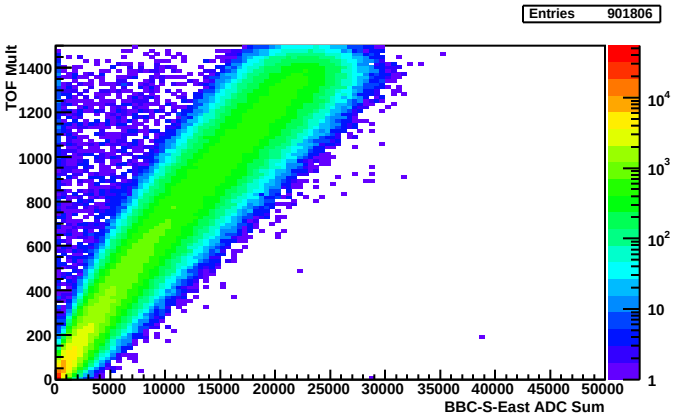

BBQ-VP003 (HI threshold)
Entries 1.44289e+07
TAC
QT Input Channel

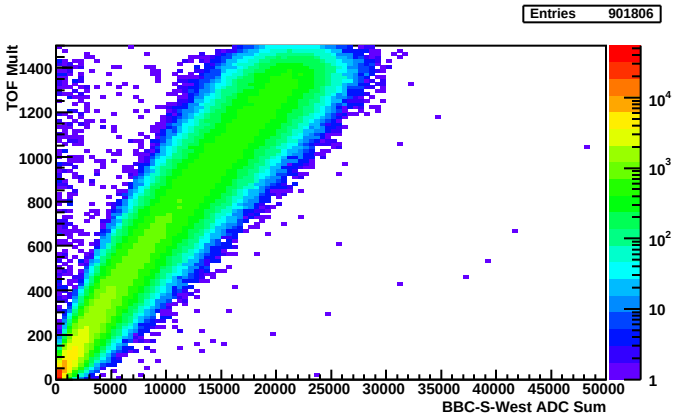

BBQ-VP004 (HI threshold)
Entries 1.44289e+07
ADC
QT Input Channel

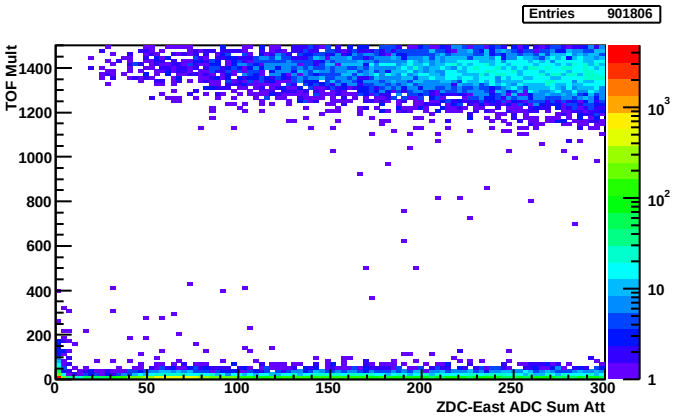

BBQ-VP004 (HI threshold)
Entries 1.44289e+07
TAC
QT Input Channel

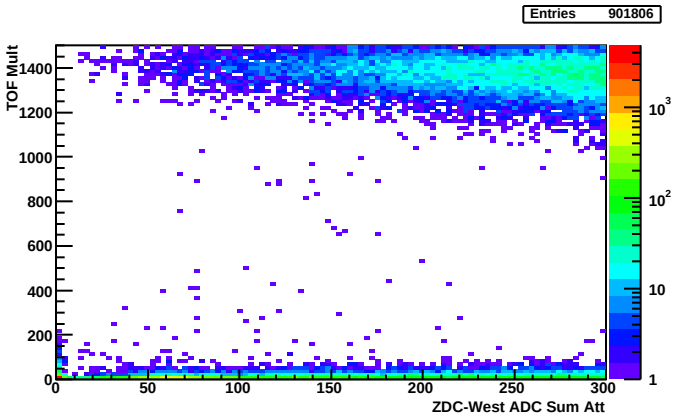

Entries 901806
TOF Mult
BBC-L-East ADC Sum

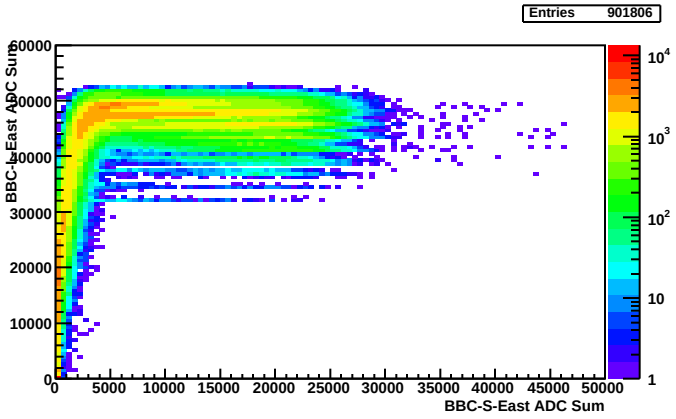

Entries 901806
TOF Mult
BBC-L-West ADC Sum

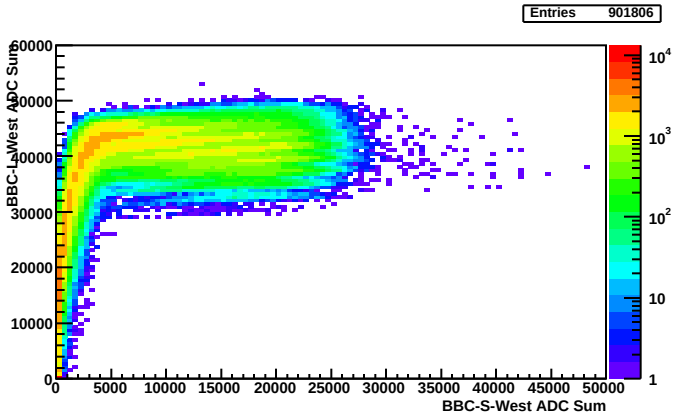

Entries 901806
TOF Mult
BBC-S-East ADC Sum

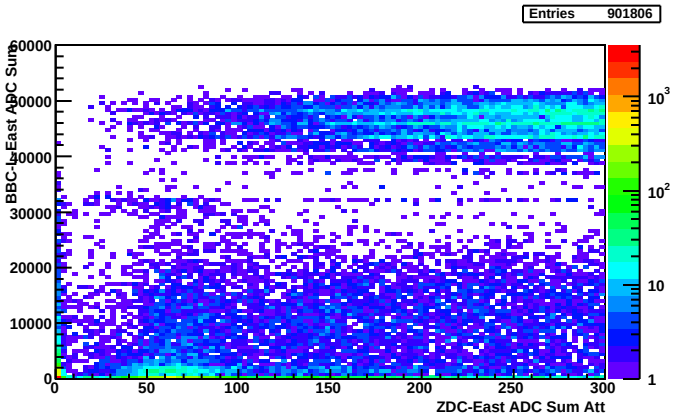

Entries 901806
TOF Mult
BBC-S-West ADC Sum

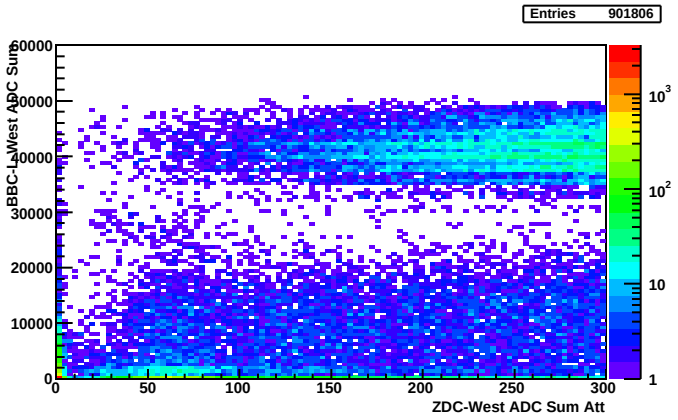

Entries 901806
TOF Mult
ZDC-East ADC Sum Att

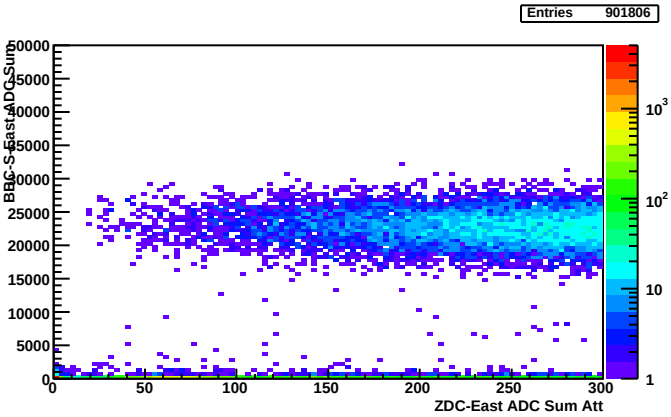

Entries 901806
TOF Mult
ZDC-West ADC Sum Att


Entries 901806
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 901806
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 901806
BBC-L-East ADC Sum
ZDC-East ADC Sum Att


Entries 901806
BBC-L-West ADC Sum
ZDC-West ADC Sum Att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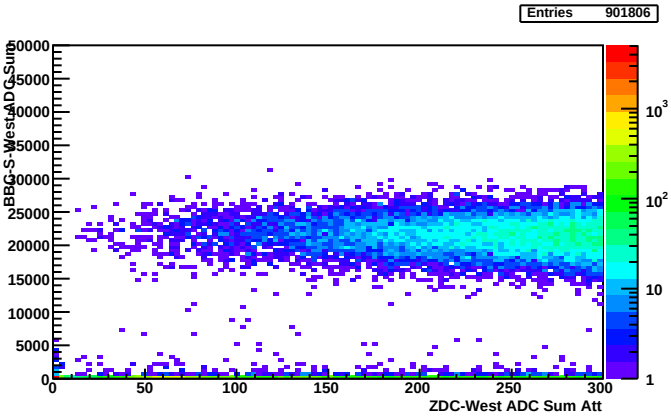
BBC-S-East ADC Sum
ZDC-East ADC Sum Att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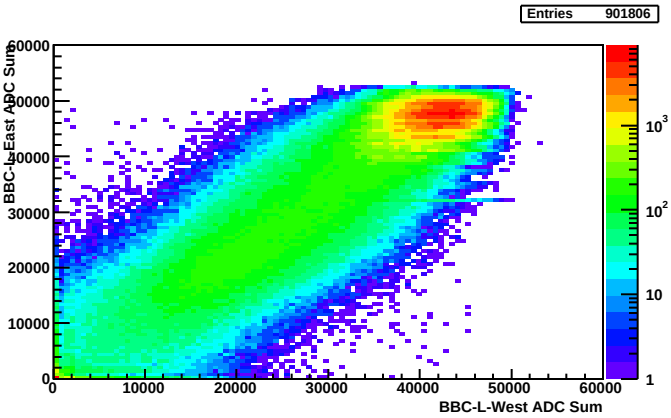
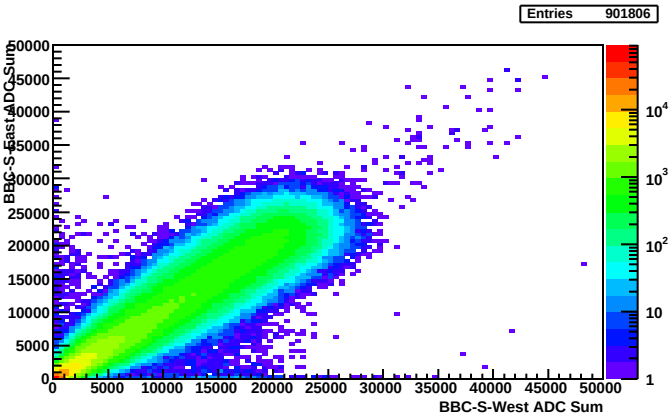
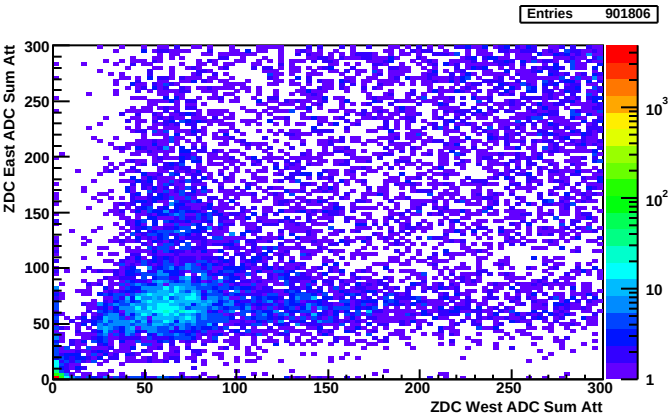
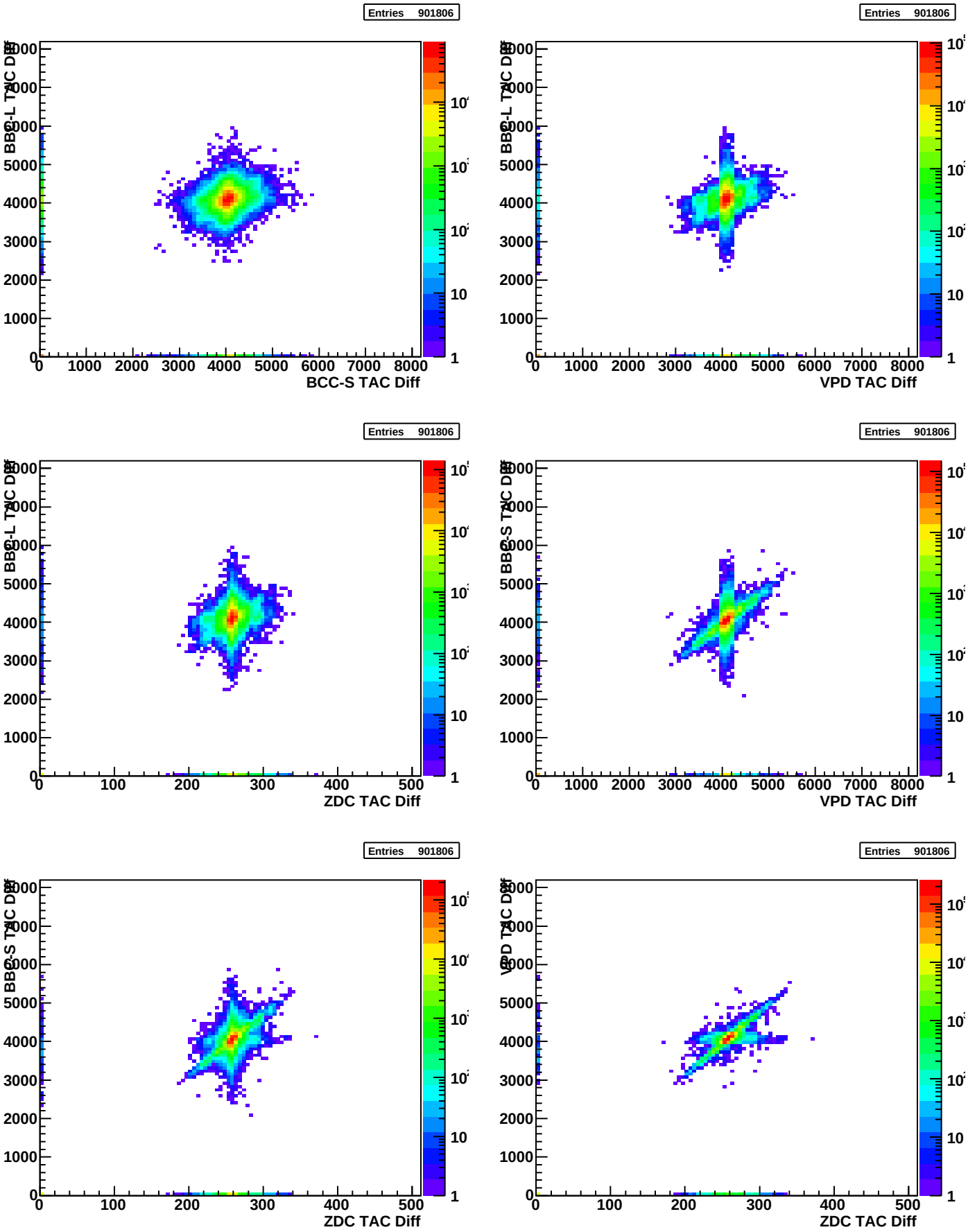
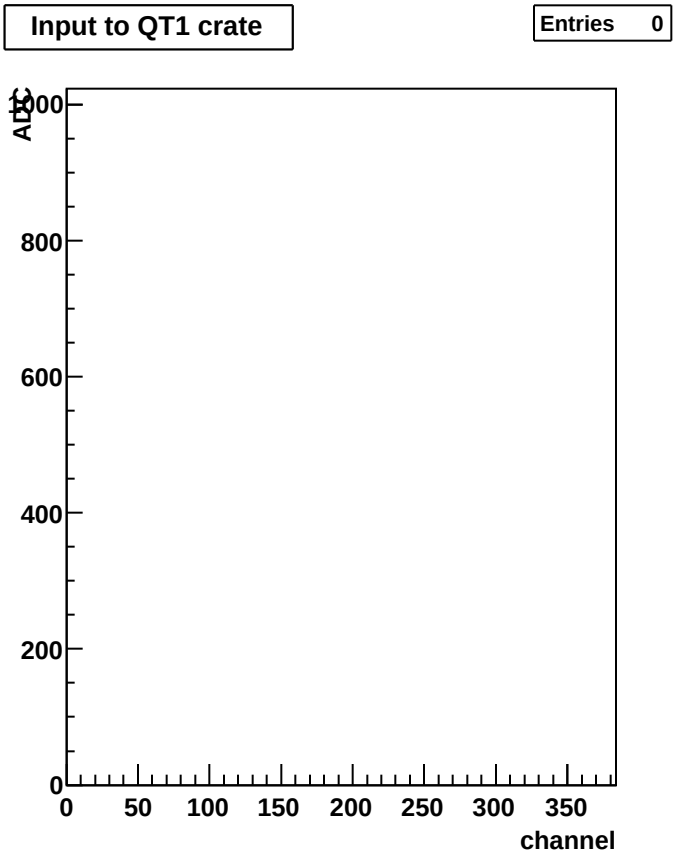
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

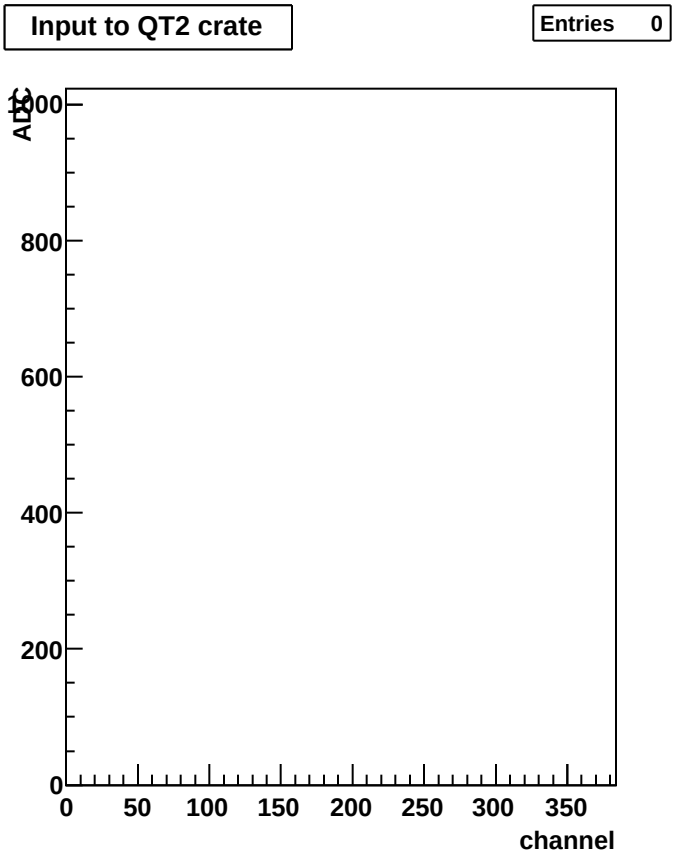

Entries 901806
BBC-L-East ADC Sum
BBC-L-West ADC Sum

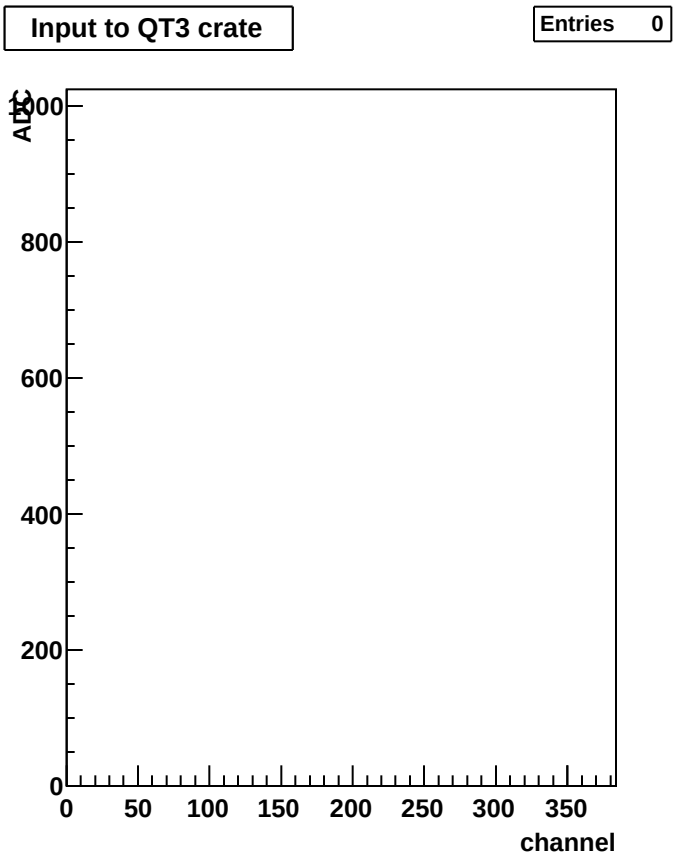

Entries 901806
BBC-S-East ADC Sum
BBC-S-West ADC Sum

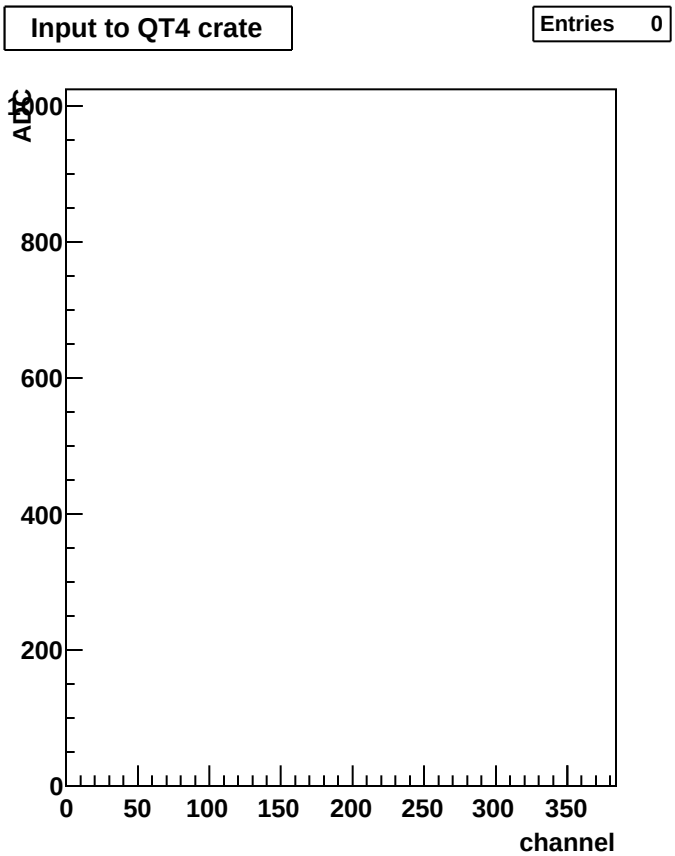

Entries 901806
ZDC East ADC Sum Att
ZDC West ADC Sum Att

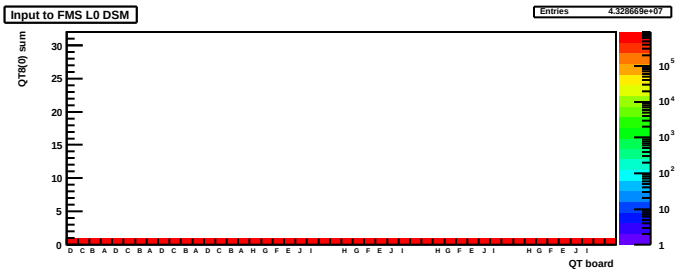

Entries 901806
BBC-L TAC Diff
BCC-S TAC Diff
Entries 901806
BBC-L TAC Diff
VPD TAC Diff
Entries 901806
BBC-L TAC Diff
ZDC TAC Diff
Entries 901806
BBC-S TAC Diff
VPD TAC Diff
Entries 901806
BBC-S TAC Diff
ZDC TAC Diff
Entries 901806
VPD TAC Diff
ZDC TAC Diff

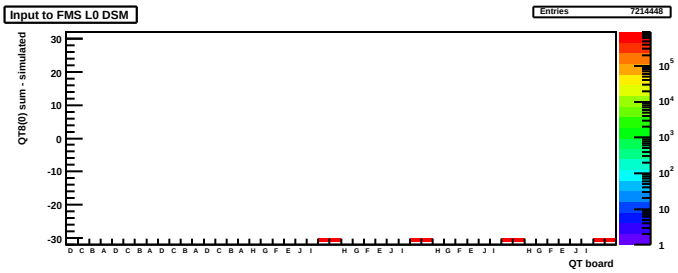

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

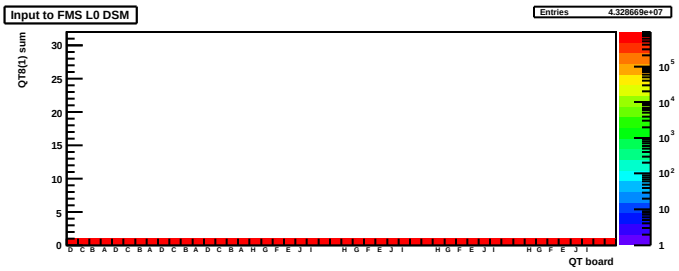

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

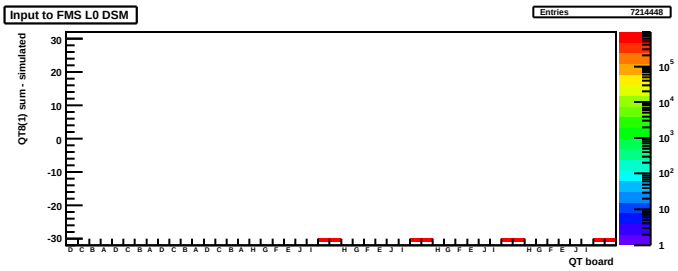

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

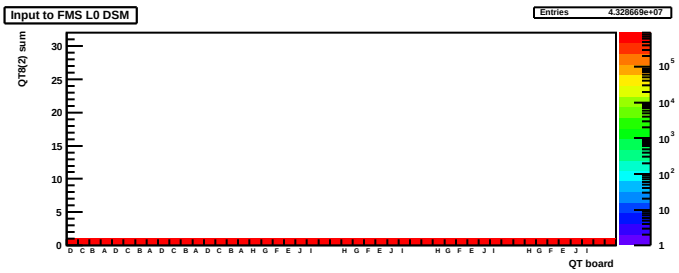

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

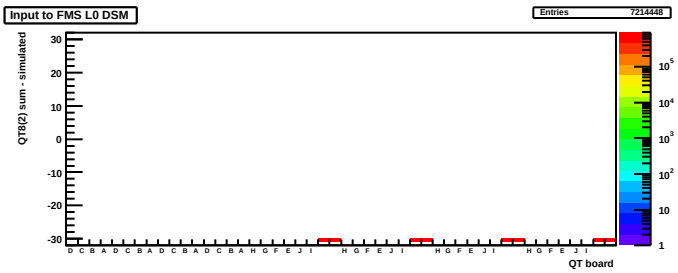

Input to FMS L0 DSM
Entries 4.328669e+07
QT8(0) sum
QT board

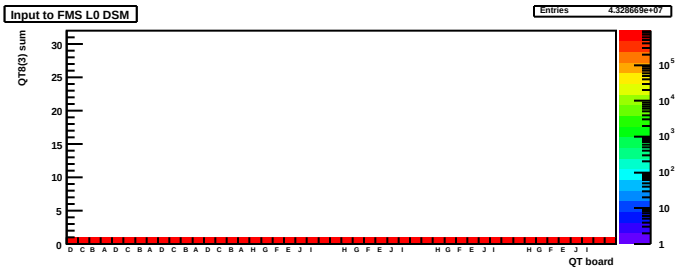

Input to FMS L0 DSM
Entries 7214448
QT8(0) sum - simulated
QT board

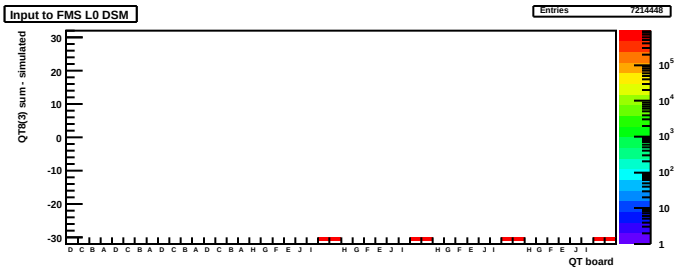

Input to FMS L0 DSM
Entries 4.328669e+07
QT8(1) sum
QT board

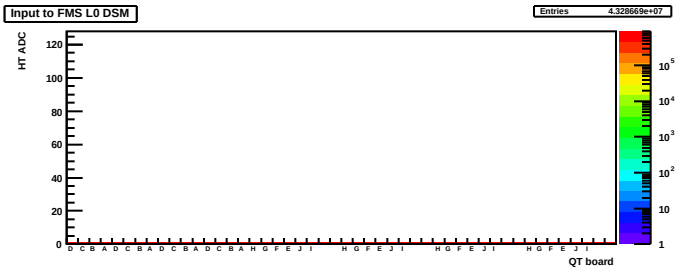

Input to FMS L0 DSM
Entries 7214448
QT8(1) sum - simulated
QT board

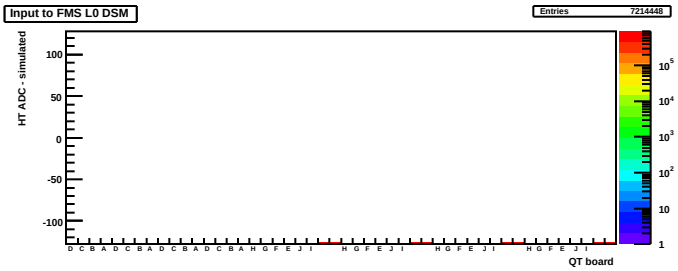

Input to FMS L0 DSM
Entries 4.328669e+07
QT8(2) sum
QT board

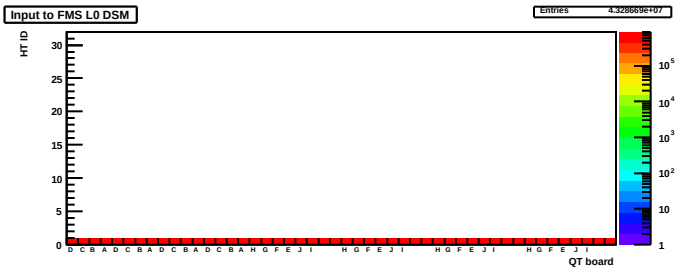

Input to FMS L0 DSM
Entries 7214448
QT8(2) sum - simulated
QT board

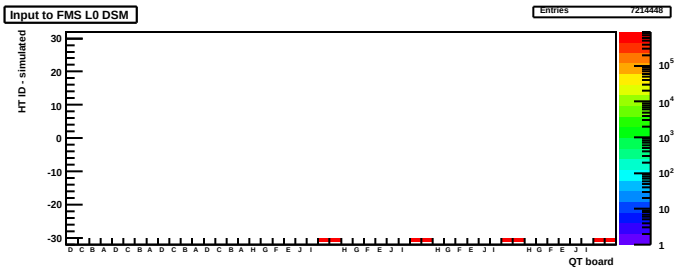

Input to FMS L0 DSM
Entries 4.328669e+07
QT8(3) sum
QT board

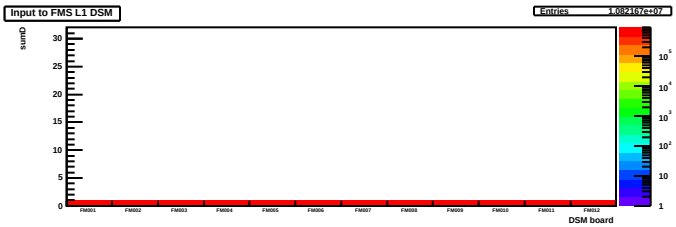

Input to FMS L0 DSM
Entries 7214448
QT8(3) sum - simulated
QT board

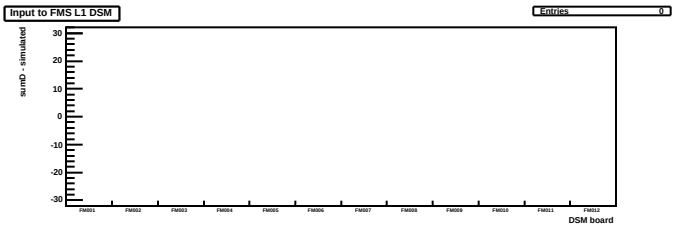

Input to FMS L0 DSM
Entries 4.328669e+07
HT ADC
QT board

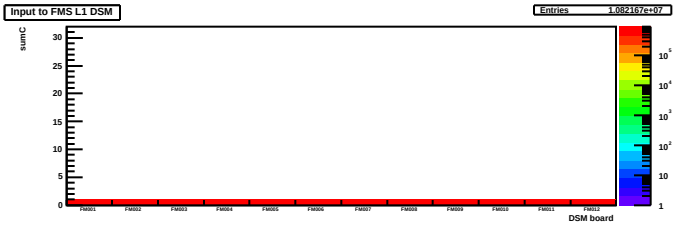

Input to FMS L0 DSM
Entries 7214448
HT ADC - simulated
QT board

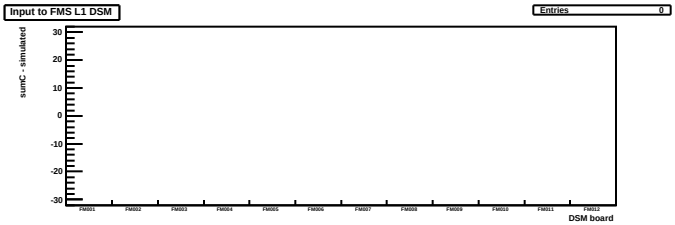

Input to FMS L0 DSM
Entries 4.328669e+07
HT ID
QT board

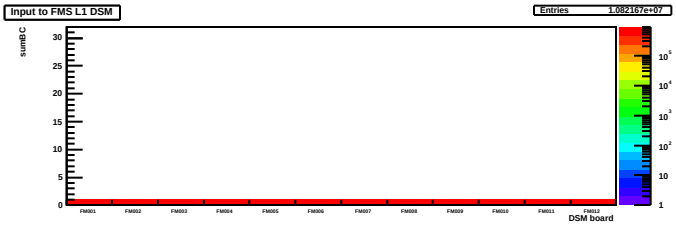

Input to FMS L0 DSM
Entries 7214448
HT ID - simulated
QT board

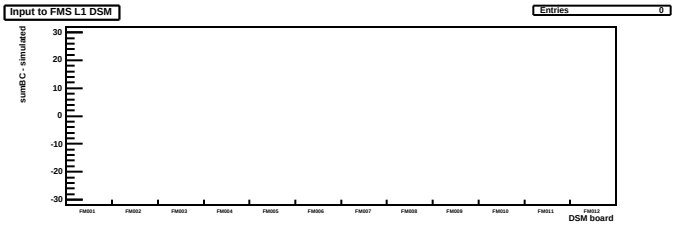

Input to FMS L1 DSM
Entries 1.082167e+07
sumD
DSM board

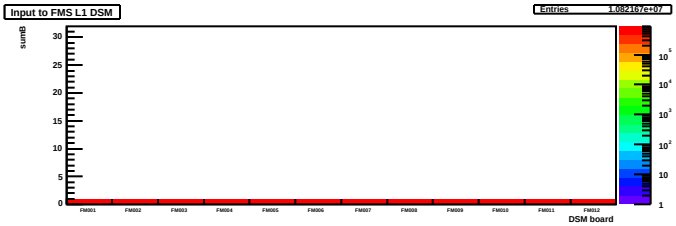

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

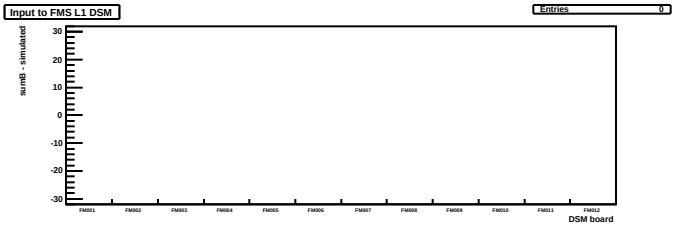

Input to FMS L1 DSM
Entries 1.082167e+07
sumC
DSM board

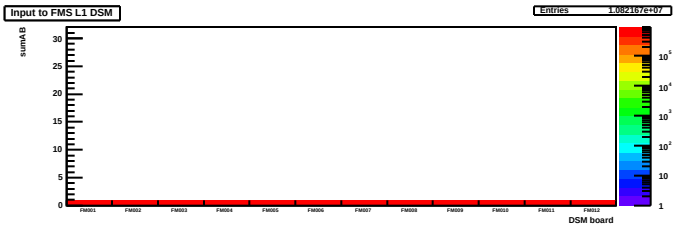

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

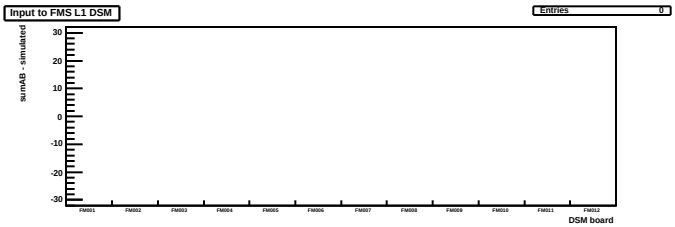

Input to FMS L1 DSM
Entries 1.082167e+07
sumBC
DSM board

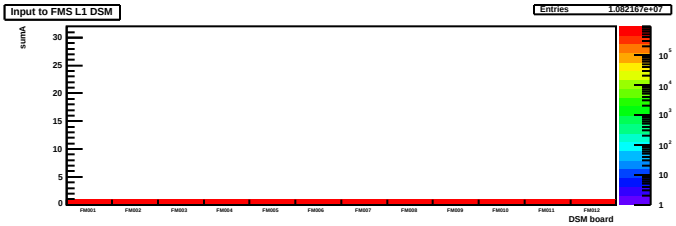

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

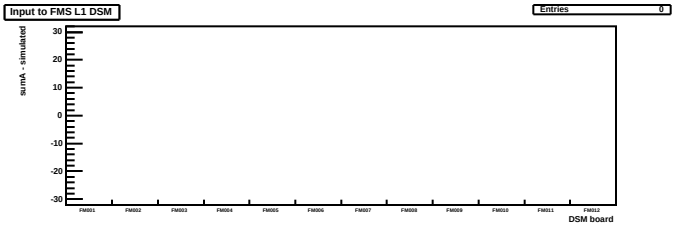

Input to FMS L1 DSM
Entries 1.082167e+07
sumB
DSM board

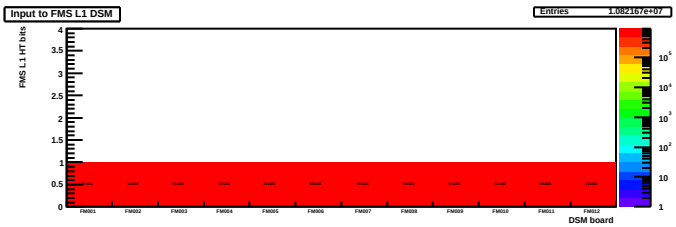

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

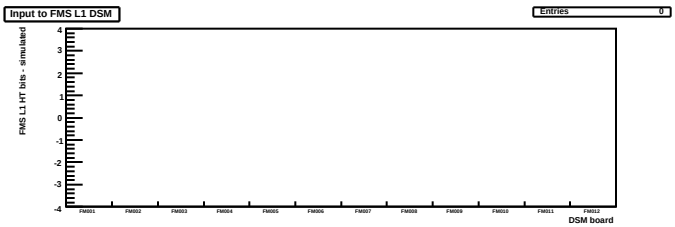

Input to FMS L1 DSM
Entries 1.082167e+07
sumAB
DSM board

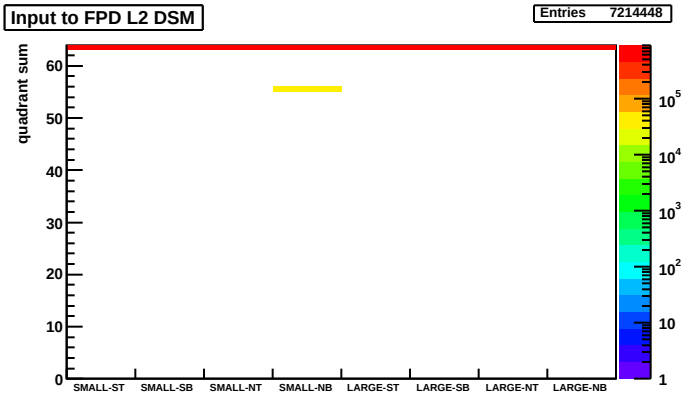

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

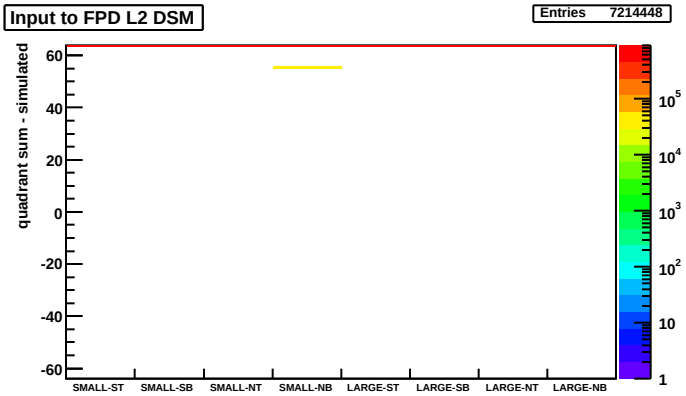

Input to FMS L1 DSM
Entries 1.082167e+07
sumA
DSM board

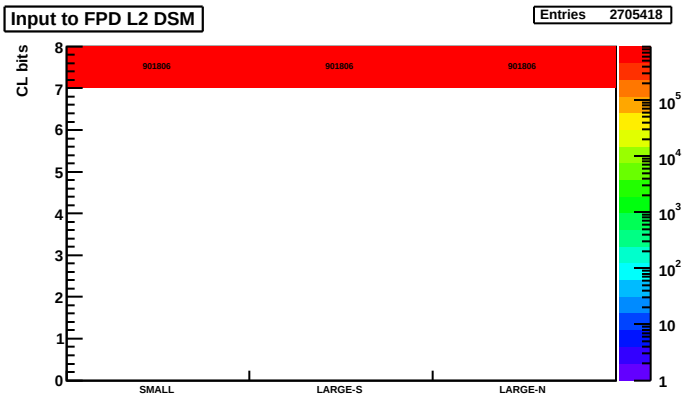

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

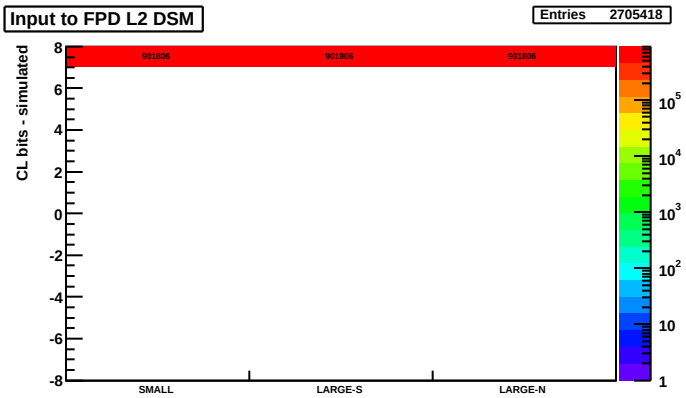

Input to FMS L1 DSM
Entries 1.082167e+07
FMS L1 HT bits
DSM board

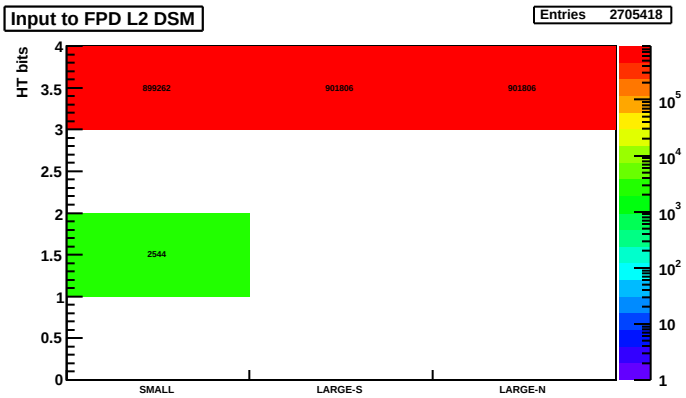

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

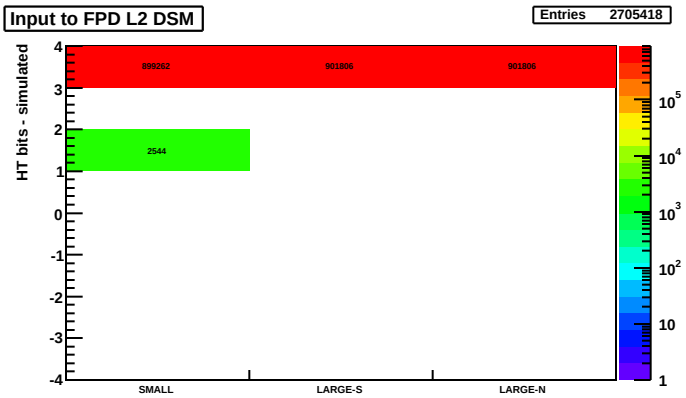

Input to FPD L2 DSM
Entries 7214448
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

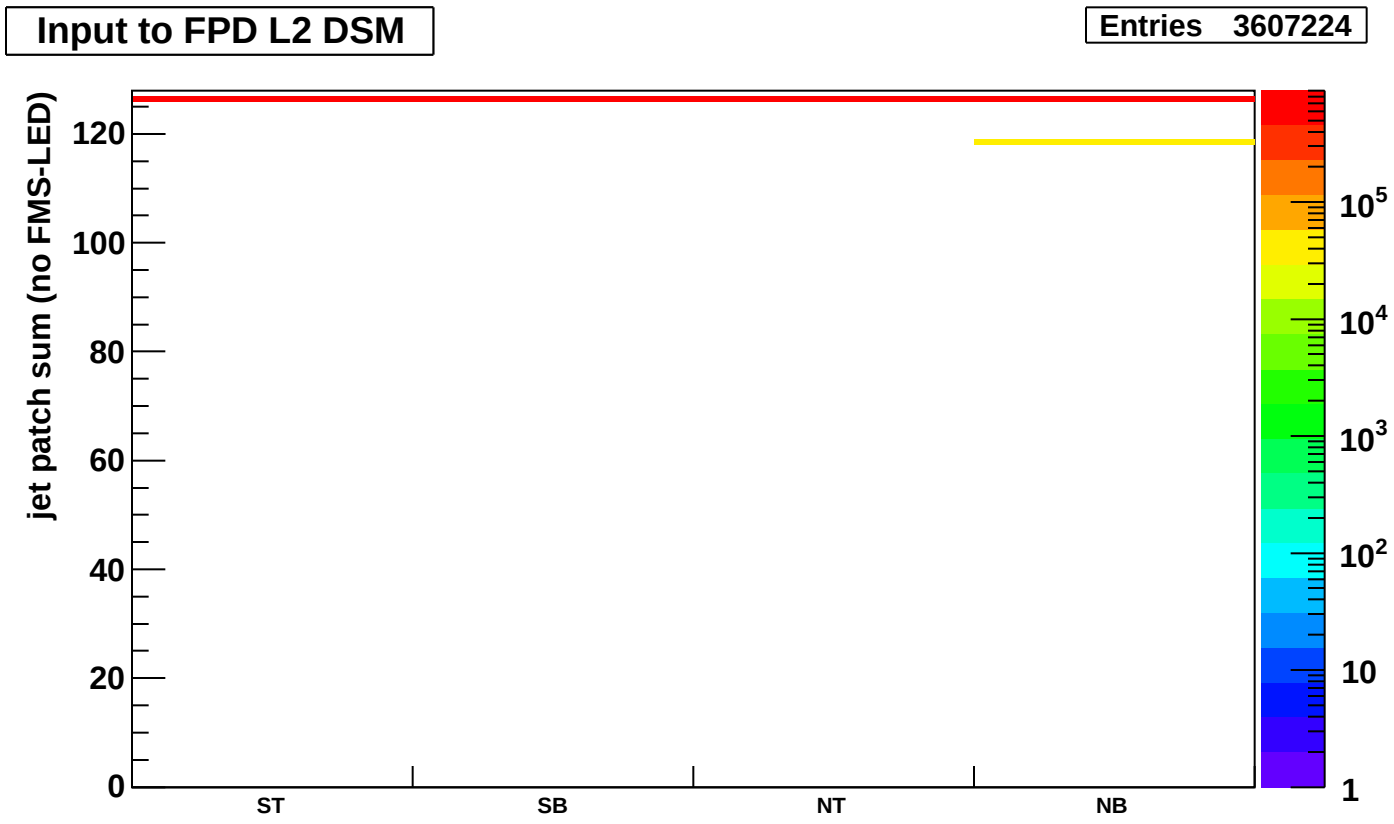

Input to FPD L2 DSM
Entries 7214448
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

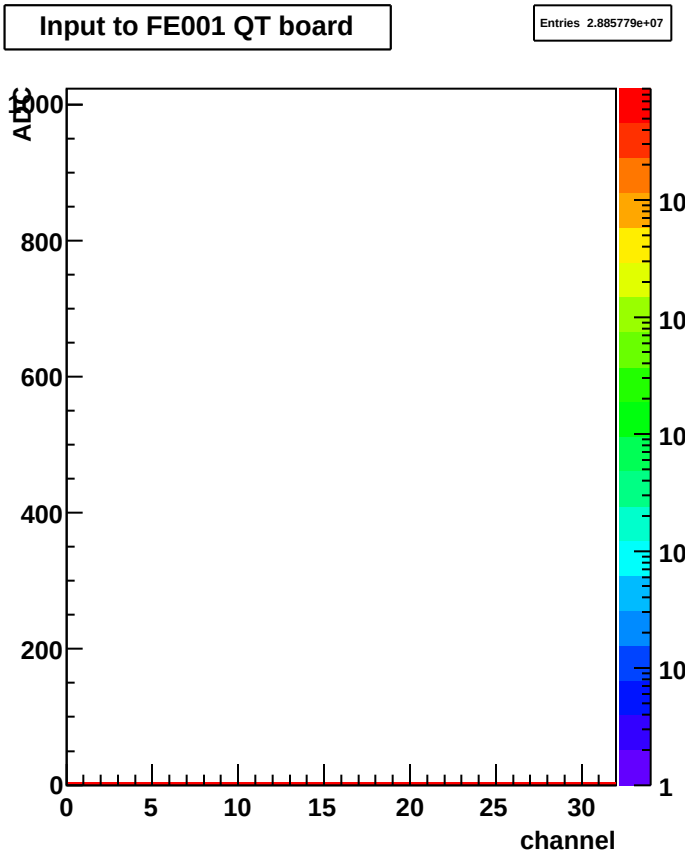

Input to FPD L2 DSM
Entries 2705418
CL bits
901806
901806
901806
SMALL
LARGE-S
LARGE-N

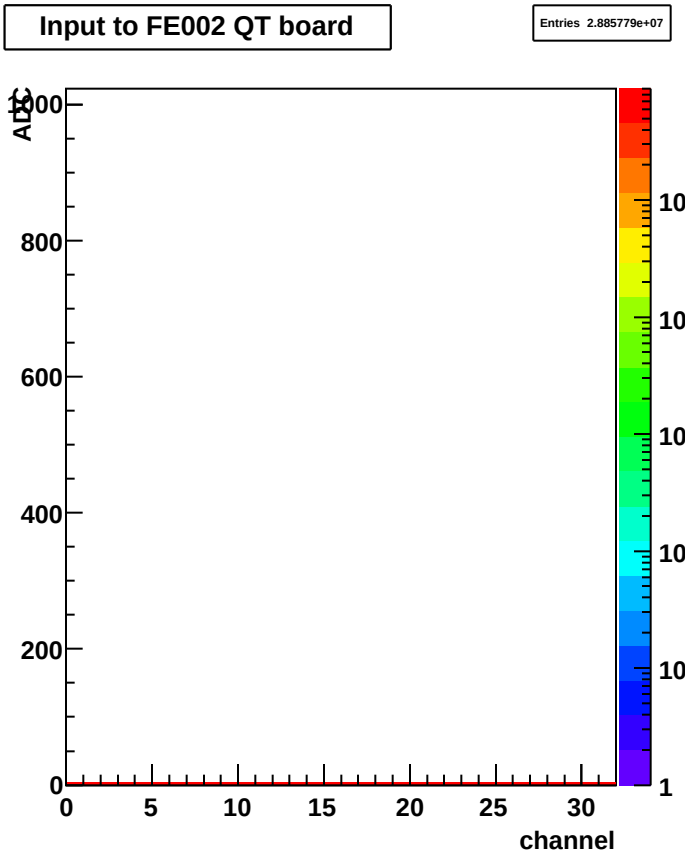

Input to FPD L2 DSM
Entries 2705418
CL bits - simulated
901806
901806
901806
SMALL
LARGE-S
LARGE-N

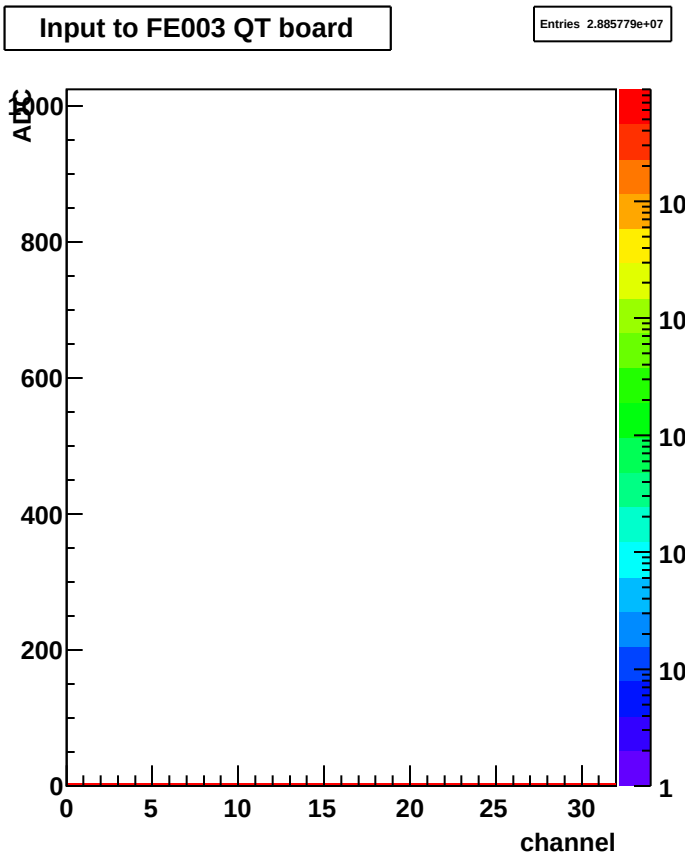

Input to FPD L2 DSM
Entries 2705418
HT bits
899262
901806
901806
2544
SMALL
LARGE-S
LARGE-N

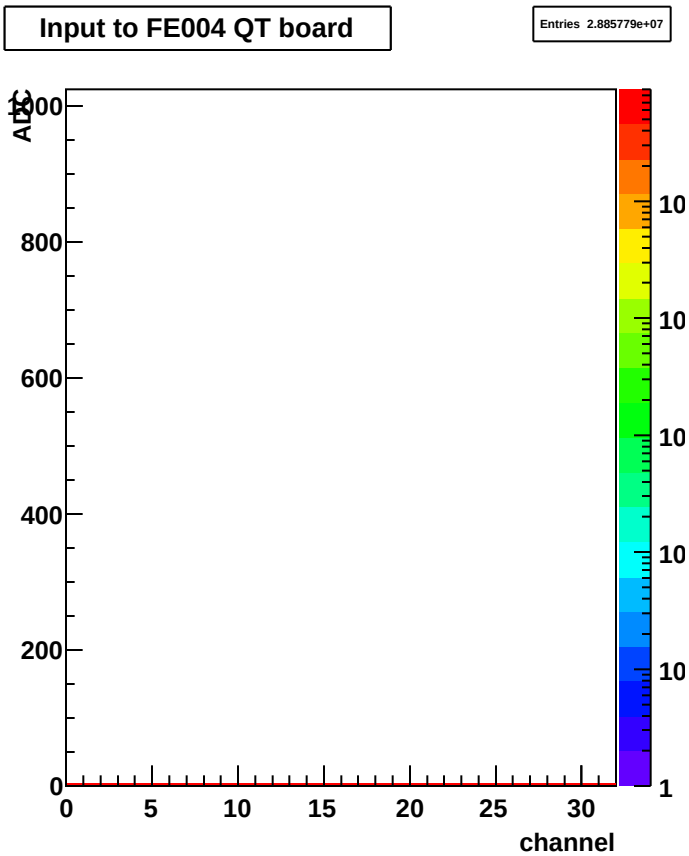

Input to FPD L2 DSM
Entries 2705418
HT bits - simulated
899262
901806
901806
2544
SMALL
LARGE-S
LARGE-N


Input to FPD L2 DSM
Entries 3607224
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

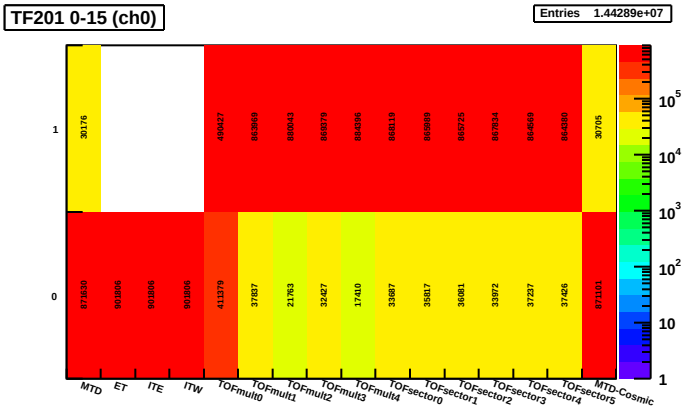

Input to FE001 QT board
Entries 2.885779e+07
ADC
channel


Input to FE002 QT board
Entries 2.885779e+07
ADC
channel

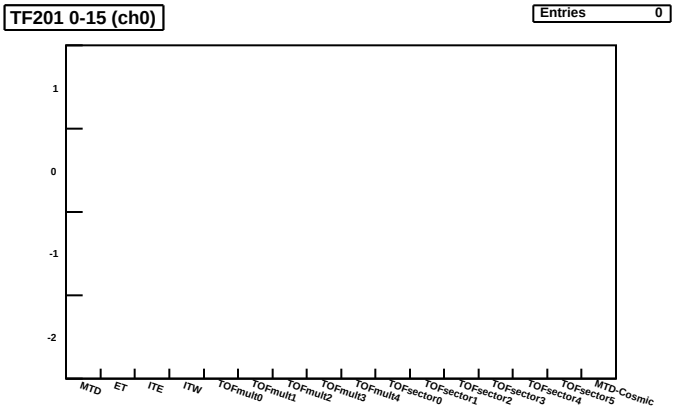

Input to FE003 QT board
Entries 2.885779e+07
ADC
channel


Input to FE004 QT board
Entries 2.885779e+07
ADC
channel

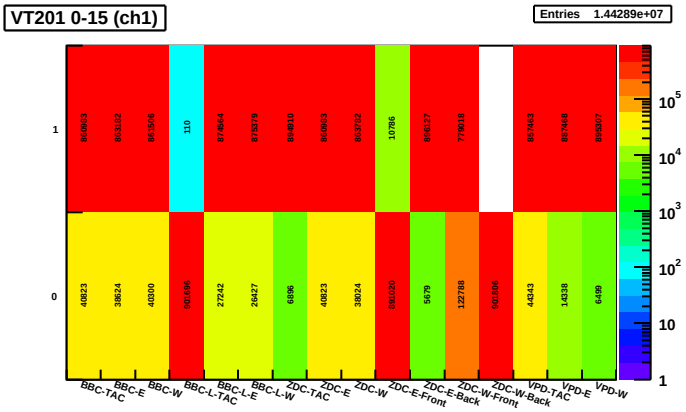
TF201 0-15 (ch0)



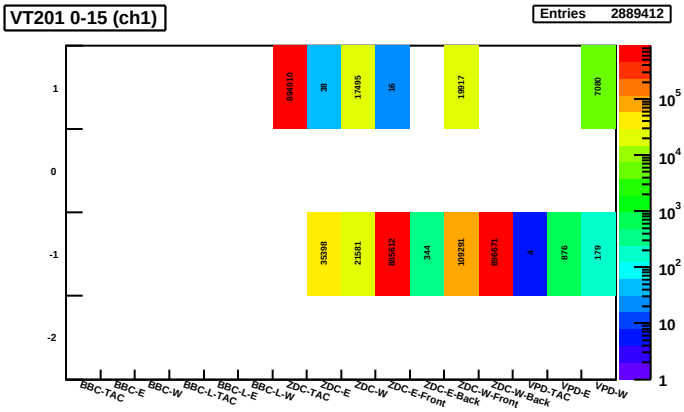
TF201 0-15 (ch0)



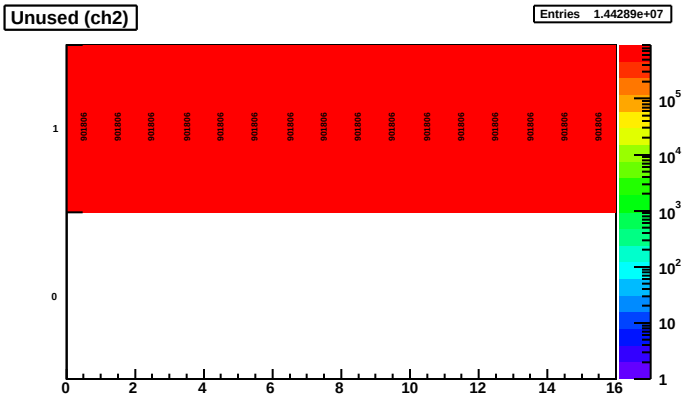
VT201 0-15 (ch1)

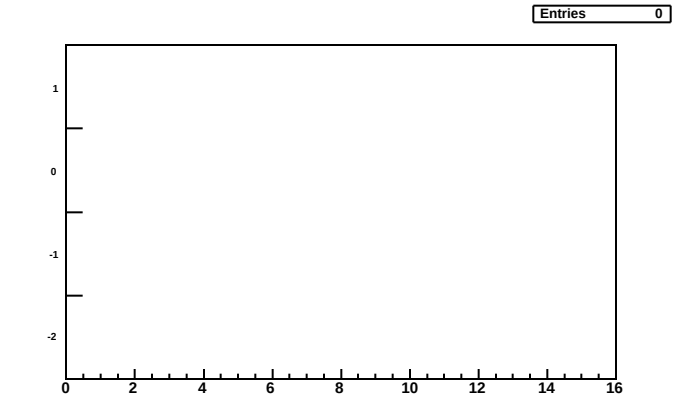


VT201 0-15 (ch1)

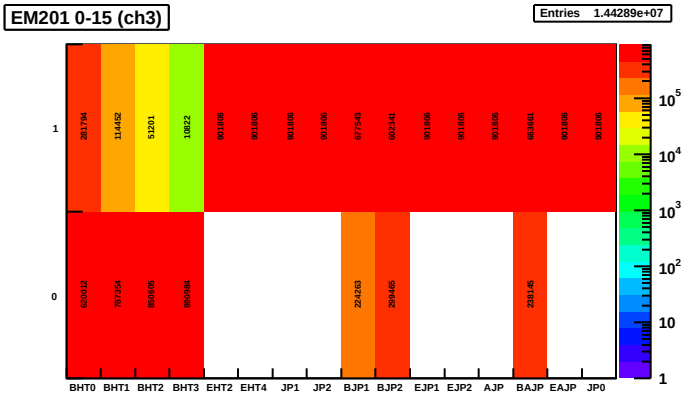


Unused (ch2)

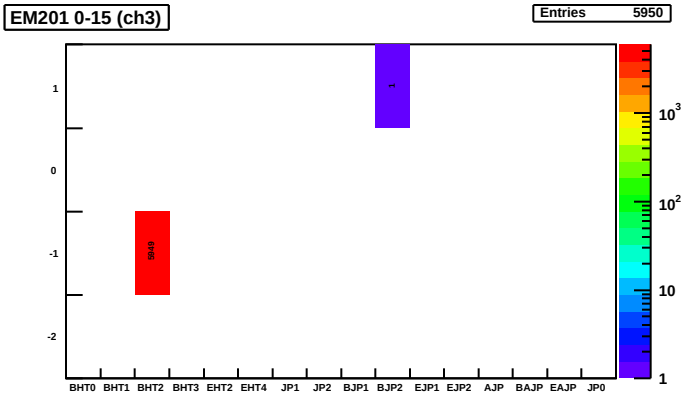




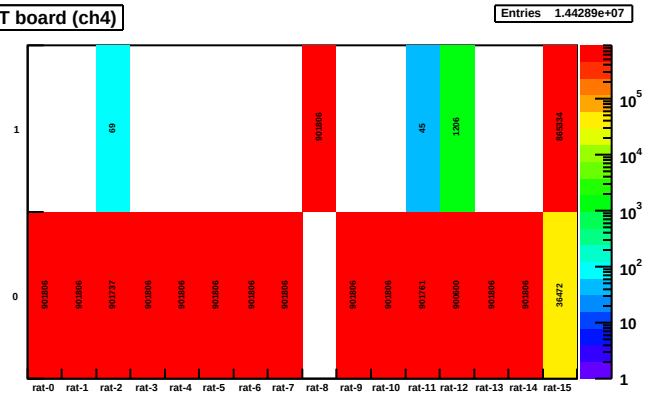
EM201 0-15 (ch3)

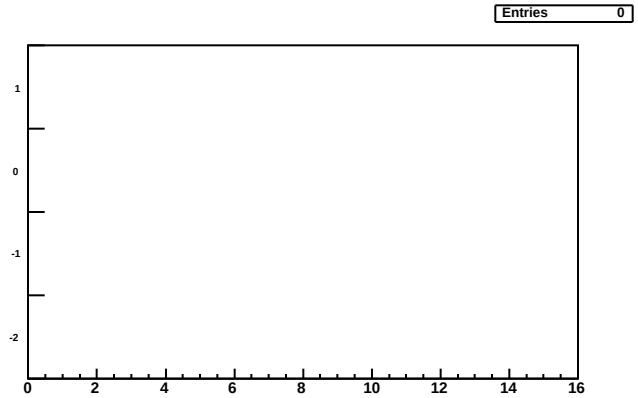


EM201 0-15 (ch3)

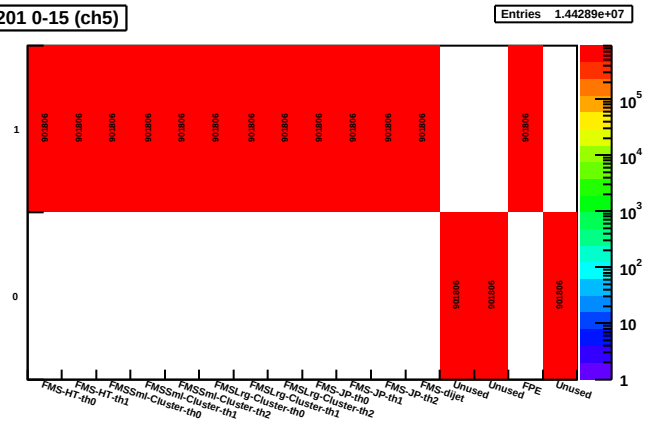


RAT board (ch4)

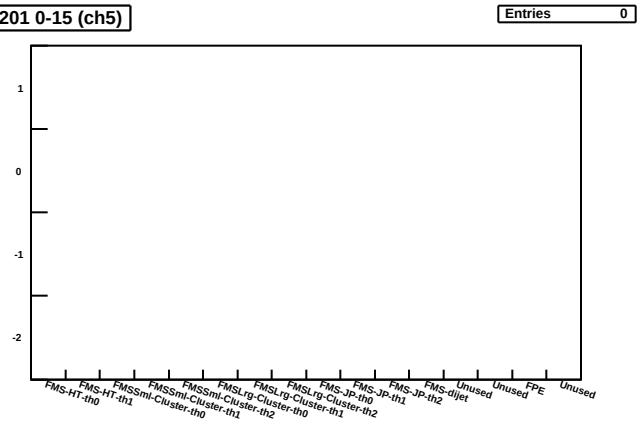




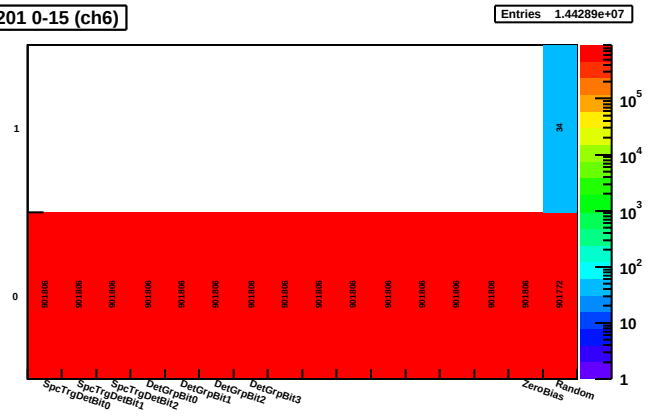
FP201 0-15 (ch5)

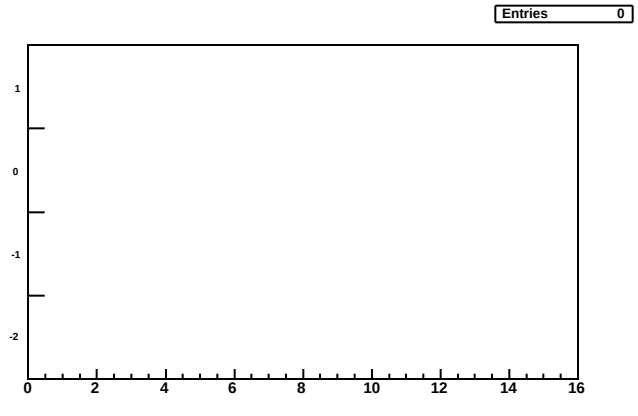
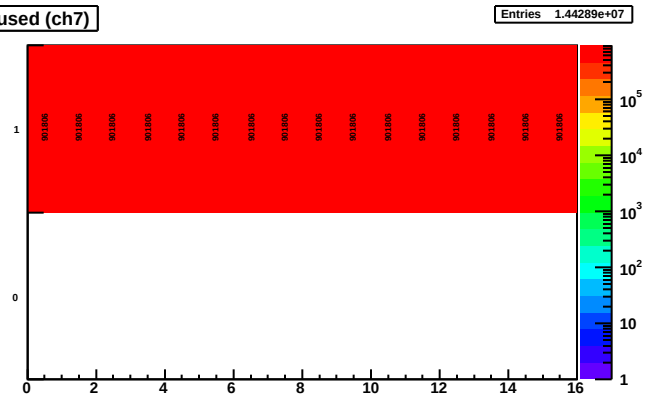
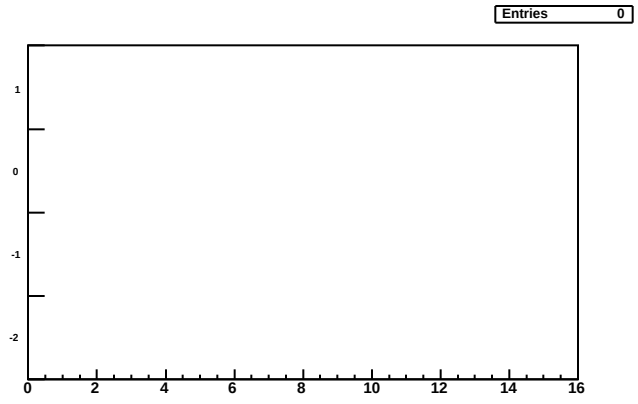


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)