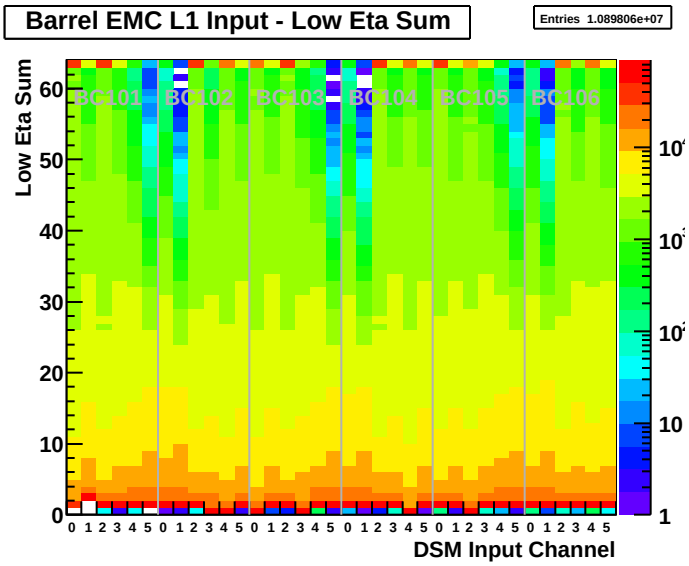
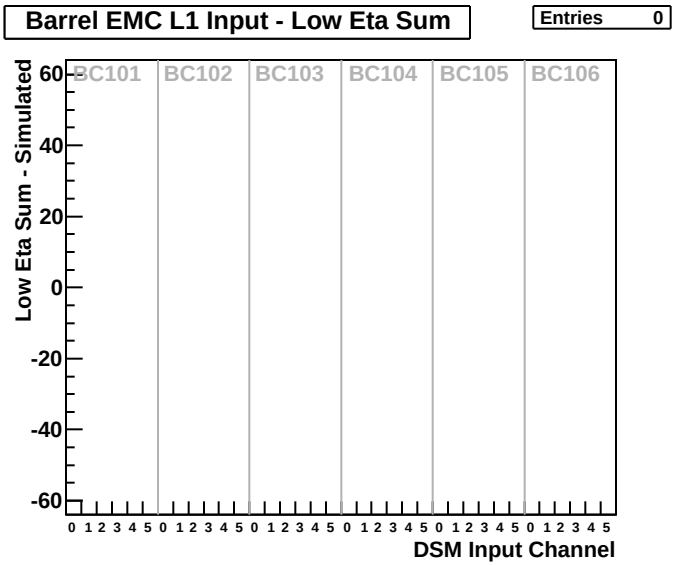
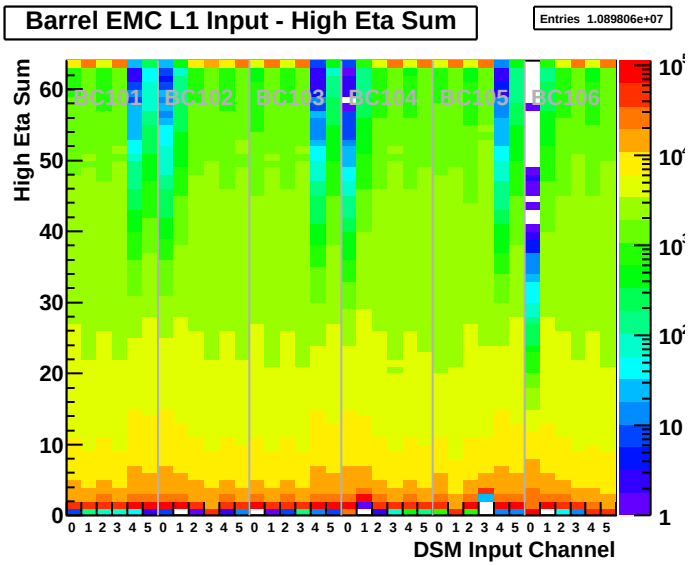
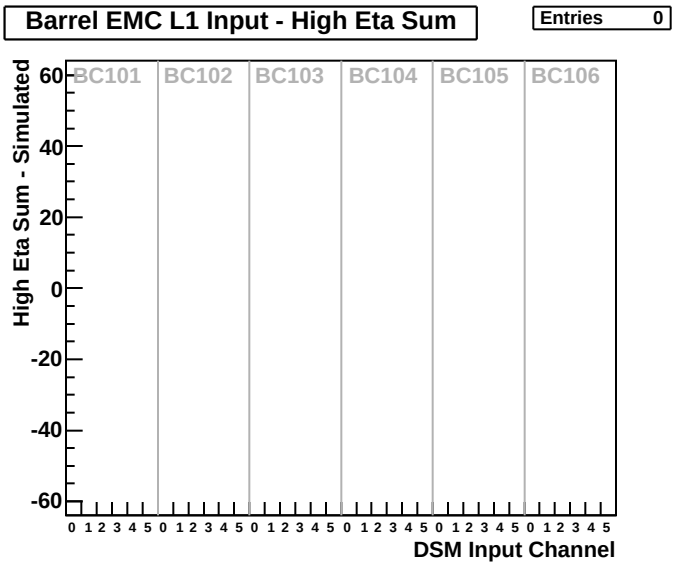
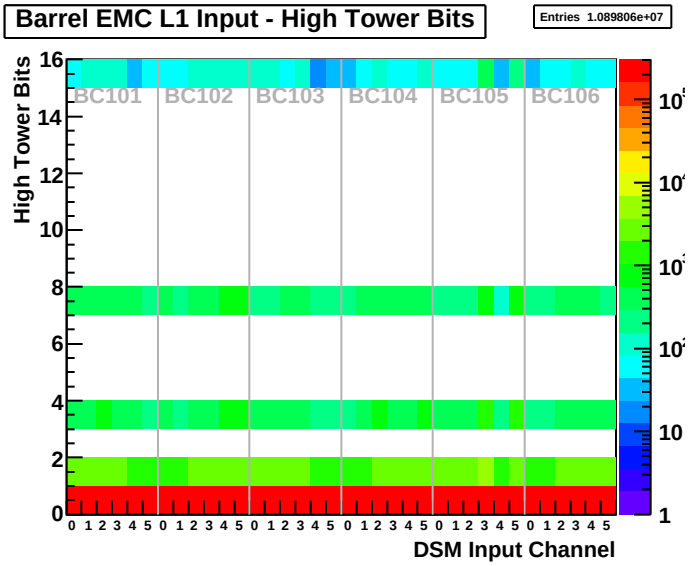
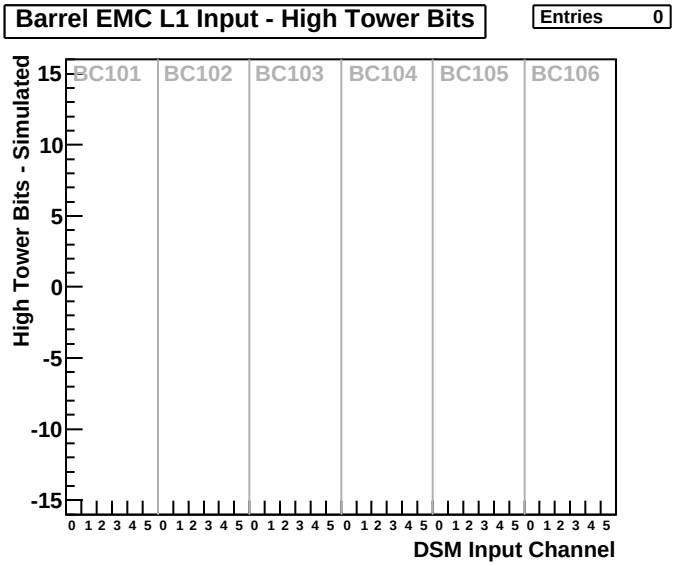

Barrel EMC L1 Input - Low Eta Sum
Entries 1.089806e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

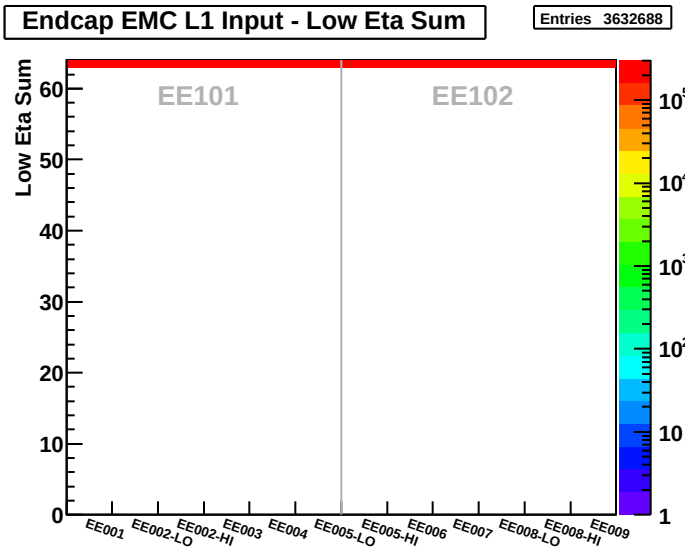

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

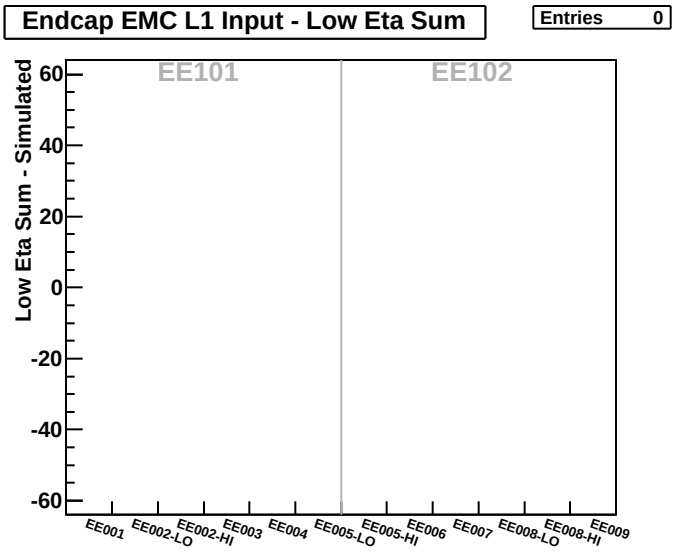

Barrel EMC L1 Input - High Eta Sum
Entries 1.089806e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

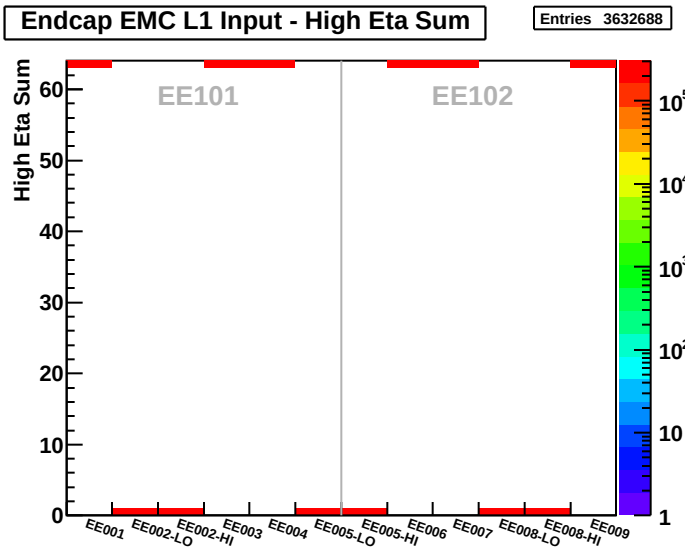

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

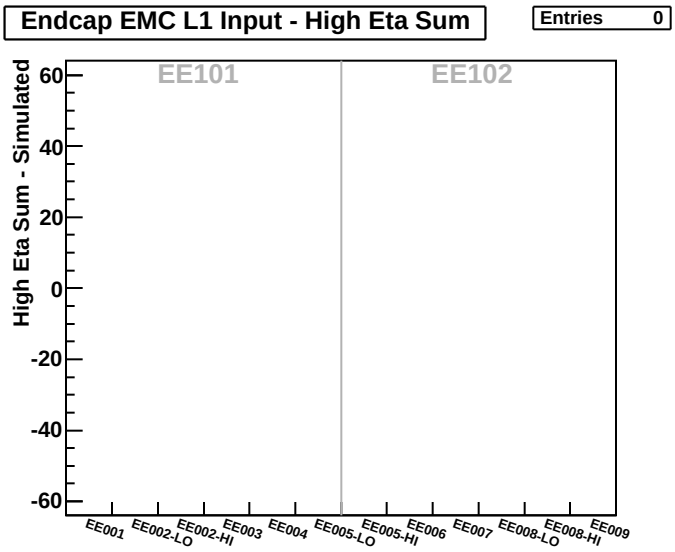

Barrel EMC L1 Input - High Tower Bits
Entries 1.089806e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

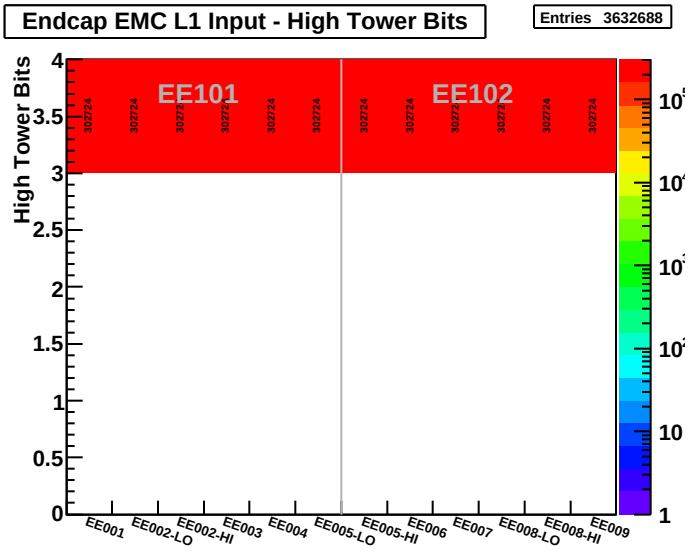

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

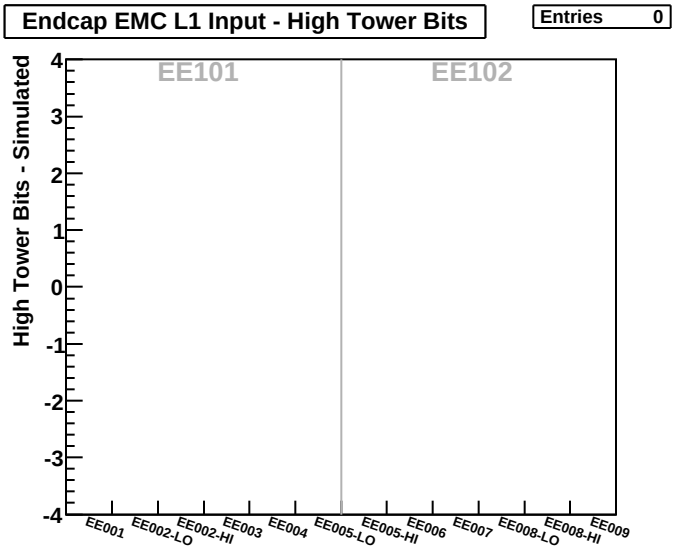

Endcap EMC L1 Input - Low Eta Sum
Entries 3632688
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

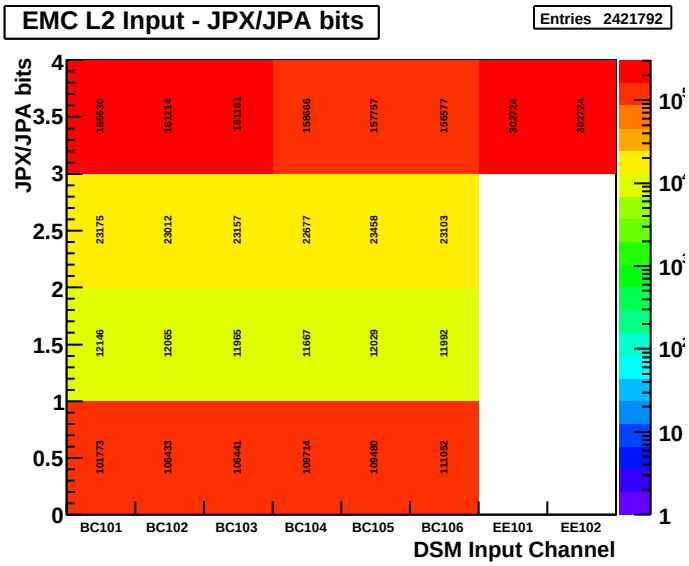

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

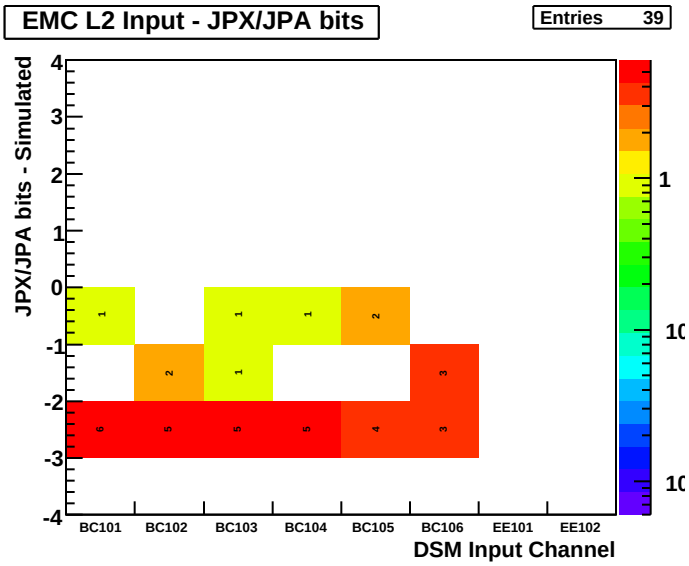

Endcap EMC L1 Input - High Eta Sum
Entries 3632688
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

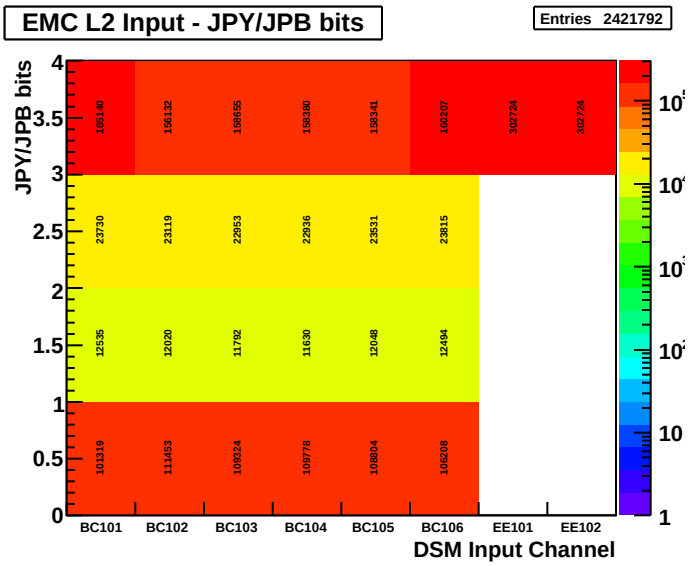

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

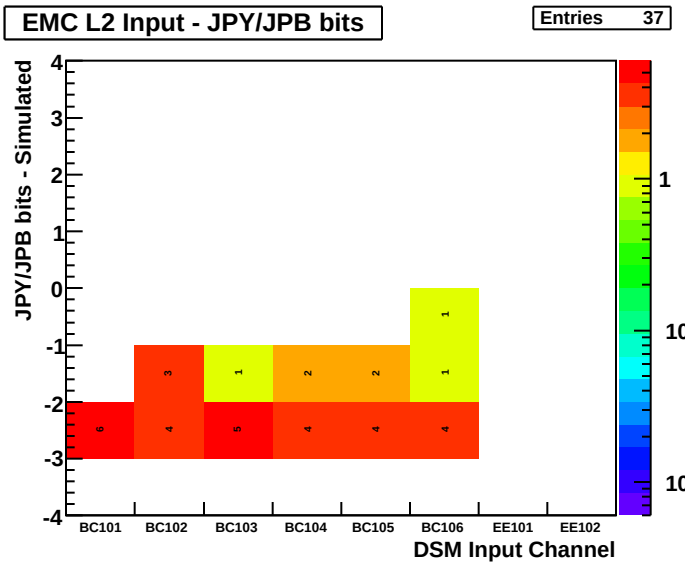

Endcap EMC L1 Input - High Tower Bits
Entries 3632688
High Tower Bits
EE101
EE102
302724
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

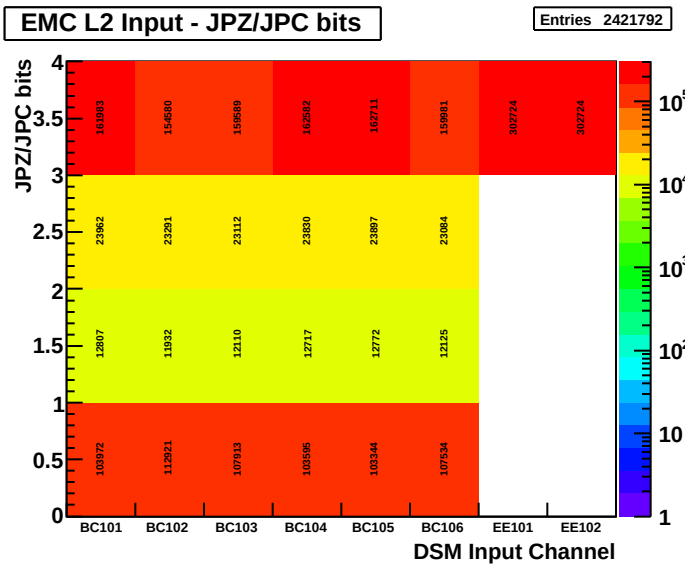

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

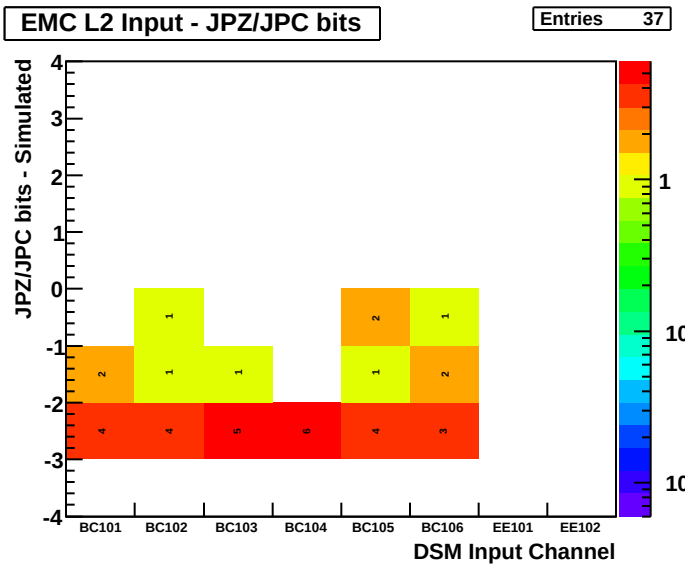

EMC L2 Input - JPX/JPA bits
Entries 2421792
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
165630 161214 161161 158666 157757 156877 302724 302724
23175 23012 23157 22677 23458 23103
12146 12065 11965 11667 12029 11992
101773 106433 106441 109714 109480 111052

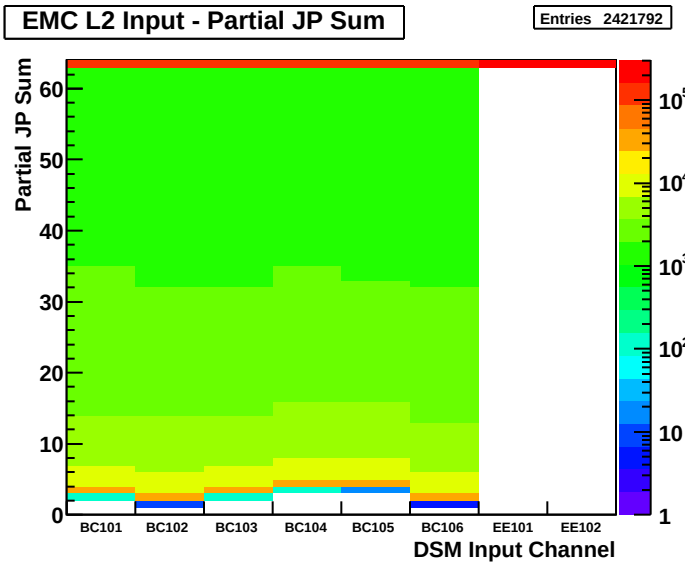

EMC L2 Input - JPX/JPA bits
Entries 39
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

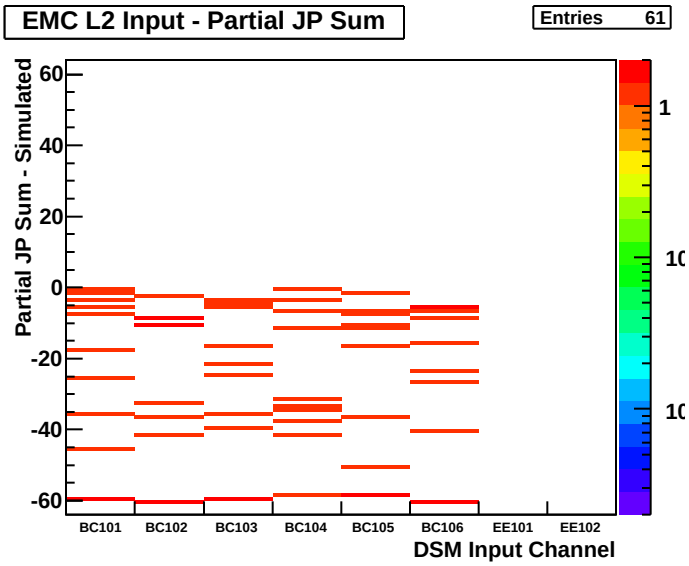

EMC L2 Input - JPY/JPB bits
Entries 2421792
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
165140 156132 158655 158380 159341 160207 302724 302724
23730 23119 22953 22936 23531 23815
12535 12020 11792 11630 12048 12494
103319 111453 109324 109778 108804 106208

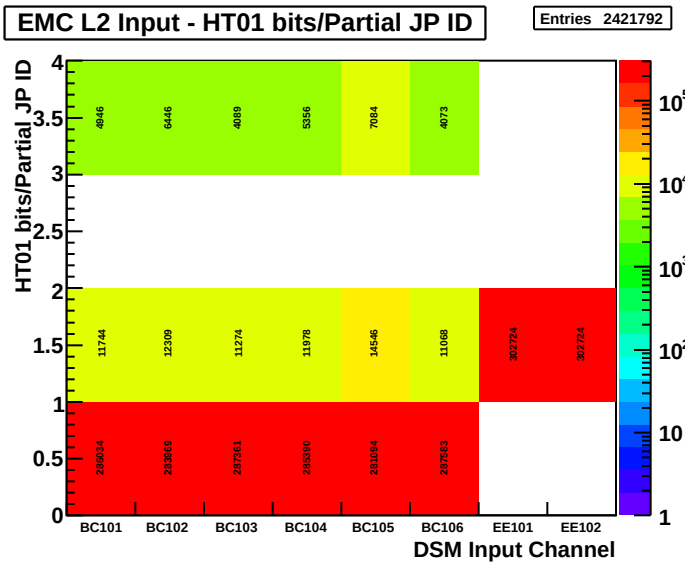

EMC L2 Input - JPY/JPB bits
Entries 37
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

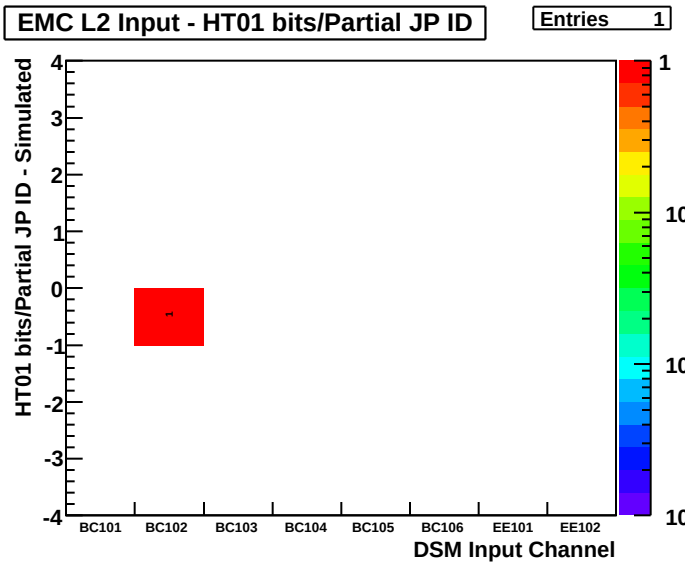

EMC L2 Input - JPZ/JPC bits
Entries 2421792
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
161963 154580 159589 162582 162711 159981 302724 302724
23962 23291 23112 23830 23897 23084
12807 11932 12110 12717 12772 12125
103972 112921 107913 103595 103344 107534

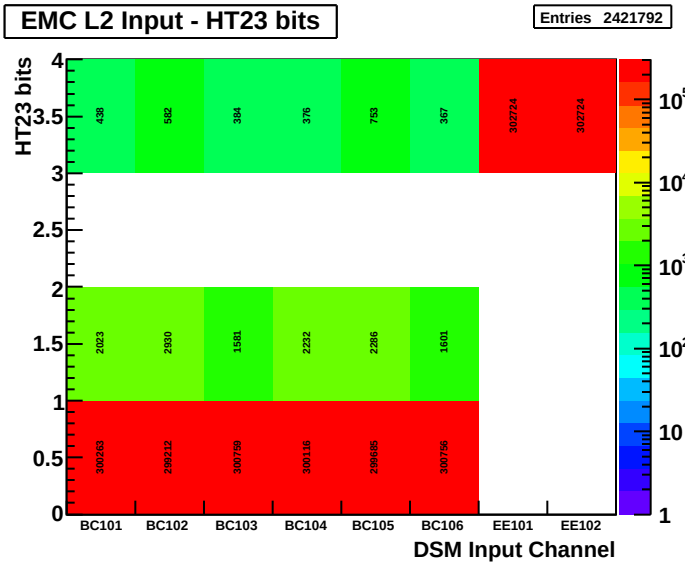

EMC L2 Input - JPZ/JPC bits
Entries 37
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

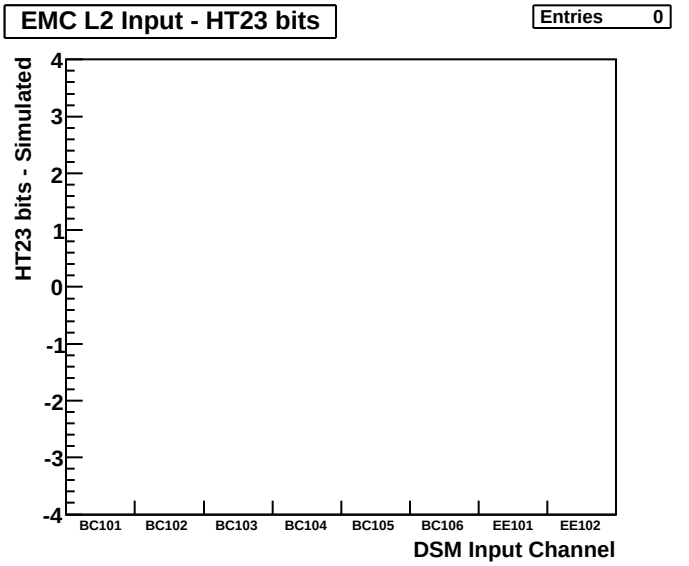

EMC L2 Input - Partial JP Sum
Entries 2421792
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

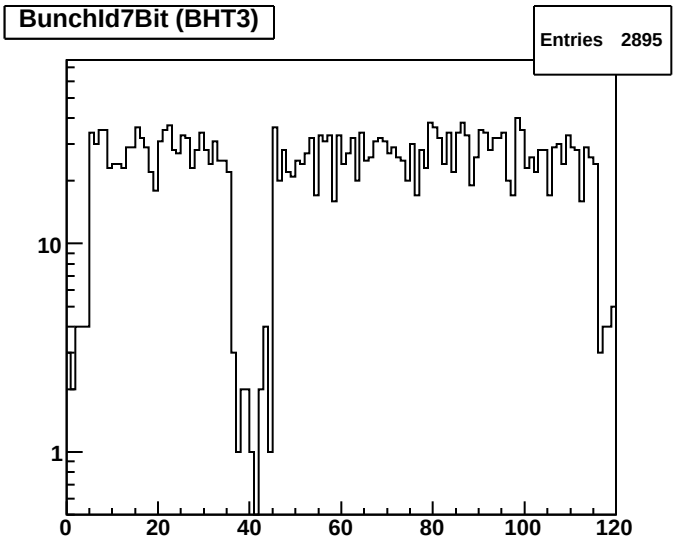

EMC L2 Input - Partial JP Sum
Entries 61
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

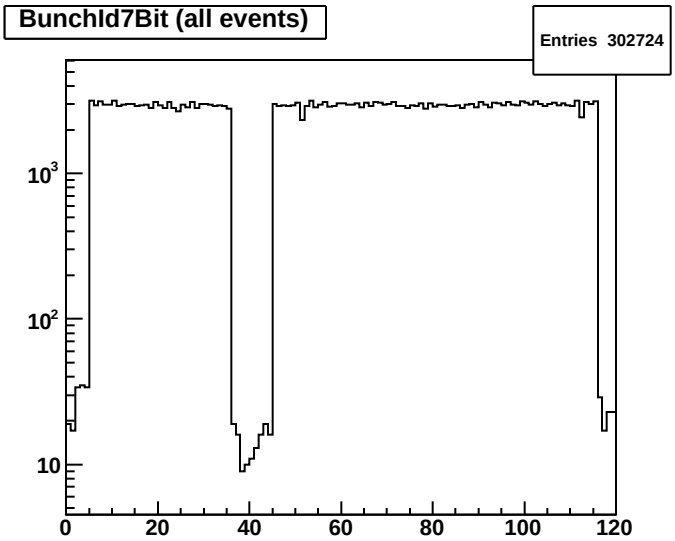

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2421792
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

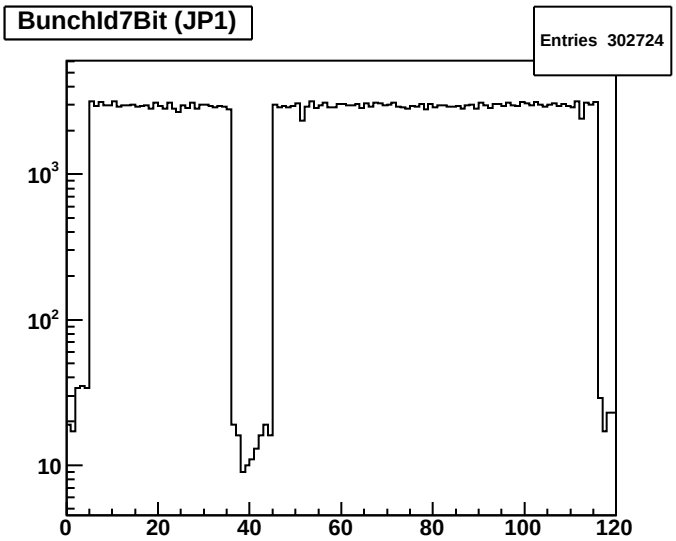

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

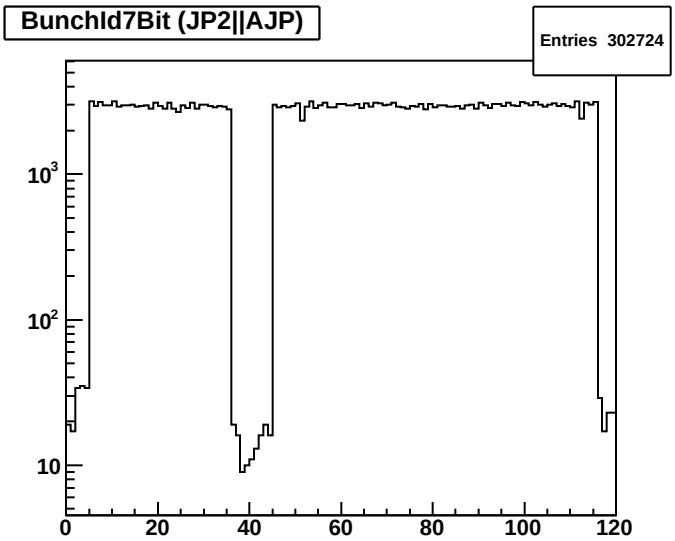

EMC L2 Input - HT23 bits
Entries 2421792
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

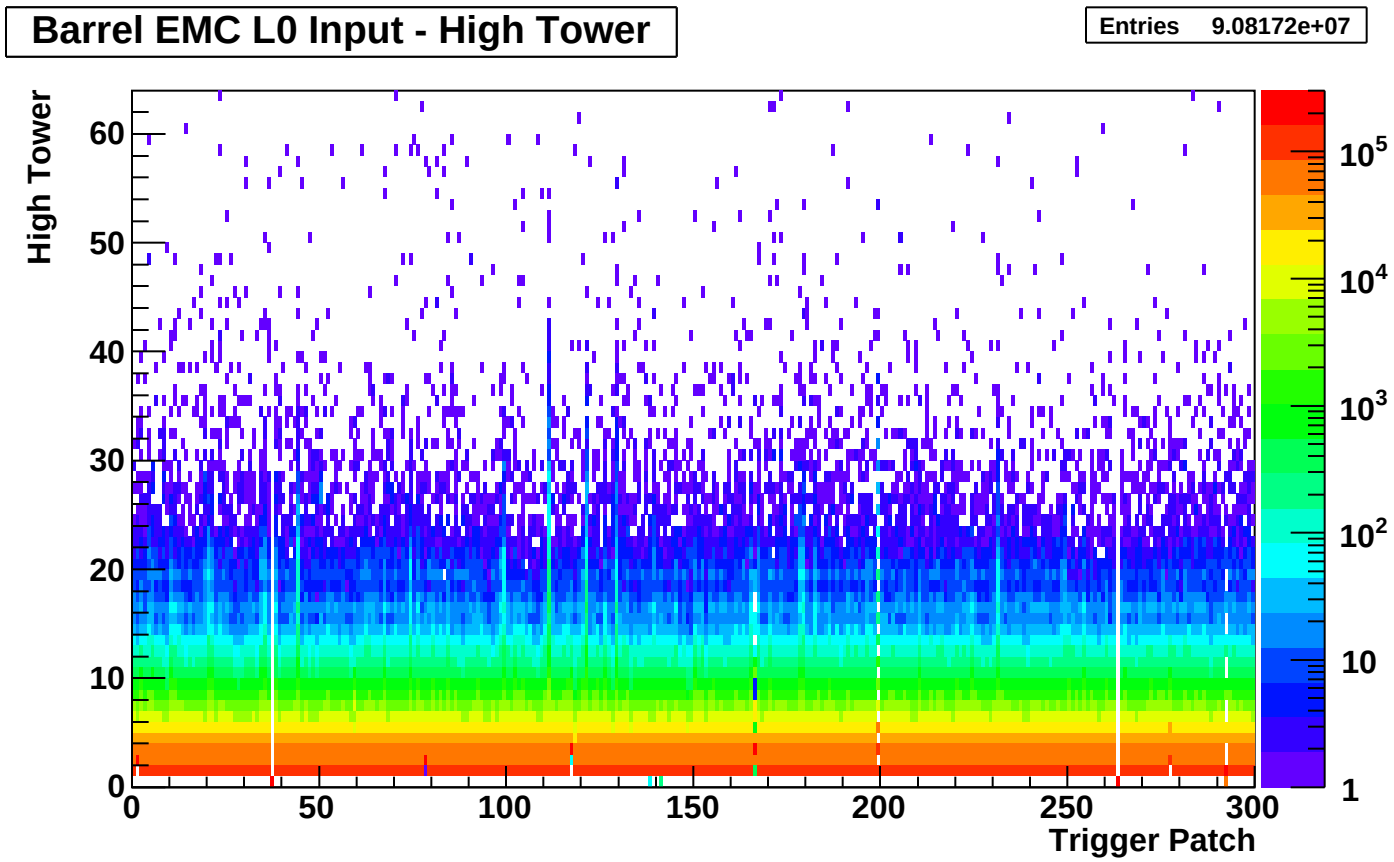

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

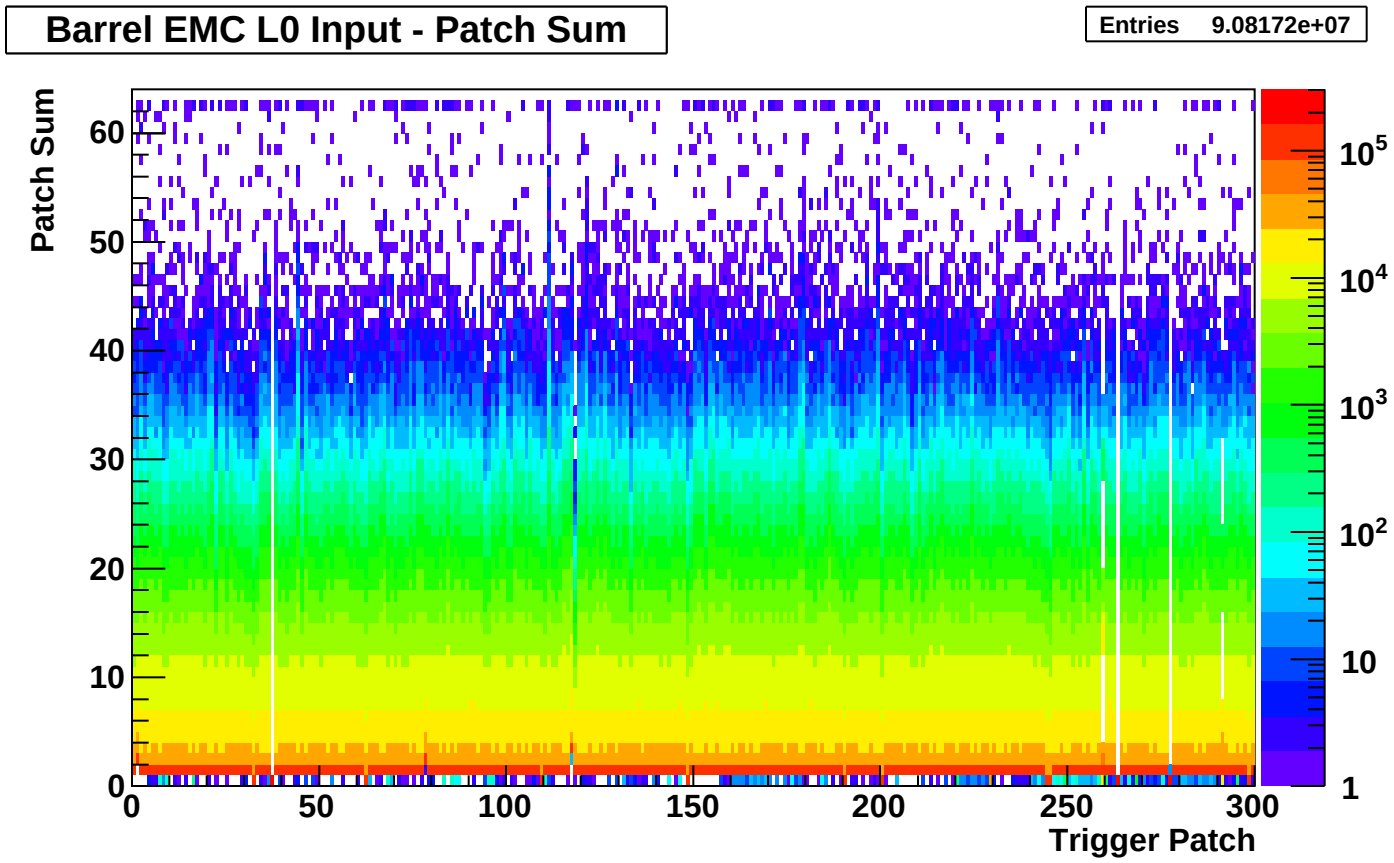

BunchId7Bit (BHT3)
Entries 2895
10
1
0
20
40
60
80
100
120

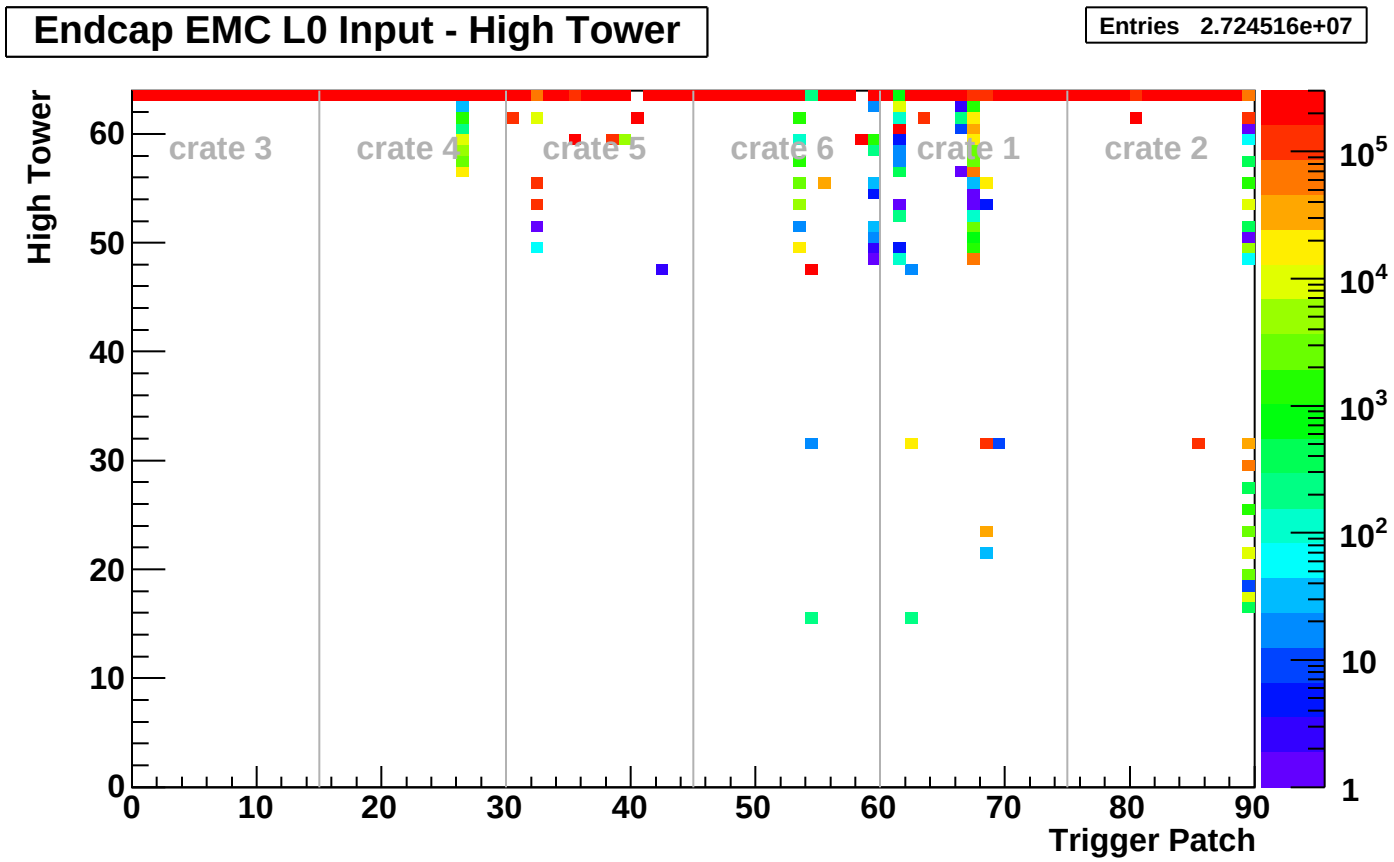

BunchId7Bit (all events)
Entries 302724
10^3
10^2
10
0
20
40
60
80
100
120

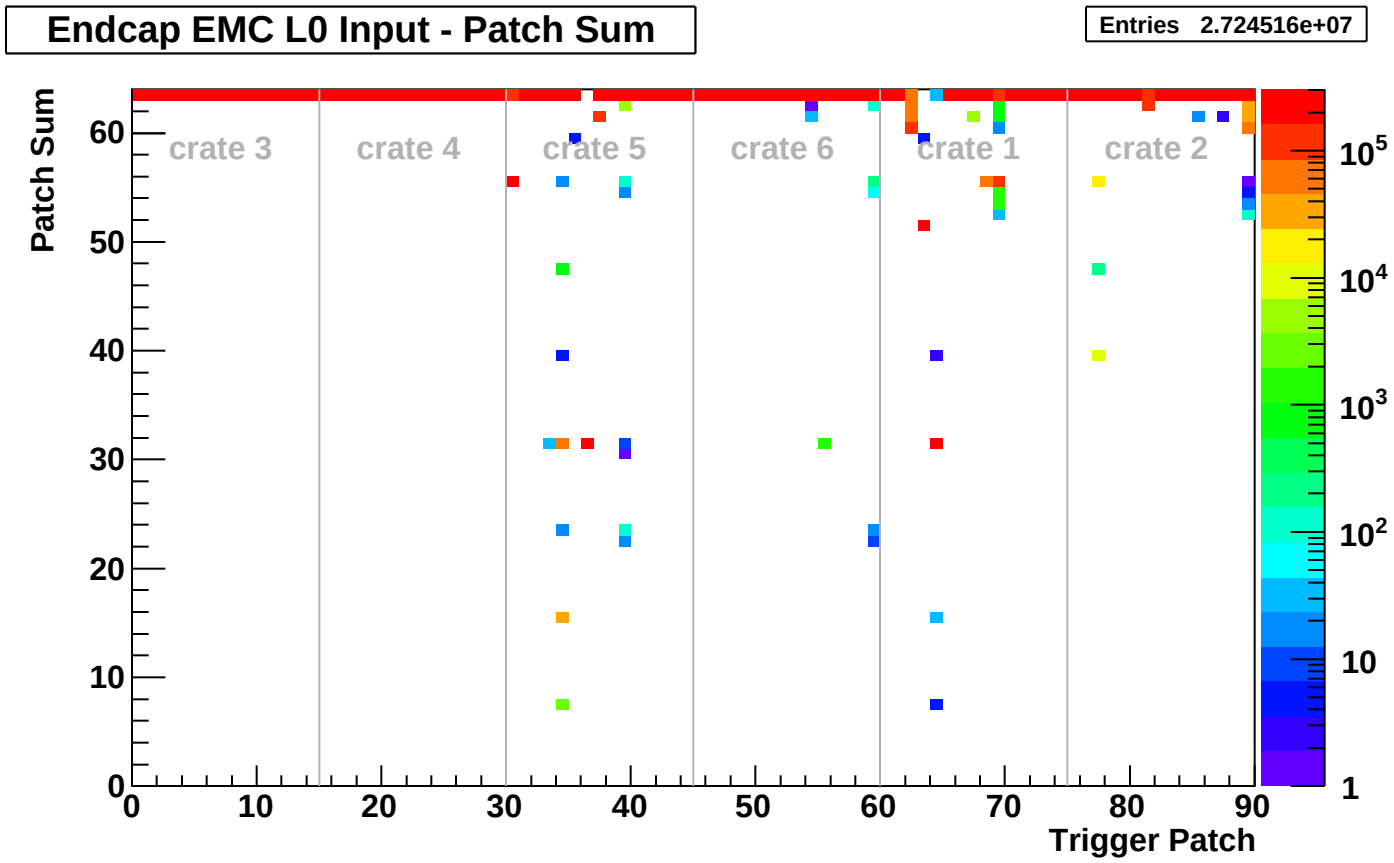

BunchId7Bit (JP1)
Entries 302724
10^3
10^2
10
0
20
40
60
80
100
120

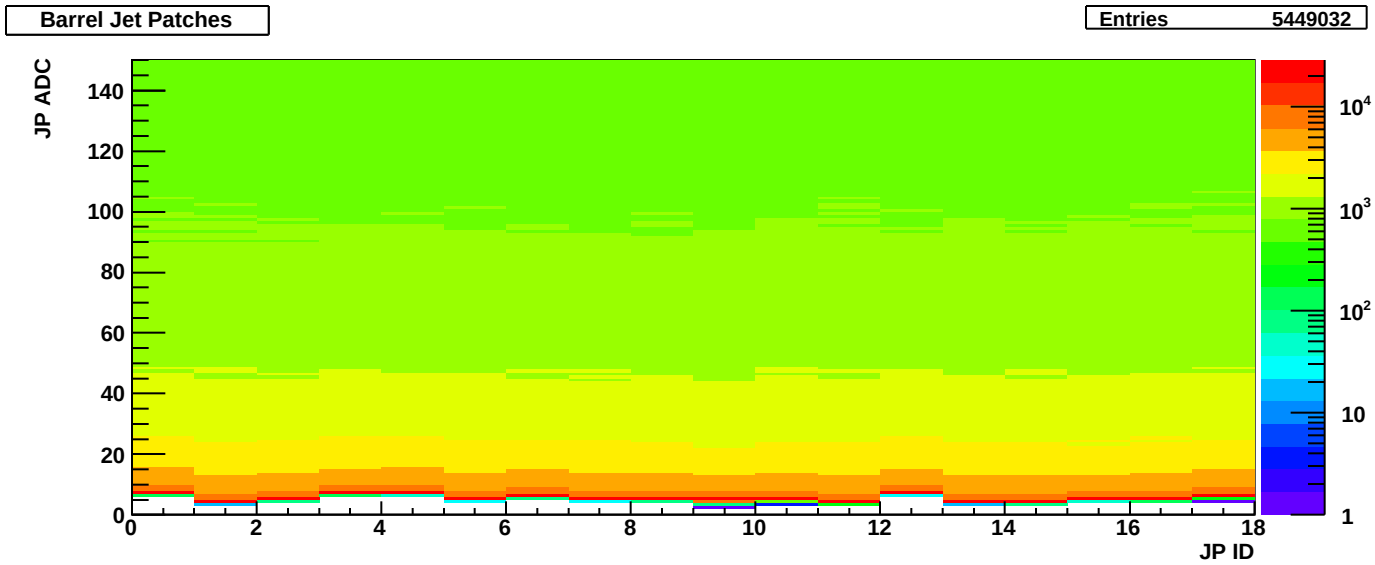

BunchId7Bit (JP2||AJP)
Entries 302724
10^3
10^2
10
0
20
40
60
80
100
120

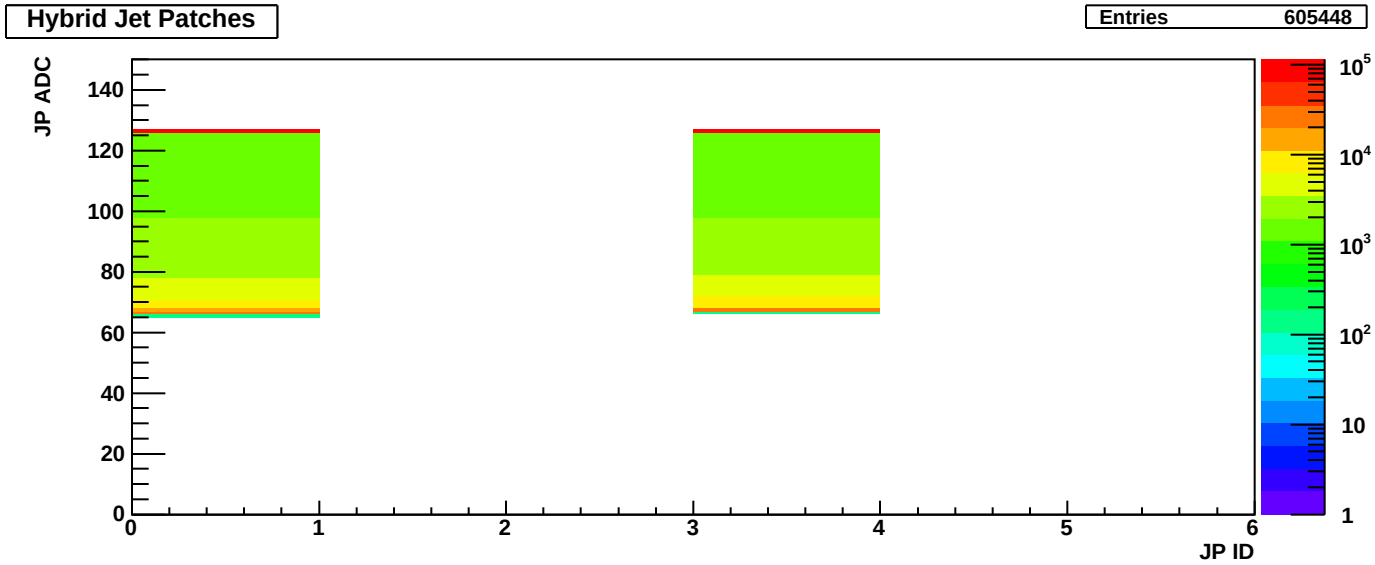

Barrel EMC L0 Input - High Tower
Entries 9.08172e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

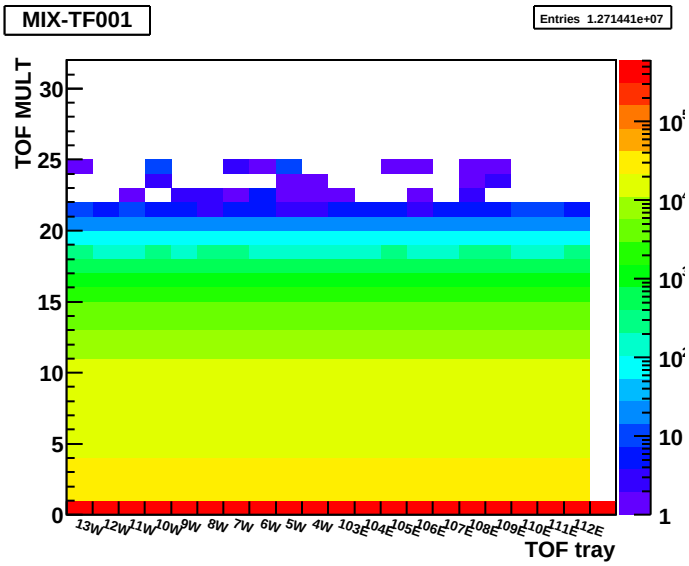

Barrel EMC L0 Input - Patch Sum
Entries 9.08172e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

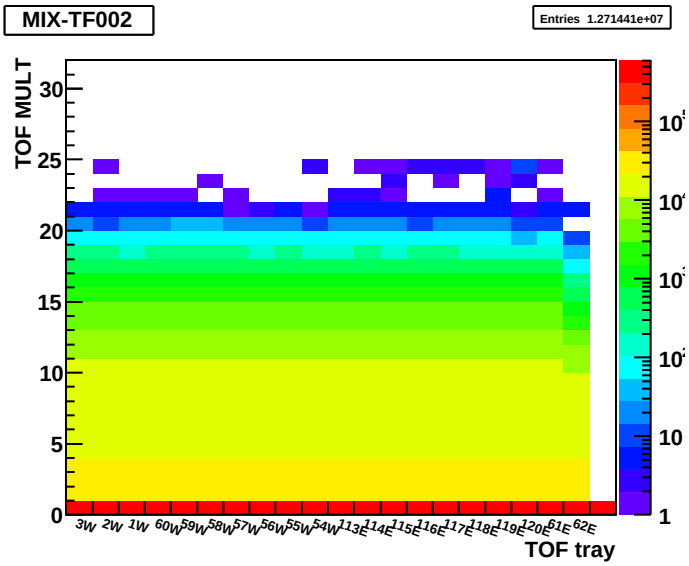

Endcap EMC L0 Input - High Tower
Entries 2.724516e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

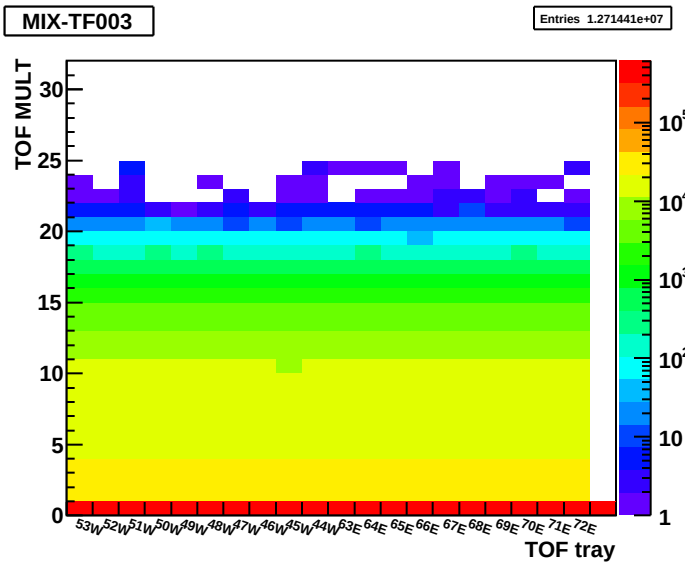

Endcap EMC L0 Input - Patch Sum
Entries 2.724516e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

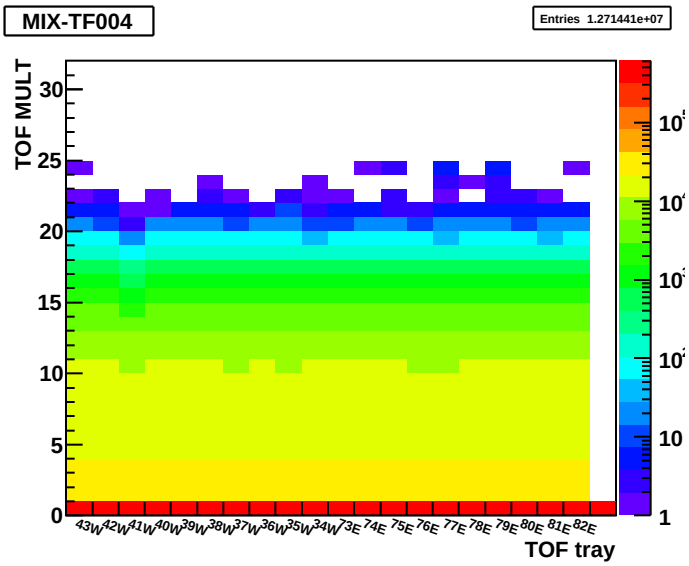

Barrel Jet Patches
Entries 5449032
JP ADC
JP ID

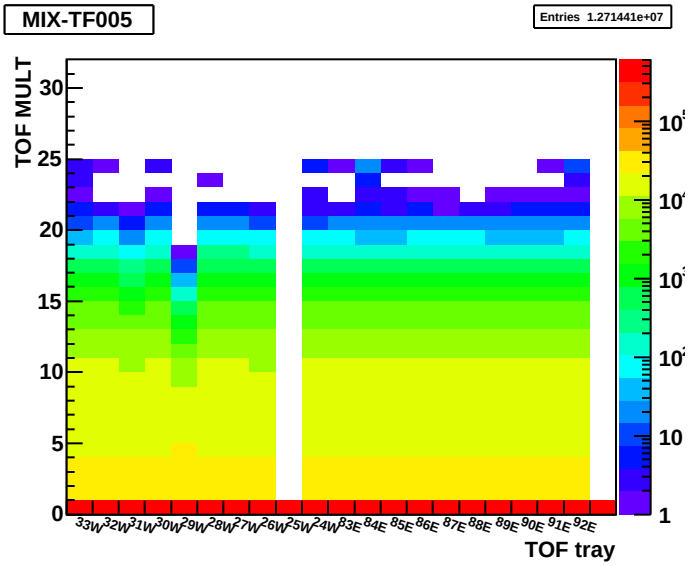

Hybrid Jet Patches
Entries 605448
JP ADC
JP ID

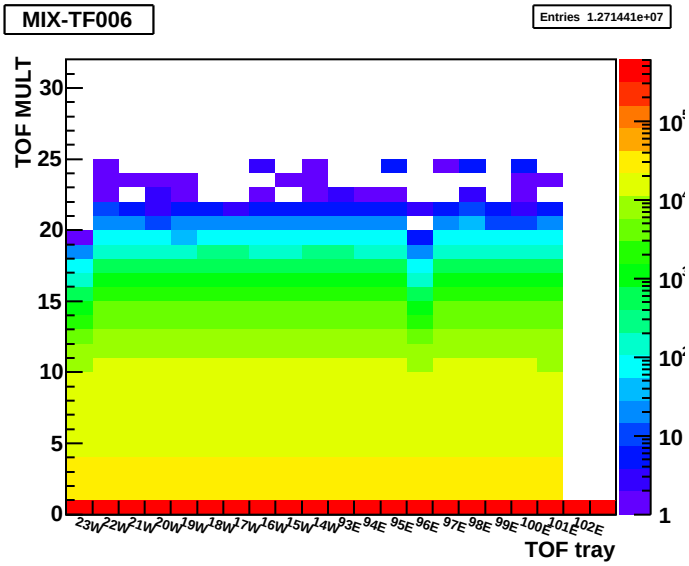

MIX-TF001
Entries 1.271441e+07
TOF MULT
TOF tray

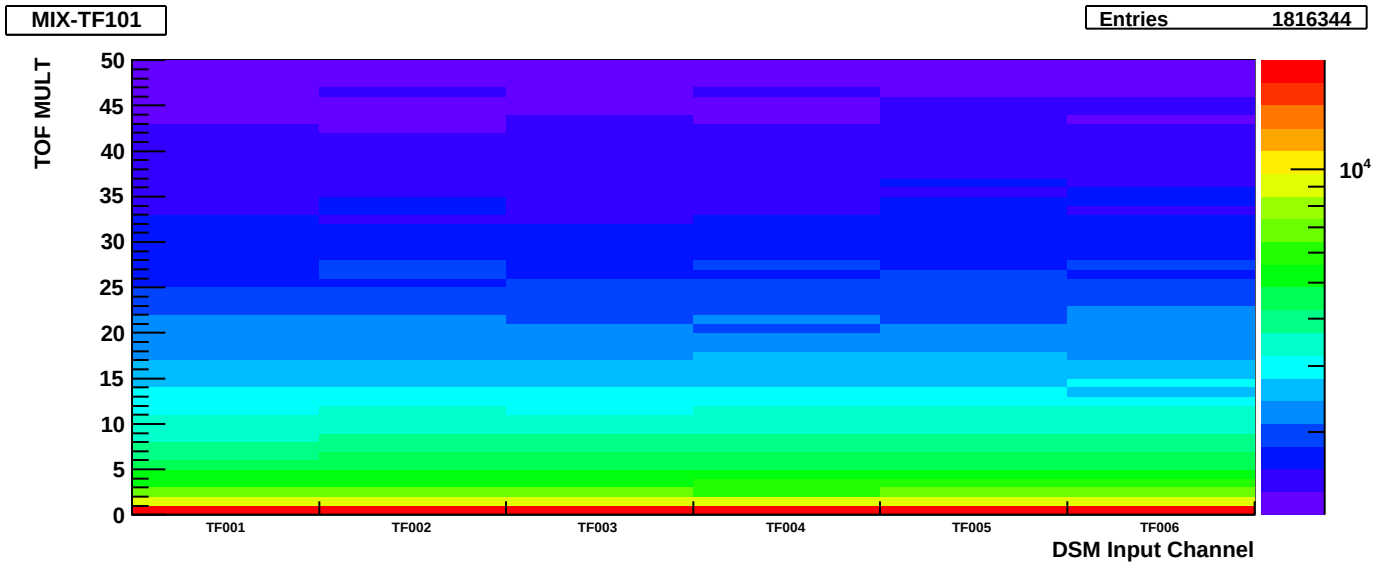

MIX-TF002
Entries 1.271441e+07
TOF MULT
TOF tray

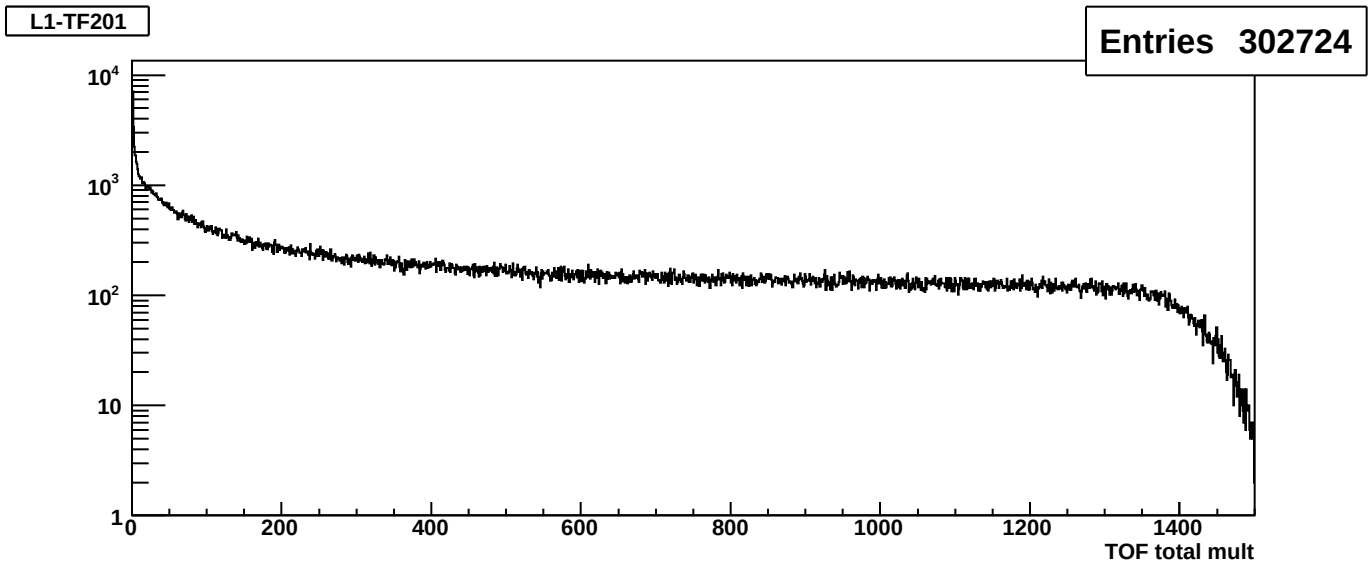

MIX-TF003
Entries 1.271441e+07
TOF MULT
TOF tray

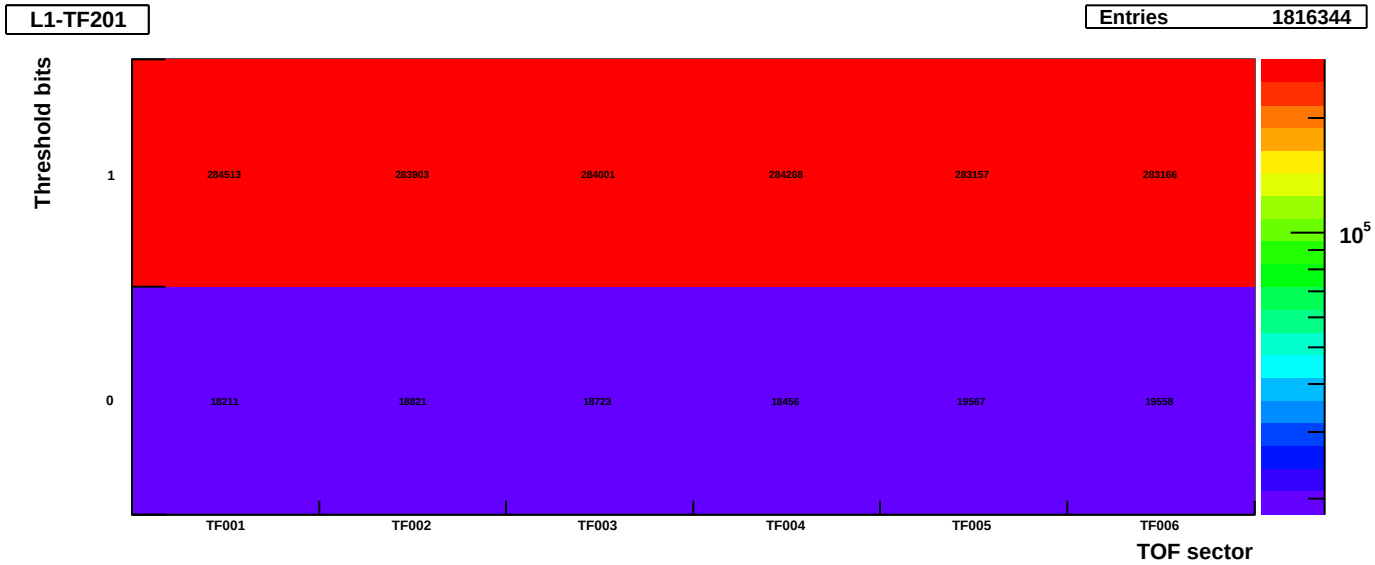

MIX-TF004
Entries 1.271441e+07
TOF MULT
TOF tray

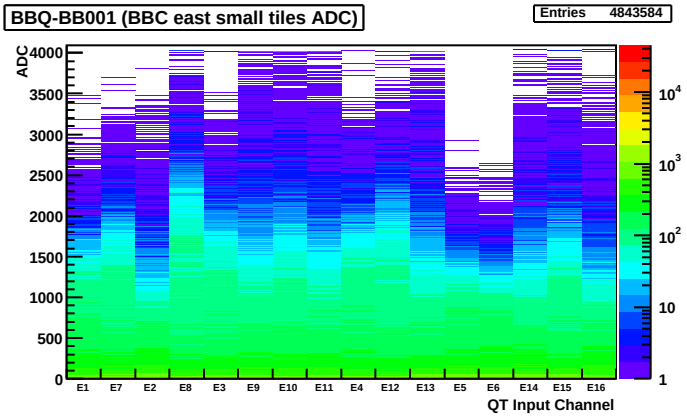

MIX-TF005
Entries 1.271441e+07
TOF MULT
TOF tray

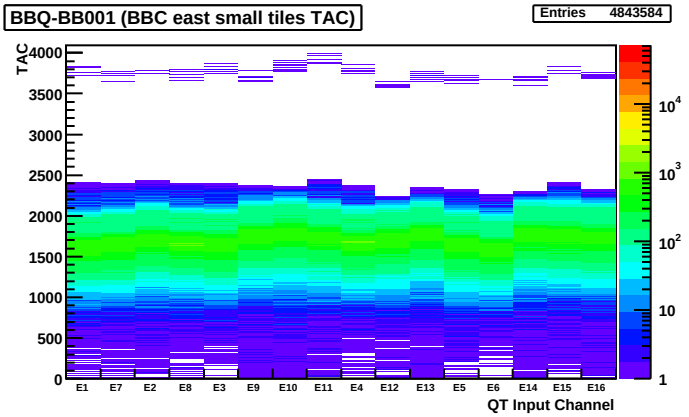

MIX-TF006
Entries 1.271441e+07
TOF MULT
TOF tray

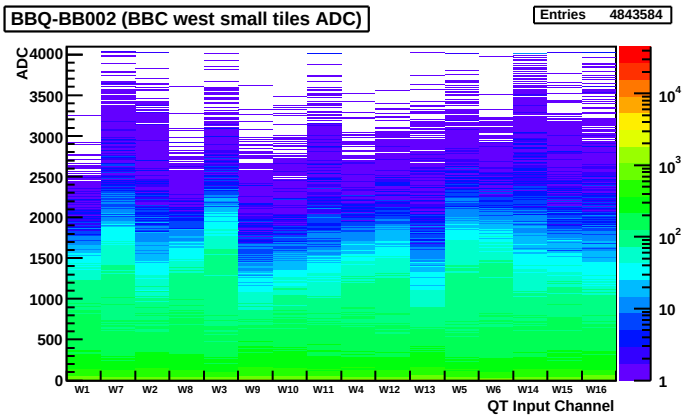

MIX-TF101
Entries 1816344
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

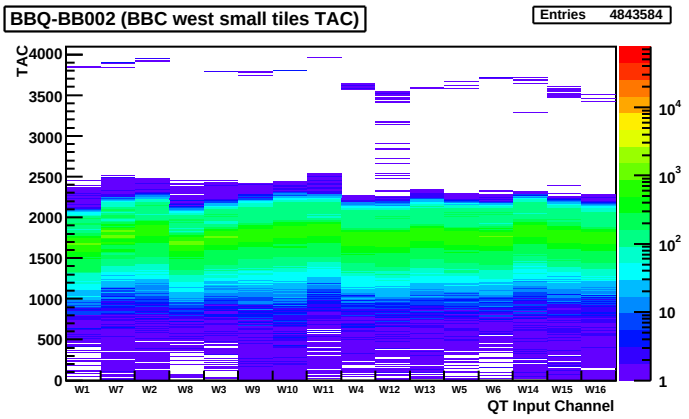

L1-TF201
Entries 302724
10^4
10^3
10^2
10
1
0
200
400
600
800
1000
1200
1400
TOF total mult

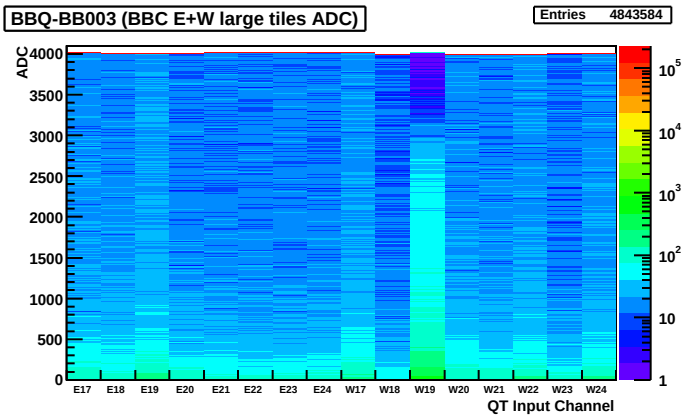

L1-TF201
Entries 1816344
Threshold bits
1
0
284513
283903
284001
284268
283157
283166
18211
18821
18723
18456
19567
19558
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

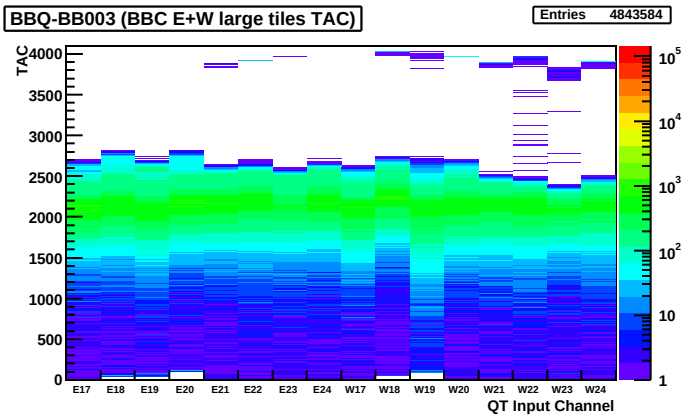

BBQ-BB001 (BBC east small tiles ADC)
Entries 4843584
ADC
QT Input Channel

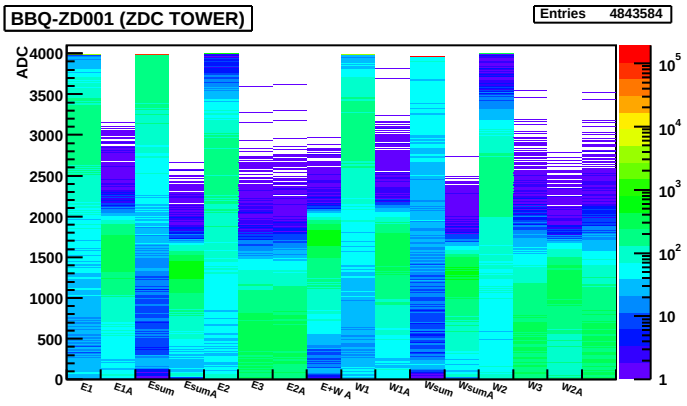

BBQ-BB001 (BBC east small tiles TAC)
Entries 4843584
TAC
QT Input Channel

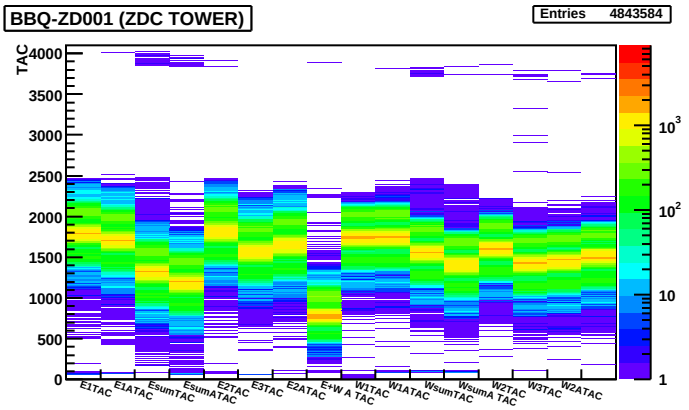

BBQ-BB002 (BBC west small tiles ADC)
Entries 4843584
ADC
QT Input Channel

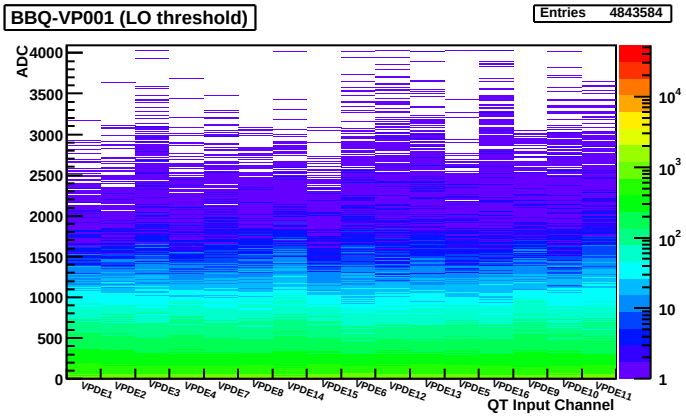

BBQ-BB002 (BBC west small tiles TAC)
Entries 4843584
TAC
QT Input Channel

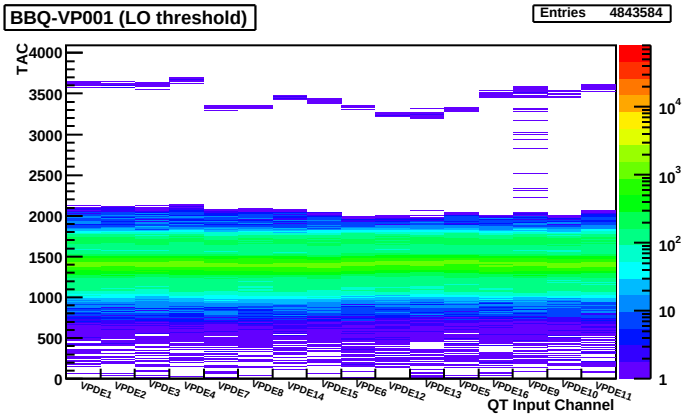

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4843584
ADC
QT Input Channel

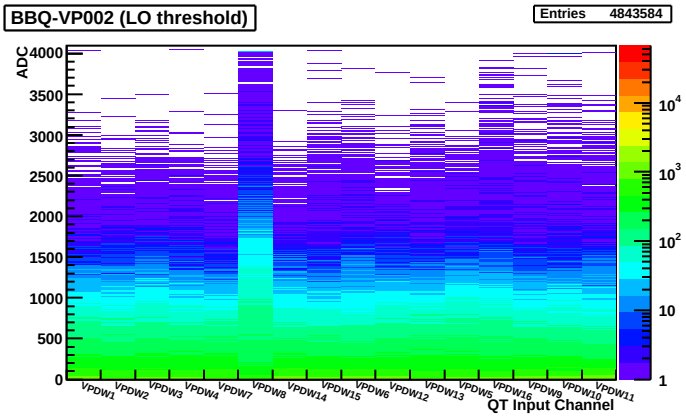

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4843584
TAC
QT Input Channel

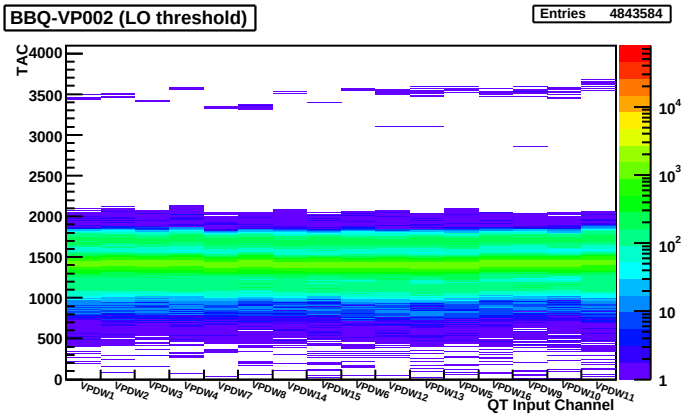

BBQ-ZD001 (ZDC TOWER)
Entries 4843584
ADC

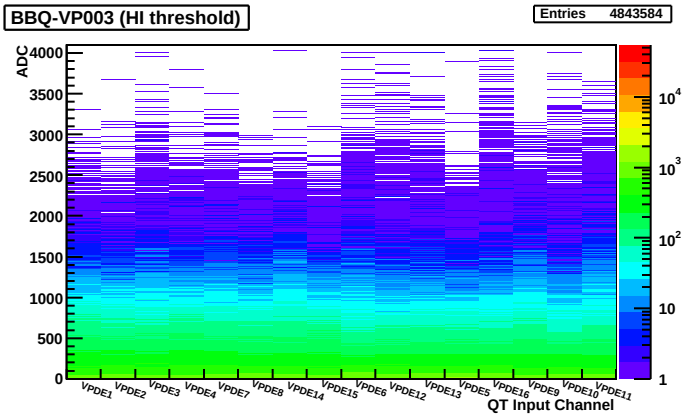

BBQ-ZD001 (ZDC TOWER)
Entries 4843584
TAC

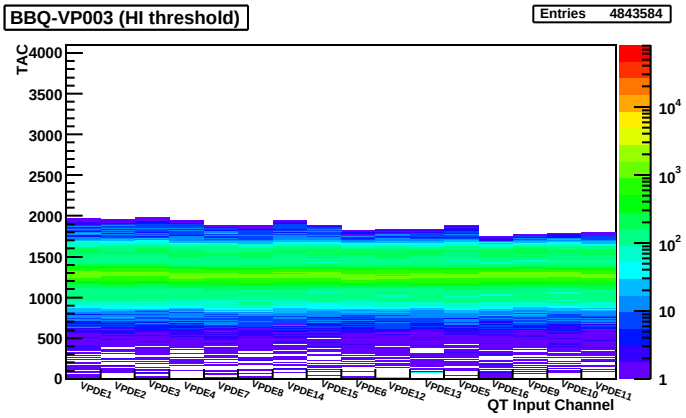

BBQ-VP001 (LO threshold)
Entries 4843584
ADC
QT Input Channel

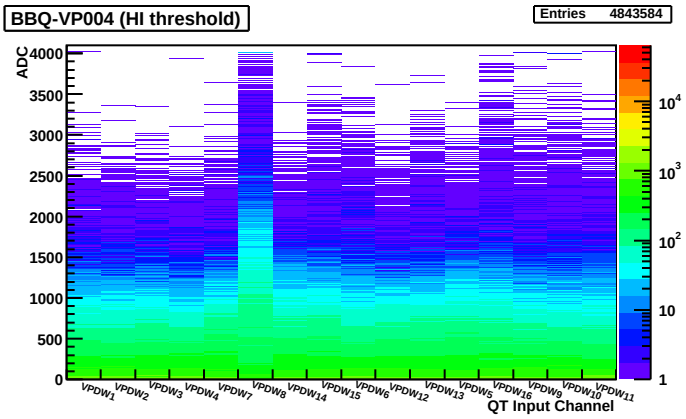

BBQ-VP001 (LO threshold)
Entries 4843584
TAC
QT Input Channel

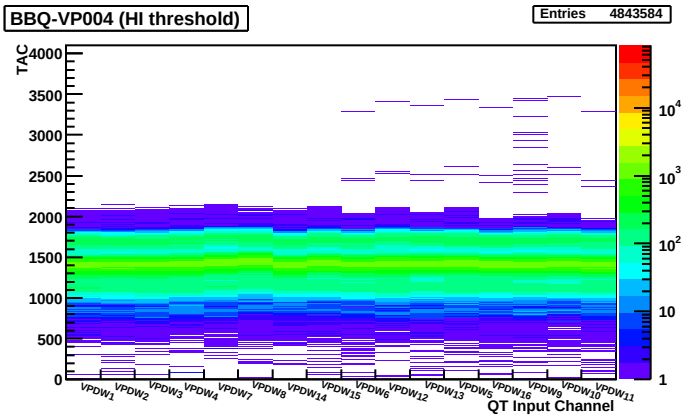

BBQ-VP002 (LO threshold)
Entries 4843584
ADC
QT Input Channel

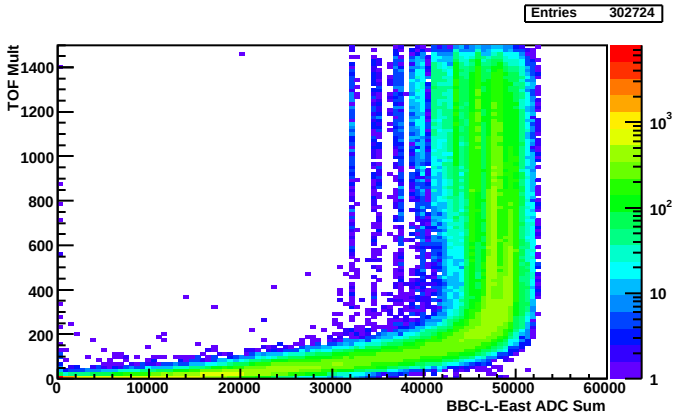

BBQ-VP002 (LO threshold)
Entries 4843584
TAC
QT Input Channel

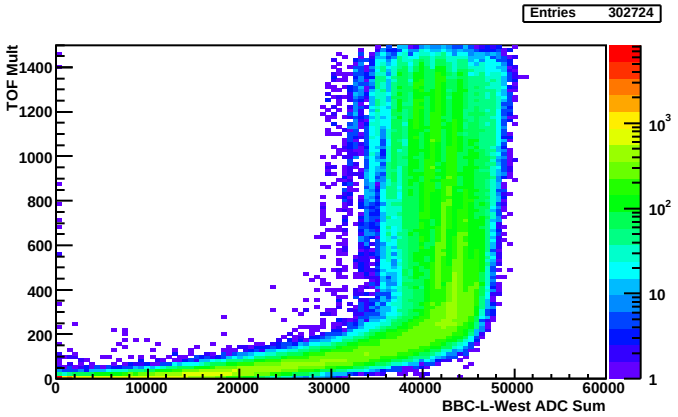

BBQ-VP003 (HI threshold)
Entries 4843584
ADC
QT Input Channel

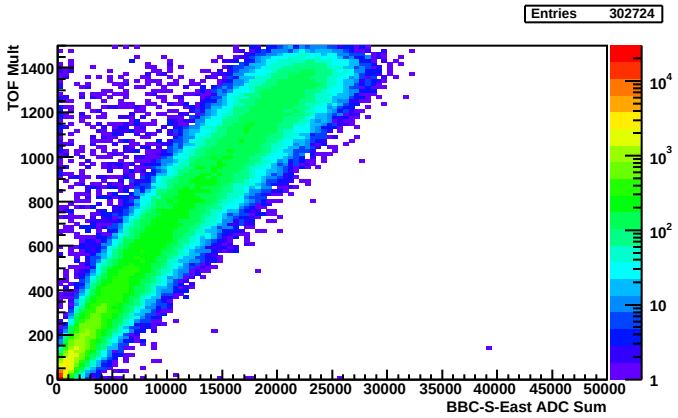

BBQ-VP003 (HI threshold)
Entries 4843584
TAC
QT Input Channel

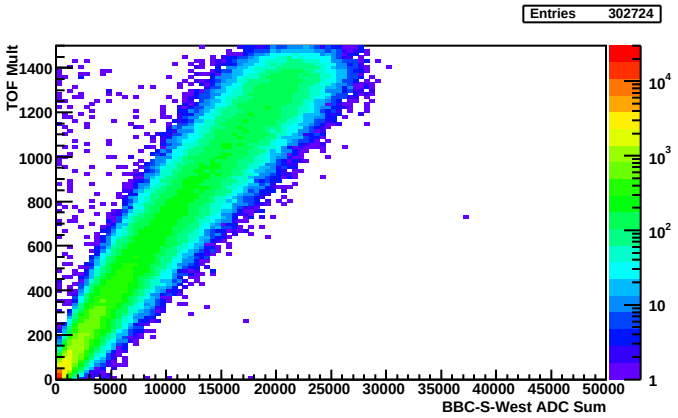

BBQ-VP004 (HI threshold)
Entries 4843584
ADC
QT Input Channel

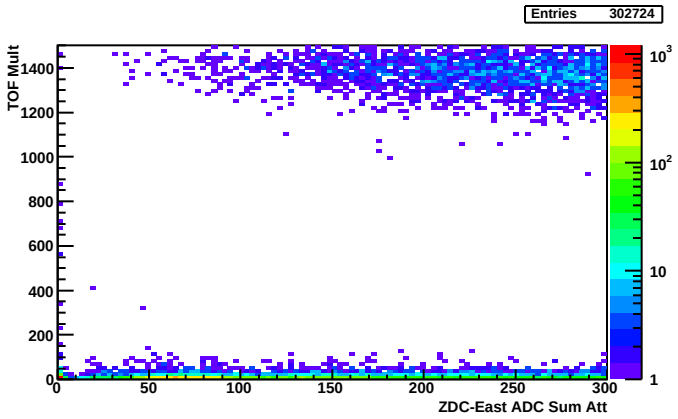

BBQ-VP004 (HI threshold)
Entries 4843584
TAC
QT Input Channel

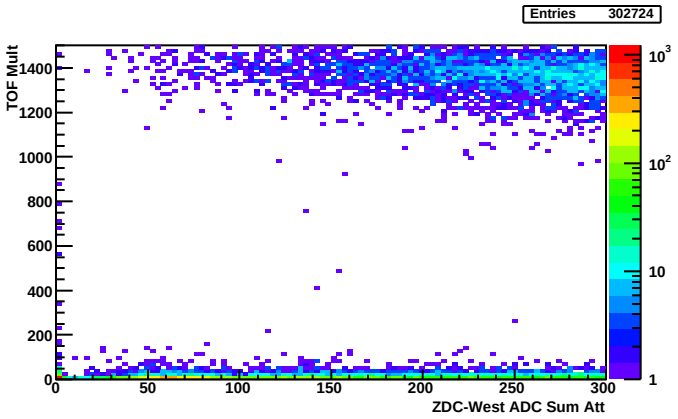

Entries 302724
TOF Mult
BBC-L-East ADC Sum

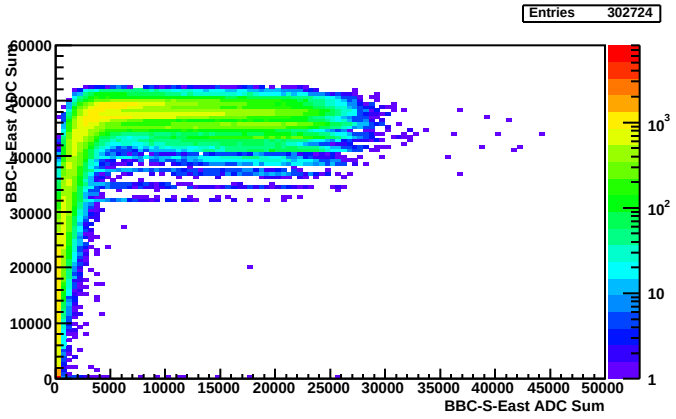

Entries 302724
TOF Mult
BBC-L-West ADC Sum

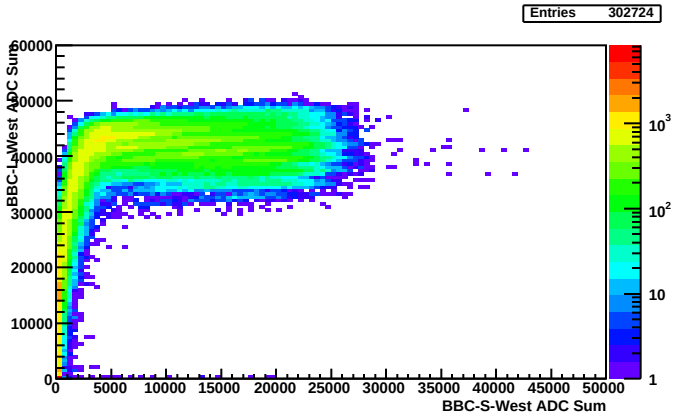

Entries 302724
TOF Mult
BBC-S-East ADC Sum

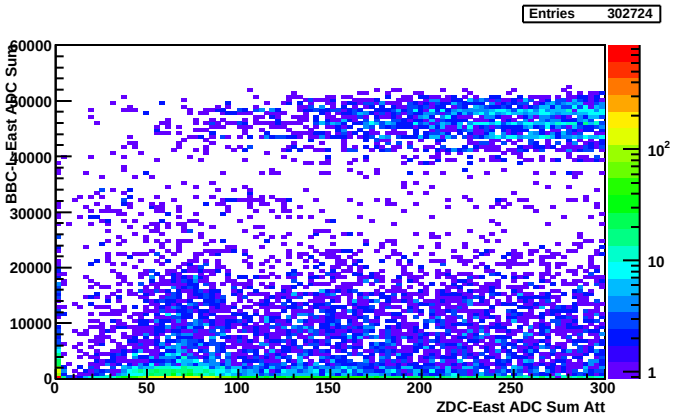

Entries 302724
TOF Mult
BBC-S-West ADC Sum

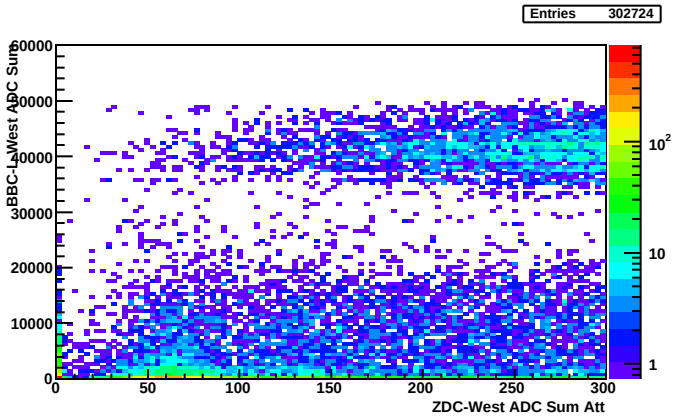

Entries 302724
TOF Mult
ZDC-East ADC Sum Att

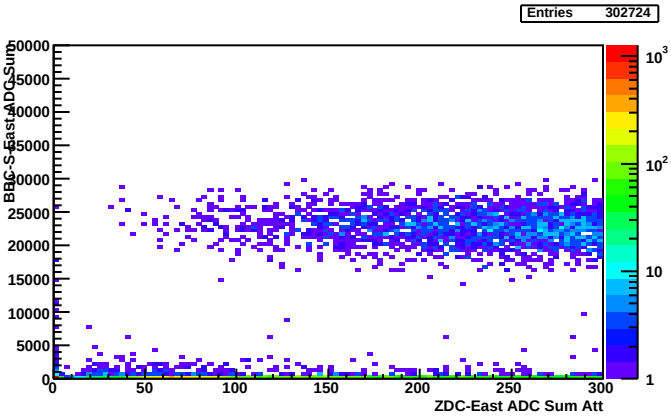

Entries 302724
TOF Mult
ZDC-West ADC Sum Att

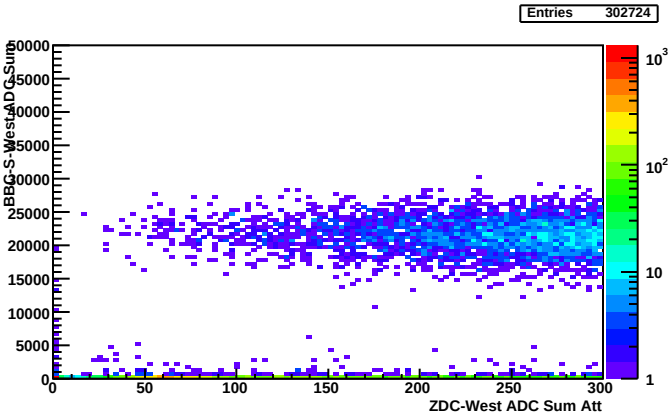

Entries 302724
BBC-L-East ADC Sum
BBC-S-East ADC Sum

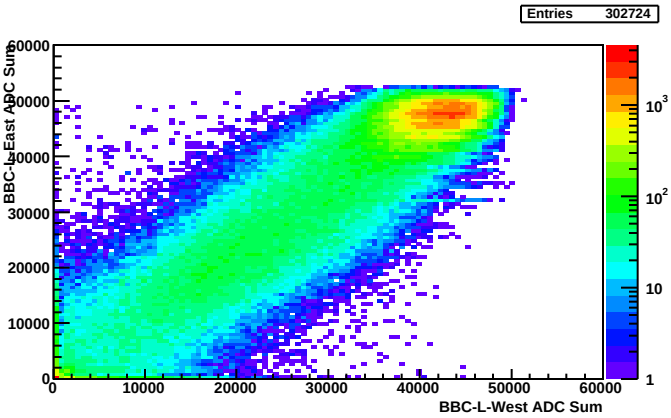

Entries 302724
BBC-L-West ADC Sum
BBC-S-West ADC Sum

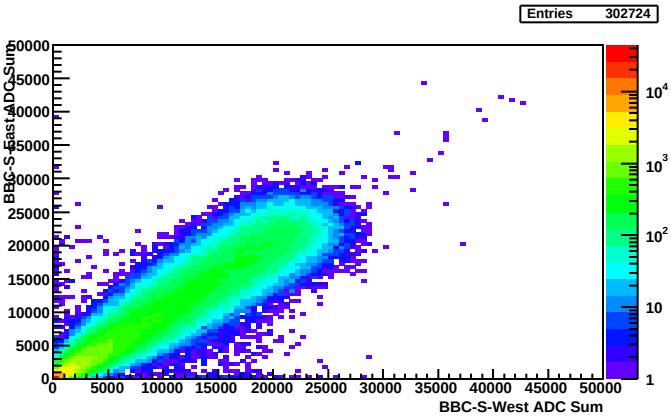

Entries 302724
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

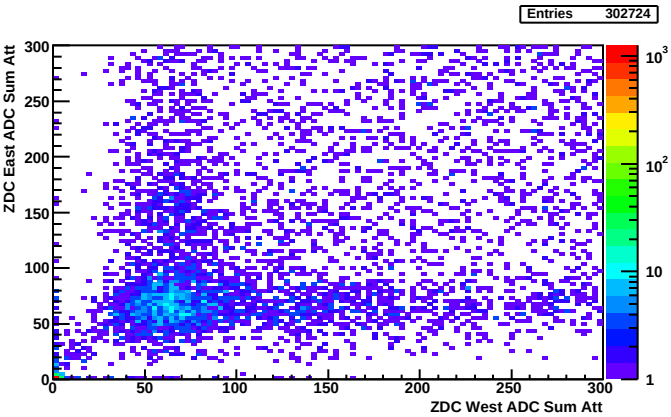

Entries 302724
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

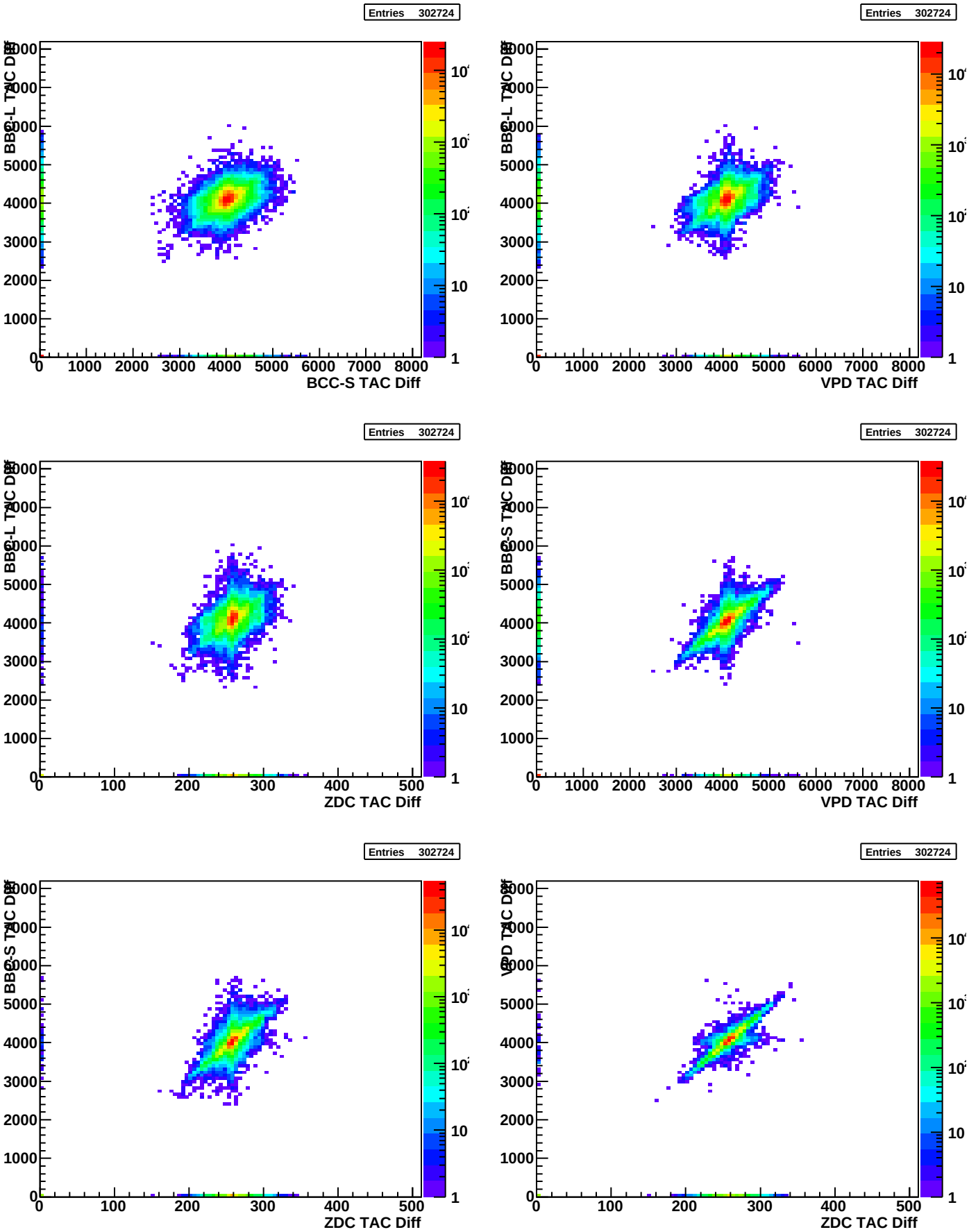

Entries 302724
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

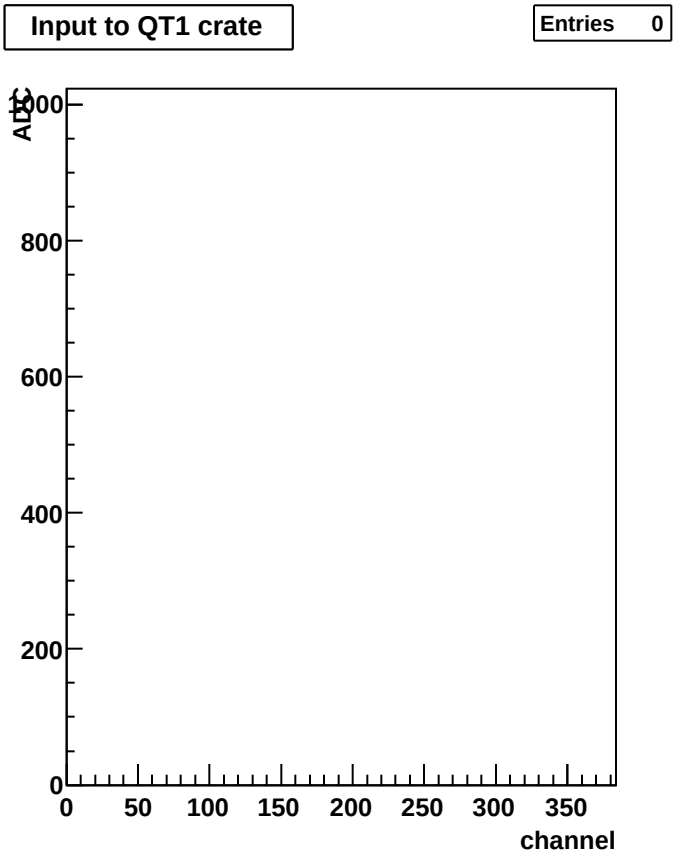

Entries 302724
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

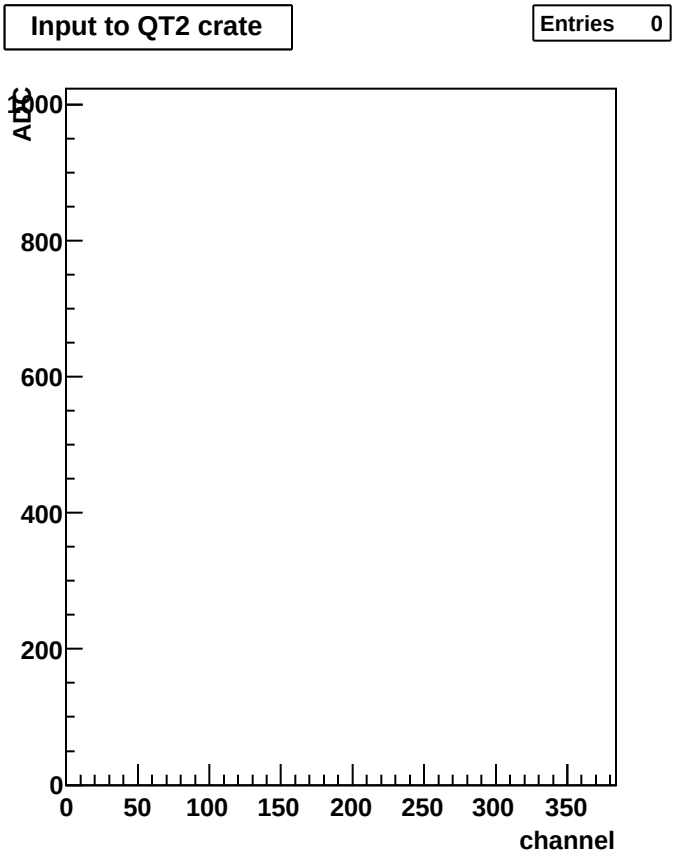

Entries 302724
BBC-L-East ADC Sum
BBC-L-West ADC Sum

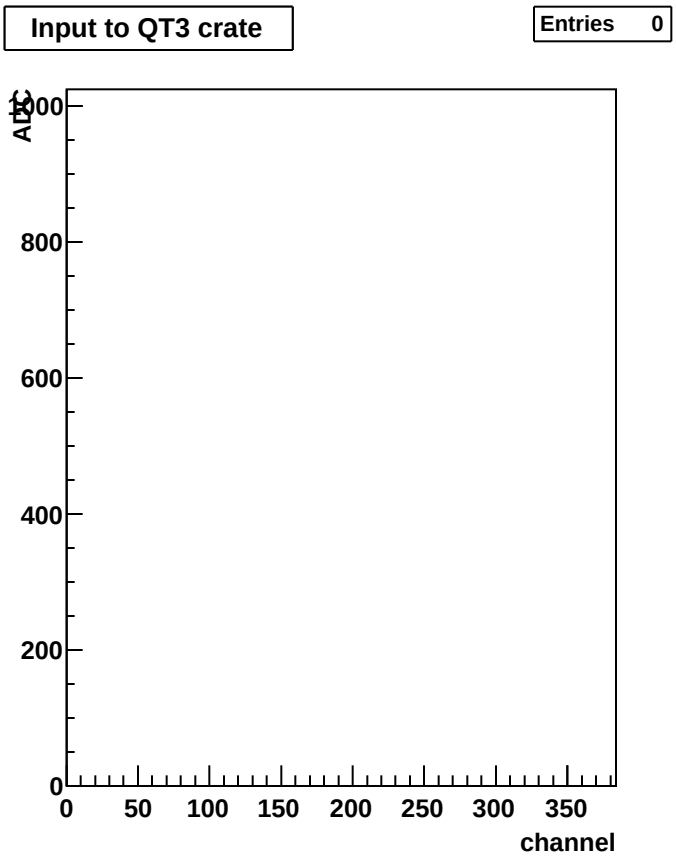

Entries 302724
BBC-S-East ADC Sum
BBC-S-West ADC Sum

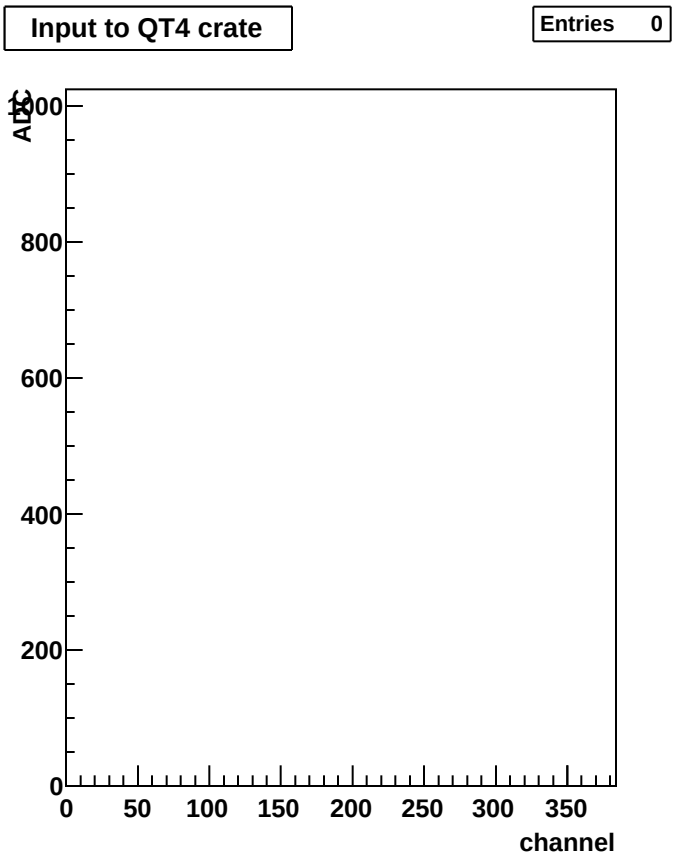

Entries 302724
ZDC East ADC Sum Att
ZDC West ADC Sum Att

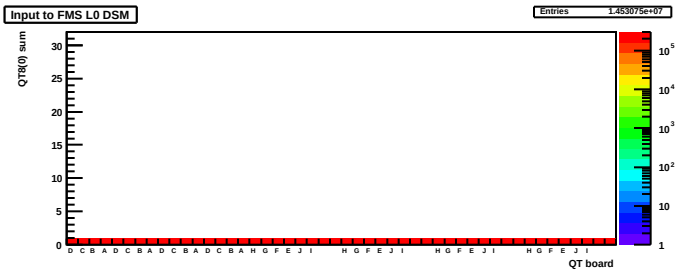

Entries 302724
BBC-L TAC Diff
BCC-S TAC Diff
Entries 302724
BBC-L TAC Diff
VPD TAC Diff
Entries 302724
BBC-L TAC Diff
ZDC TAC Diff
Entries 302724
BBC-S TAC Diff
VPD TAC Diff
Entries 302724
BBC-S TAC Diff
ZDC TAC Diff
Entries 302724
VPD TAC Diff
ZDC TAC Diff

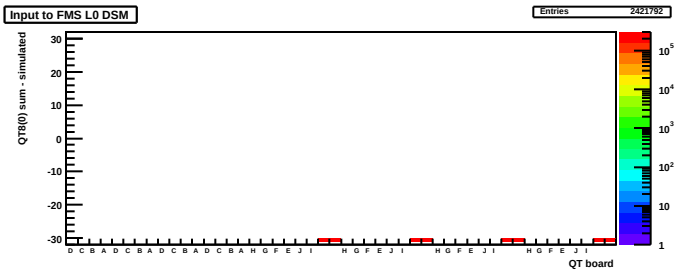

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

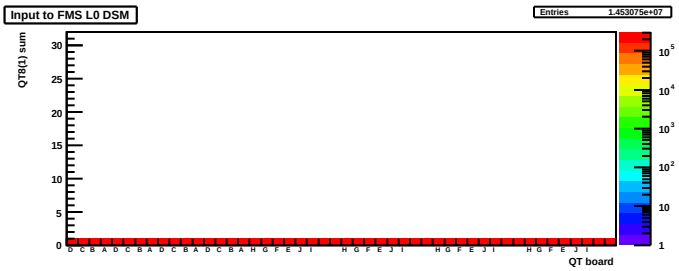

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

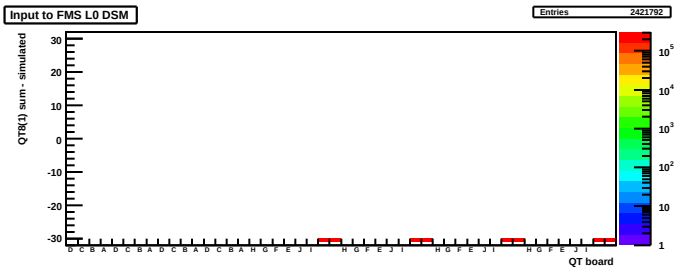

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

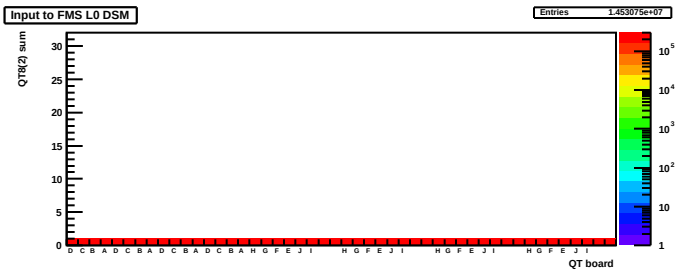

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

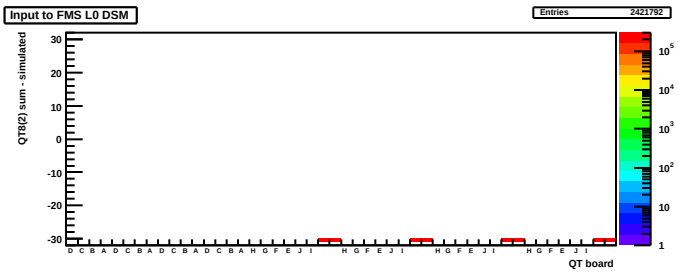

Input to FMS L0 DSM
Entries 1.453075e+07
QT8(0) sum
QT board

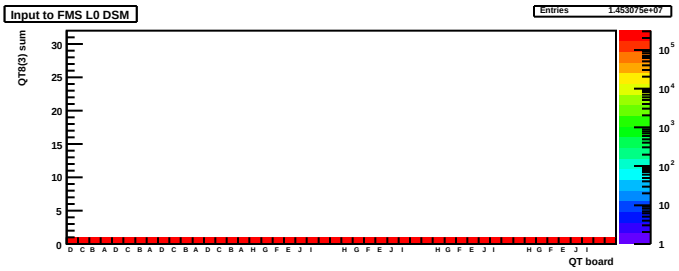

Input to FMS L0 DSM
Entries 2421792
QT8(0) sum - simulated
QT board

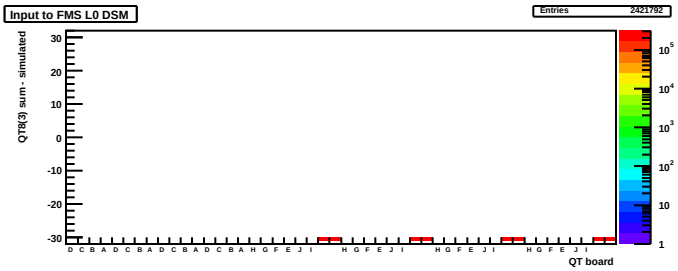

Input to FMS L0 DSM
Entries 1.453075e+07
QT8(1) sum
QT board

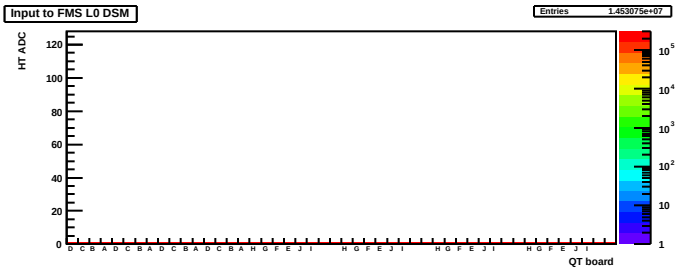

Input to FMS L0 DSM
Entries 2421792
QT8(1) sum - simulated
QT board

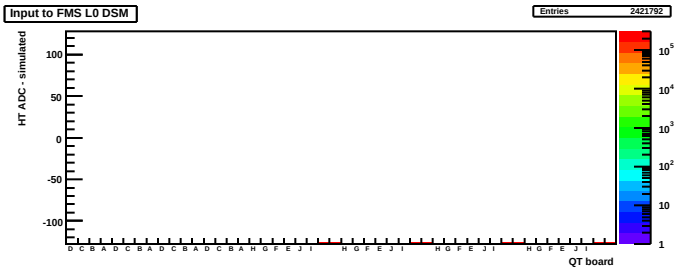

Input to FMS L0 DSM
Entries 1.453075e+07
QT8(2) sum
QT board

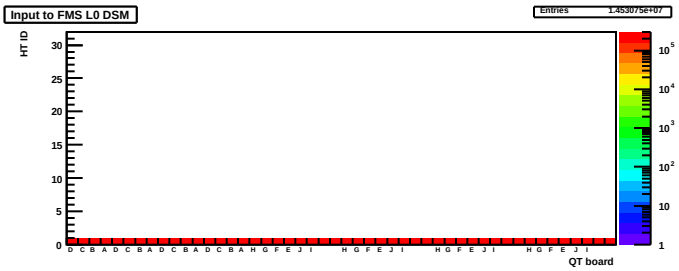

Input to FMS L0 DSM
Entries 2421792
QT8(2) sum - simulated
QT board

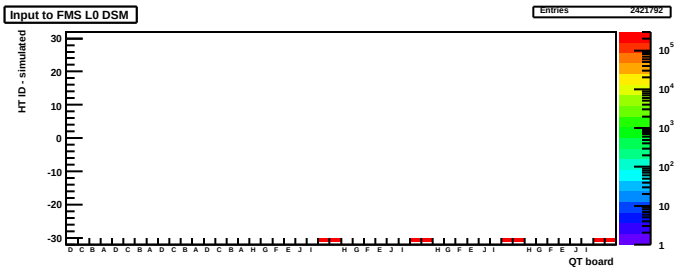

Input to FMS L0 DSM
Entries 1.453075e+07
QT8(3) sum
QT board

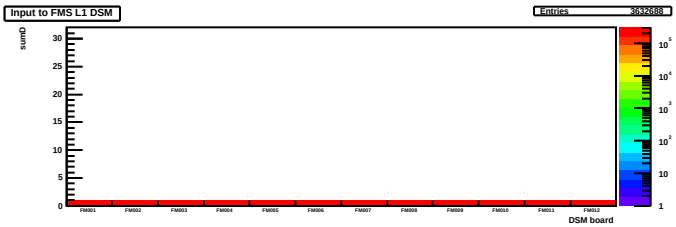

Input to FMS L0 DSM
Entries 2421792
QT8(3) sum - simulated
QT board

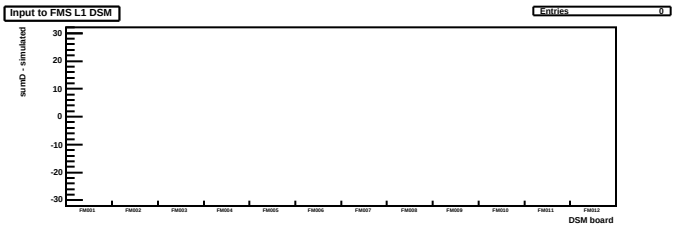

Input to FMS L0 DSM
Entries 1.453075e+07
HT ADC
QT board

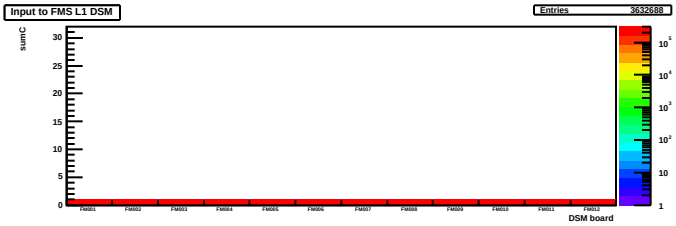

Input to FMS L0 DSM
Entries 2421792
HT ADC - simulated
QT board

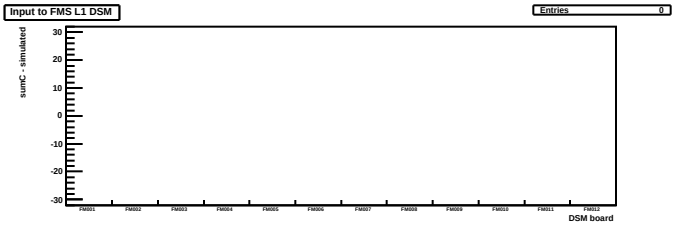

Input to FMS L0 DSM
Entries 1.453075e+07
HT ID
QT board

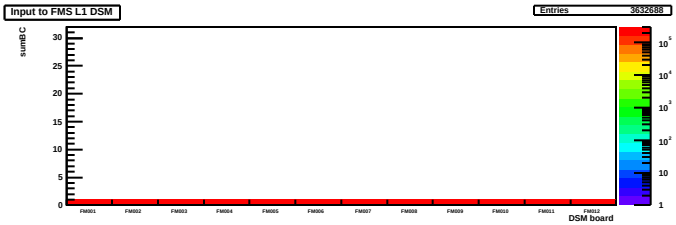

Input to FMS L0 DSM
Entries 2421792
HT ID - simulated
QT board

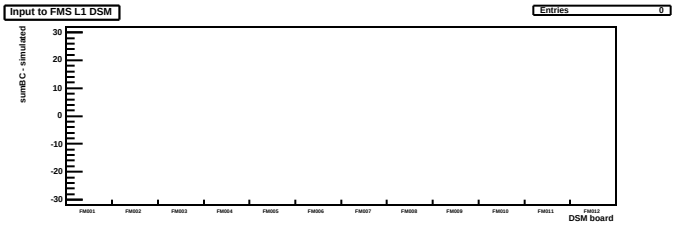

Input to FMS L1 DSM
Entries 3632688
sumD
DSM board

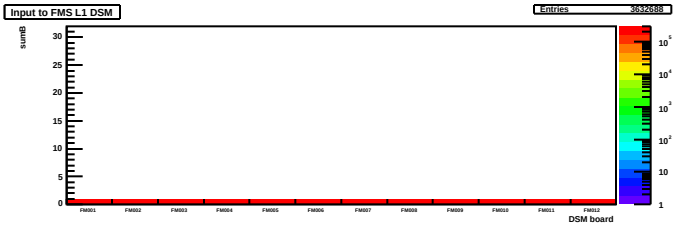

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

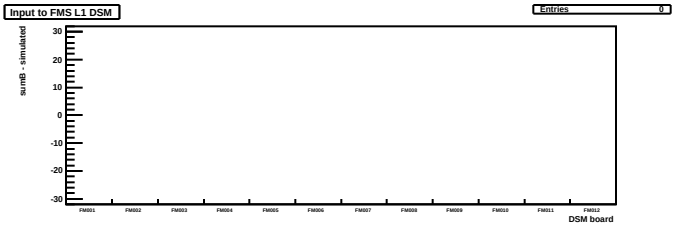

Input to FMS L1 DSM
Entries 3632688
sumC
DSM board

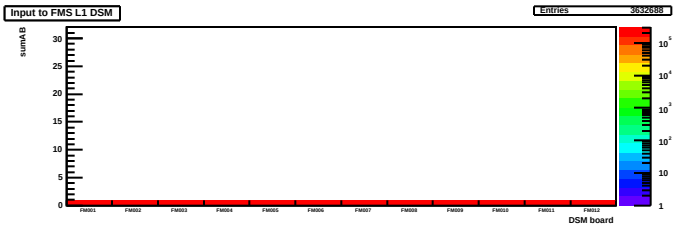

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

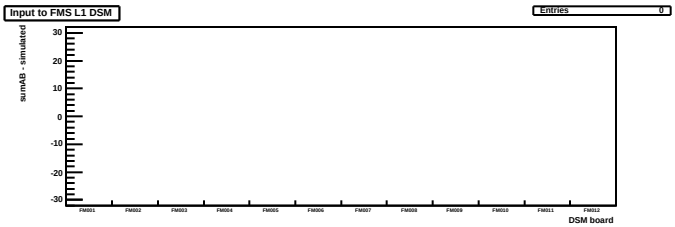

Input to FMS L1 DSM
Entries 3632688
sumBC
DSM board

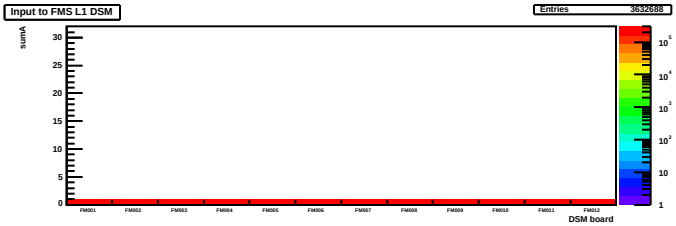

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

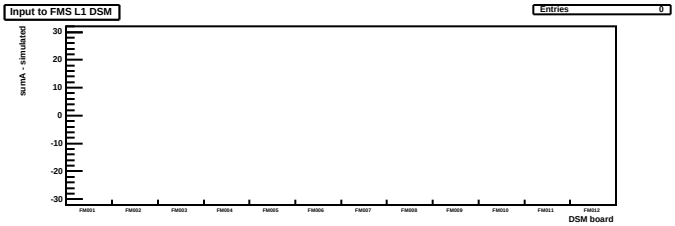

Input to FMS L1 DSM
Entries 3632688
sumB
DSM board

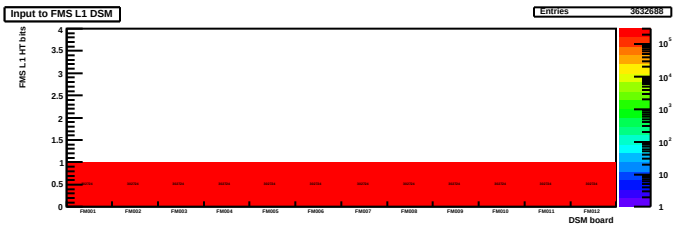

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

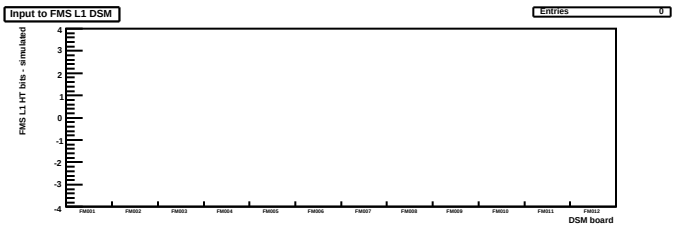

Input to FMS L1 DSM
Entries 3632688
sumAB
DSM board

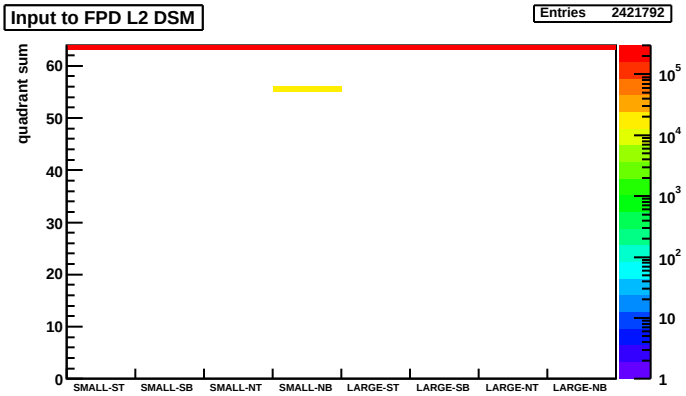

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

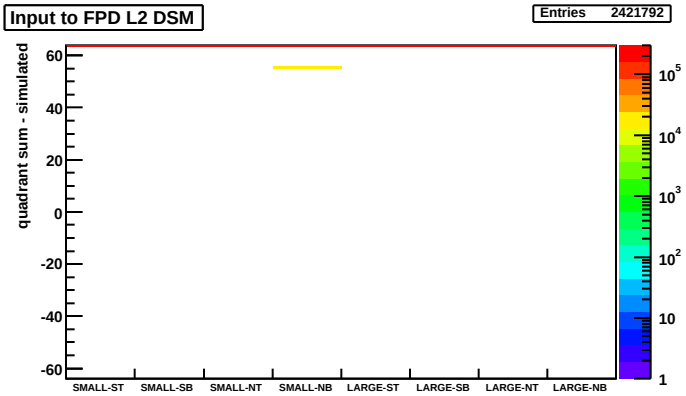

Input to FMS L1 DSM
Entries 3632688
sumA
DSM board

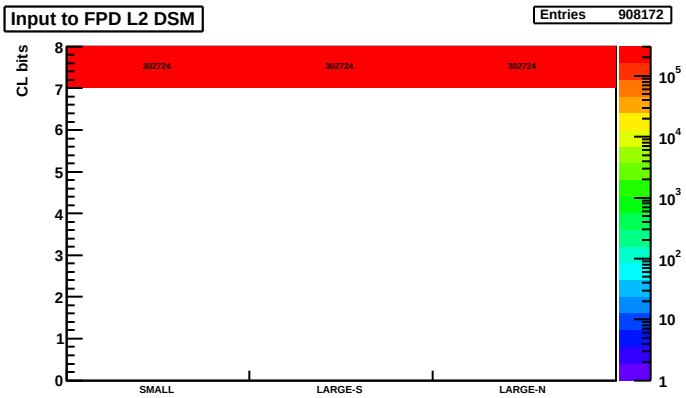

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

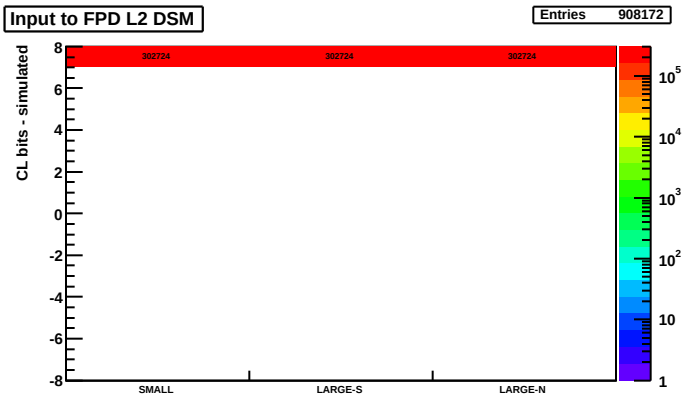

Input to FMS L1 DSM
Entries 3632688
FMS L1 HT bits
DSM board

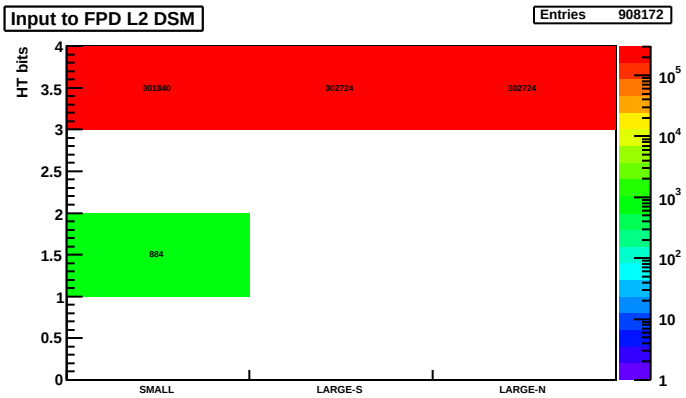

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

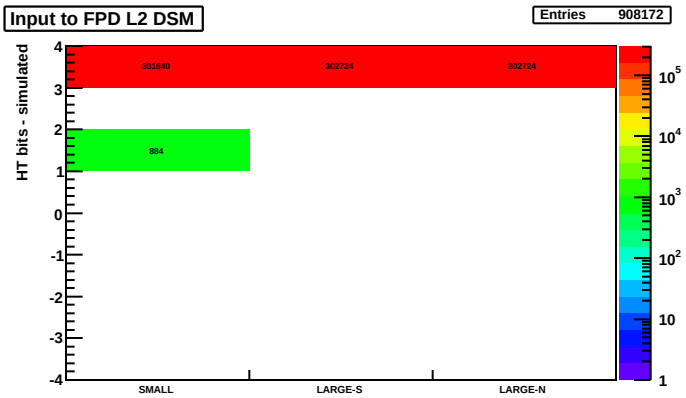

Input to FPD L2 DSM
Entries 2421792
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

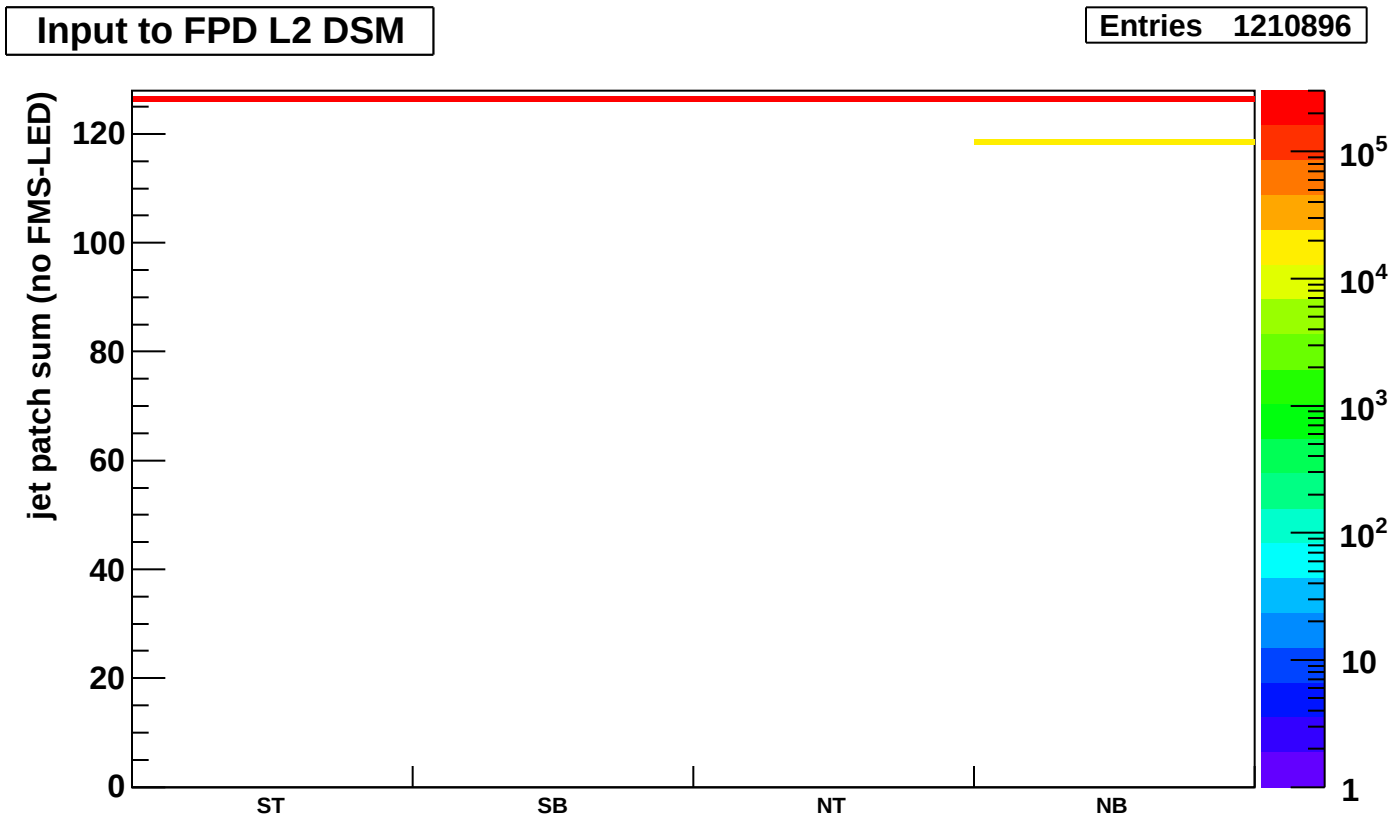

Input to FPD L2 DSM
Entries 2421792
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

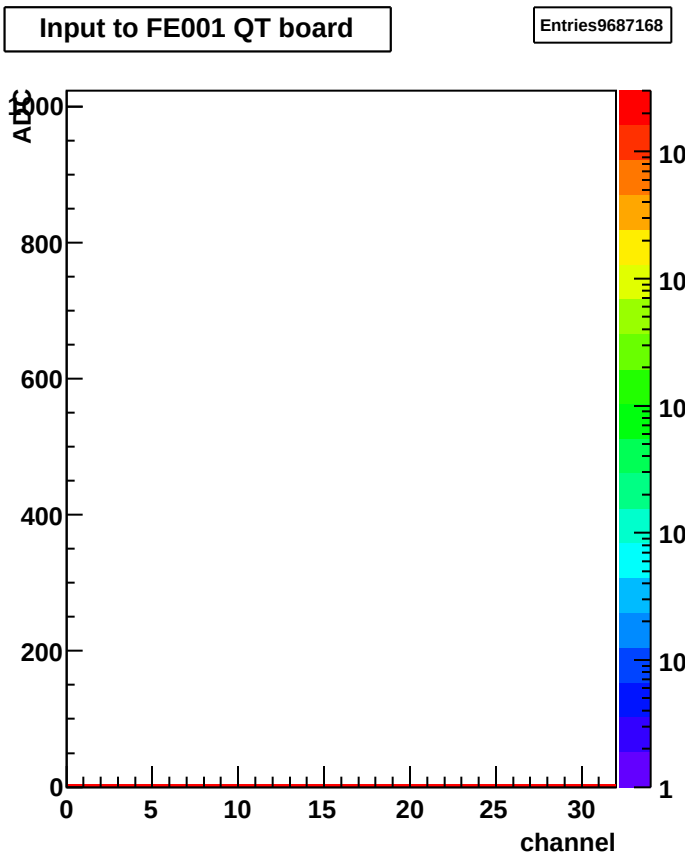

Input to FPD L2 DSM
Entries 908172
CL bits
302724
302724
302724
SMALL
LARGE-S
LARGE-N

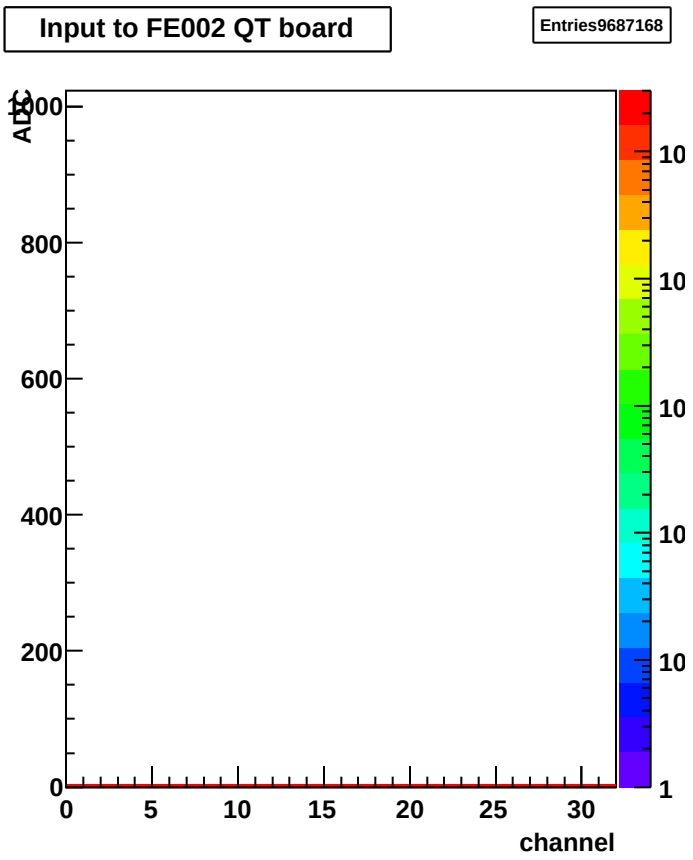

Input to FPD L2 DSM
Entries 908172
CL bits - simulated
302724
302724
302724
SMALL
LARGE-S
LARGE-N

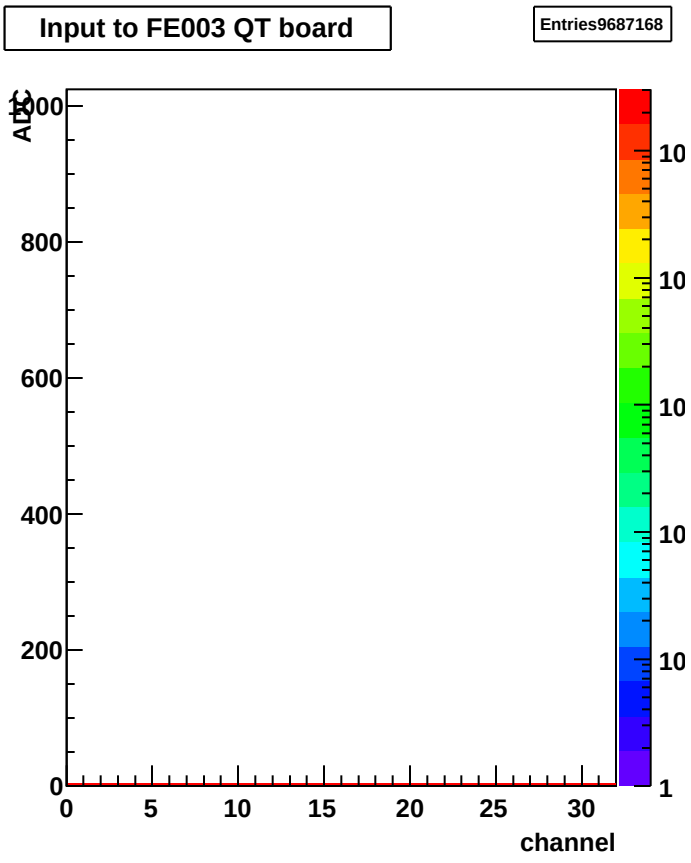

Input to FPD L2 DSM
Entries 908172
HT bits
301840
302724
302724
884
SMALL
LARGE-S
LARGE-N

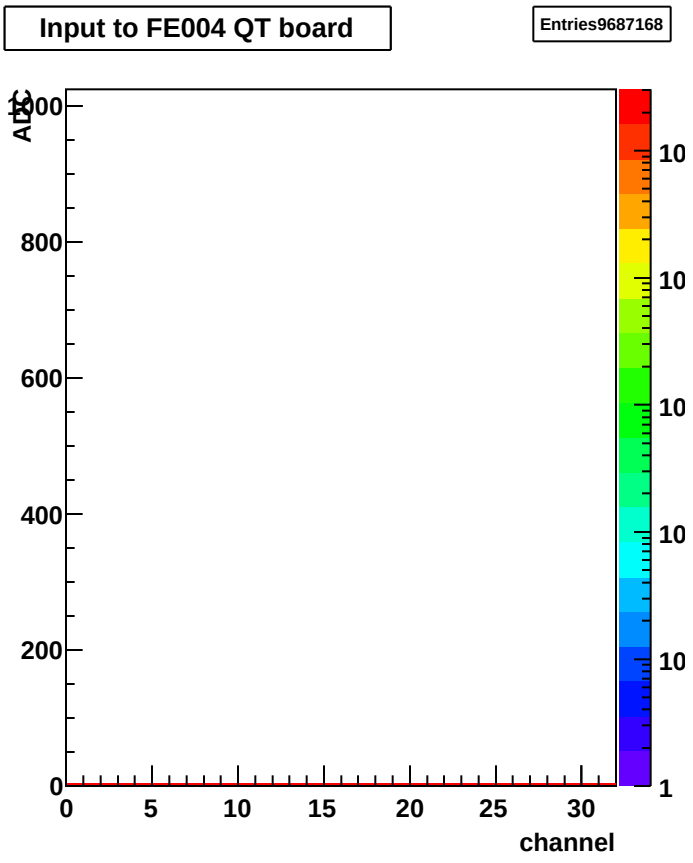

Input to FPD L2 DSM
Entries 908172
HT bits - simulated
301840
302724
302724
884
SMALL
LARGE-S
LARGE-N


Input to FPD L2 DSM
Entries 1210896
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

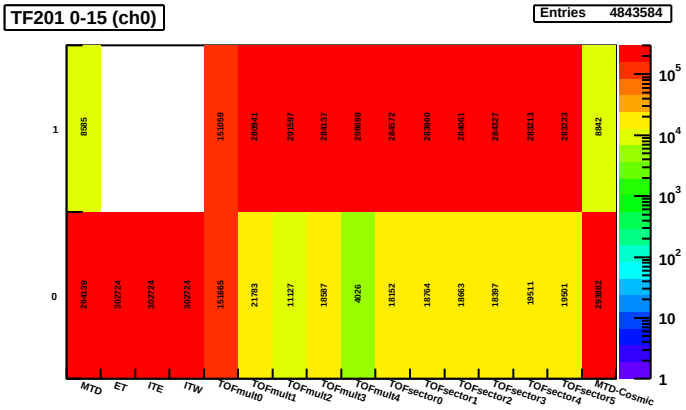

Input to FE001 QT board
Entries9687168
ADC
channel


Input to FE002 QT board
Entries9687168
ADC
channel

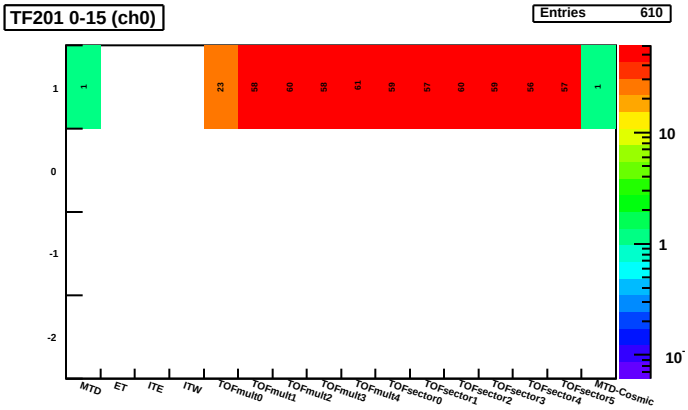

Input to FE003 QT board
Entries9687168
ADC
channel


Input to FE004 QT board
Entries9687168
ADC
channel

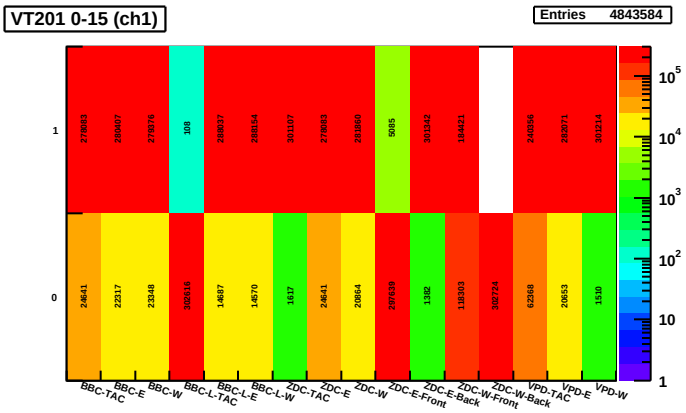
TF201 0-15 (ch0)



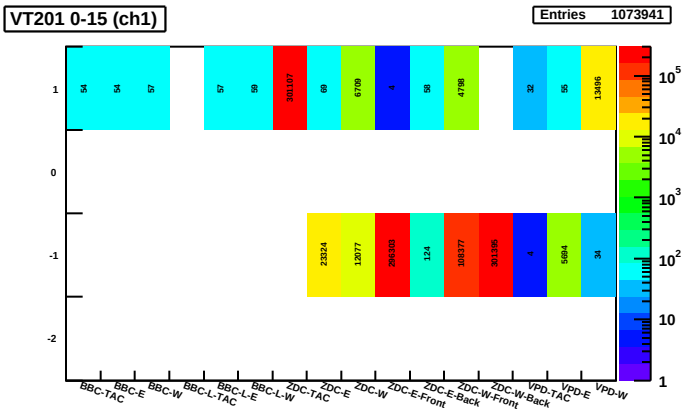
TF201 0-15 (ch0)



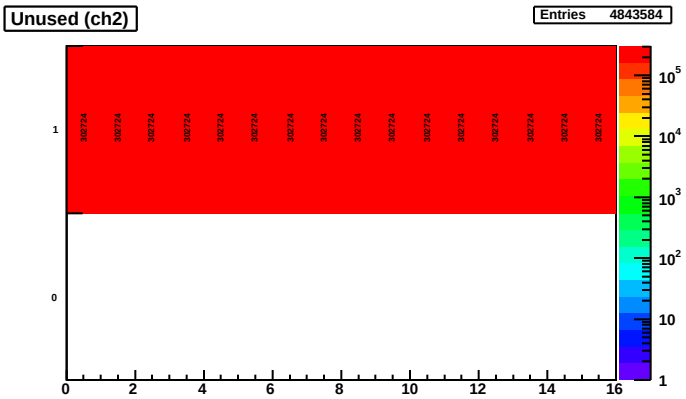
VT201 0-15 (ch1)

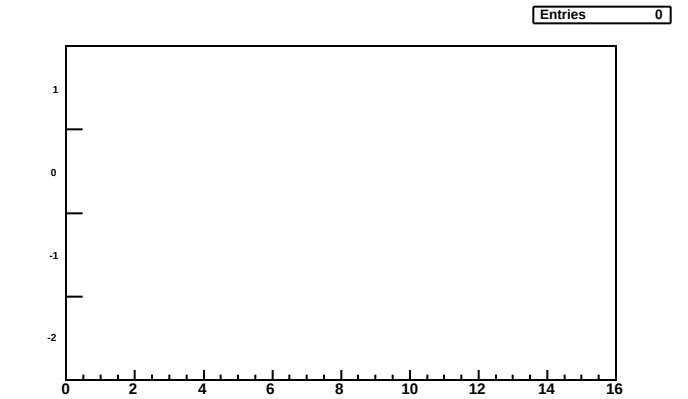


VT201 0-15 (ch1)

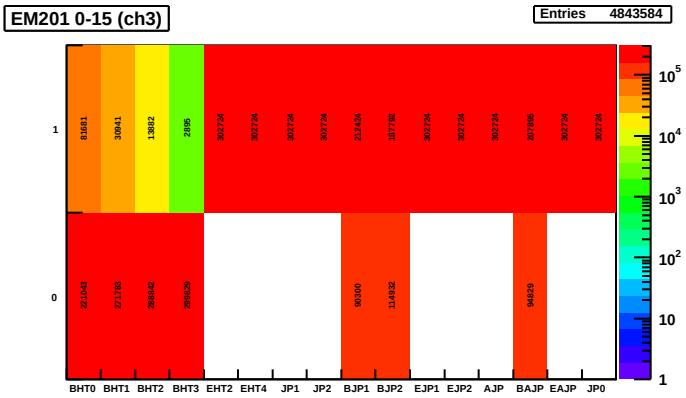


Unused (ch2)

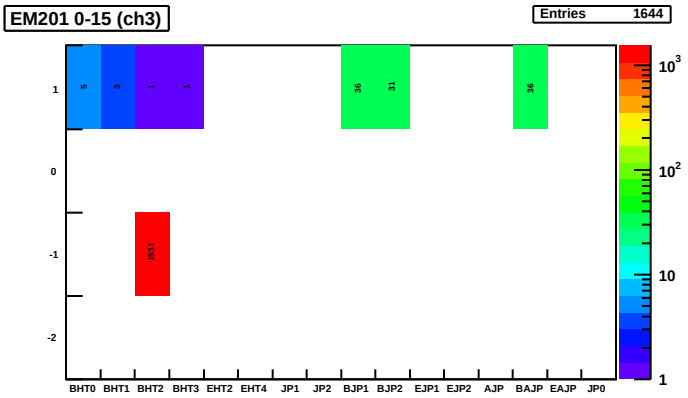




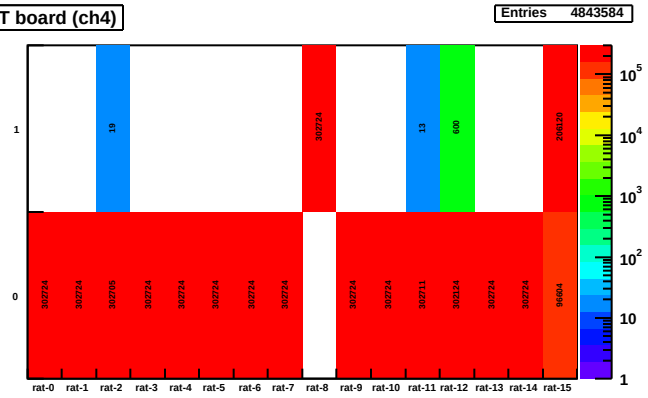
EM201 0-15 (ch3)

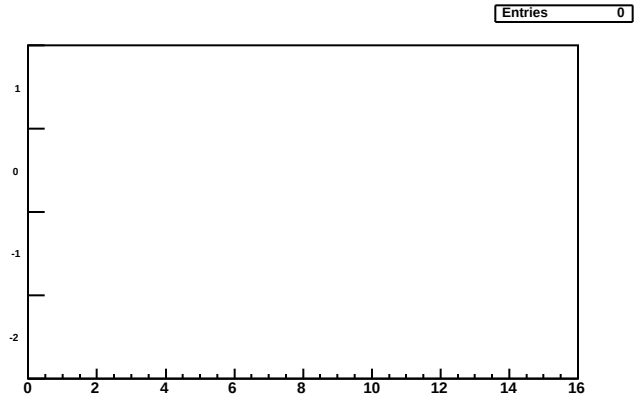


EM201 0-15 (ch3)

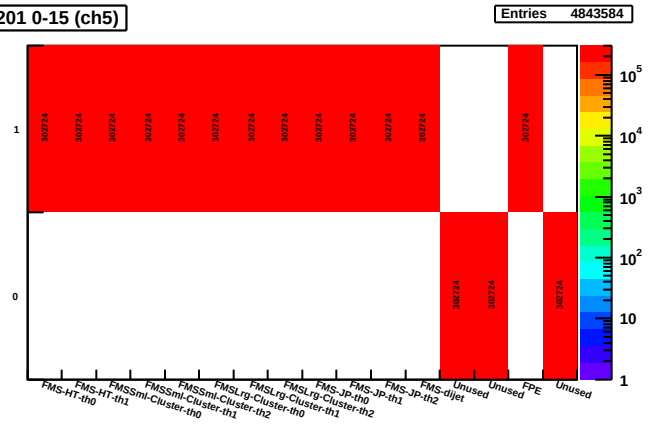


RAT board (ch4)

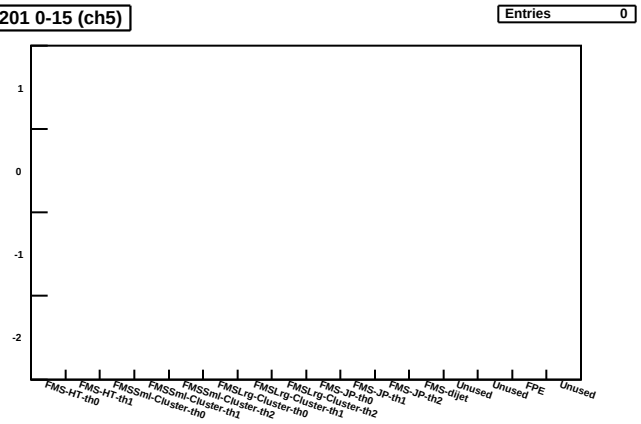




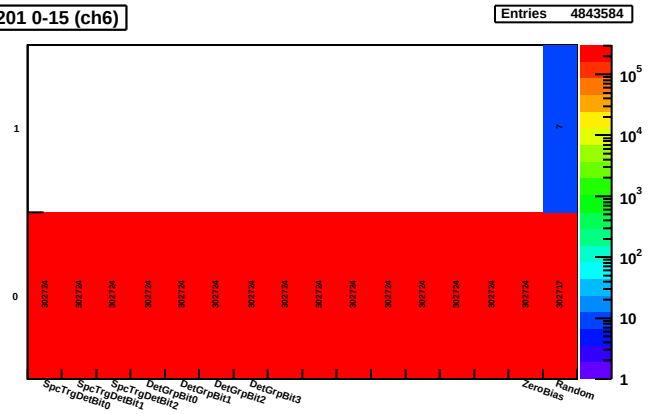
FP201 0-15 (ch5)

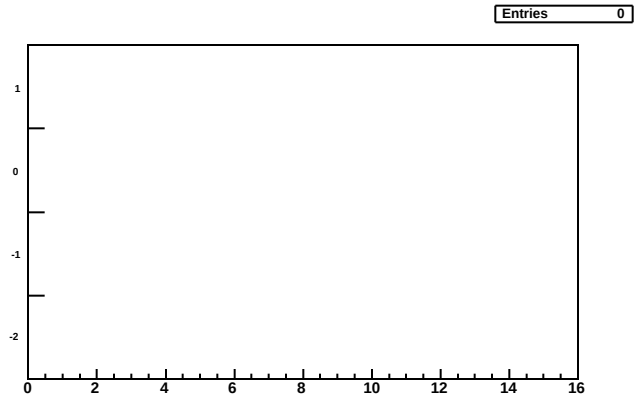
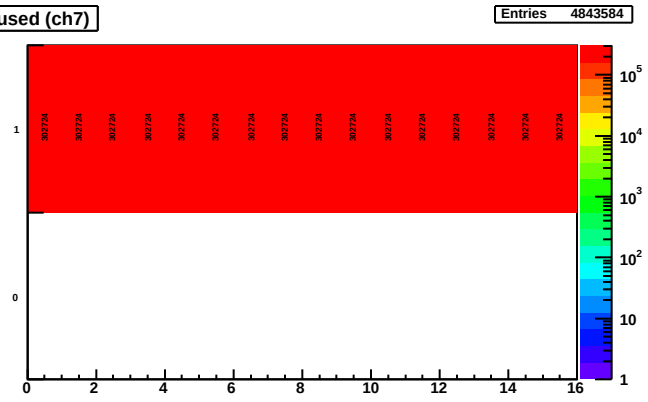
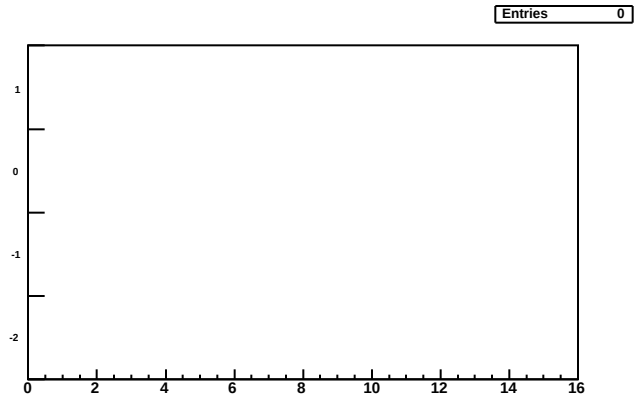


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)