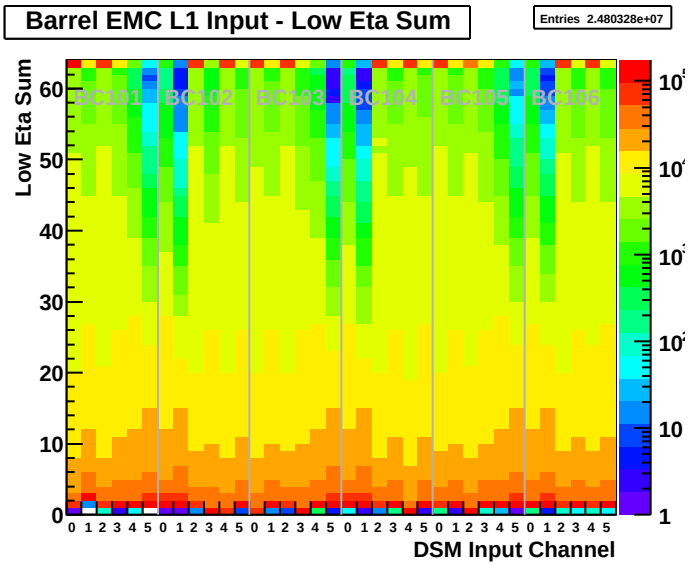
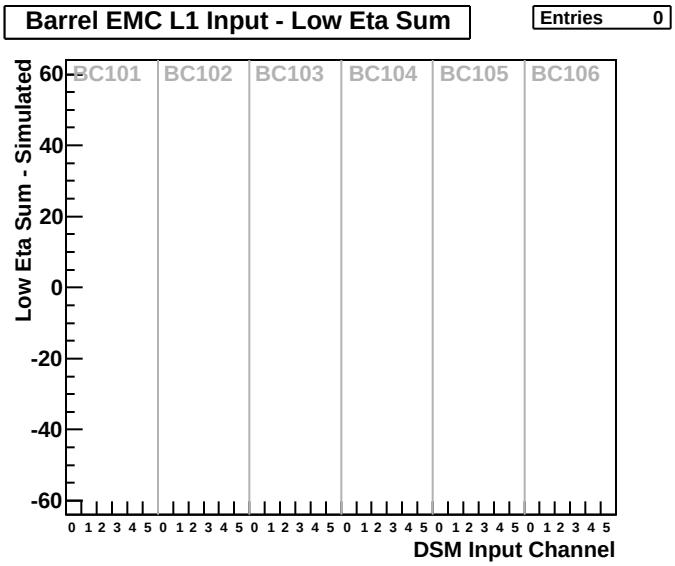
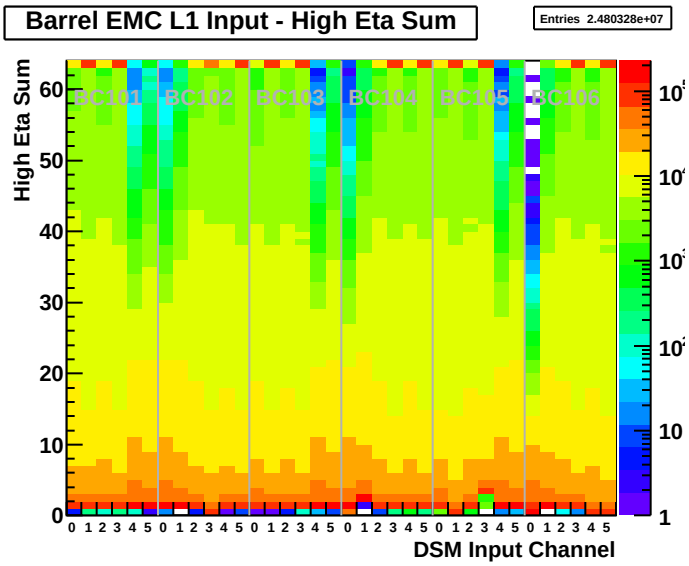
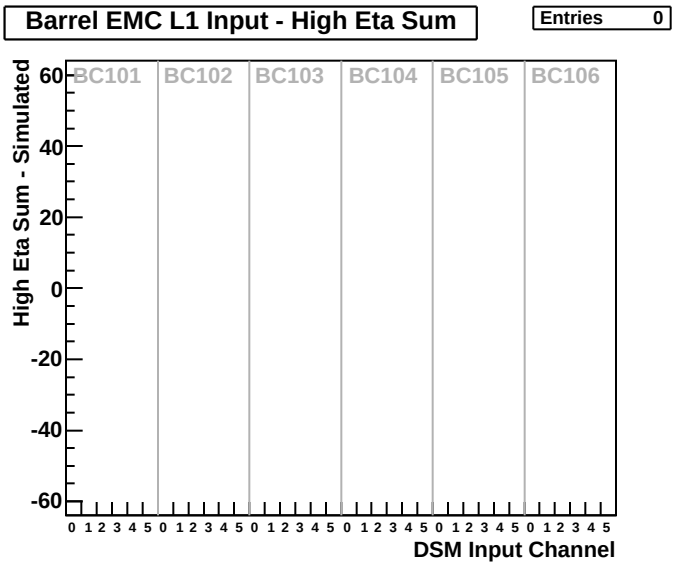
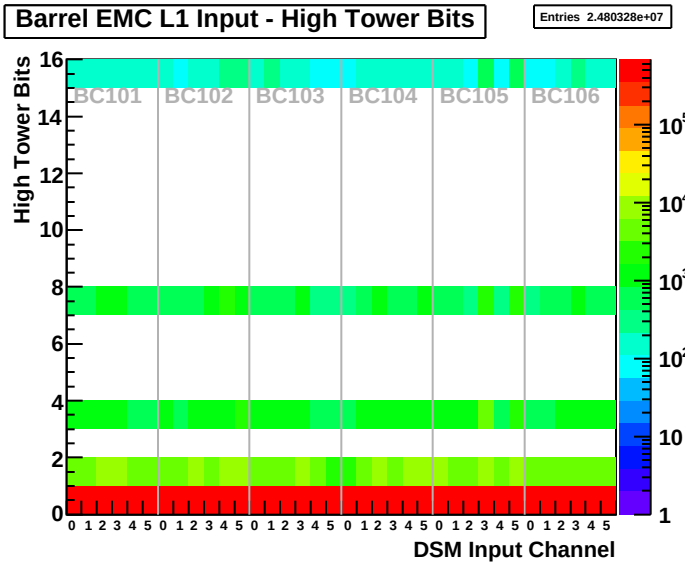
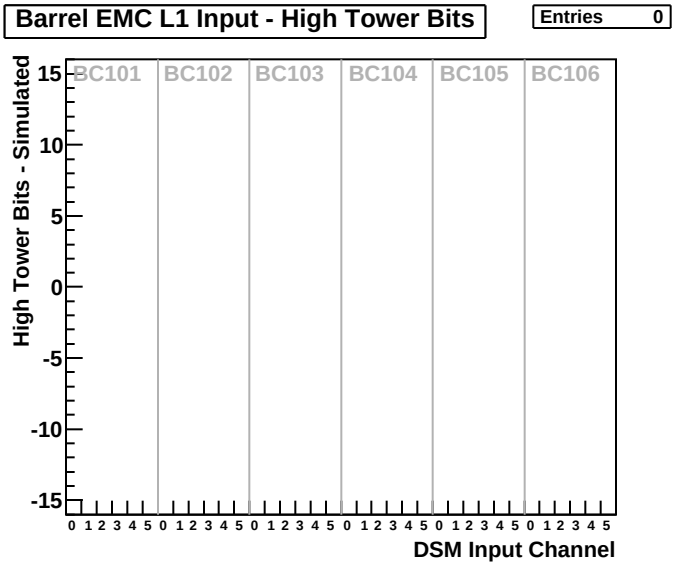

Barrel EMC L1 Input - Low Eta Sum
Entries 2.480328e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

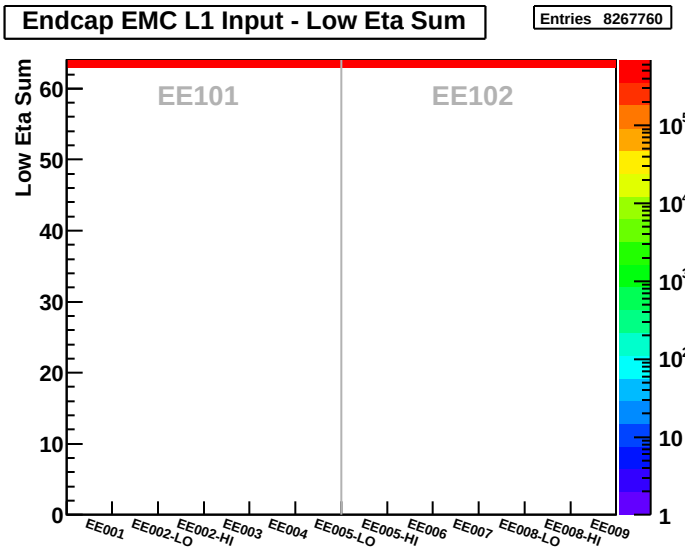

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

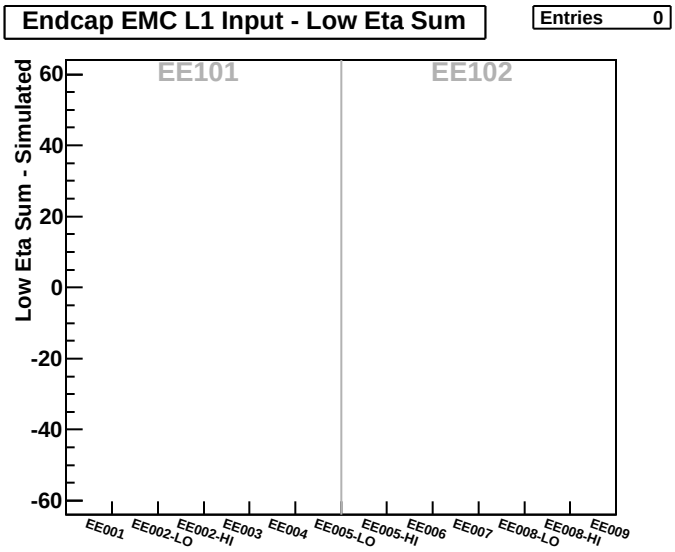

Barrel EMC L1 Input - High Eta Sum
Entries 2.480328e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

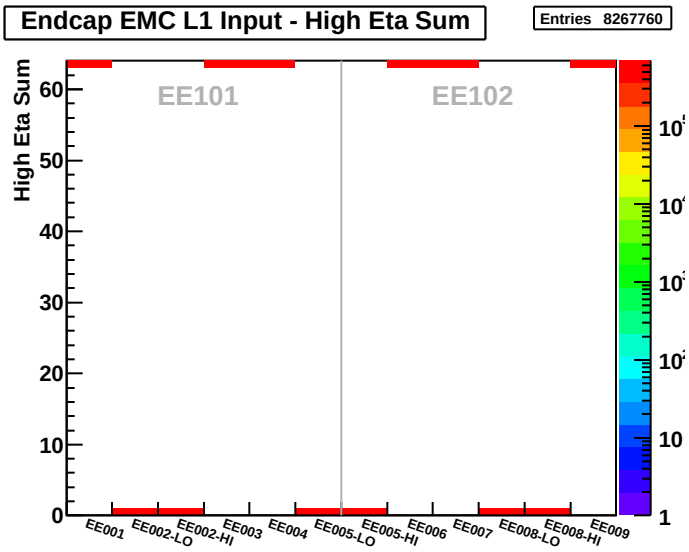

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

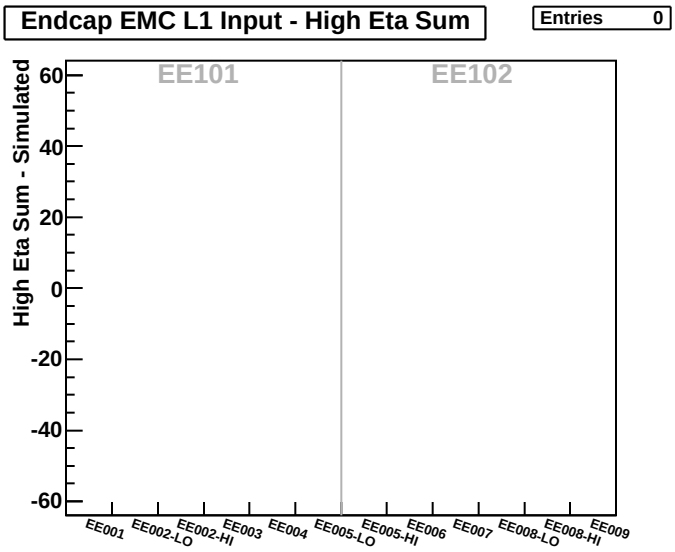

Barrel EMC L1 Input - High Tower Bits
Entries 2.480328e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

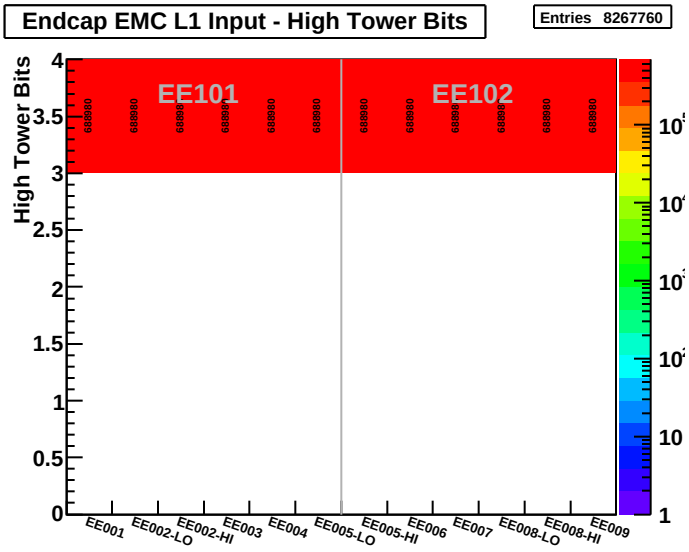

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

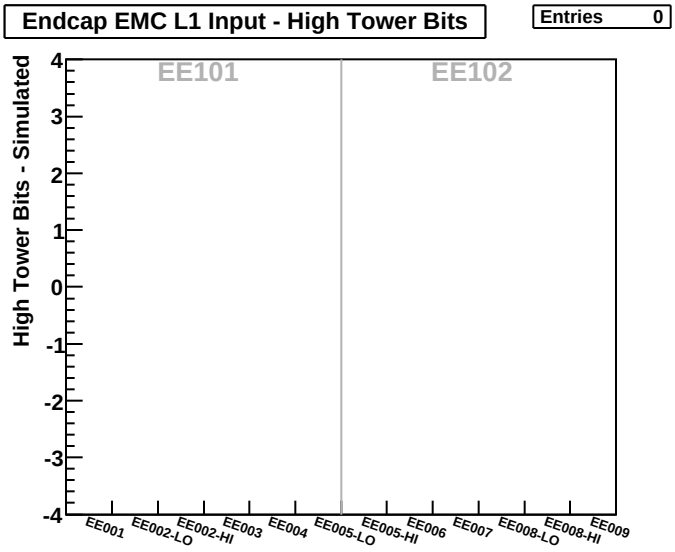

Endcap EMC L1 Input - Low Eta Sum
Entries 8267760
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

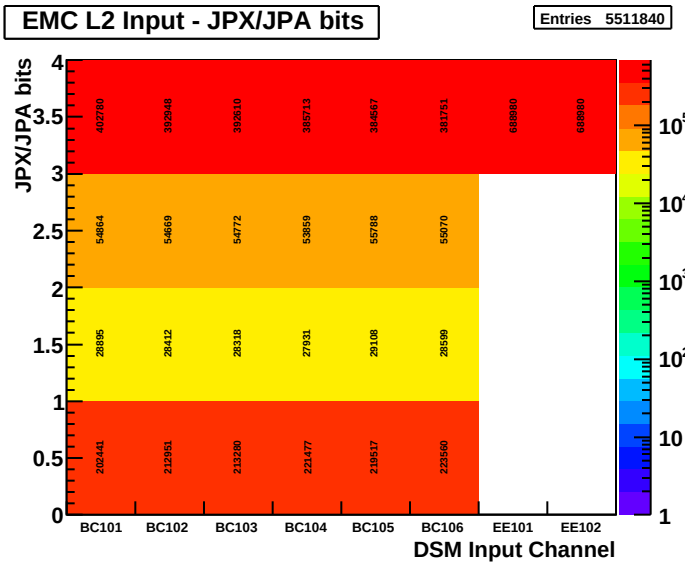

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

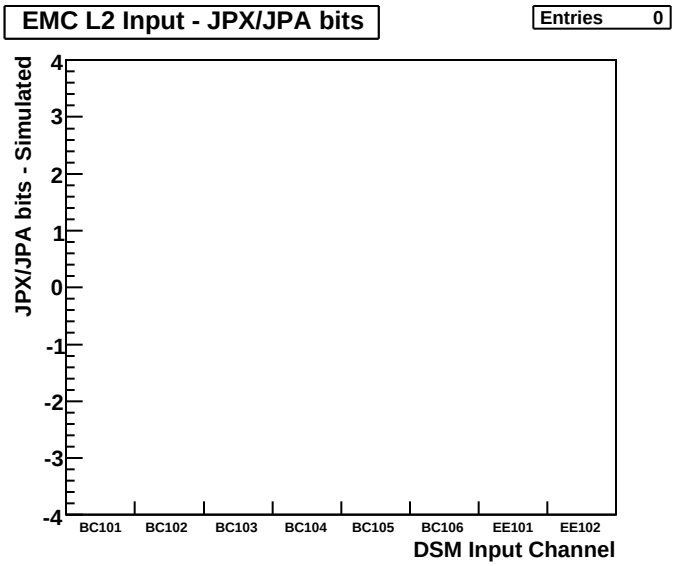

Endcap EMC L1 Input - High Eta Sum
Entries 8267760
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

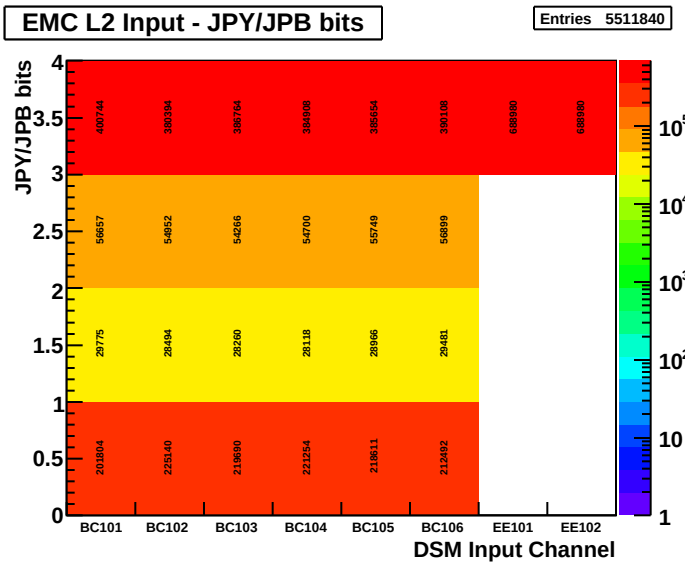

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

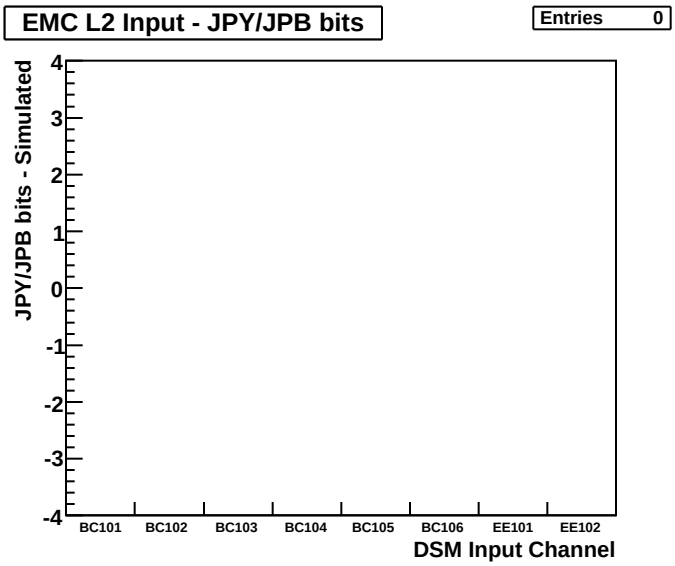

Endcap EMC L1 Input - High Tower Bits
Entries 8267760
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

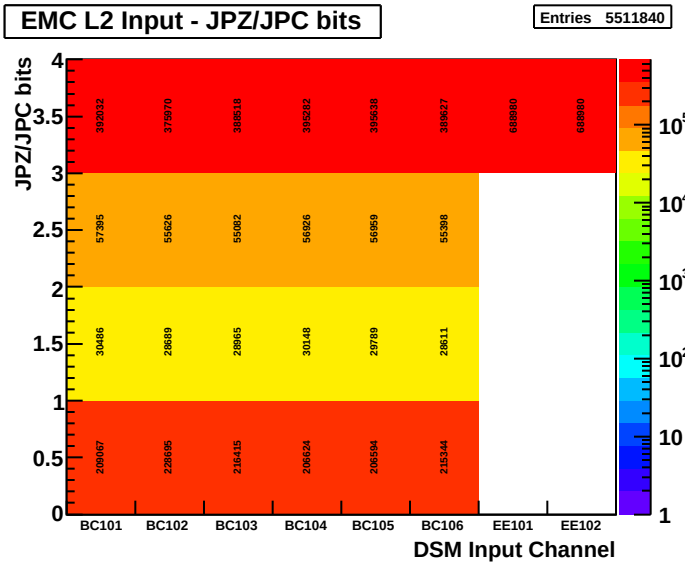

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

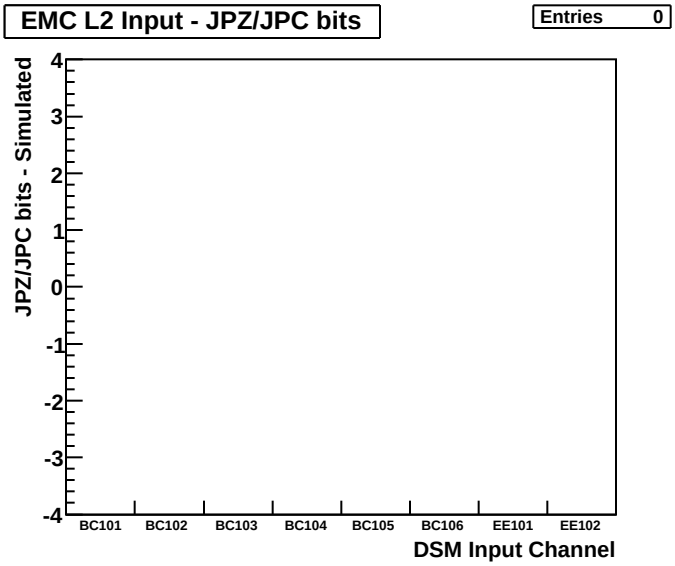

EMC L2 Input - JPX/JPA bits
Entries 5511840
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
402780 392948 392610 385713 384567 381751 688980 688980
54864 54669 54772 53859 55788 55070
28895 28412 28318 27931 29108 28599
202441 212951 213280 221477 219517 223560

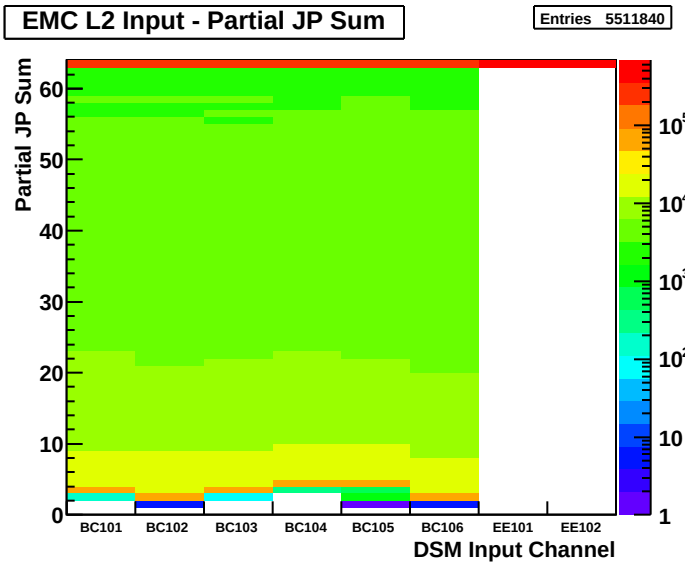

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

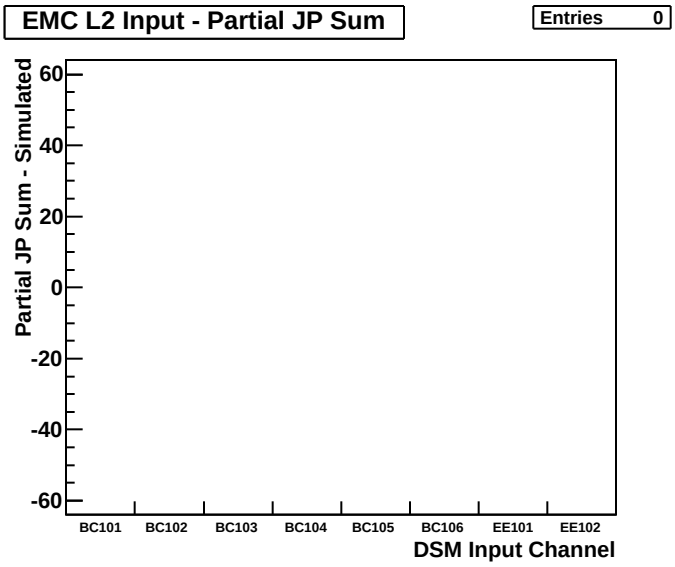

EMC L2 Input - JPY/JPB bits
Entries 5511840
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
400744 390394 386764 384908 385654 390108 688980 688980
56657 54952 54266 54700 55749 56899
29775 28494 28260 28118 28966 29481
201804 223140 219690 221254 218611 212492

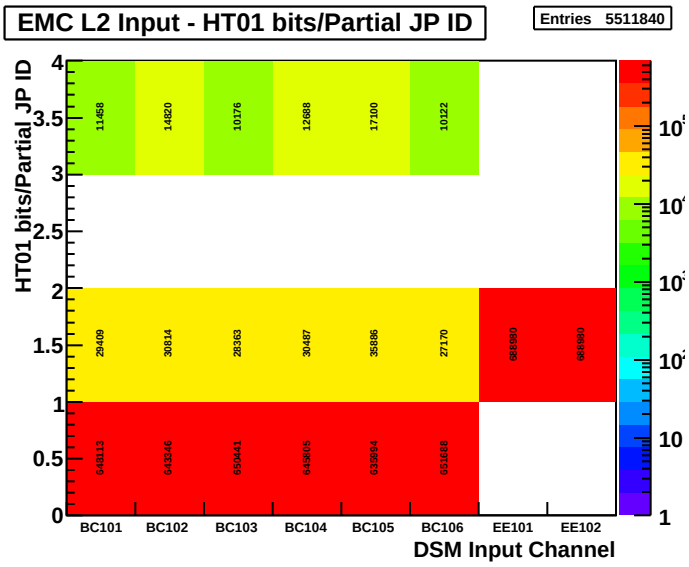

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

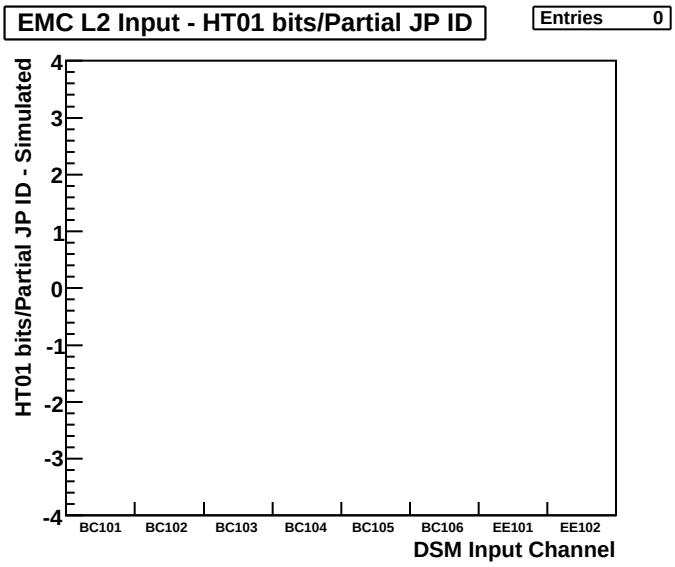

EMC L2 Input - JPZ/JPC bits
Entries 5511840
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
393032 379970 380518 393282 395638 389627 688980 688980
57395 55626 55082 56926 56959 55398
30486 28689 28965 30148 29789 28611
209067 228695 216415 206624 206594 215344

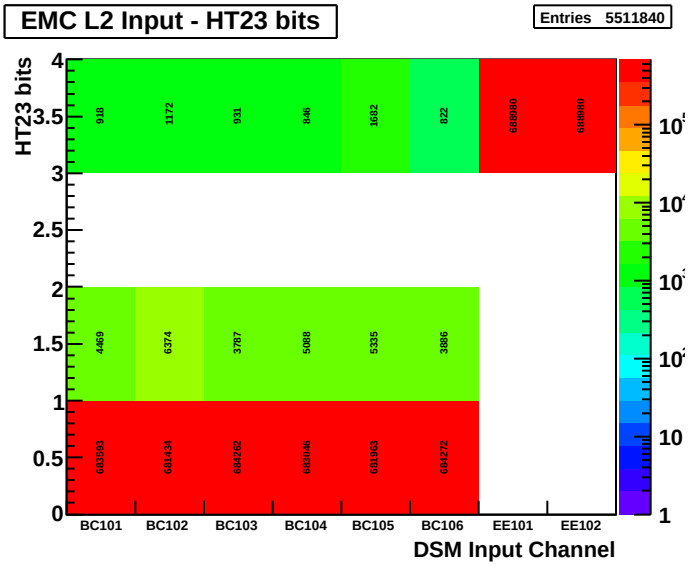

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

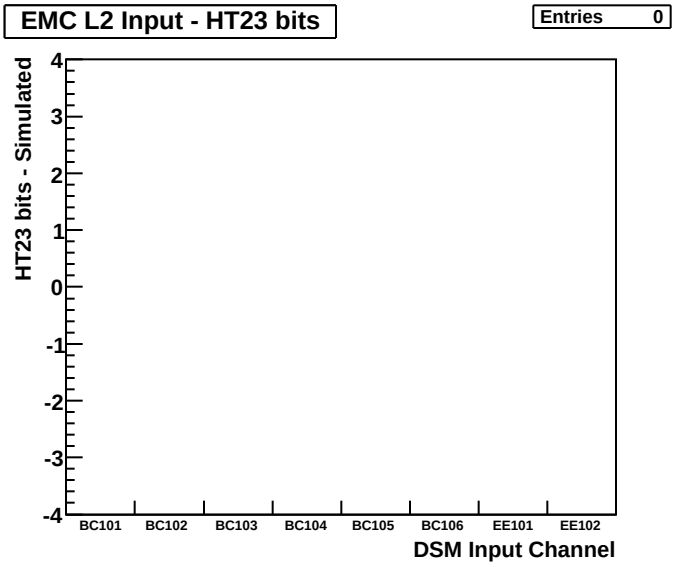

EMC L2 Input - Partial JP Sum
Entries 5511840
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

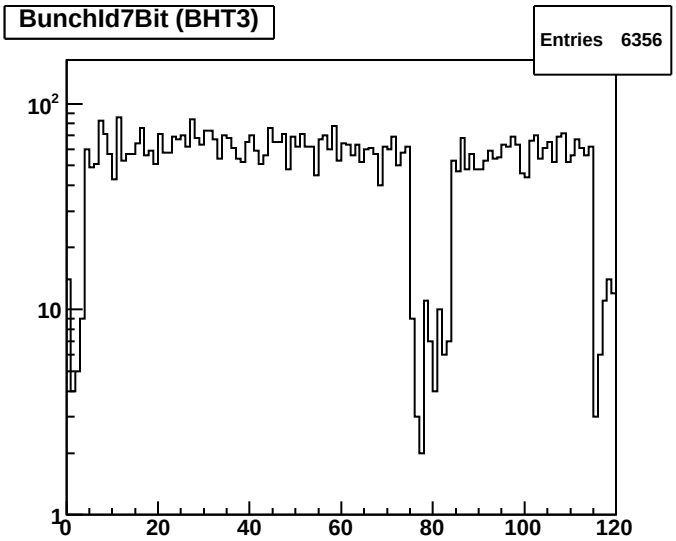

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

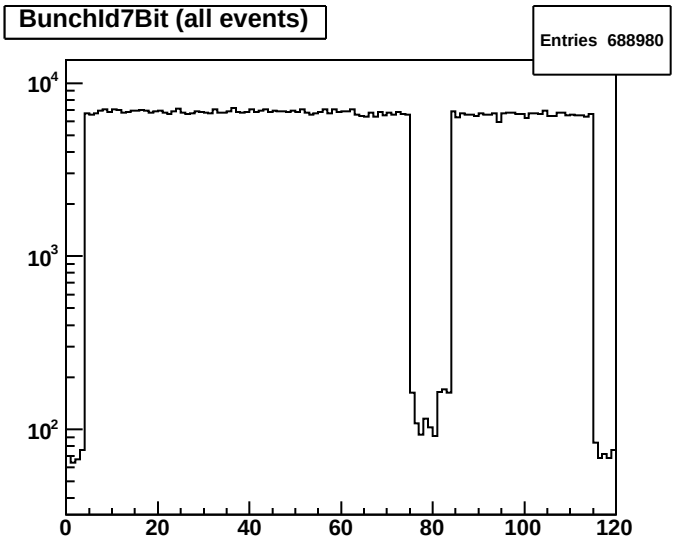

EMC L2 Input - HT01 bits/Partial JP ID
Entries 5511840
HT01 bits/Partial JP ID
11458 14820 10176 12868 17100 10122
29409 30814 28363 30487 35886 27170 688980 688980
646113 643346 650441 645805 635994 651688
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

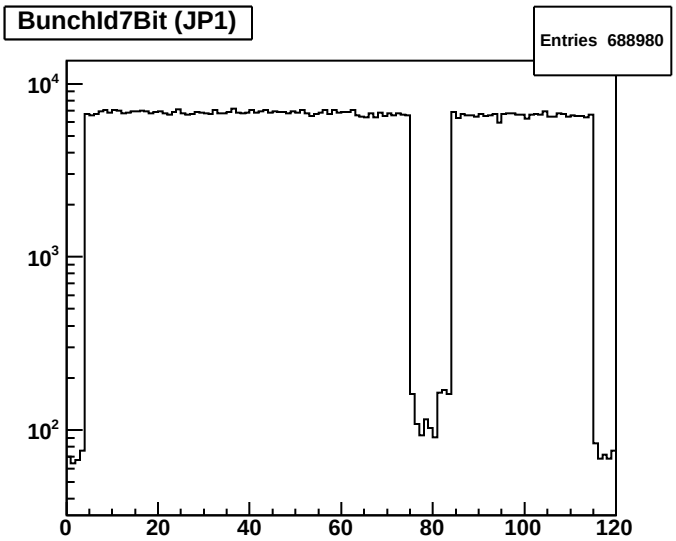

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

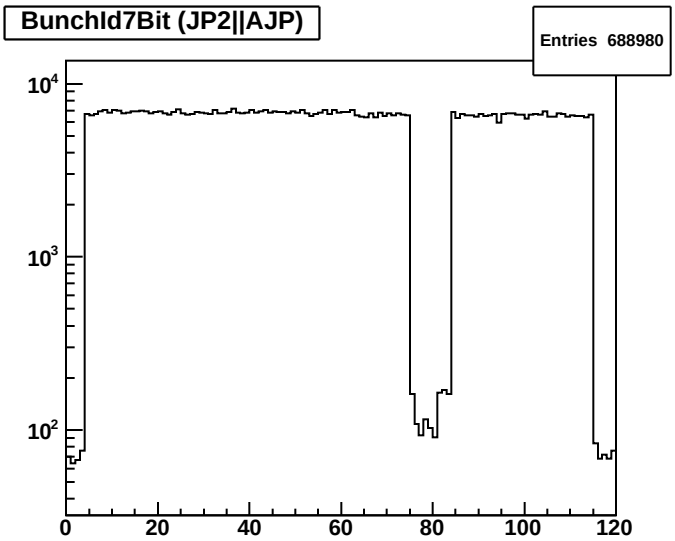

EMC L2 Input - HT23 bits
Entries 5511840
HT23 bits
918 1172 931 846 1682 822 688980 688980
4469 6374 3787 5068 5335 3886
683593 681434 684262 683046 681963 684272
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

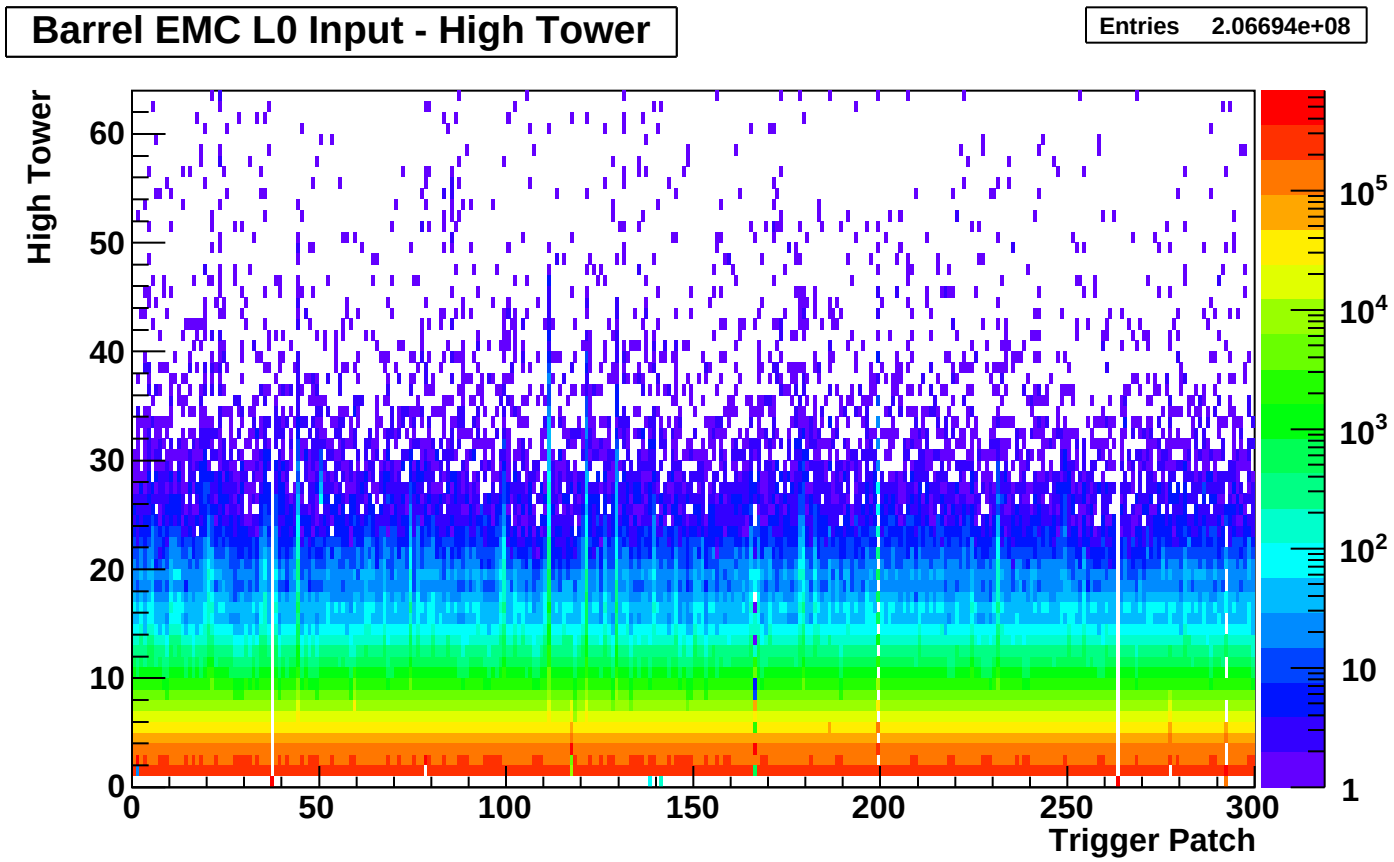

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

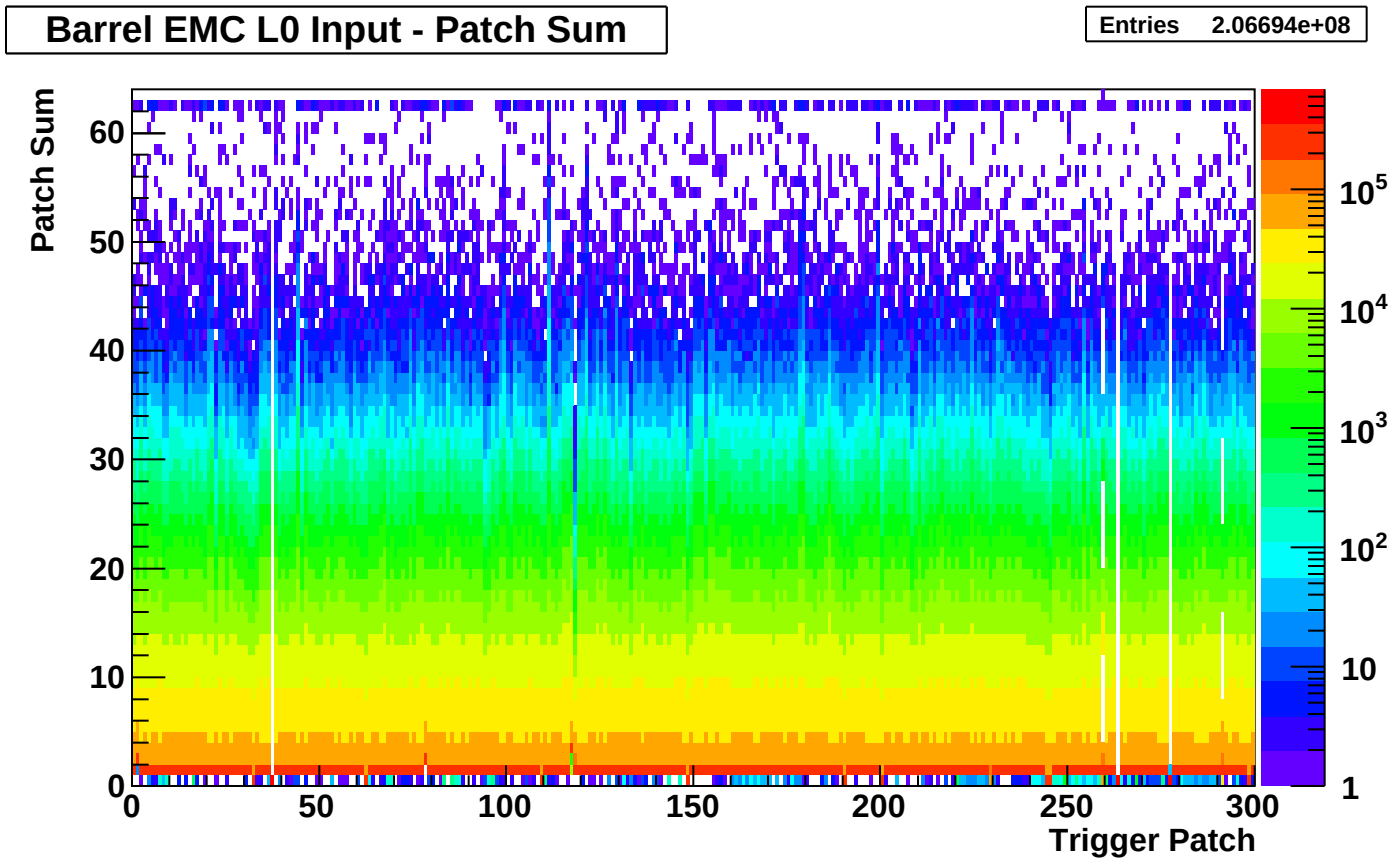

BunchId7Bit (BHT3)
Entries 6356
10^2
10
1
0
20
40
60
80
100
120

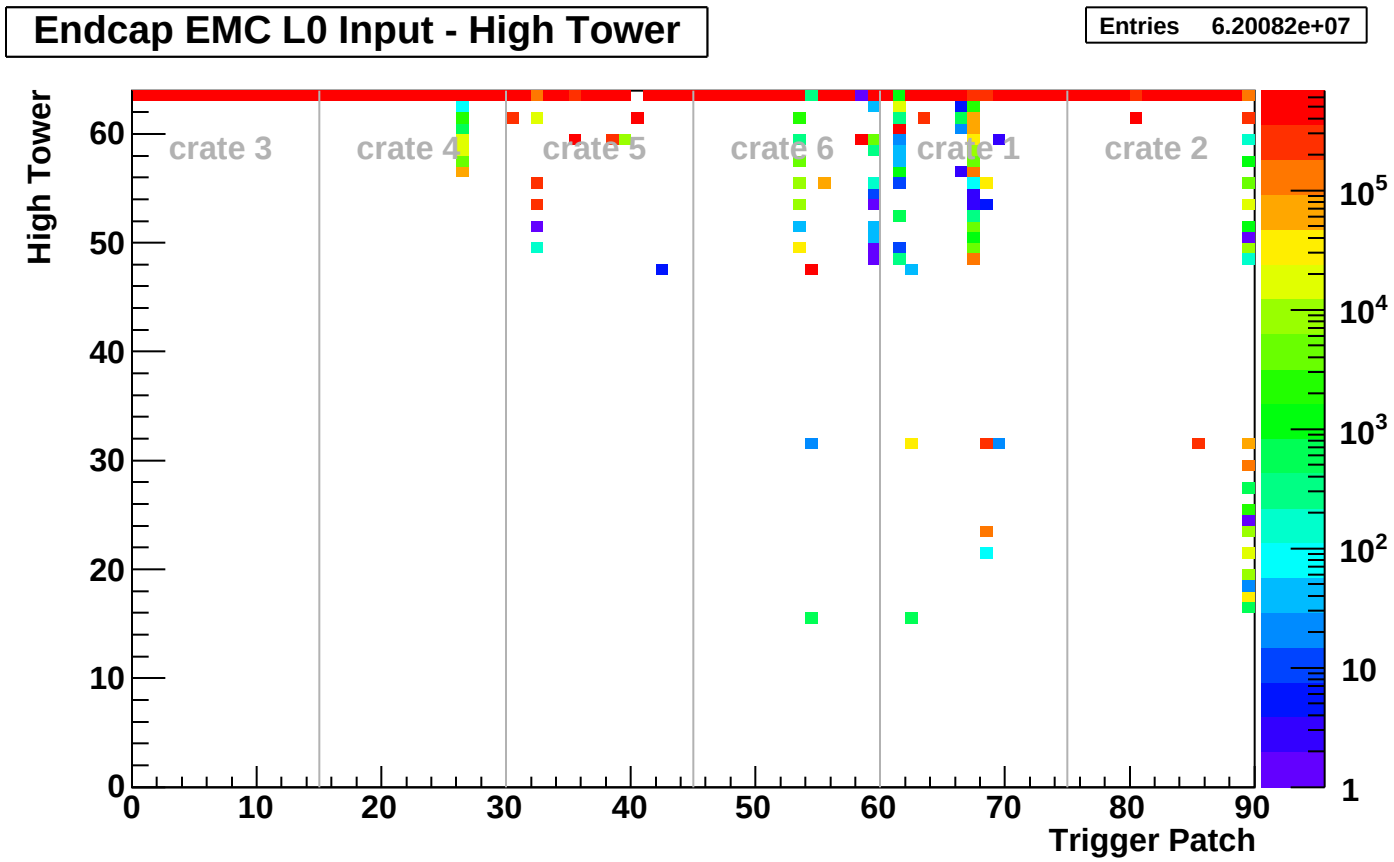

BunchId7Bit (all events)
Entries 688980
10^4
10^3
10^2
0
20
40
60
80
100
120

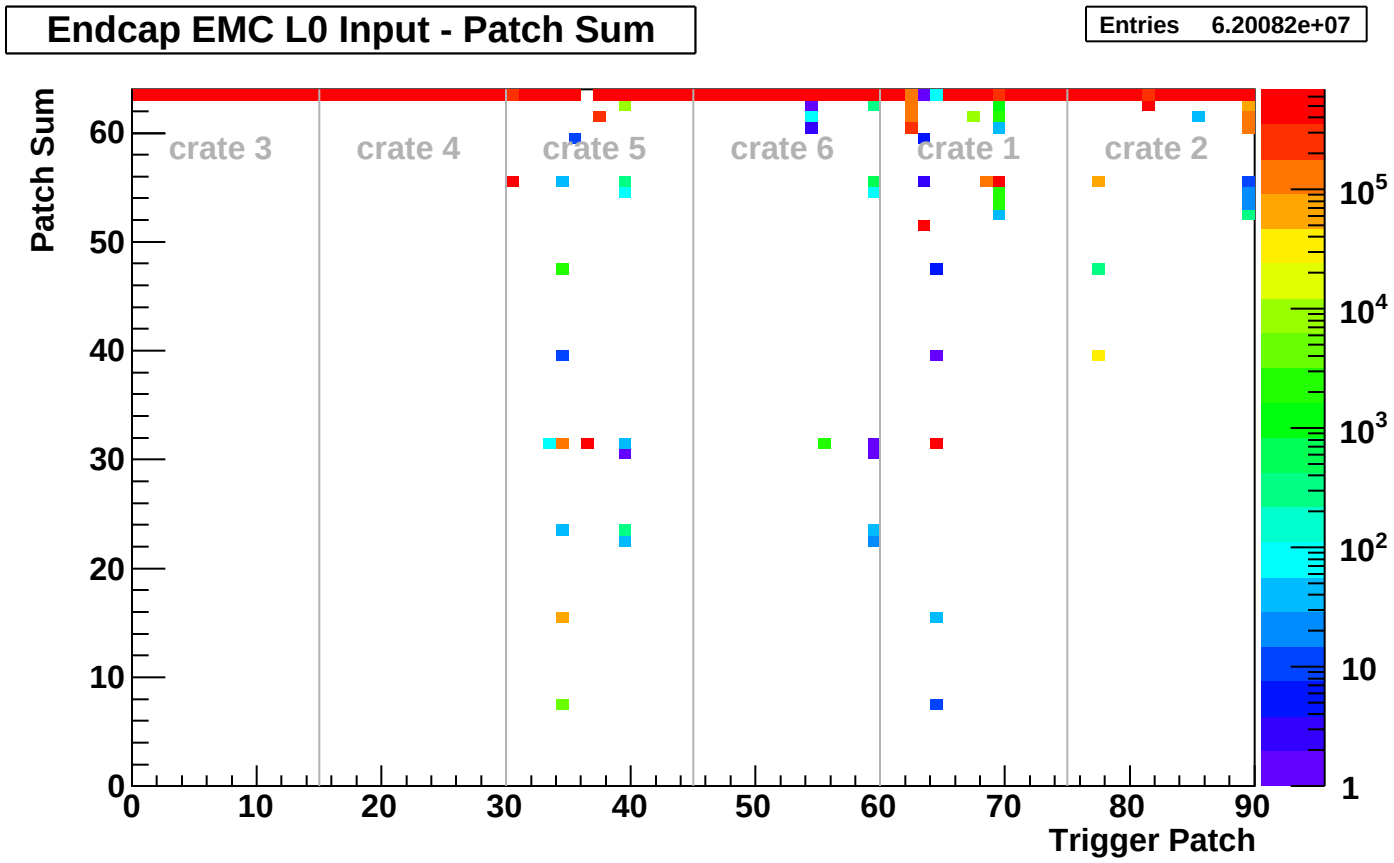

BunchId7Bit (JP1)
Entries 688980
10^4
10^3
10^2
0
20
40
60
80
100
120

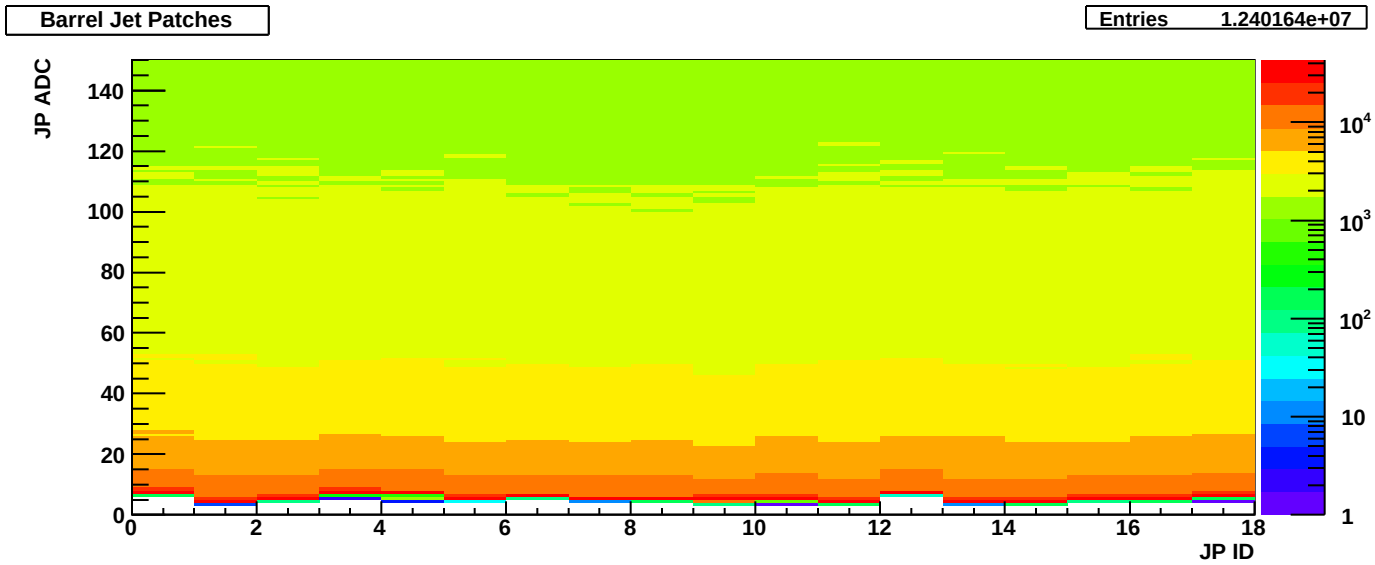

BunchId7Bit (JP2||AJP)
Entries 688980
10^4
10^3
10^2
0
20
40
60
80
100
120

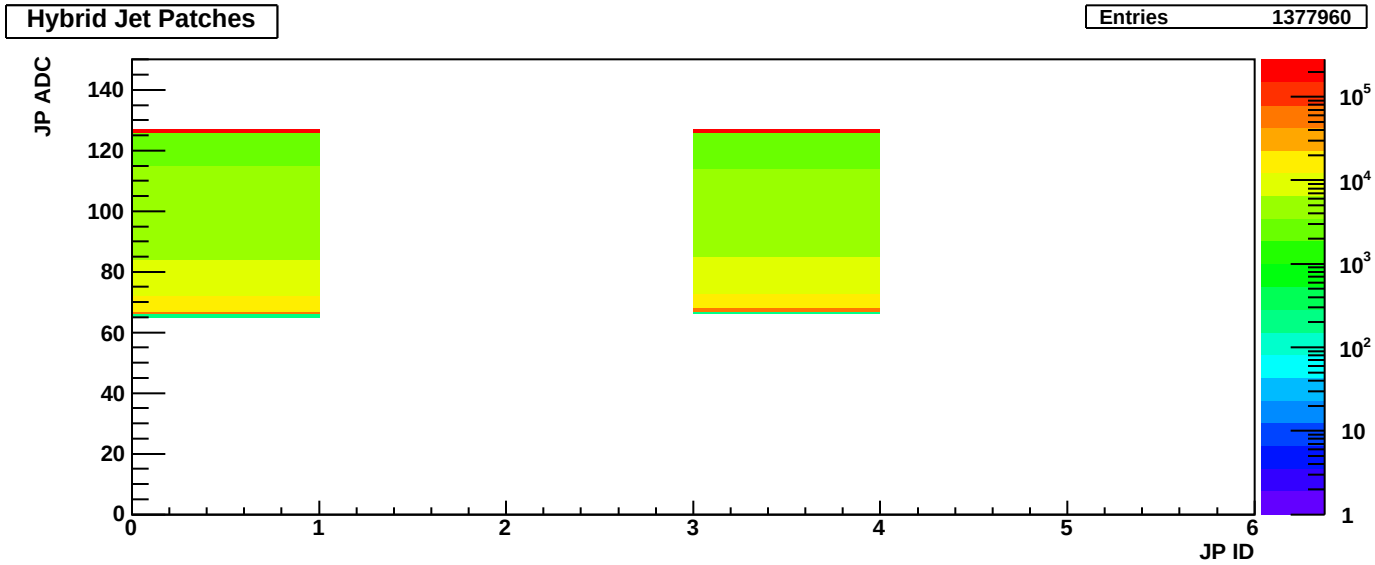

Barrel EMC L0 Input - High Tower
Entries 2.06694e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

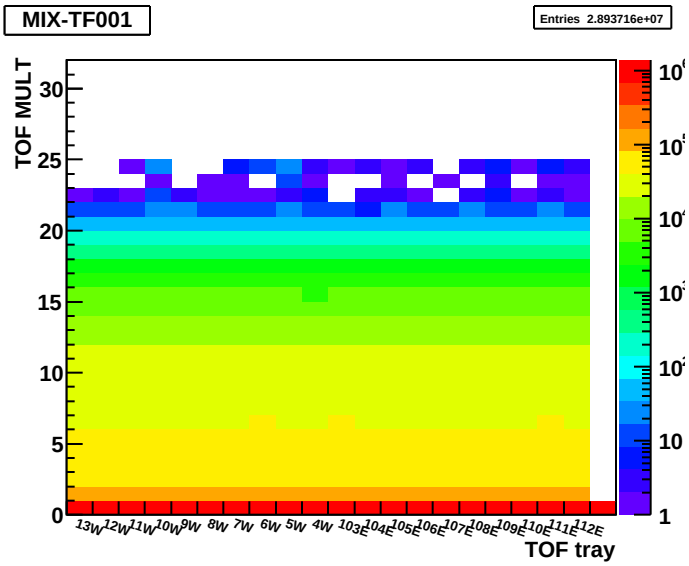

Barrel EMC L0 Input - Patch Sum
Entries 2.06694e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

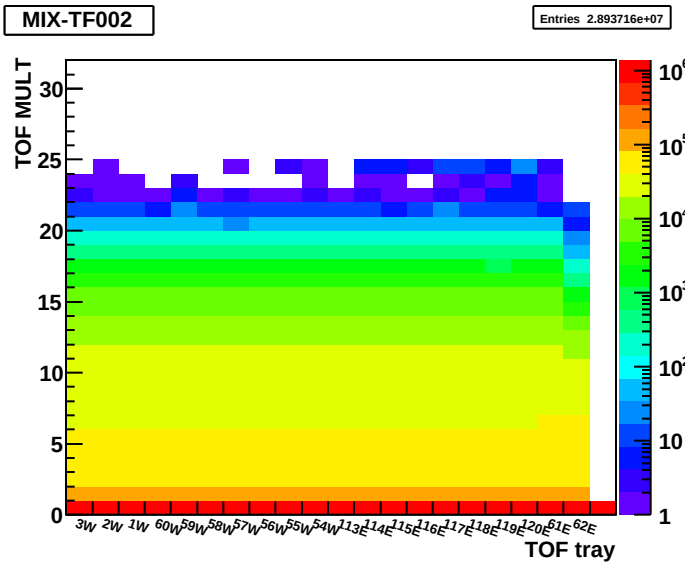

Endcap EMC L0 Input - High Tower
Entries 6.20082e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

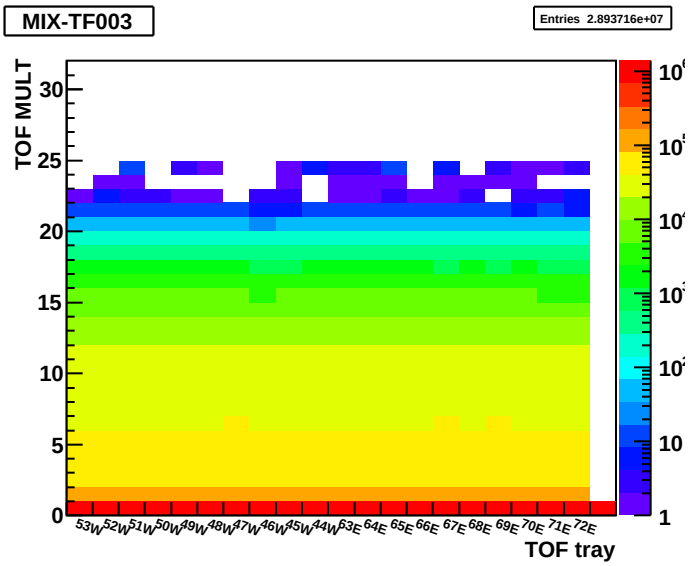

Endcap EMC L0 Input - Patch Sum
Entries 6.20082e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

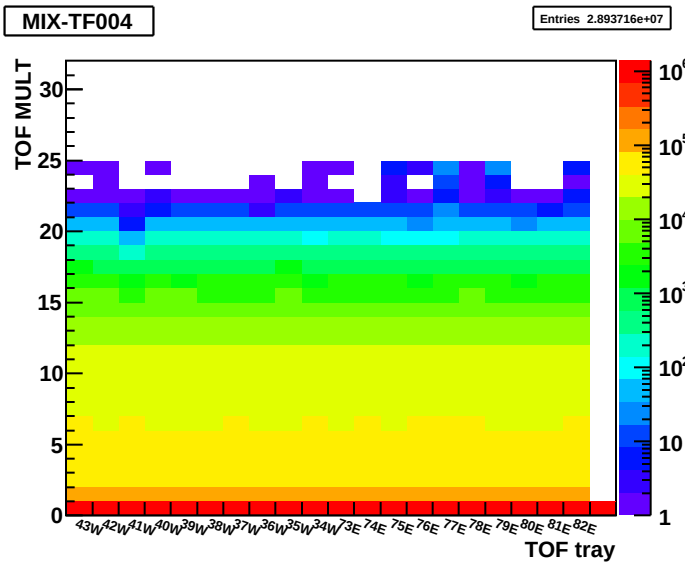

Barrel Jet Patches
Entries 1.240164e+07
JP ADC
JP ID

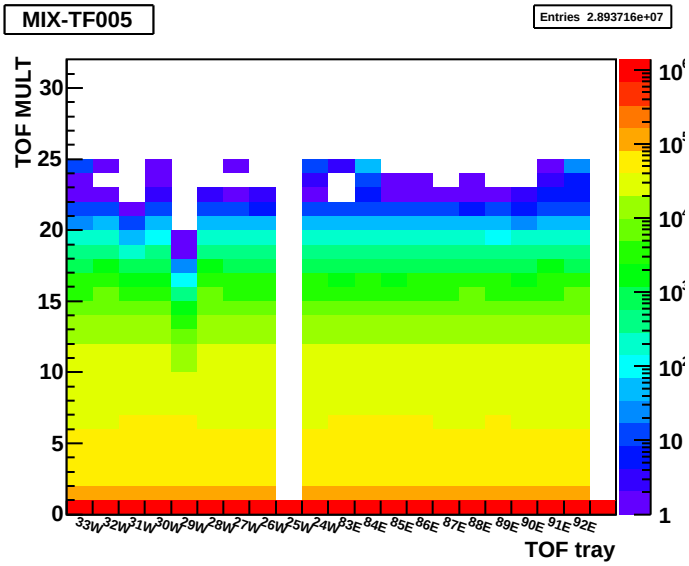

Hybrid Jet Patches
Entries 1377960
JP ADC
JP ID

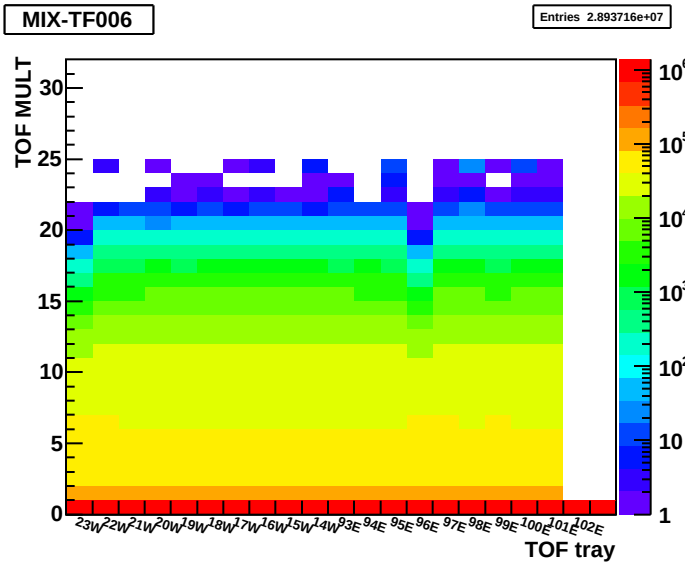

MIX-TF001
Entries 2.893716e+07
TOF MULT
TOF tray

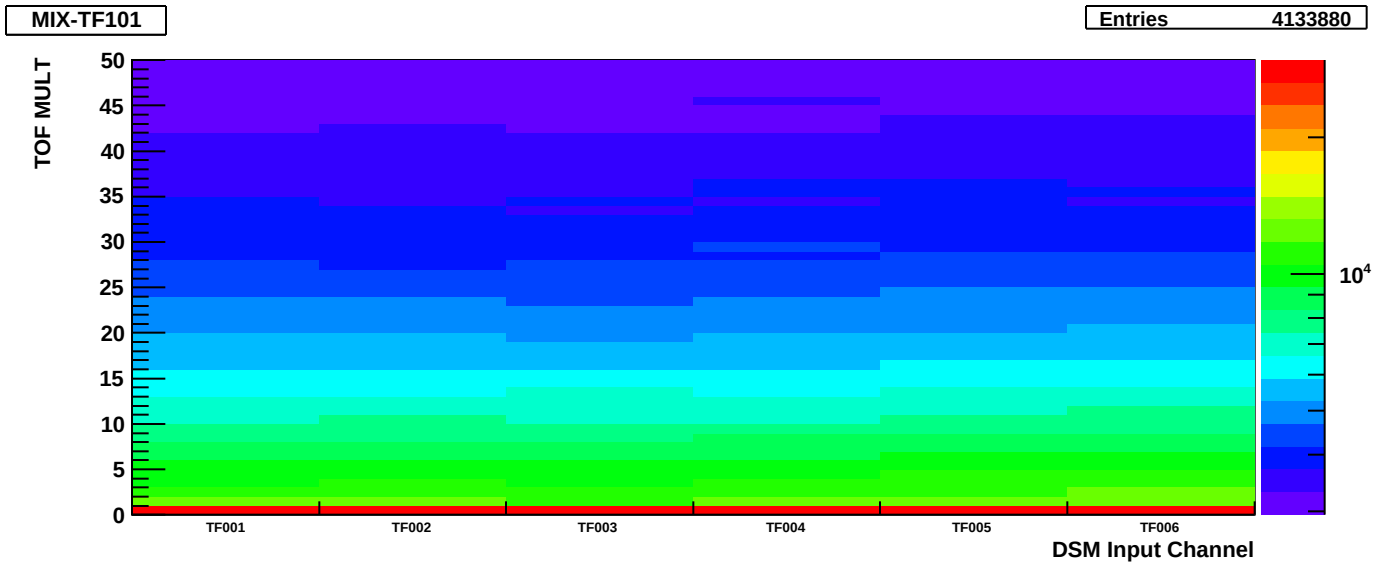

MIX-TF002
Entries 2.893716e+07
TOF MULT
TOF tray

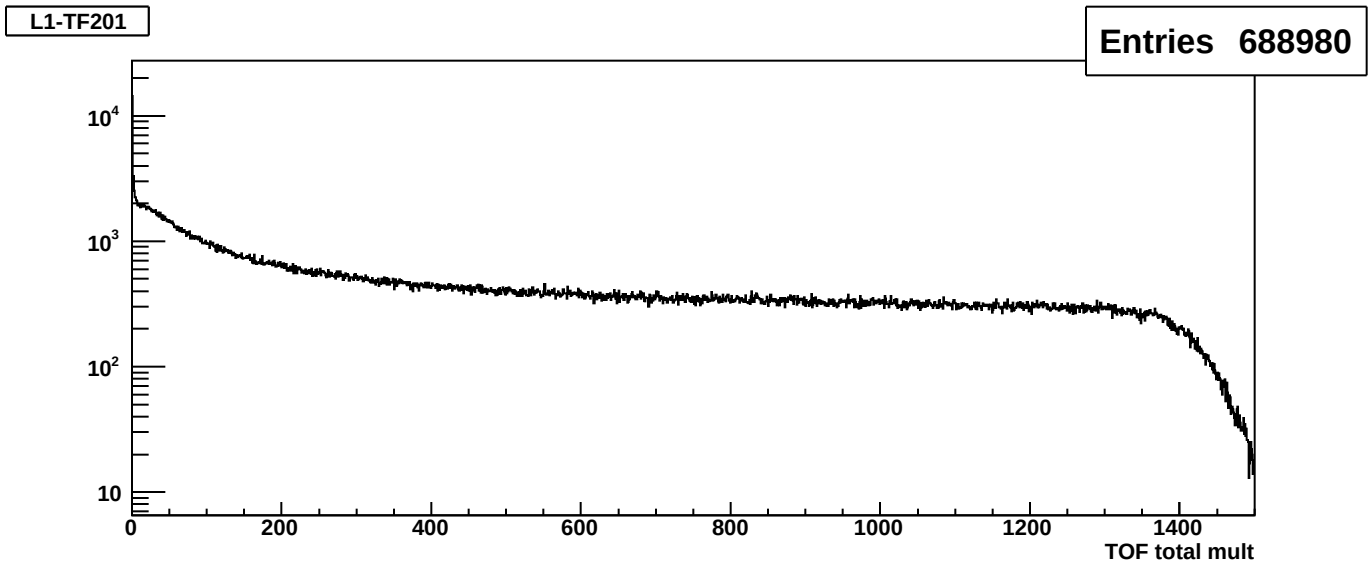

MIX-TF003
Entries 2.893716e+07
TOF MULT
TOF tray

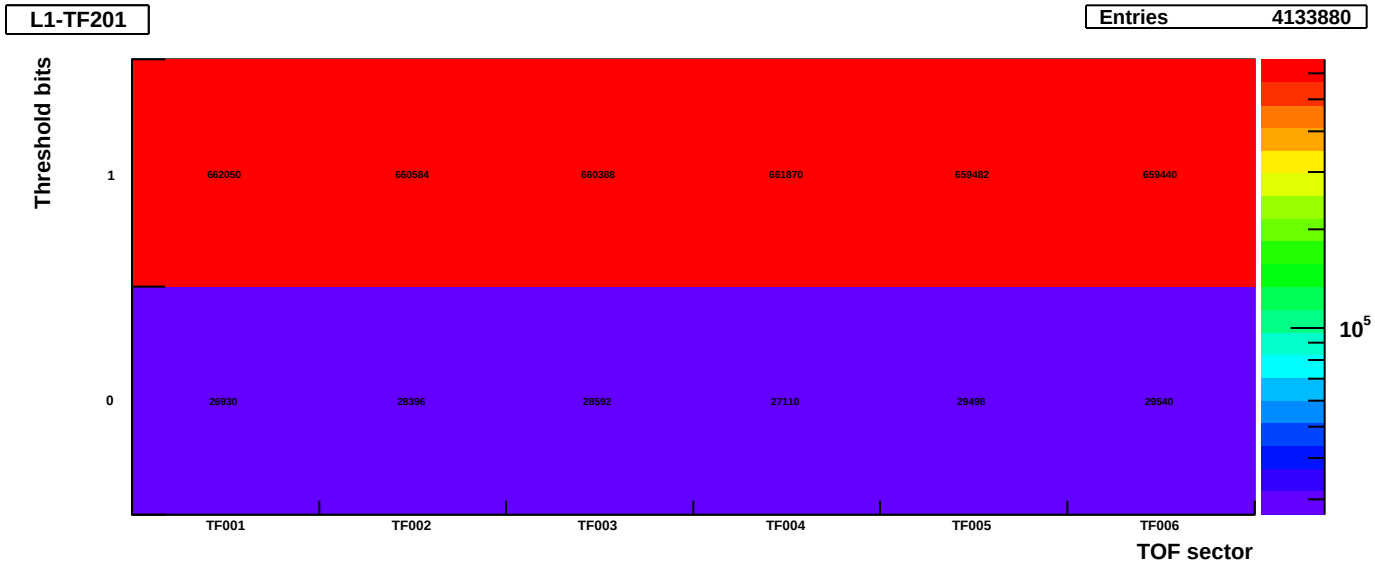

MIX-TF004
Entries 2.893716e+07
TOF MULT
TOF tray

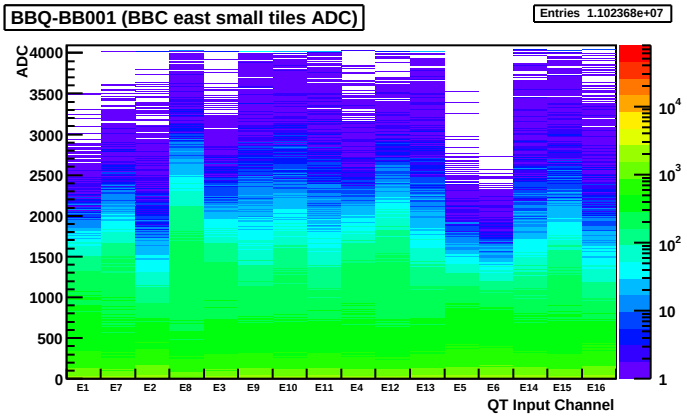

MIX-TF005
Entries 2.893716e+07
TOF MULT
TOF tray

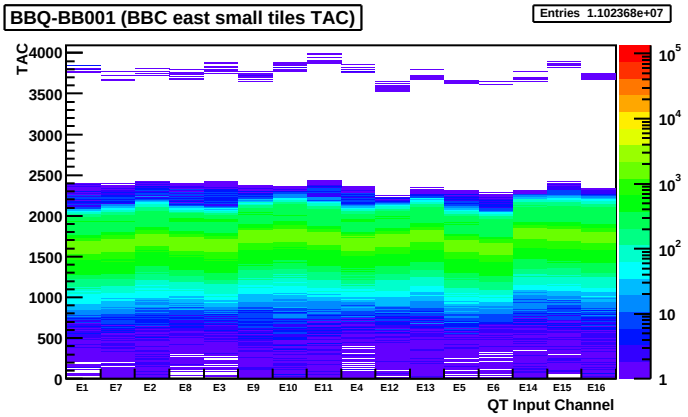

MIX-TF006
Entries 2.893716e+07
TOF MULT
TOF tray

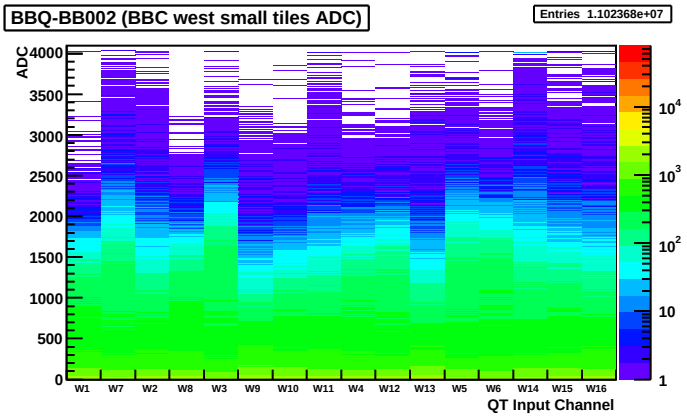

MIX-TF101
Entries 4133880
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

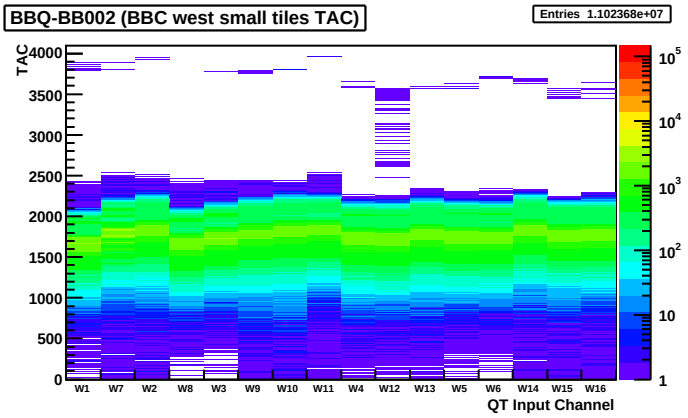

L1-TF201
Entries 688980
10^4
10^3
10^2
10
0
200
400
600
800
1000
1200
1400
TOF total mult

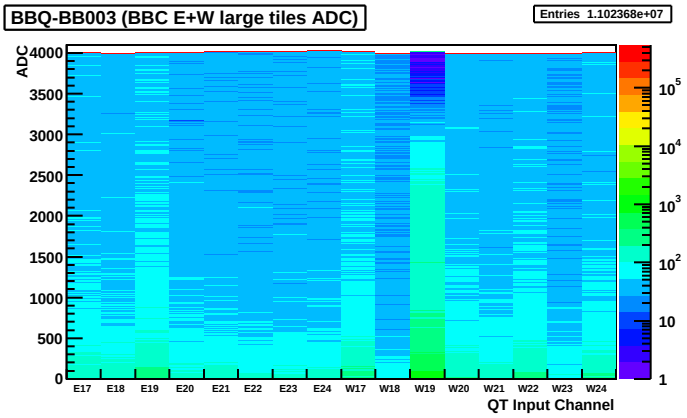

L1-TF201
Entries 4133880
Threshold bits
1
0
662050
660584
660388
661870
659482
659440
26930
28396
28592
27110
29498
29540
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

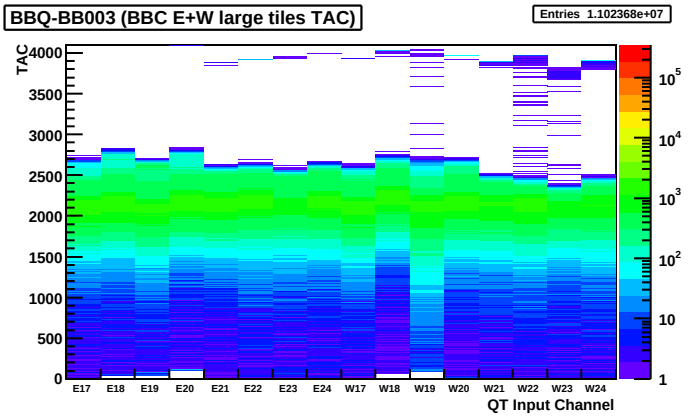

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.102368e+07
ADC
QT Input Channel

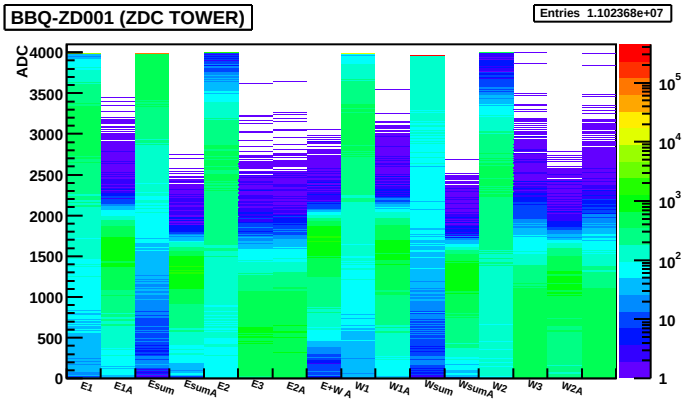

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.102368e+07
TAC
QT Input Channel

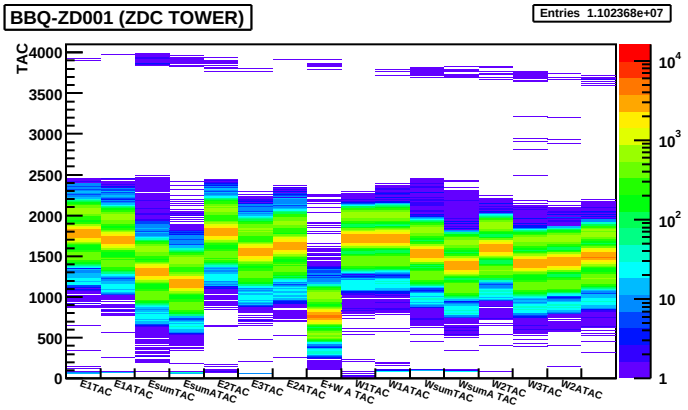

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.102368e+07
ADC
QT Input Channel

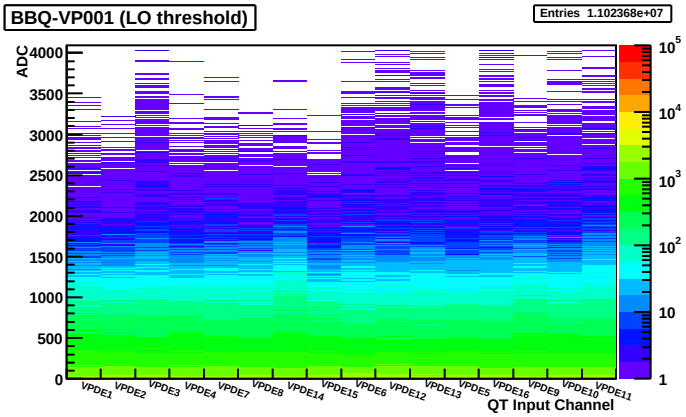

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.102368e+07
TAC
QT Input Channel

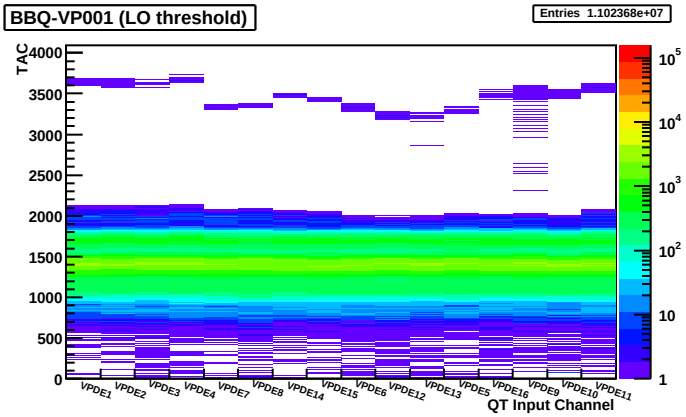

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.102368e+07
ADC
QT Input Channel

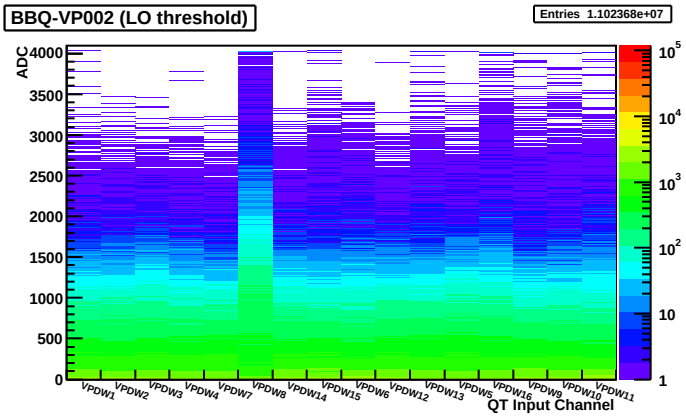

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.102368e+07
TAC
QT Input Channel

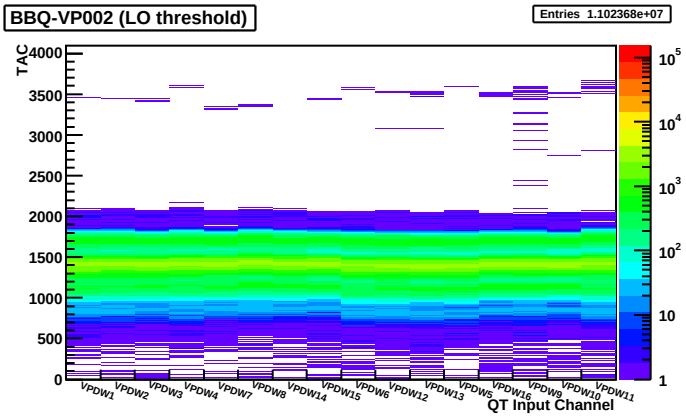

BBQ-ZD001 (ZDC TOWER)
Entries 1.102368e+07
ADC

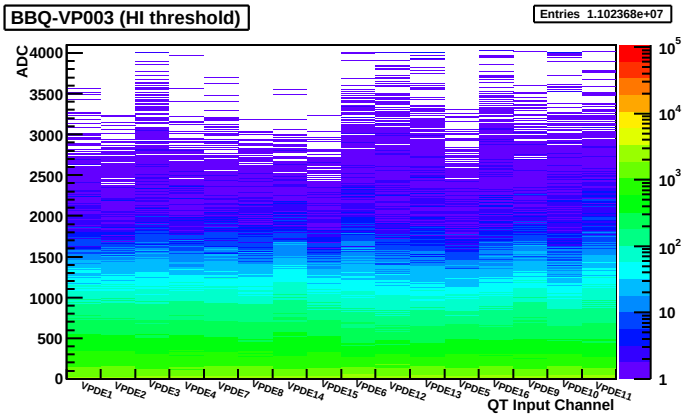

BBQ-ZD001 (ZDC TOWER)
Entries 1.102368e+07
TAC

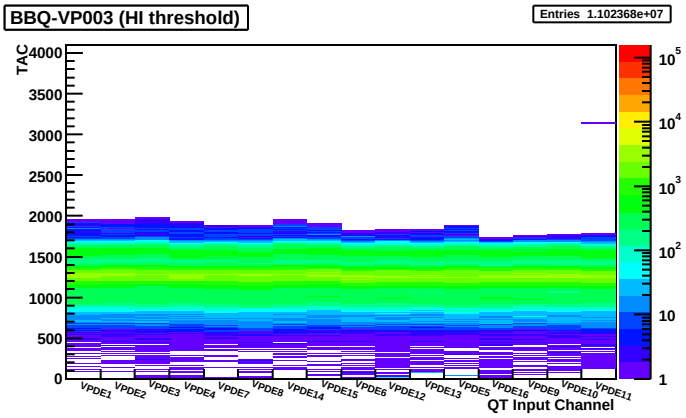

BBQ-VP001 (LO threshold)
Entries 1.102368e+07
ADC
QT Input Channel

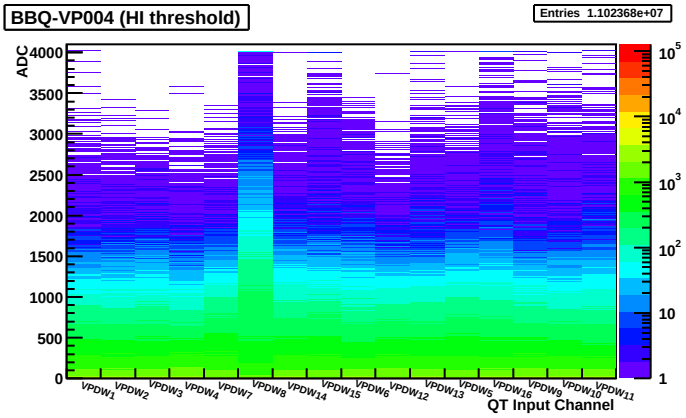

BBQ-VP001 (LO threshold)
Entries 1.102368e+07
TAC
QT Input Channel

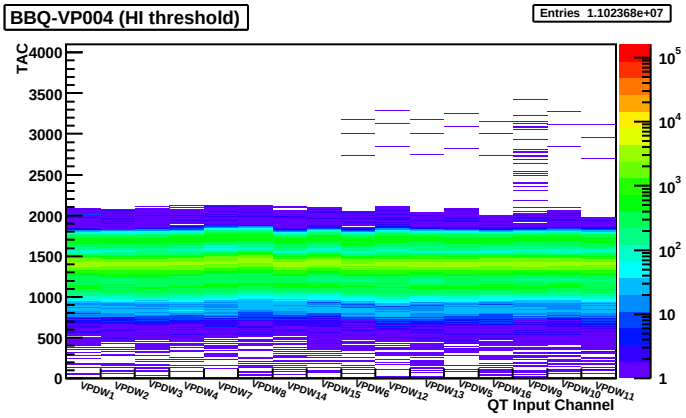

BBQ-VP002 (LO threshold)
Entries 1.102368e+07
ADC
QT Input Channel

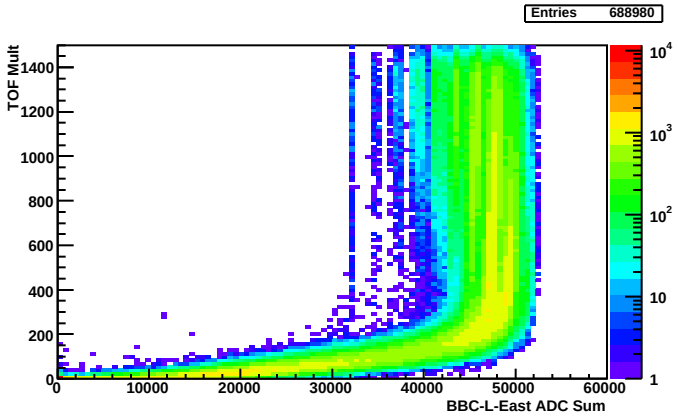

BBQ-VP002 (LO threshold)
Entries 1.102368e+07
TAC
QT Input Channel

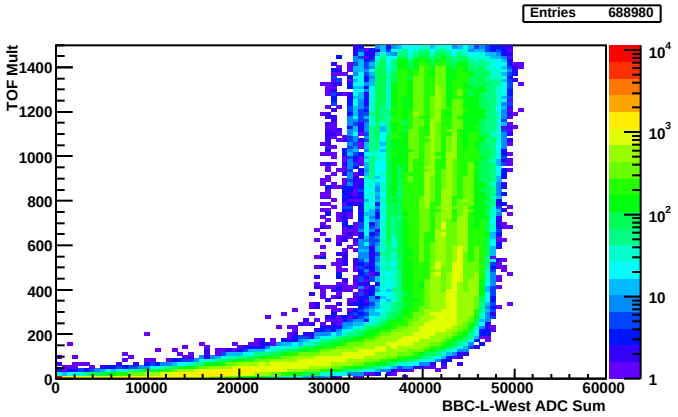

BBQ-VP003 (HI threshold)
Entries 1.102368e+07
ADC
QT Input Channel

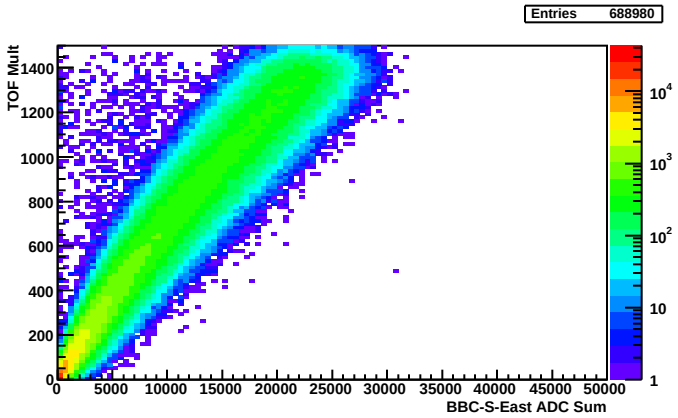

BBQ-VP003 (HI threshold)
Entries 1.102368e+07
TAC
QT Input Channel

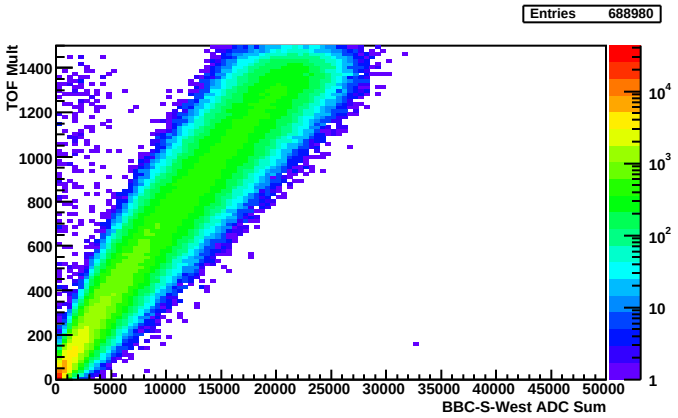

BBQ-VP004 (HI threshold)
Entries 1.102368e+07
ADC
QT Input Channel

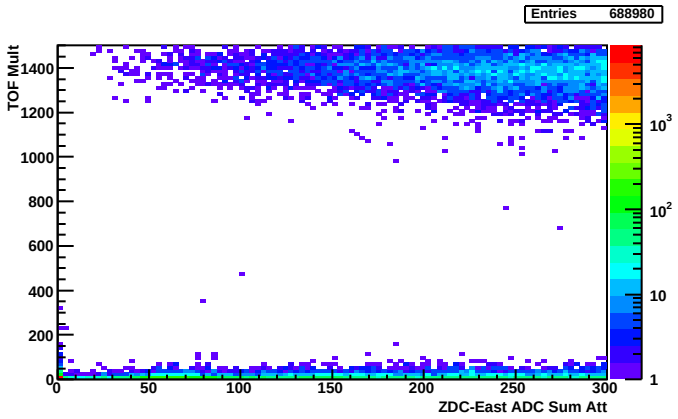

BBQ-VP004 (HI threshold)
Entries 1.102368e+07
TAC
QT Input Channel

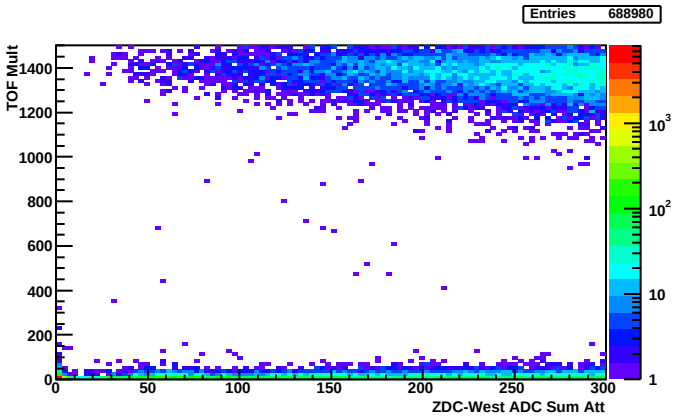

Entries 688980
TOF Mult
BBC-L-East ADC Sum

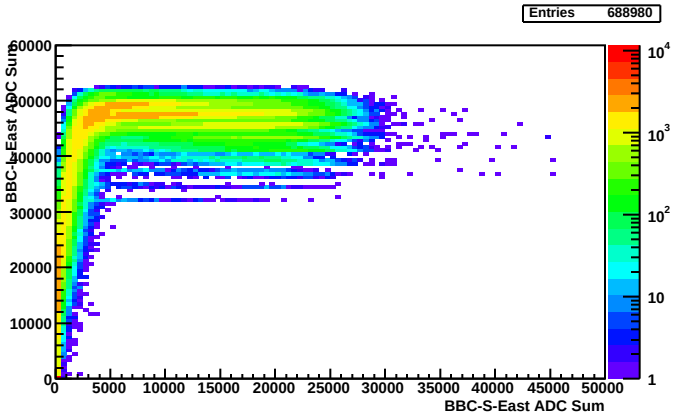

Entries 688980
TOF Mult
BBC-L-West ADC Sum

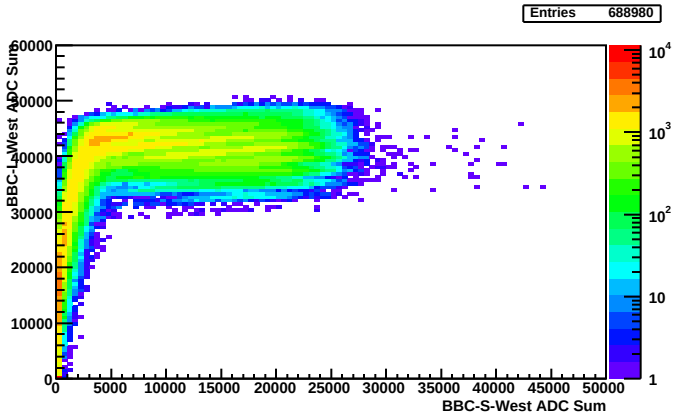

Entries 688980
TOF Mult
BBC-S-East ADC Sum

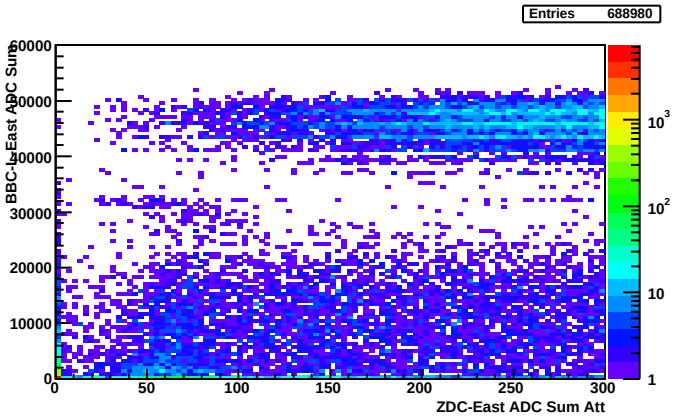

Entries 688980
TOF Mult
BBC-S-West ADC Sum

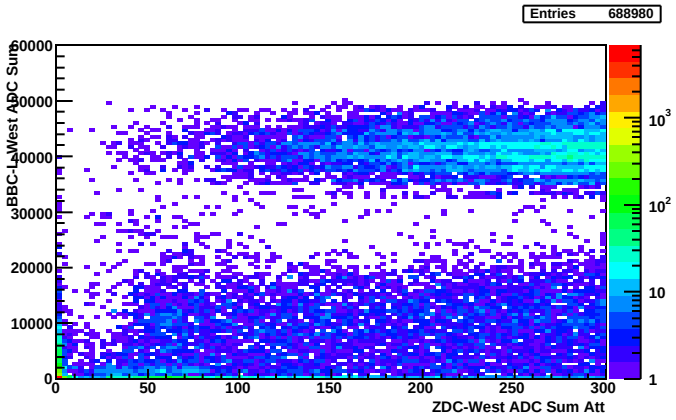

Entries 688980
TOF Mult
ZDC-East ADC Sum Att

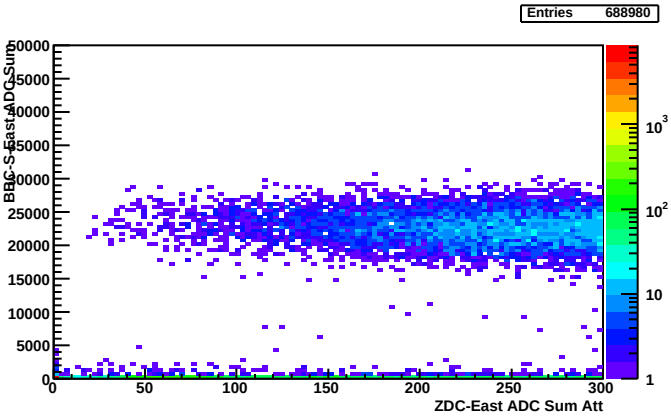

Entries 688980
TOF Mult
ZDC-West ADC Sum Att

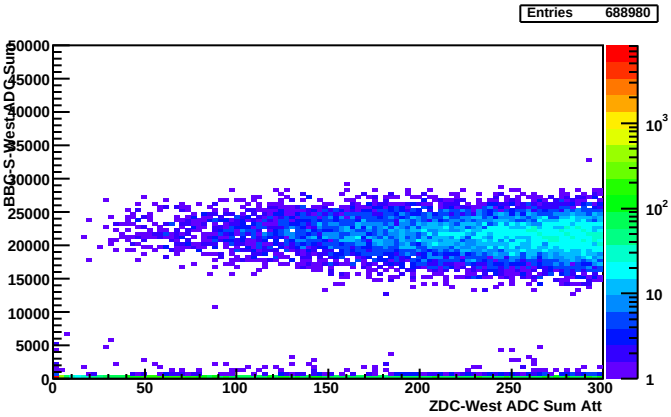

Entries 688980
BBC-L-East ADC Sum
BBC-S-East ADC Sum

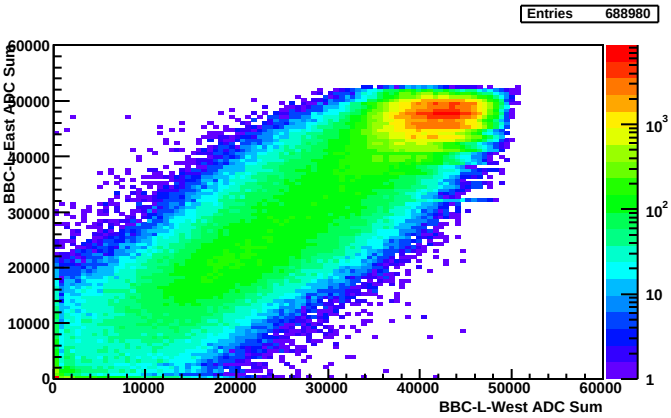

Entries 688980
BBC-L-West ADC Sum
BBC-S-West ADC Sum

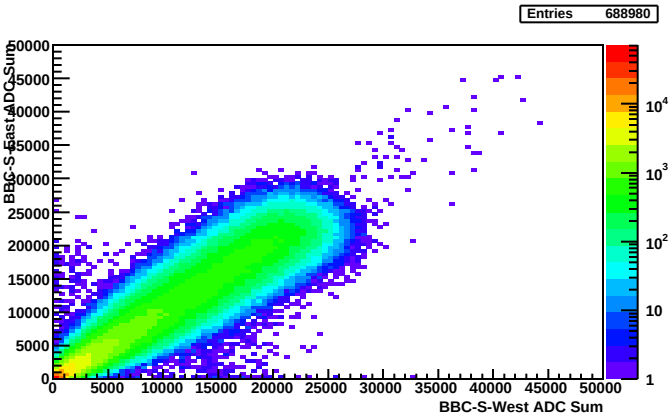

Entries 688980
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

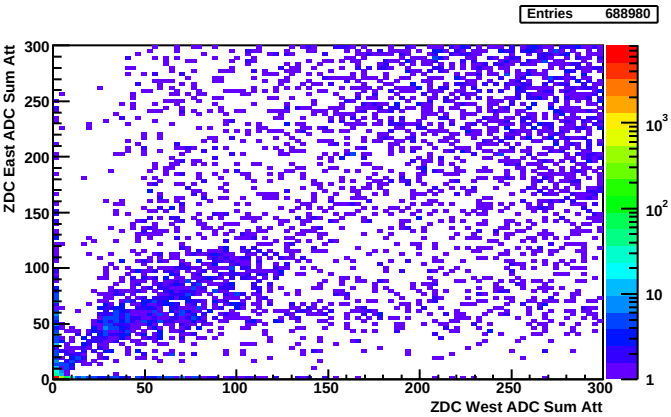

Entries 688980
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

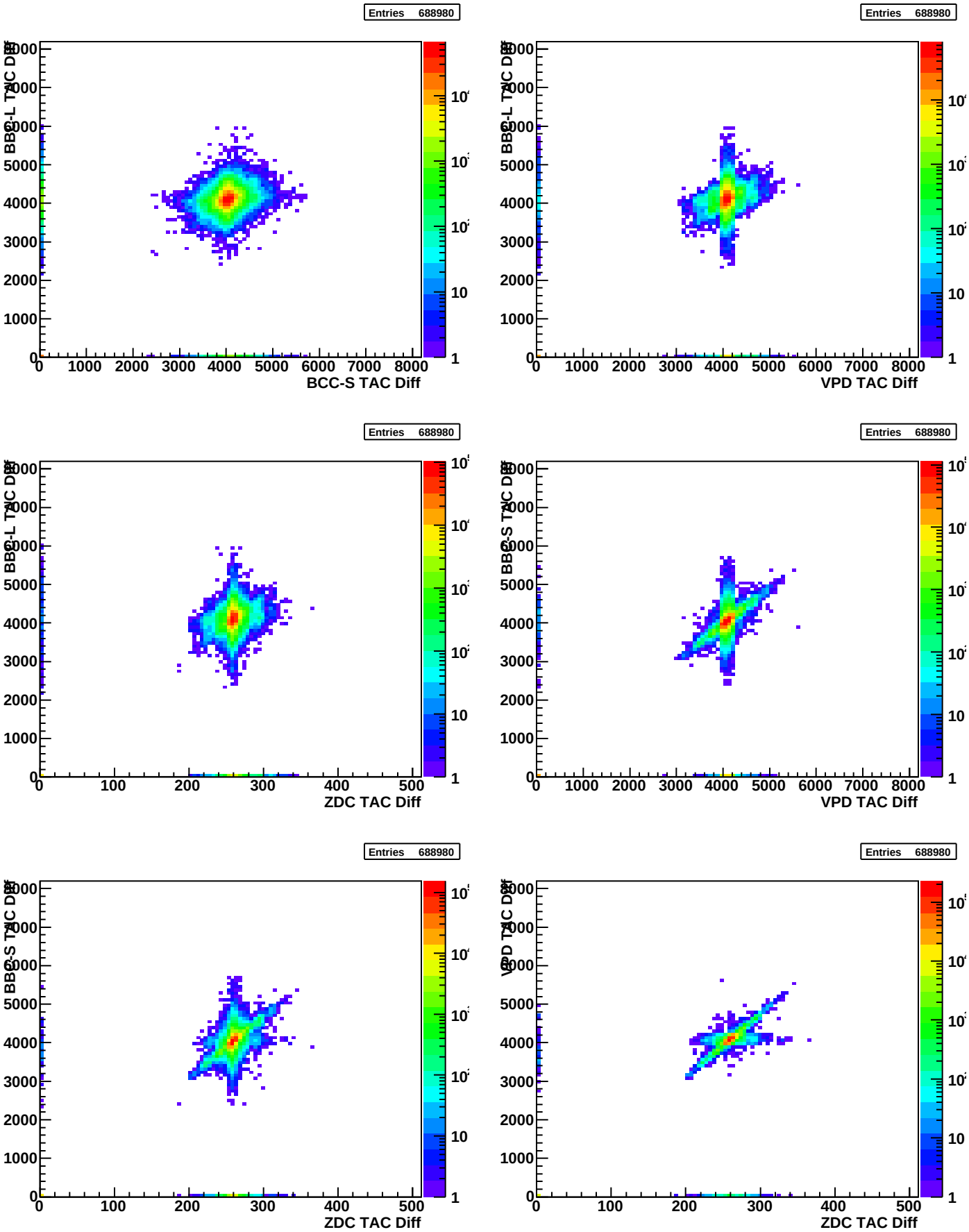

Entries 688980
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

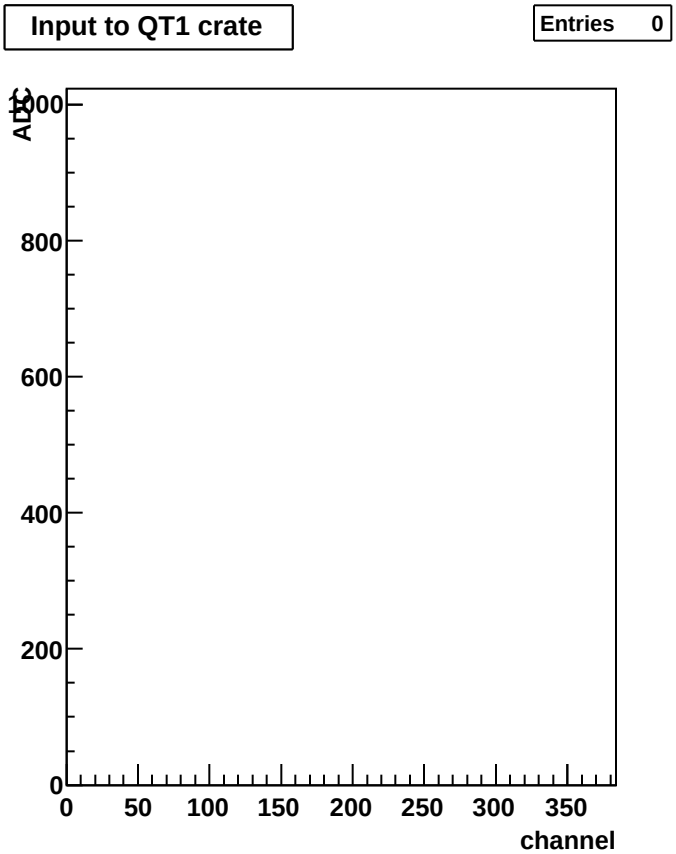

Entries 688980
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

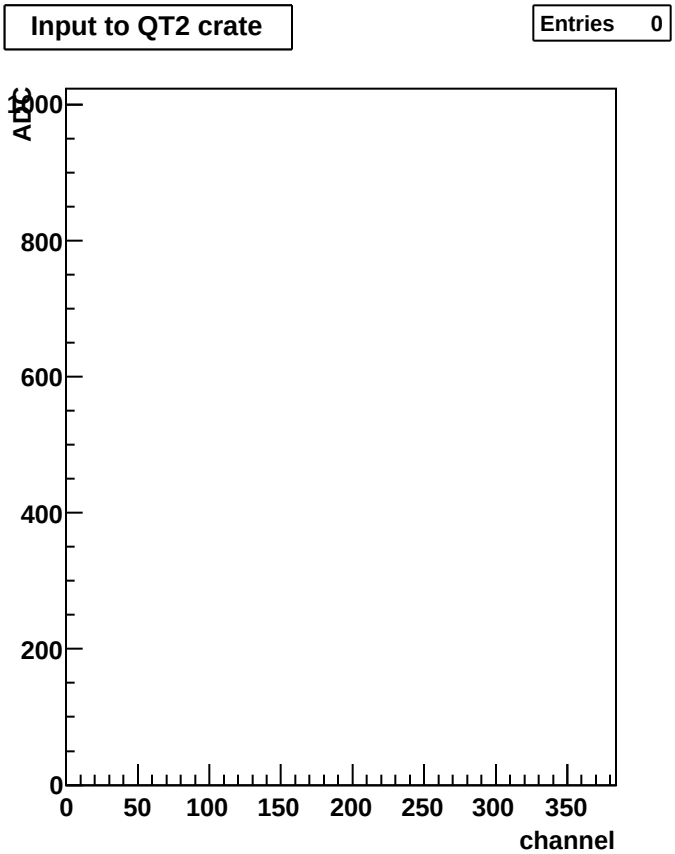

Entries 688980
BBC-L-East ADC Sum
BBC-L-West ADC Sum

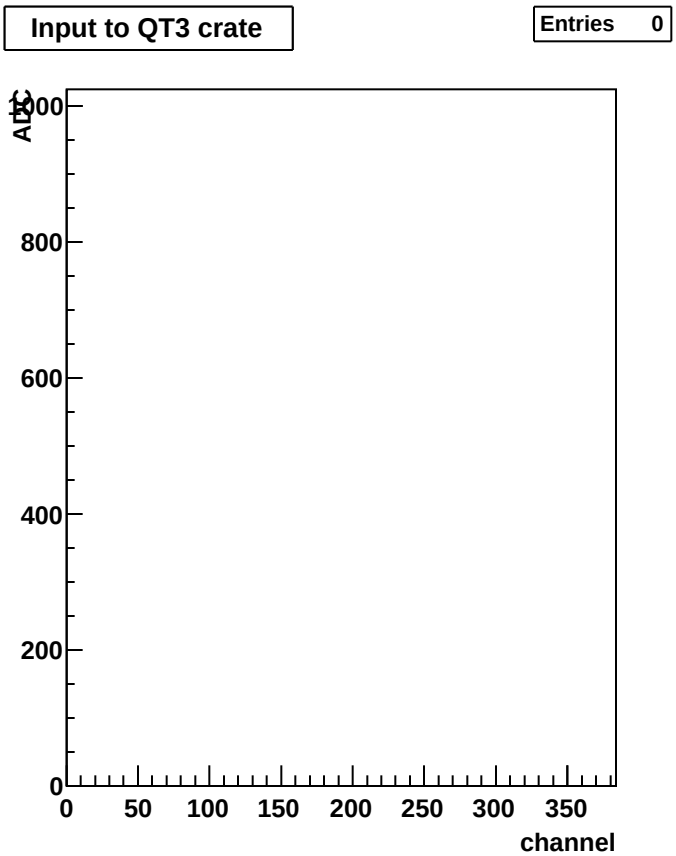

Entries 688980
BBC-S-East ADC Sum
BBC-S-West ADC Sum

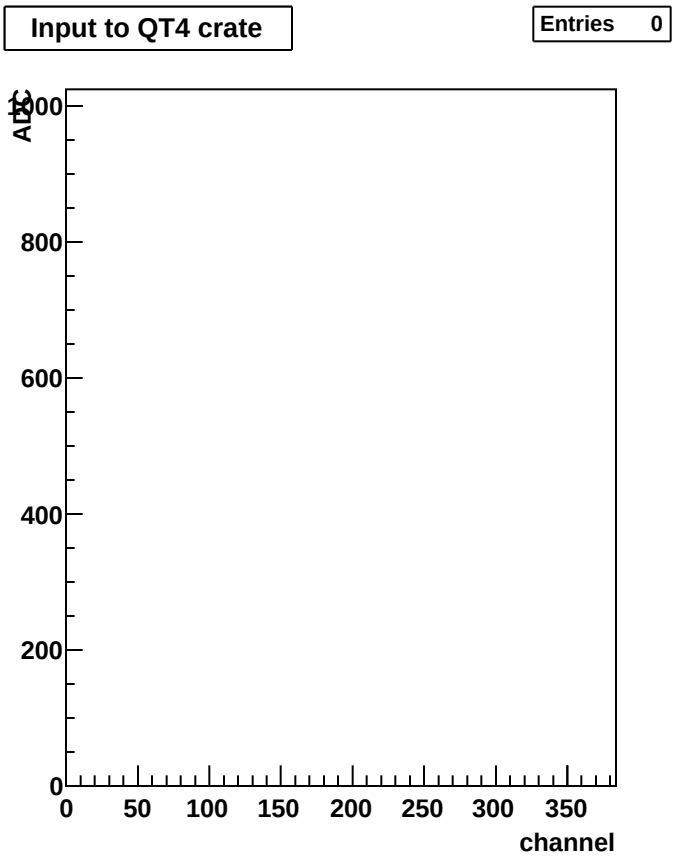

Entries 688980
ZDC East ADC Sum Att
ZDC West ADC Sum Att

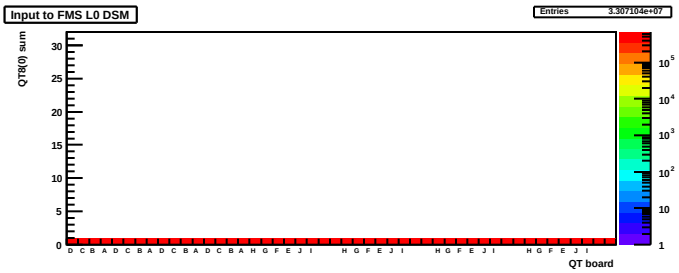

Entries 688980
BBC-L TAC Diff
BCC-S TAC Diff
Entries 688980
BBC-L TAC Diff
VPD TAC Diff
Entries 688980
BBC-L TAC Diff
ZDC TAC Diff
Entries 688980
BBC-S TAC Diff
VPD TAC Diff
Entries 688980
BBC-S TAC Diff
ZDC TAC Diff
Entries 688980
VPD TAC Diff
ZDC TAC Diff

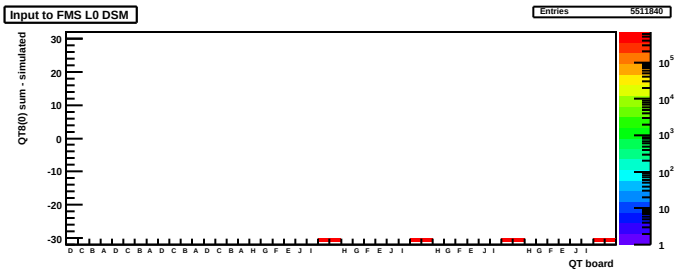

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

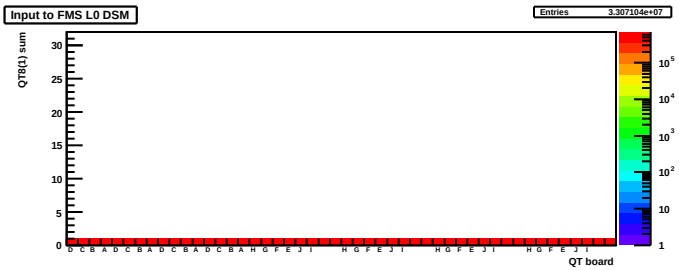

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

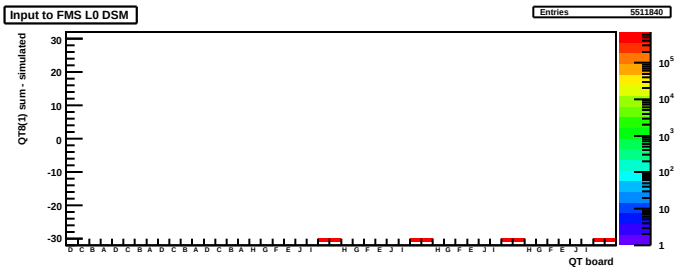

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

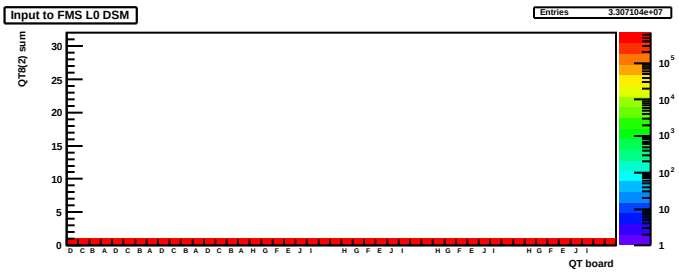

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

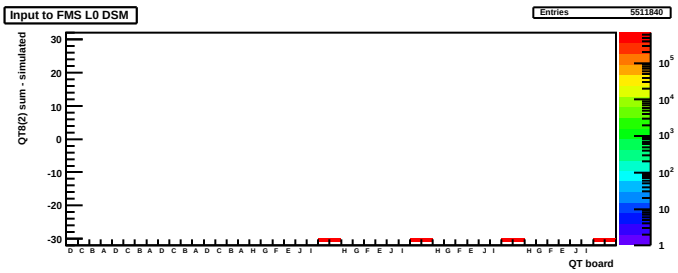

Input to FMS L0 DSM
Entries 3.307104e+07
QT8(0) sum
QT board

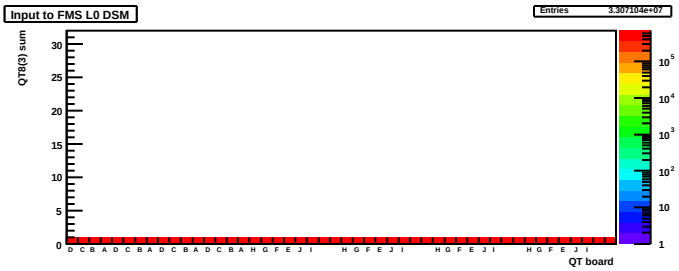

Input to FMS L0 DSM
Entries 5511840
QT8(0) sum - simulated
QT board

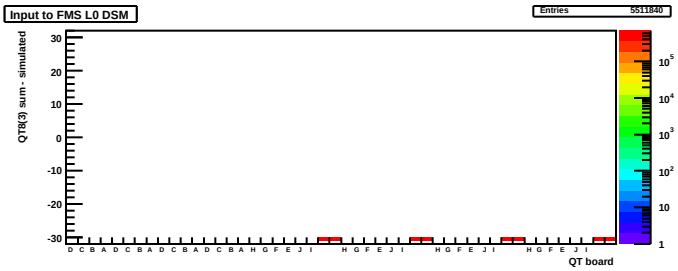

Input to FMS L0 DSM
Entries 3.307104e+07
QT8(1) sum
QT board

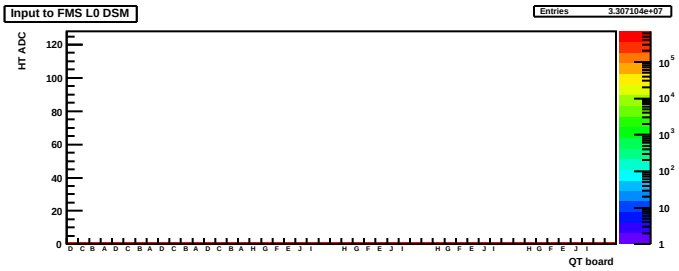

Input to FMS L0 DSM
Entries 5511840
QT8(1) sum - simulated
QT board

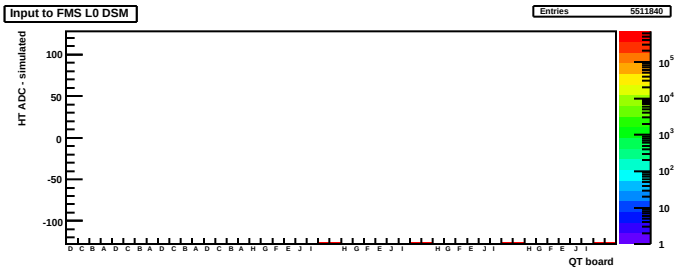

Input to FMS L0 DSM
Entries 3.307104e+07
QT8(2) sum
QT board

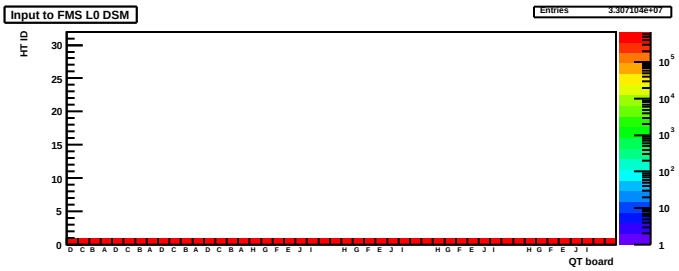

Input to FMS L0 DSM
Entries 5511840
QT8(2) sum - simulated
QT board

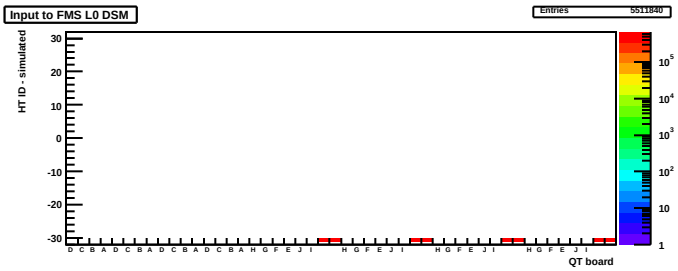

Input to FMS L0 DSM
Entries 3.307104e+07
QT8(3) sum
QT board

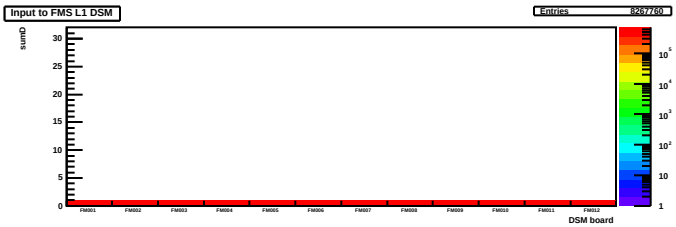

Input to FMS L0 DSM
Entries 5511840
QT8(3) sum - simulated
QT board

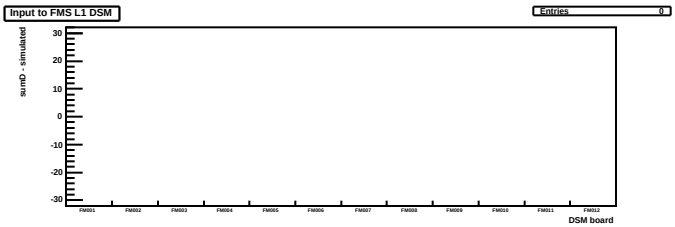

Input to FMS L0 DSM
Entries 3.307104e+07
HT ADC
QT board

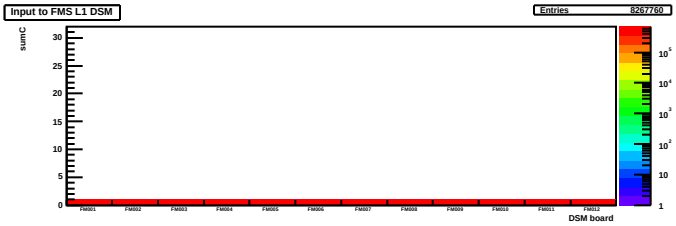

Input to FMS L0 DSM
Entries 5511840
HT ADC - simulated
QT board

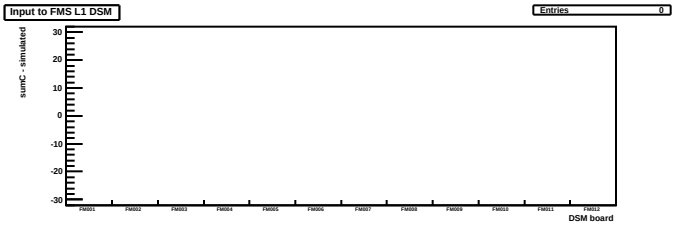

Input to FMS L0 DSM
Entries 3.307104e+07
HT ID
QT board

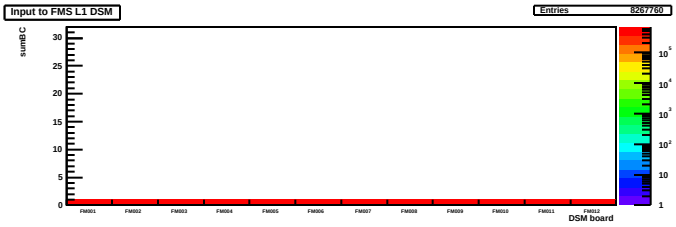

Input to FMS L0 DSM
Entries 5511840
HT ID - simulated
QT board

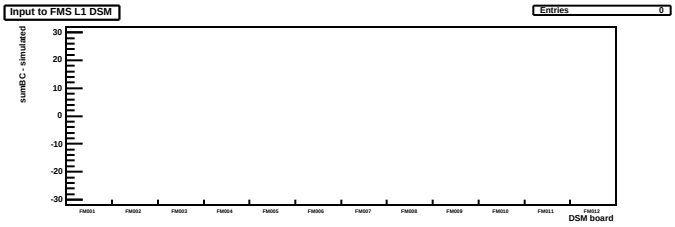

Input to FMS L1 DSM
Entries 8267760
sumD
DSM board

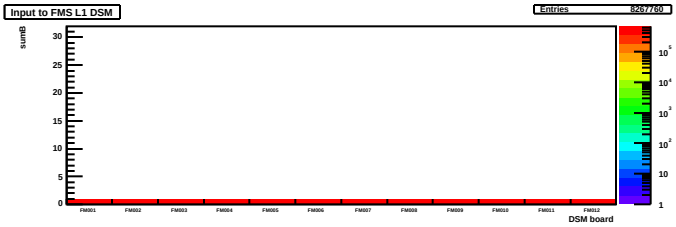

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

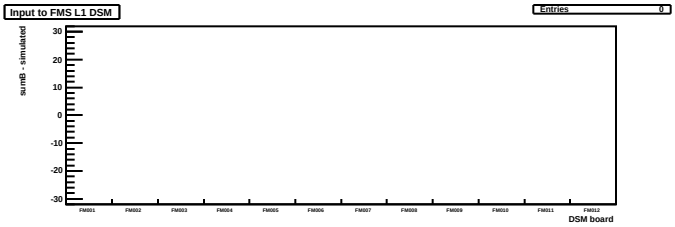

Input to FMS L1 DSM
Entries 8267760
sumC
DSM board

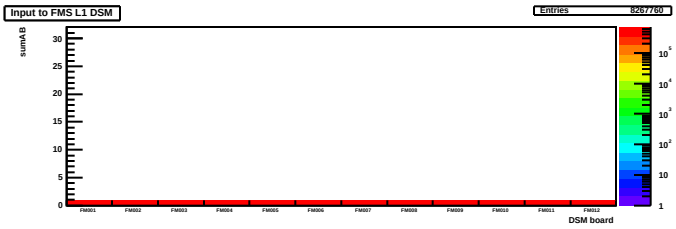

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

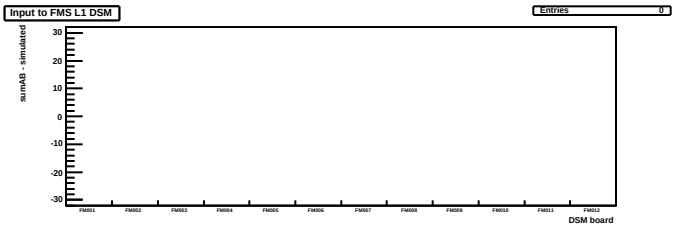

Input to FMS L1 DSM
Entries 8267760
sumBC
DSM board

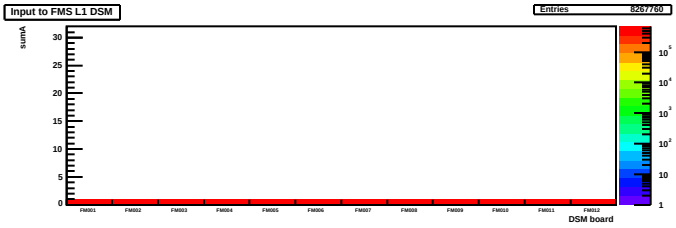

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

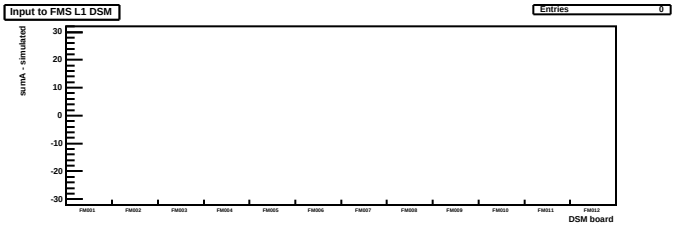

Input to FMS L1 DSM
Entries 8267760
sumB
DSM board

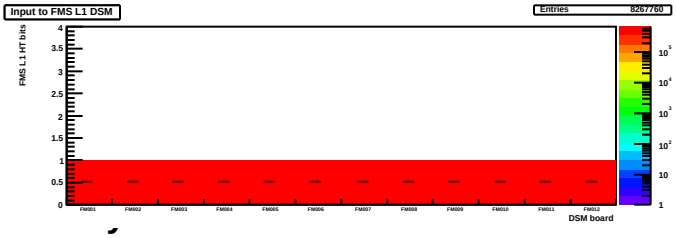

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

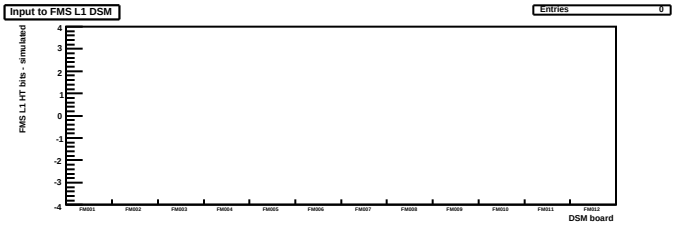

Input to FMS L1 DSM
Entries 8267760
sumAB
DSM board

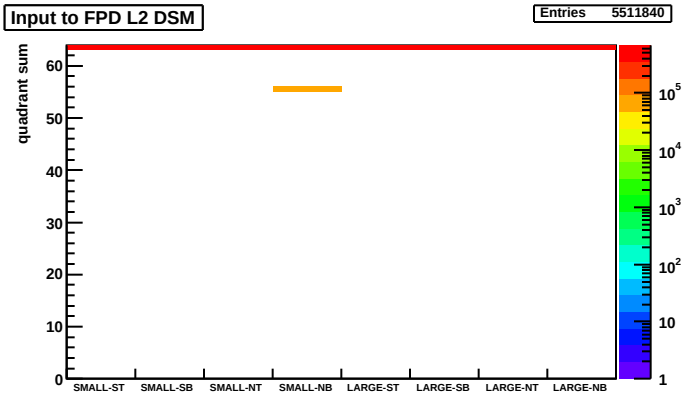

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

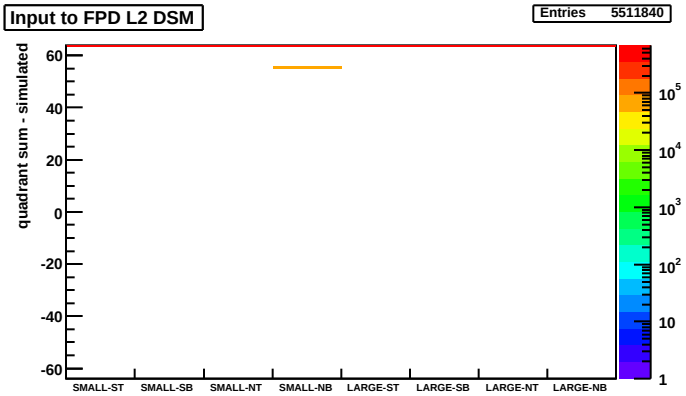

Input to FMS L1 DSM
Entries 8267760
sumA
DSM board

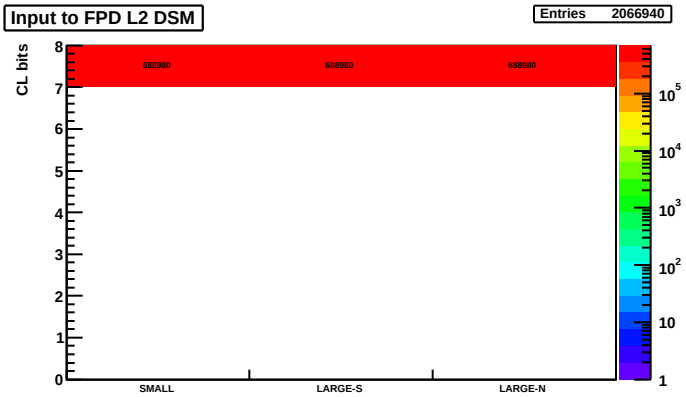

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

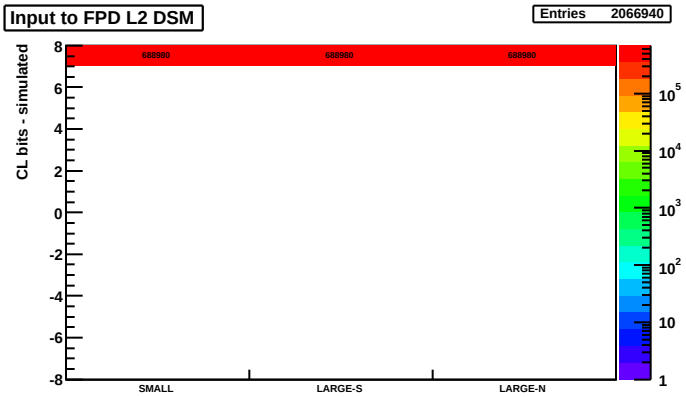

Input to FMS L1 DSM
Entries 8267760
FMS L1 HT bits
DSM board

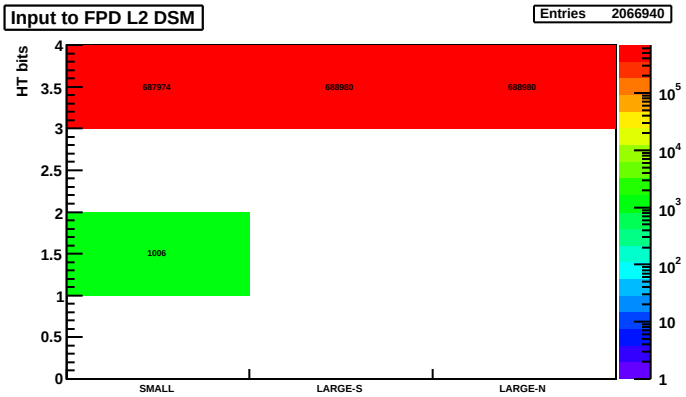

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

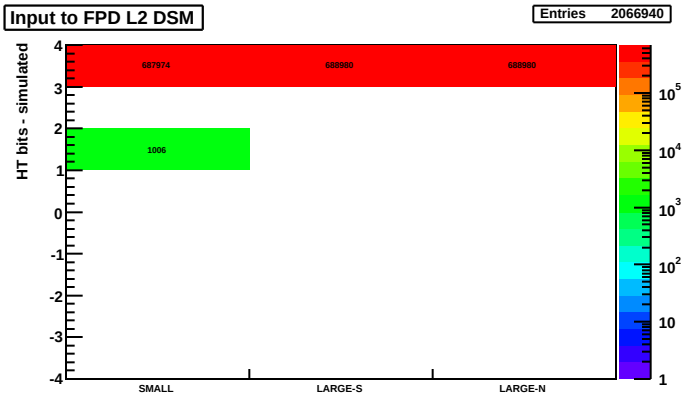

Input to FPD L2 DSM
Entries 5511840
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

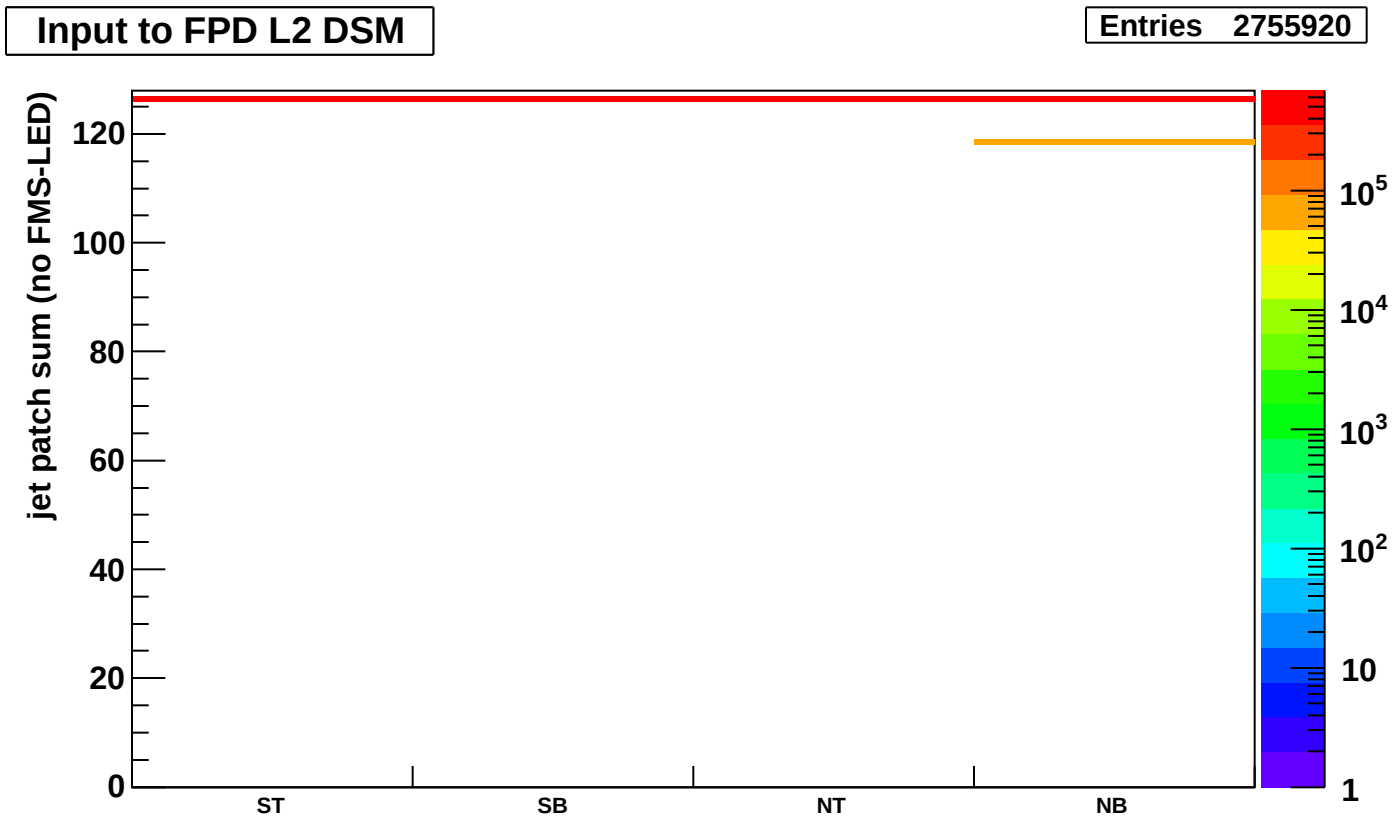

Input to FPD L2 DSM
Entries 5511840
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

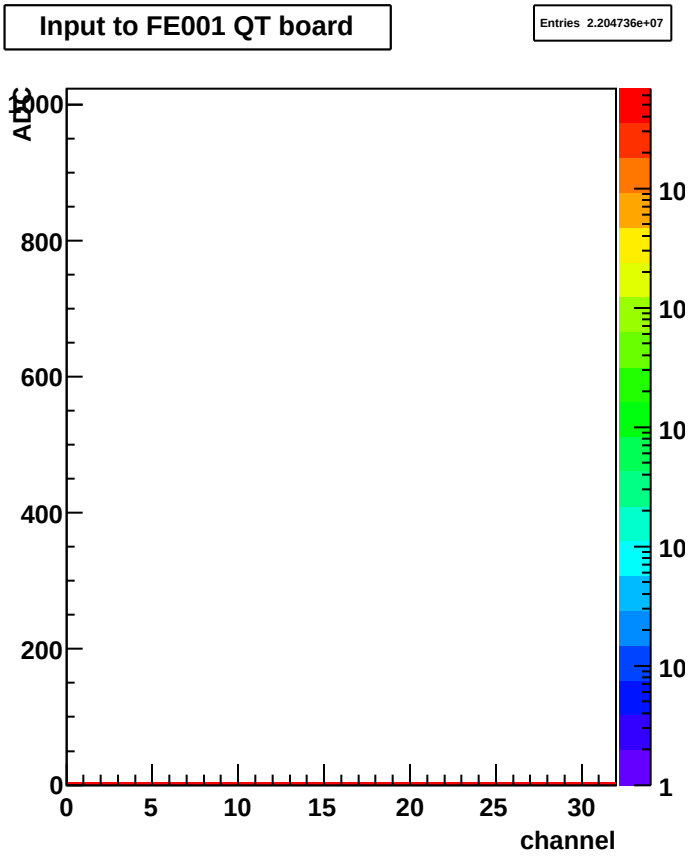

Input to FPD L2 DSM
Entries 2066940
CL bits
688980
688980
688980
SMALL
LARGE-S
LARGE-N

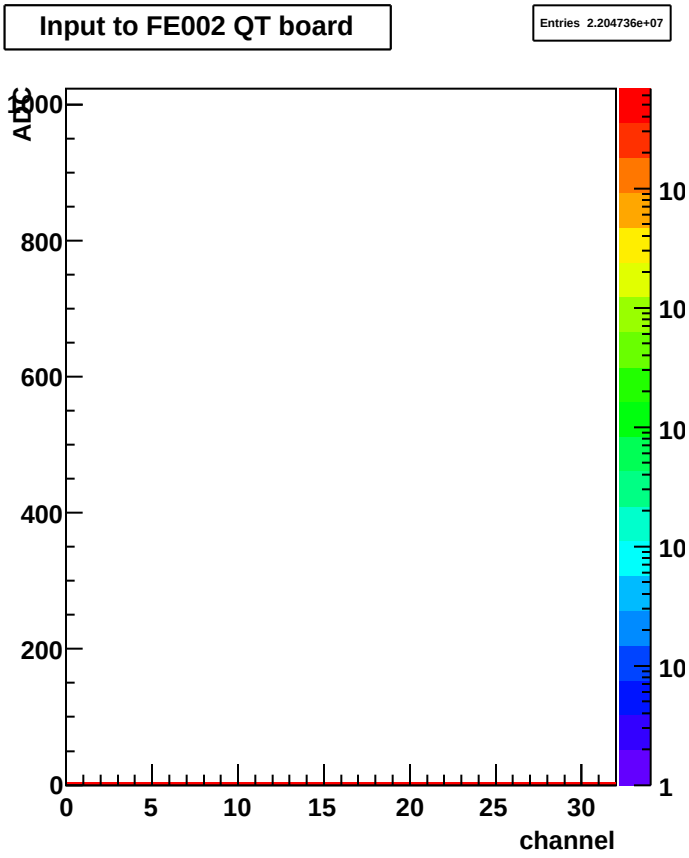

Input to FPD L2 DSM
Entries 2066940
CL bits - simulated
688980
688980
688980
SMALL
LARGE-S
LARGE-N

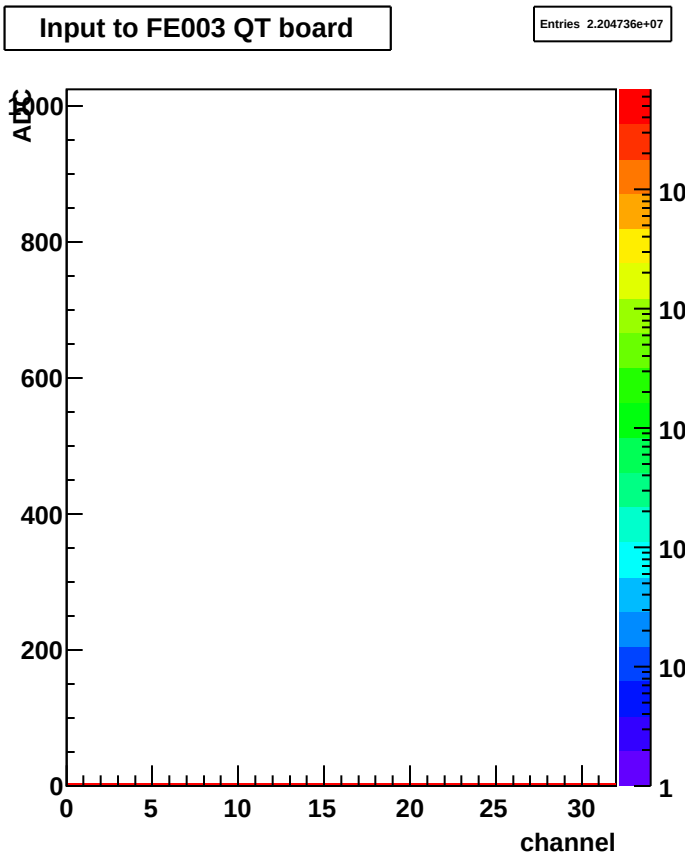

Input to FPD L2 DSM
Entries 2066940
HT bits
687974
688980
688980
1006
SMALL
LARGE-S
LARGE-N

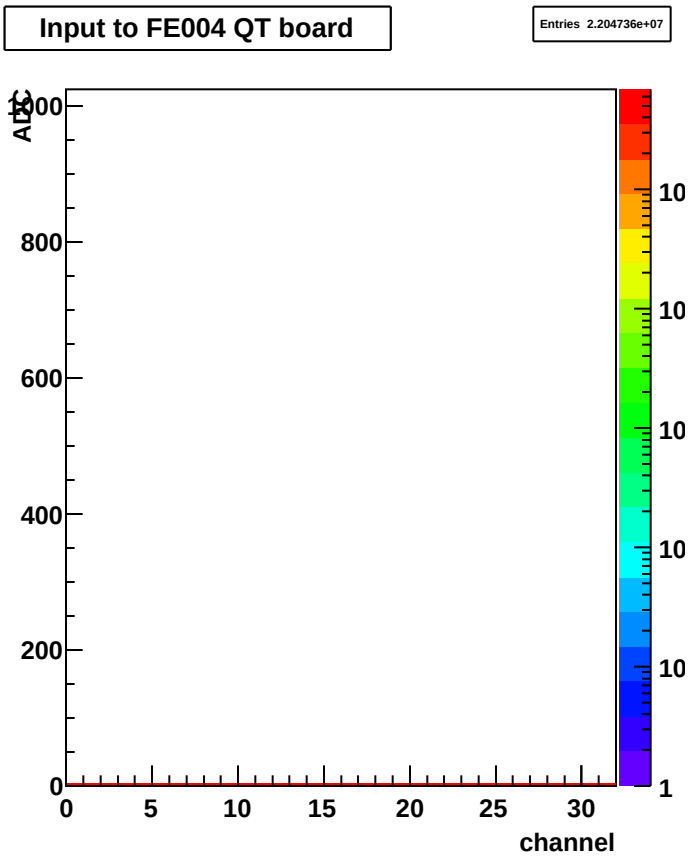

Input to FPD L2 DSM
Entries 2066940
HT bits - simulated
687974
688980
688980
1006
SMALL
LARGE-S
LARGE-N


Input to FPD L2 DSM
Entries 2755920
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

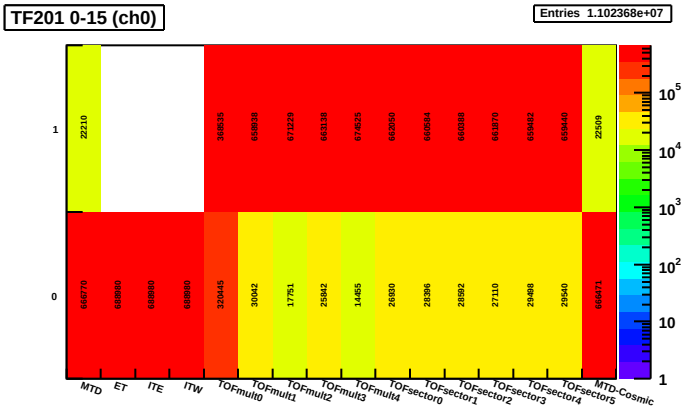

Input to FE001 QT board
Entries 2.204736e+07
ADC
channel


Input to FE002 QT board
Entries 2.204736e+07
ADC
channel

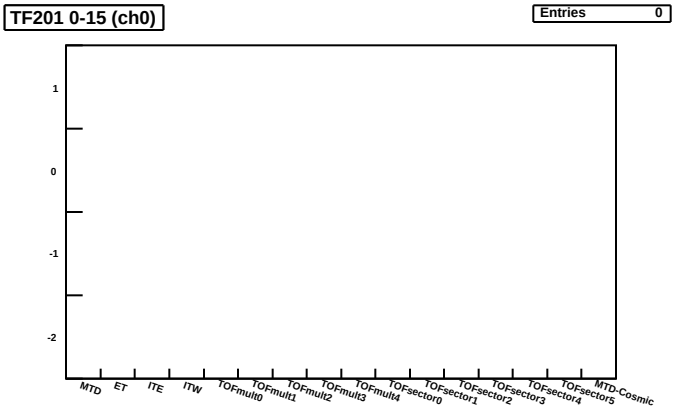

Input to FE003 QT board
Entries 2.204736e+07
ADC
channel


Input to FE004 QT board
Entries 2.204736e+07
ADC
channel

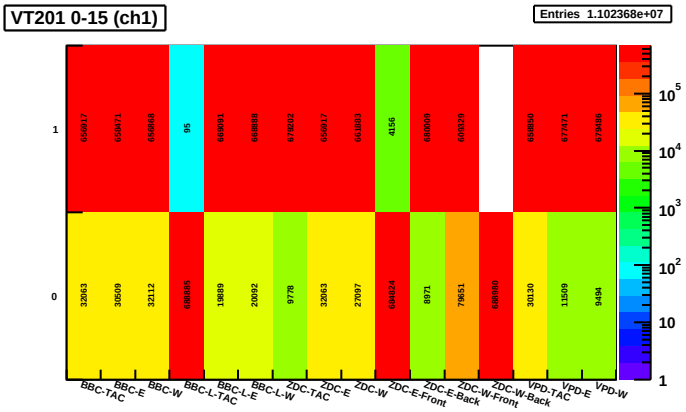
TF201 0-15 (ch0)



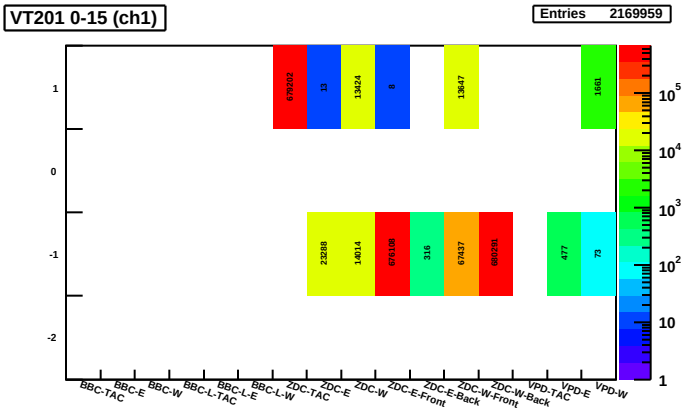
TF201 0-15 (ch0)



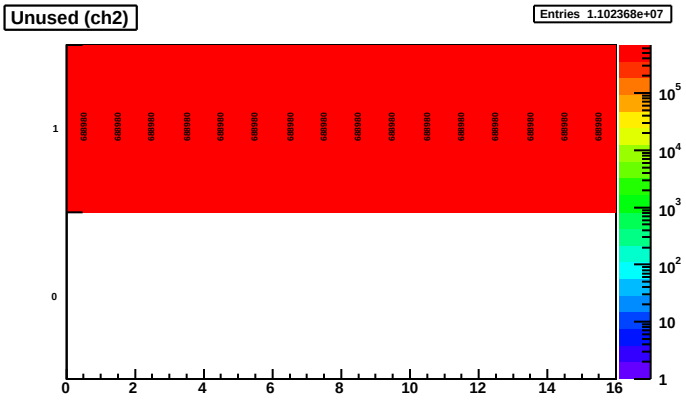
VT201 0-15 (ch1)

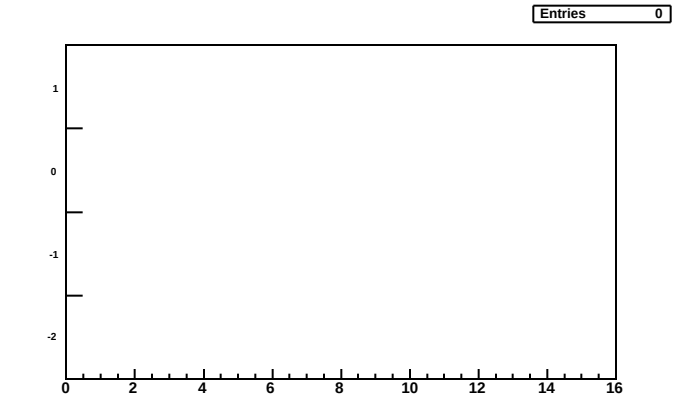


VT201 0-15 (ch1)

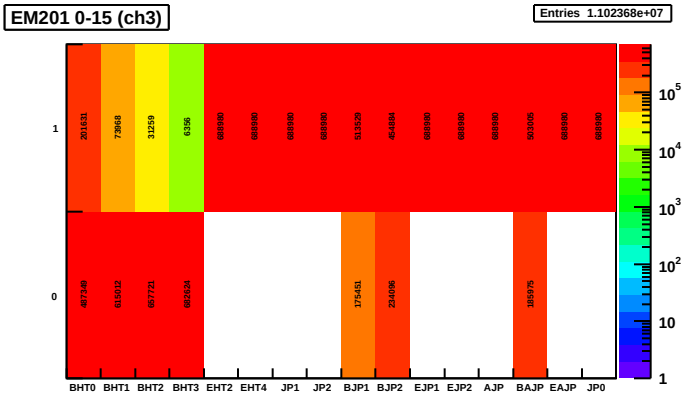


Unused (ch2)

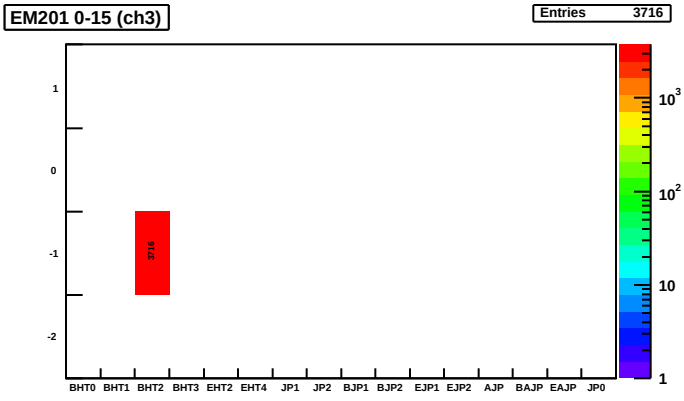




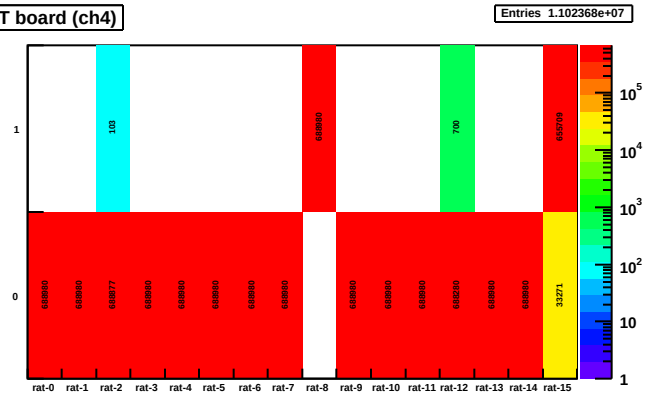
EM201 0-15 (ch3)

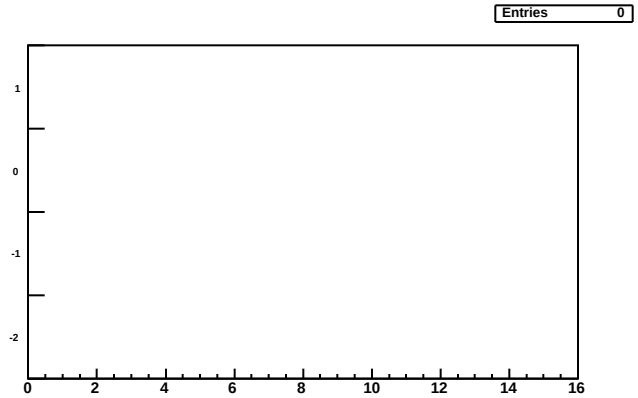


EM201 0-15 (ch3)

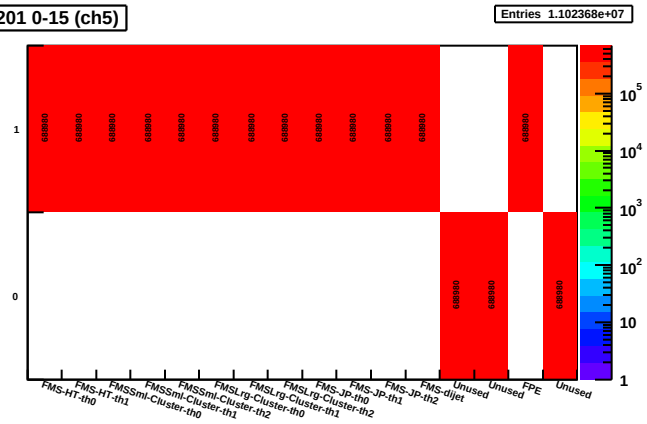


RAT board (ch4)

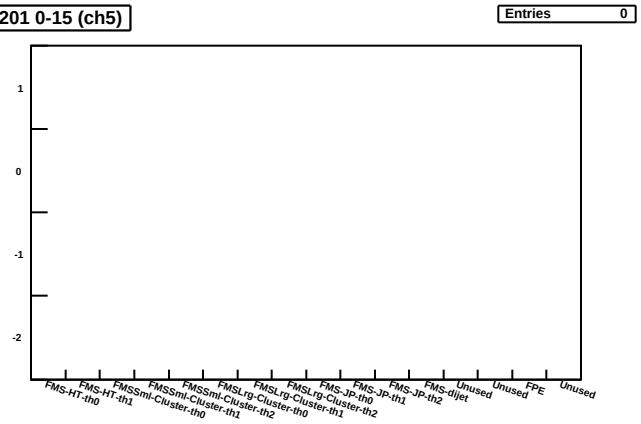




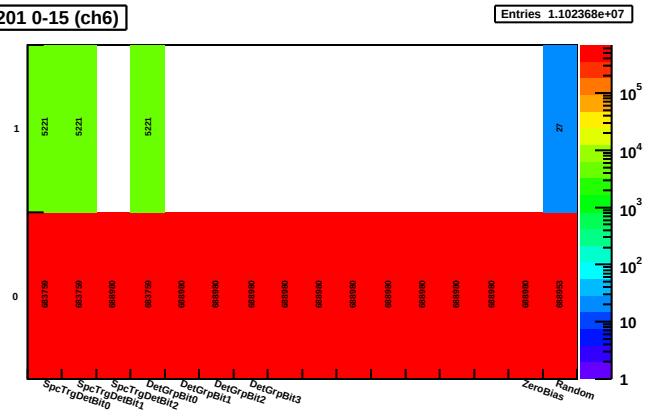
FP201 0-15 (ch5)

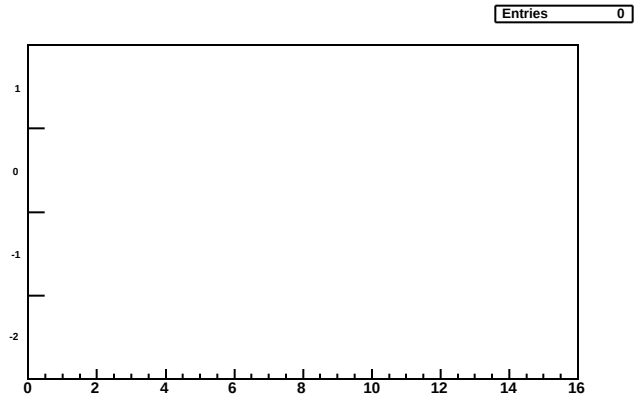
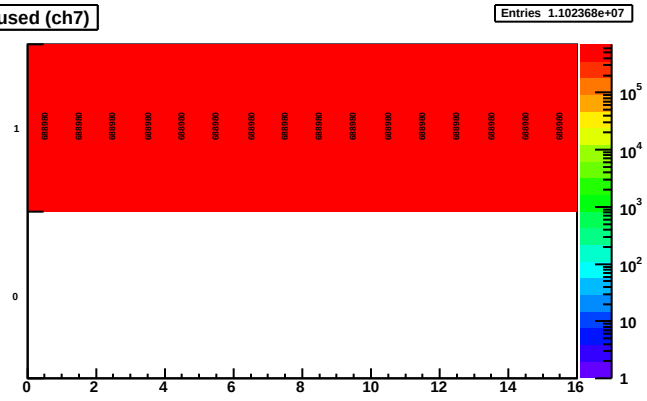
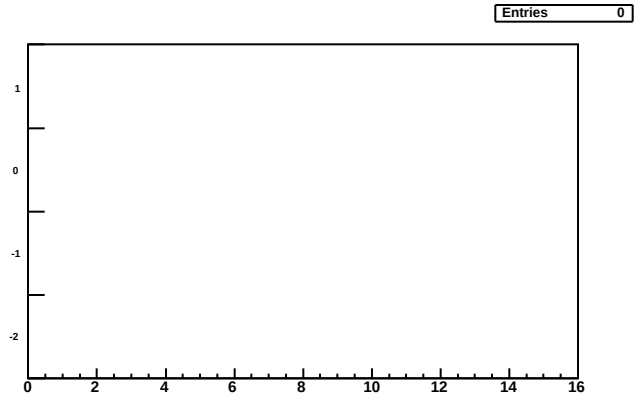


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)