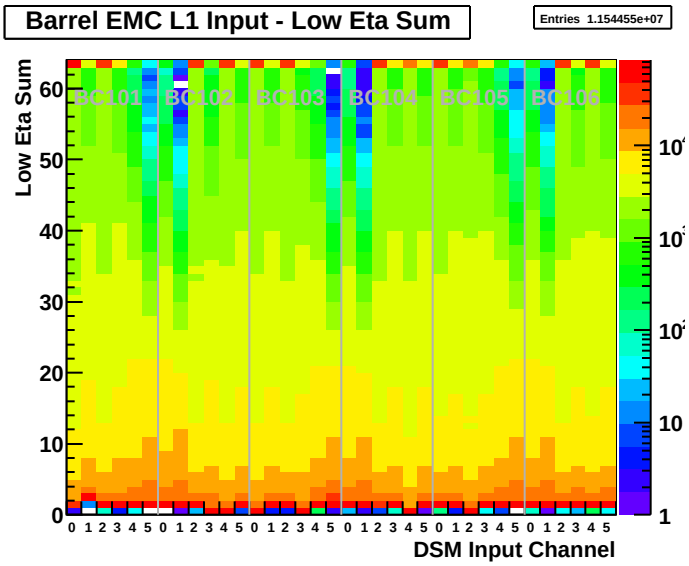
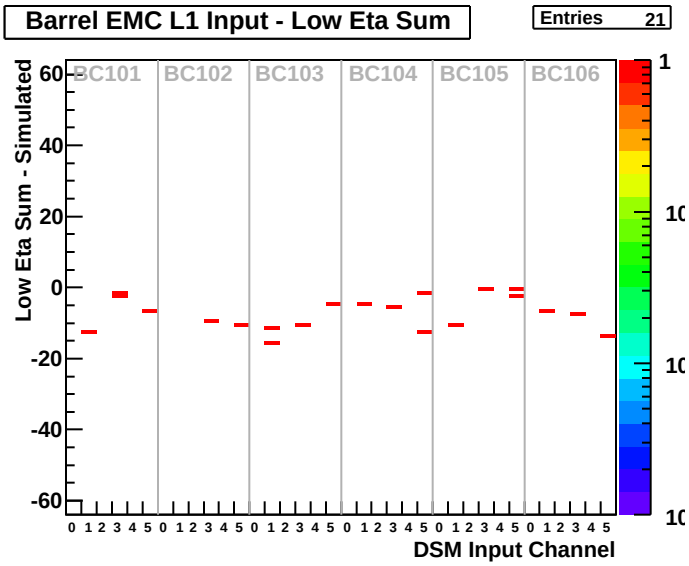
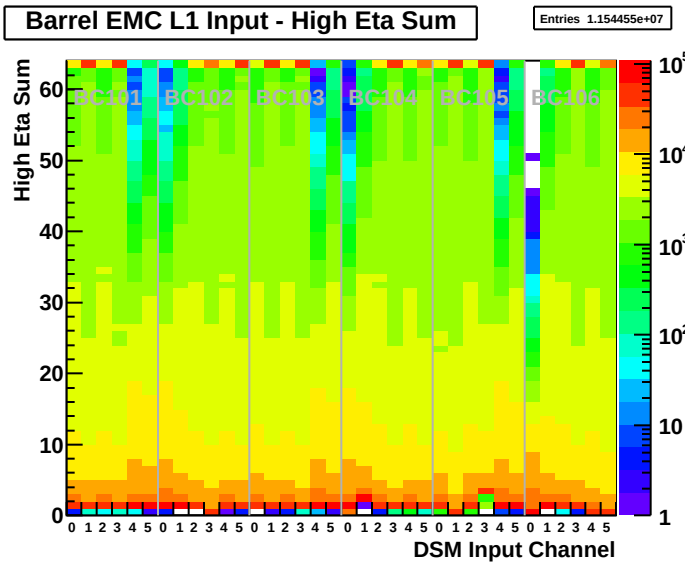
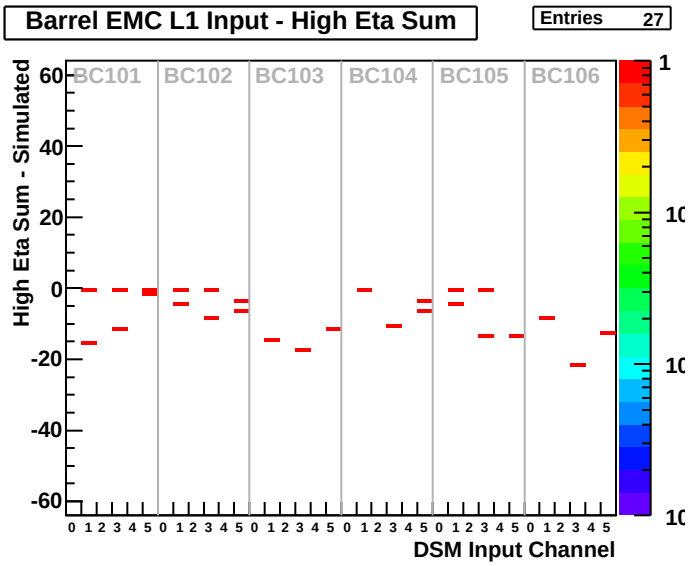
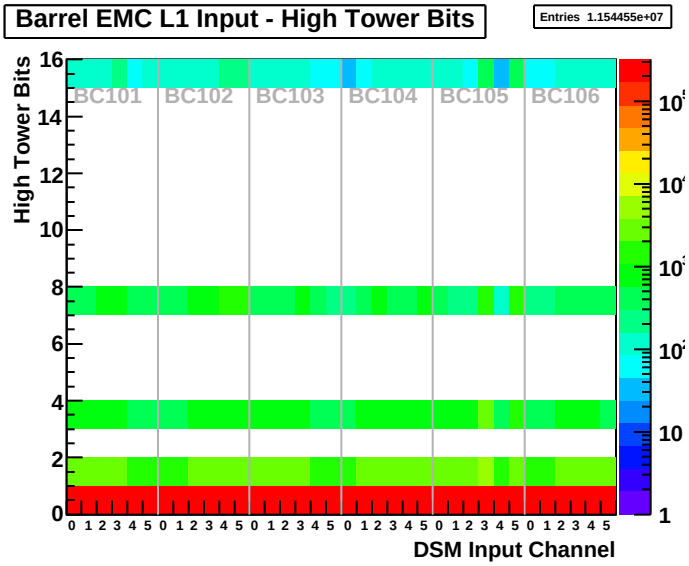
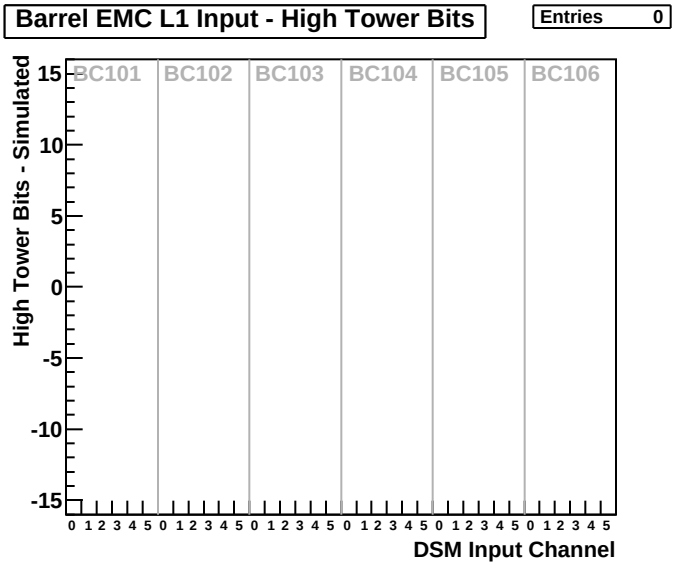

Barrel EMC L1 Input - Low Eta Sum
Entries 1.154455e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

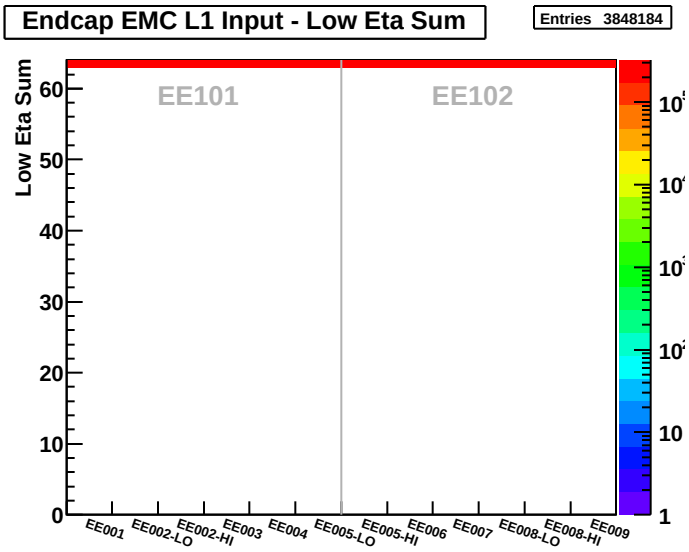

Barrel EMC L1 Input - Low Eta Sum
Entries 21
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

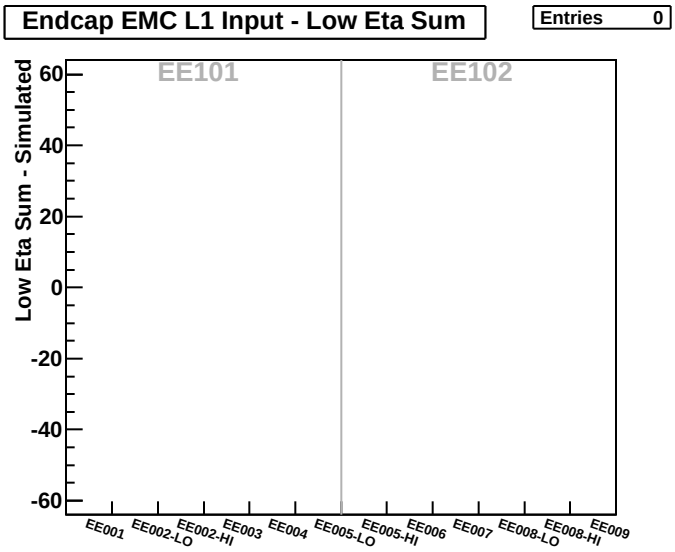

Barrel EMC L1 Input - High Eta Sum
Entries 1.154455e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

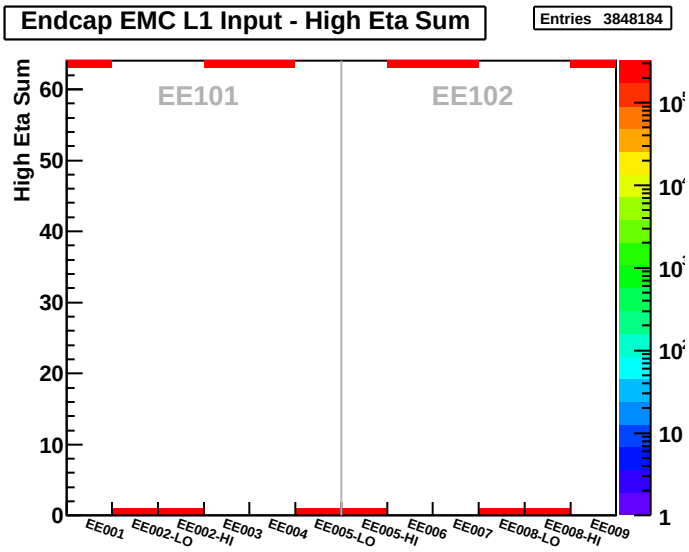

Barrel EMC L1 Input - High Eta Sum
Entries 27
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

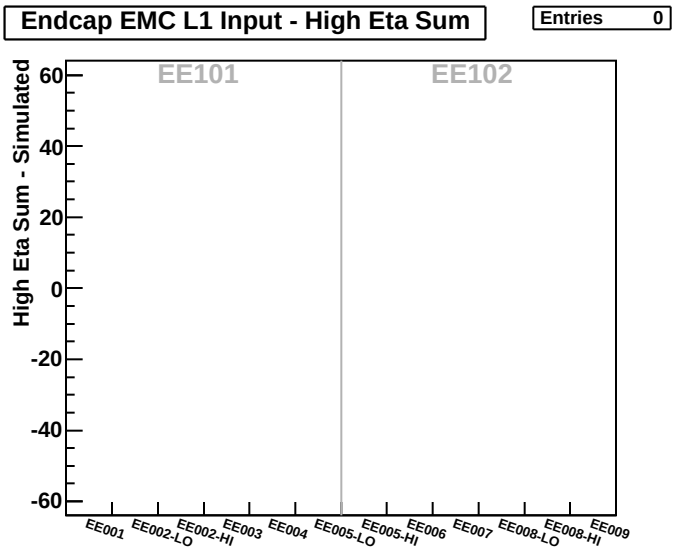

Barrel EMC L1 Input - High Tower Bits
Entries 1.154455e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

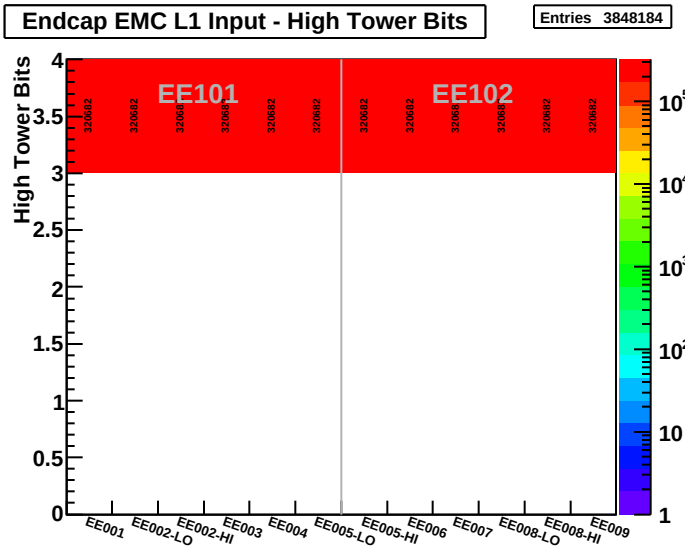

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

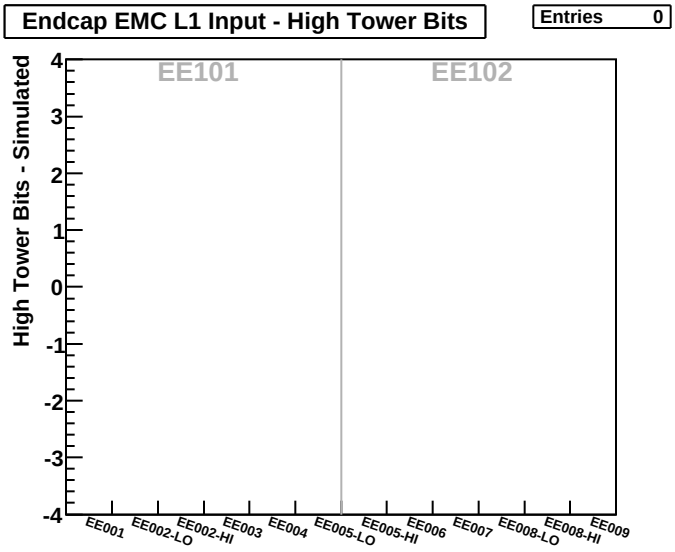

Endcap EMC L1 Input - Low Eta Sum
Entries 3848184
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

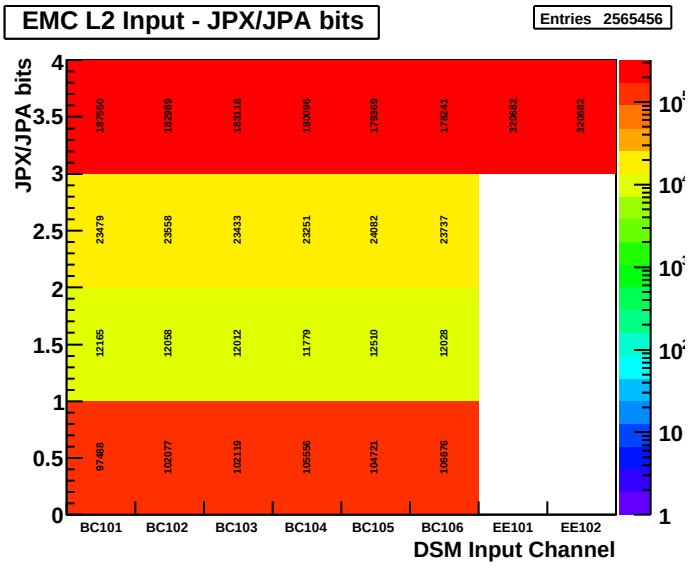

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

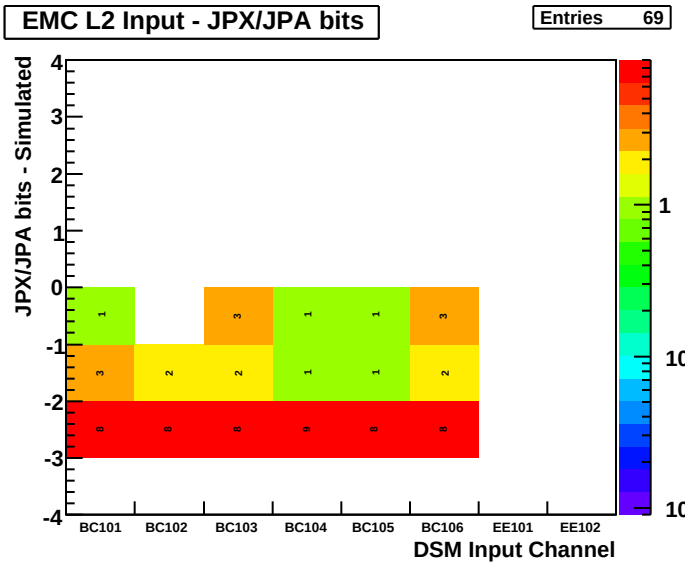

Endcap EMC L1 Input - High Eta Sum
Entries 3848184
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

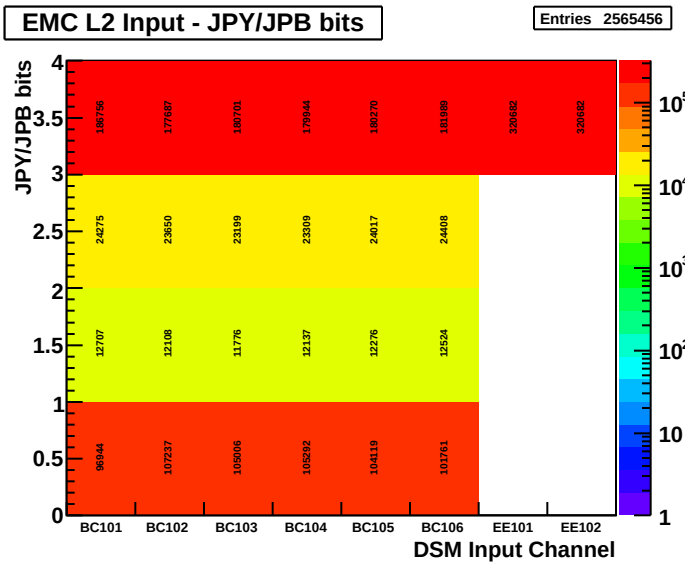

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

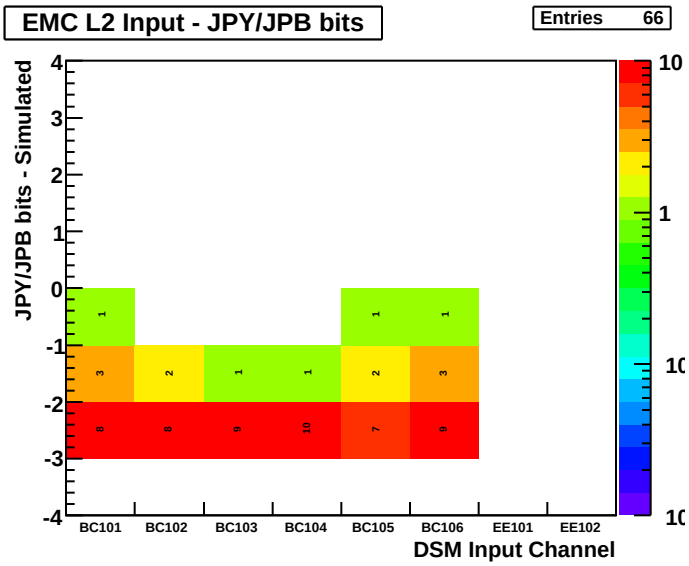

Endcap EMC L1 Input - High Tower Bits
Entries 3848184
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

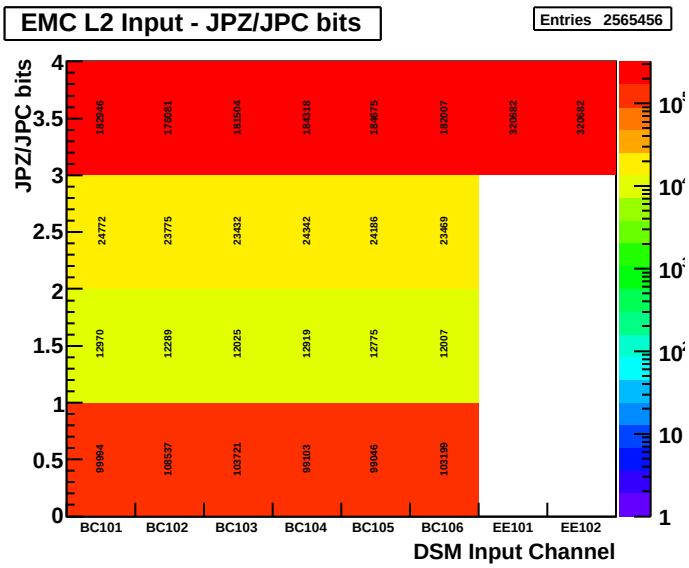

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

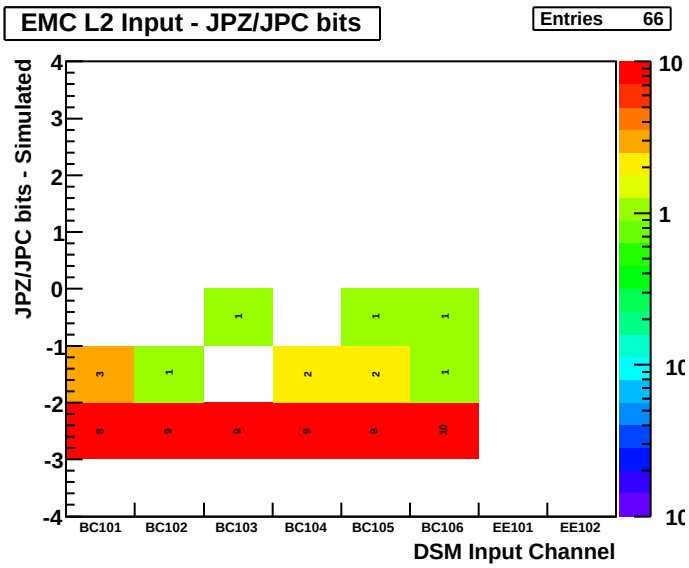

EMC L2 Input - JPX/JPA bits
Entries 2565456
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
197550 182389 183118 180096 179369 178241 320682 320682
23479 23558 23433 23251 24082 23737
12165 12058 12012 11779 12510 12028
97488 102077 102119 105556 104721 106676

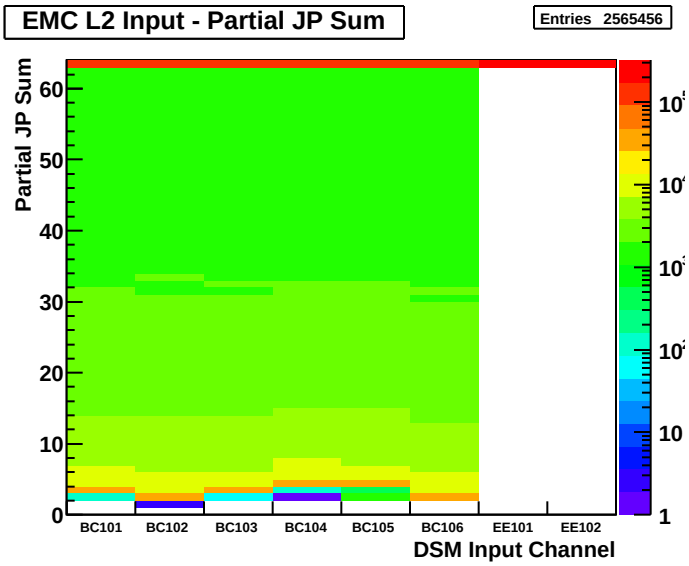

EMC L2 Input - JPX/JPA bits
Entries 69
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

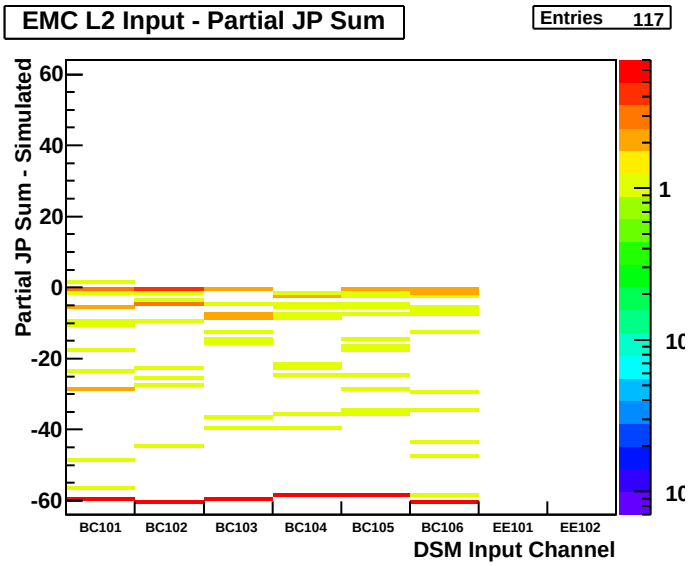

EMC L2 Input - JPY/JPB bits
Entries 2565456
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
186756 177687 180701 179944 180270 181999 320682 320682
24275 23650 23199 23309 24017 24408
12707 12108 11776 12137 12276 12524
96944 107237 105006 105292 104119 101761

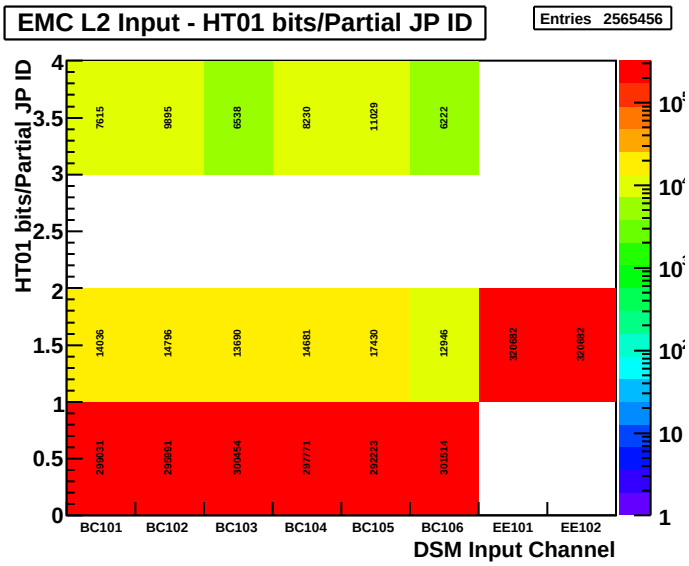

EMC L2 Input - JPY/JPB bits
Entries 66
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

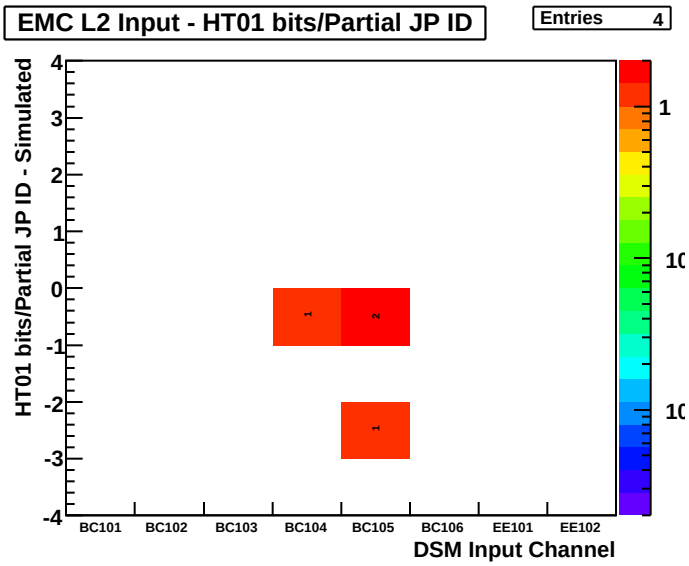

EMC L2 Input - JPZ/JPC bits
Entries 2565456
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
182946 176081 181504 184318 184675 182007 320682 320682
24772 23775 23432 24342 24186 23469
12970 12289 12025 12919 12775 12007
99994 108537 103721 99103 99046 103199

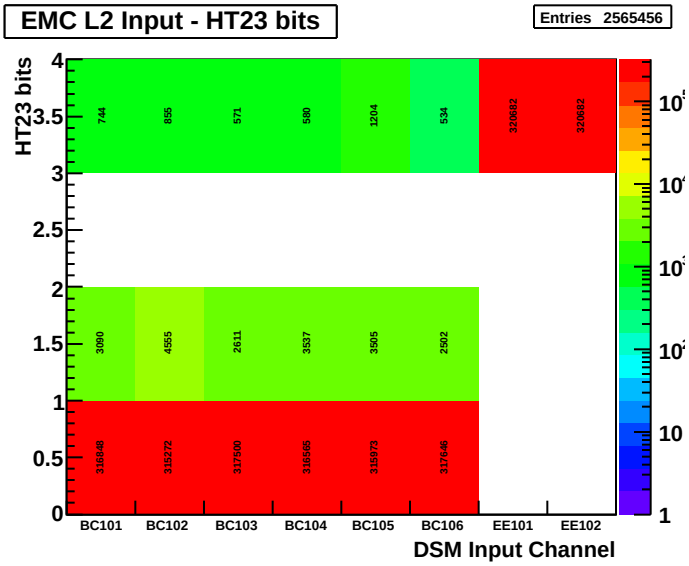

EMC L2 Input - JPZ/JPC bits
Entries 66
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

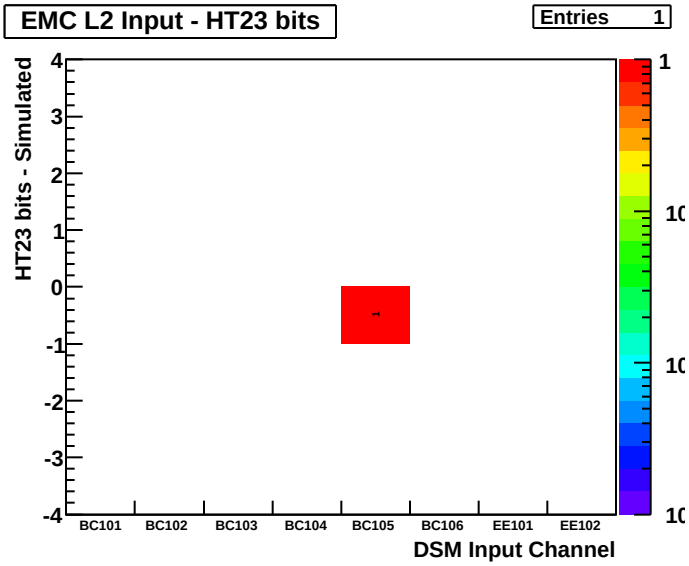

EMC L2 Input - Partial JP Sum
Entries 2565456
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

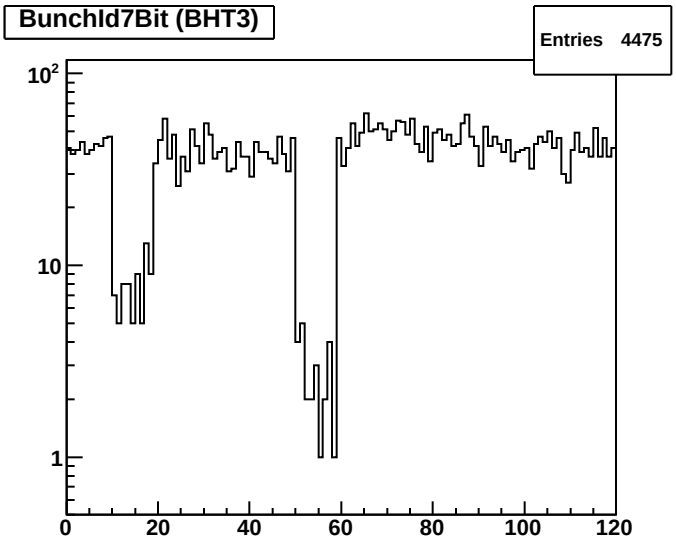

EMC L2 Input - Partial JP Sum
Entries 117
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

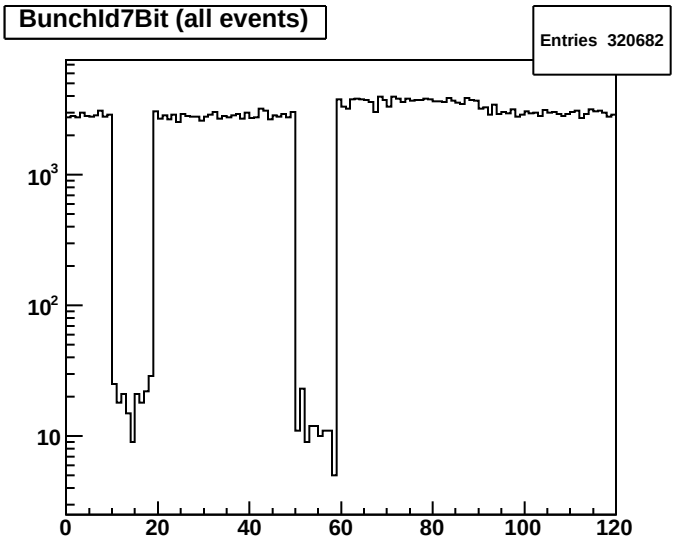

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2565456
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

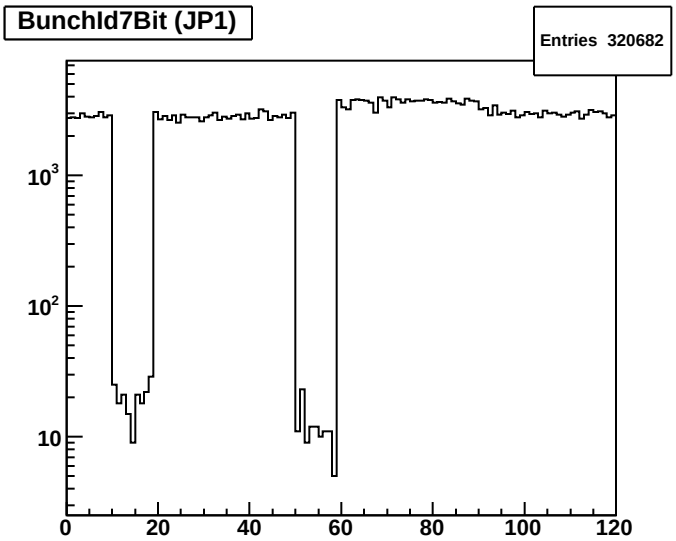

EMC L2 Input - HT01 bits/Partial JP ID
Entries 4
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

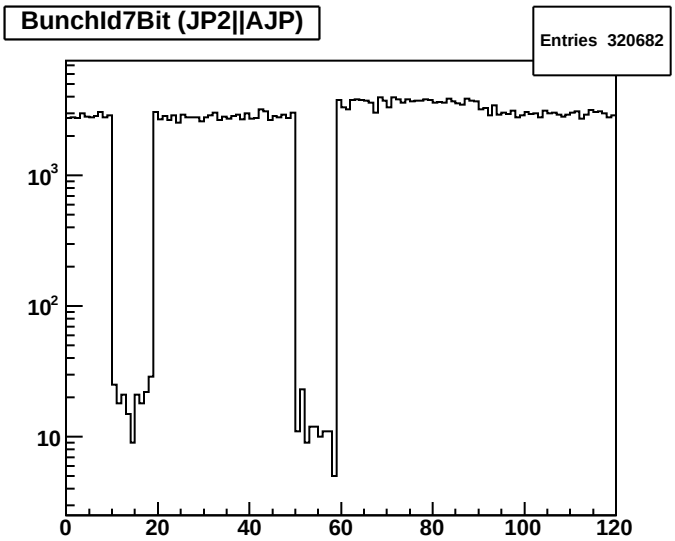

EMC L2 Input - HT23 bits
Entries 2565456
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

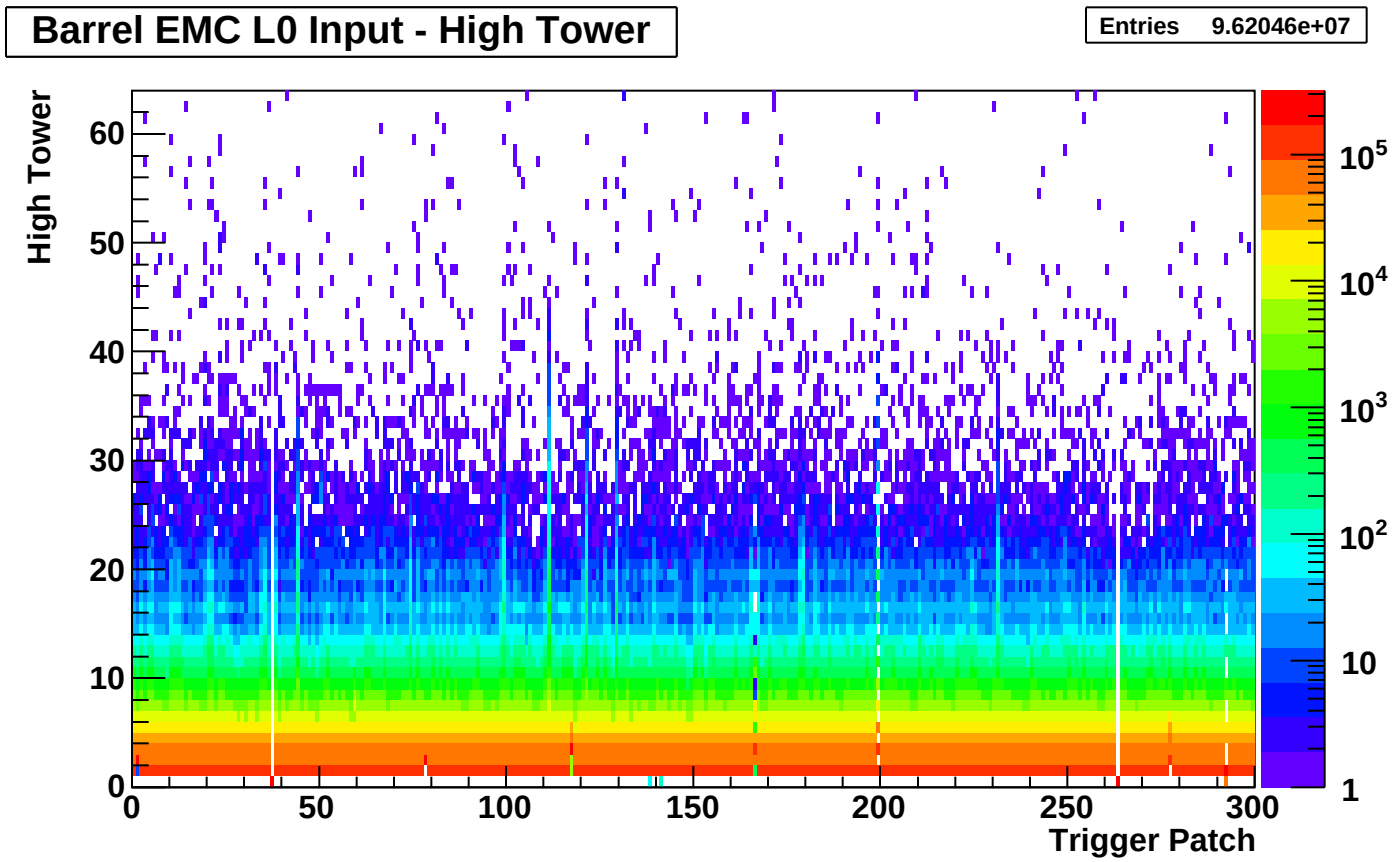

EMC L2 Input - HT23 bits
Entries 1
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

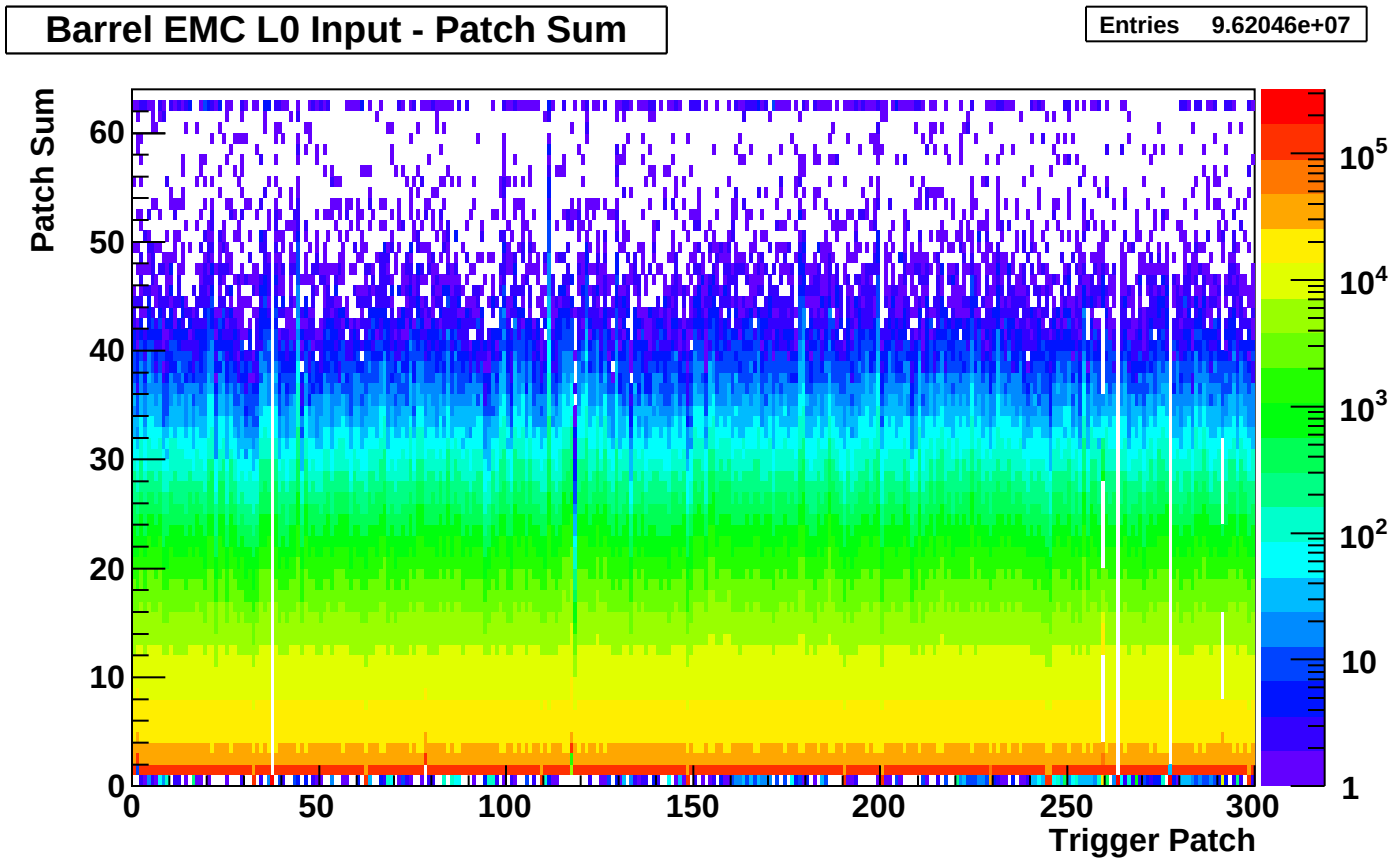

BunchId7Bit (BHT3)
Entries 4475
10^2
10
1
0
20
40
60
80
100
120

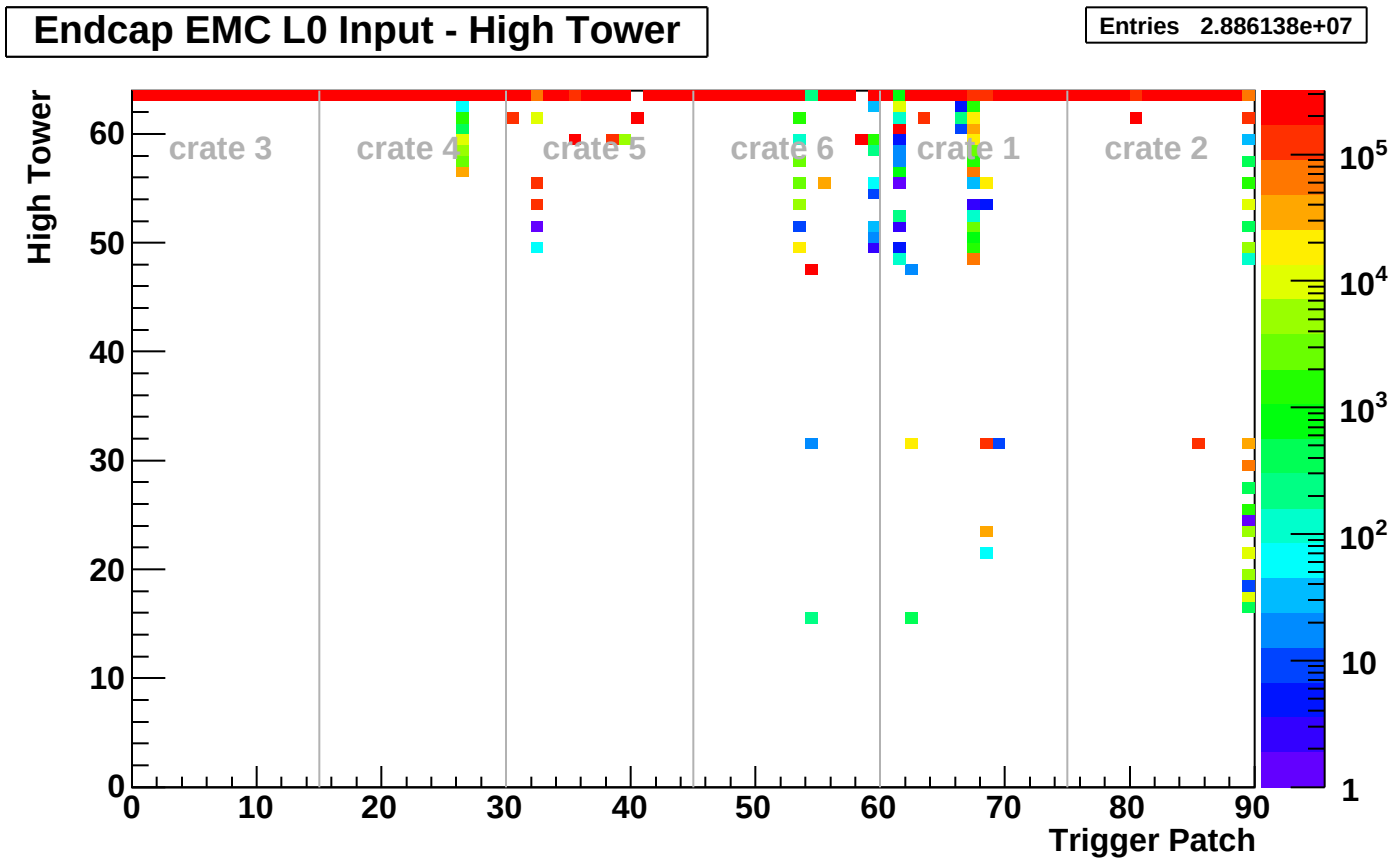

BunchId7Bit (all events)
Entries 320682
10^3
10^2
10
0
20
40
60
80
100
120

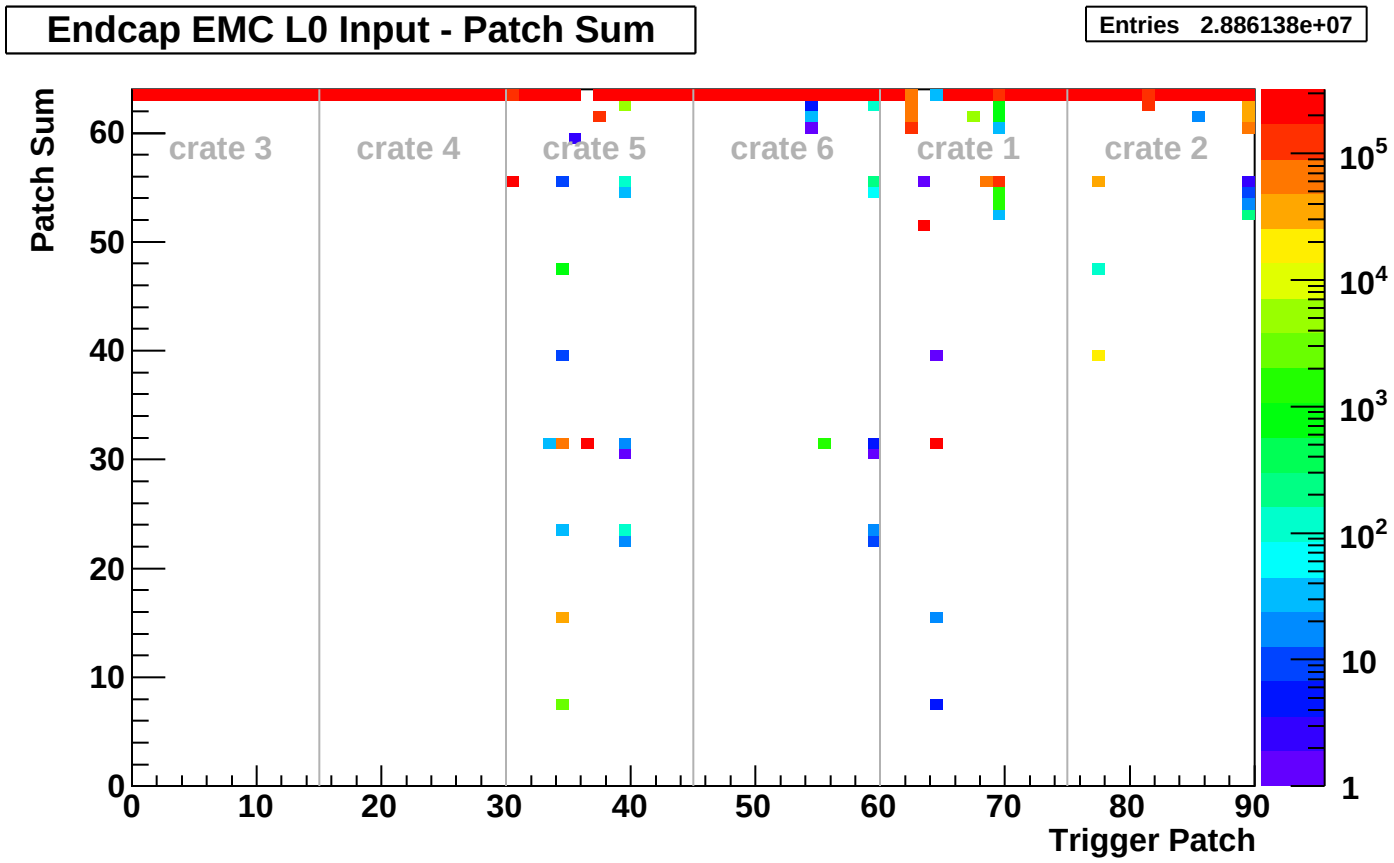

BunchId7Bit (JP1)
Entries 320682
10^3
10^2
10
0
20
40
60
80
100
120

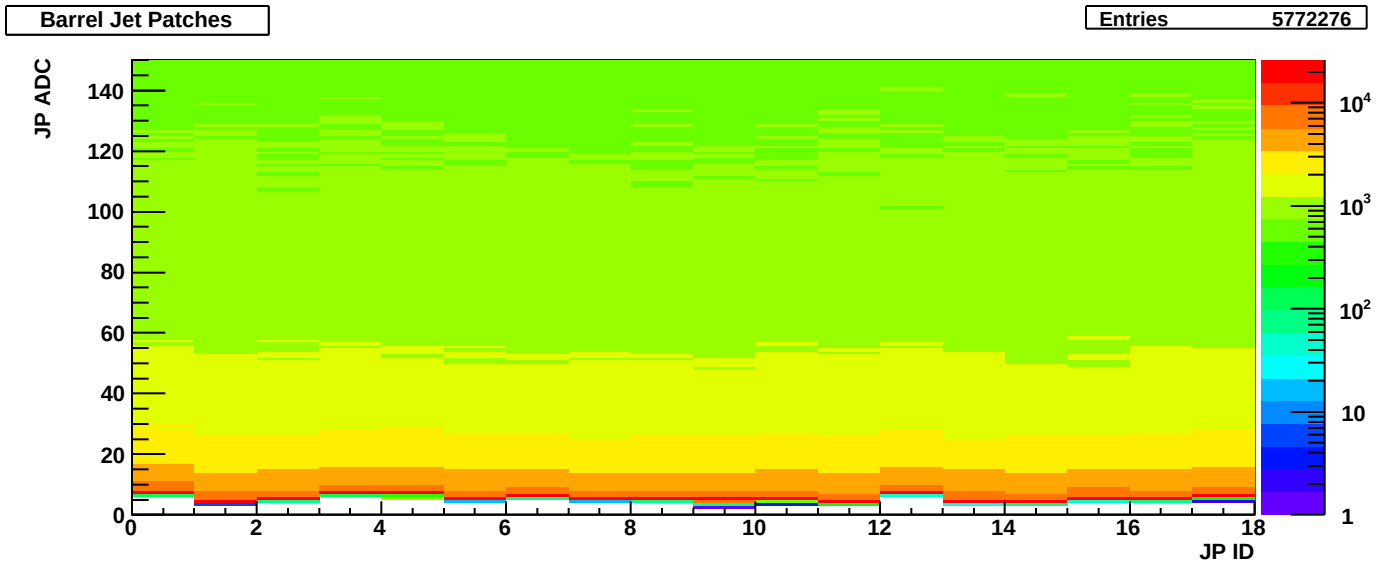

BunchId7Bit (JP2||AJP)
Entries 320682
10^3
10^2
10
0
20
40
60
80
100
120

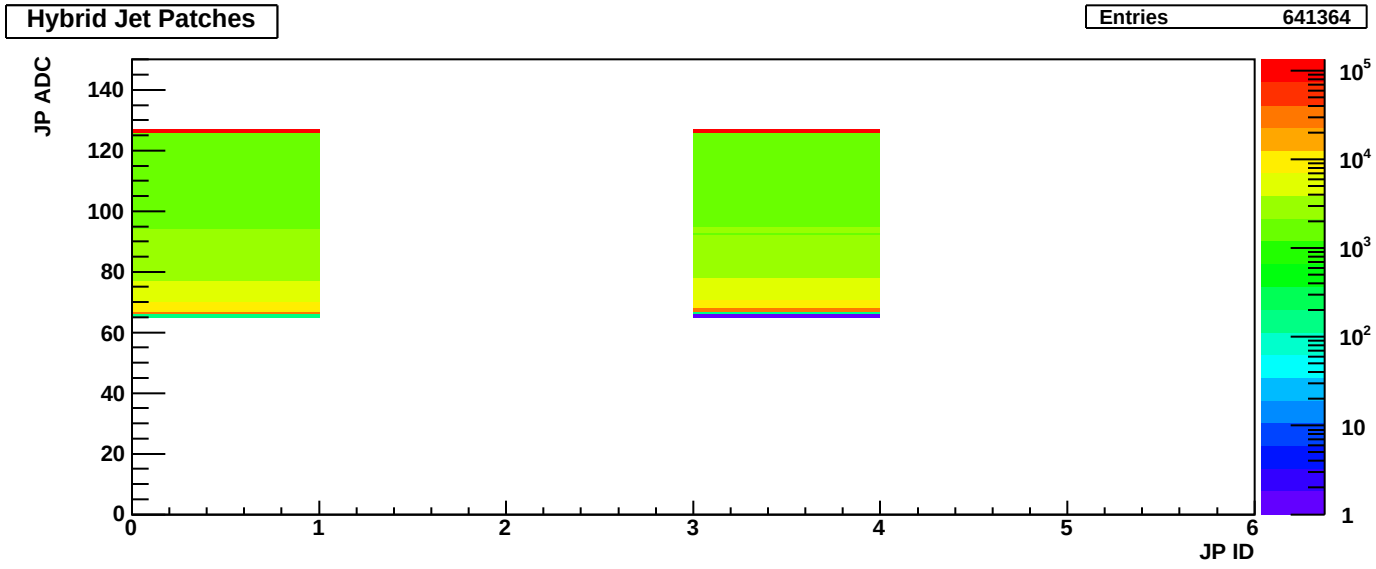

Barrel EMC L0 Input - High Tower
Entries 9.62046e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

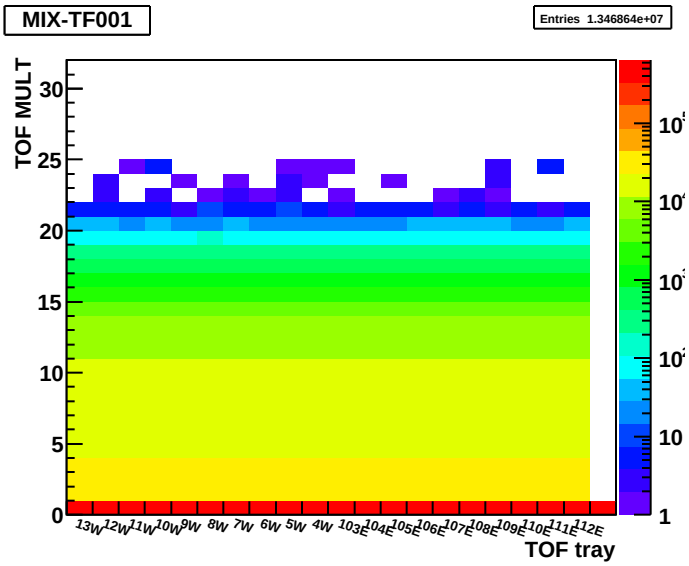

Barrel EMC L0 Input - Patch Sum
Entries 9.62046e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

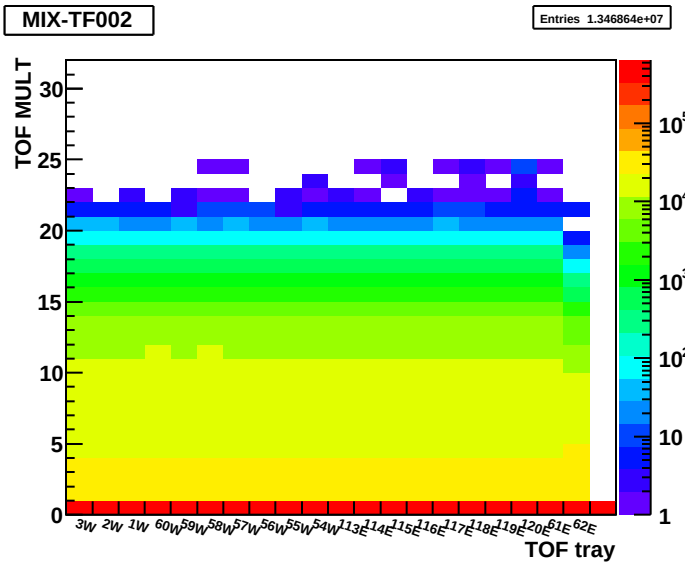

Endcap EMC L0 Input - High Tower
Entries 2.886138e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

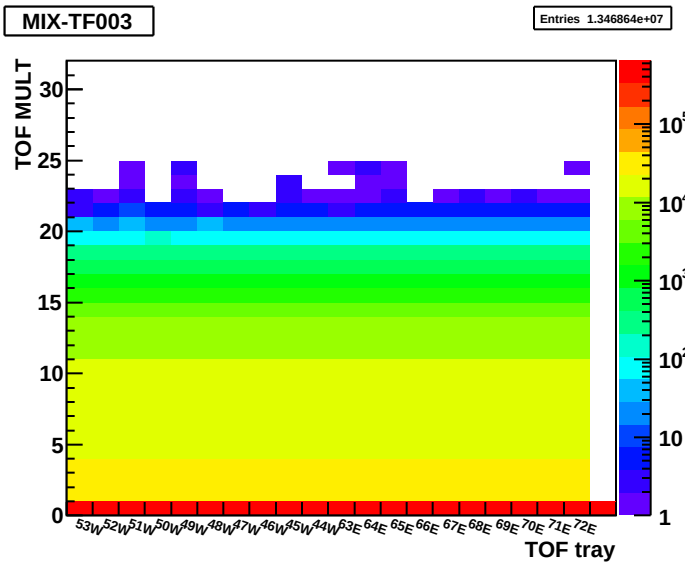

Endcap EMC L0 Input - Patch Sum
Entries 2.886138e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

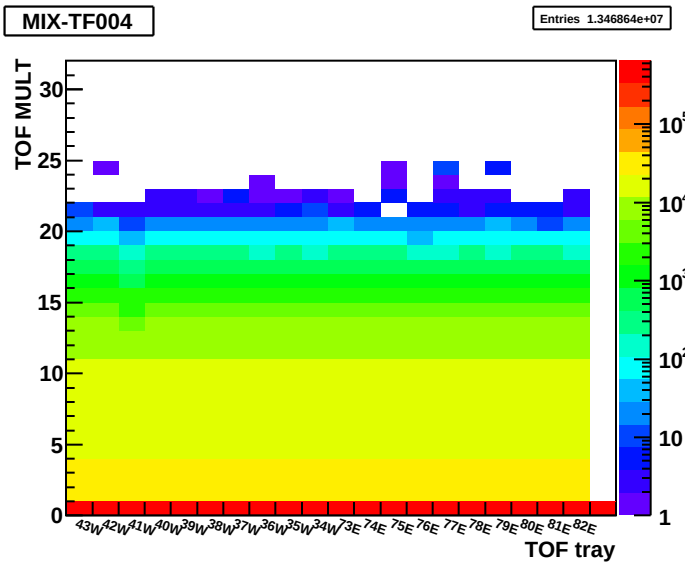

Barrel Jet Patches
Entries 5772276
JP ADC
JP ID

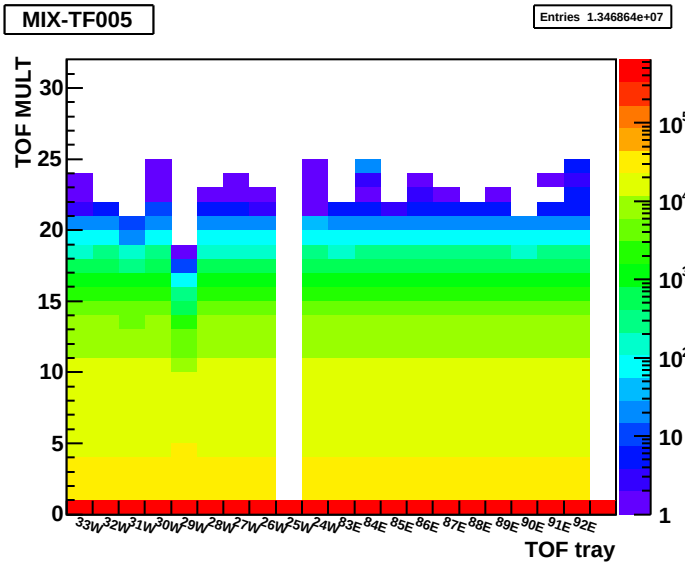

Hybrid Jet Patches
Entries 641364
JP ADC
JP ID

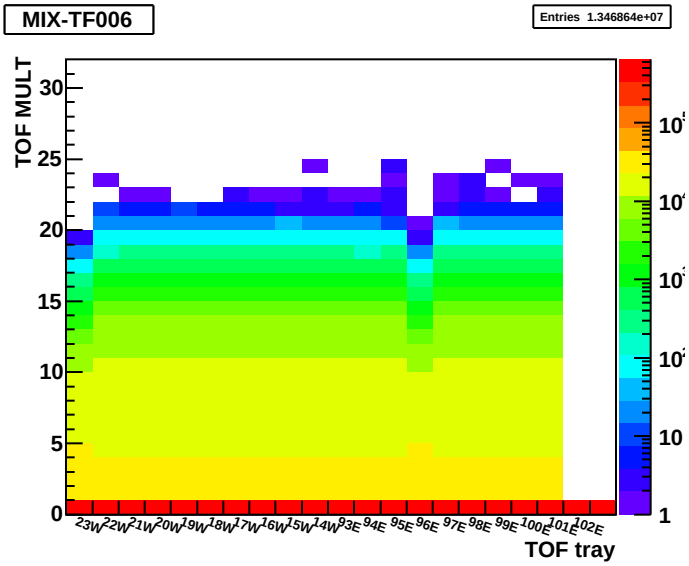

MIX-TF001
Entries 1.346864e+07
TOF MULT
TOF tray

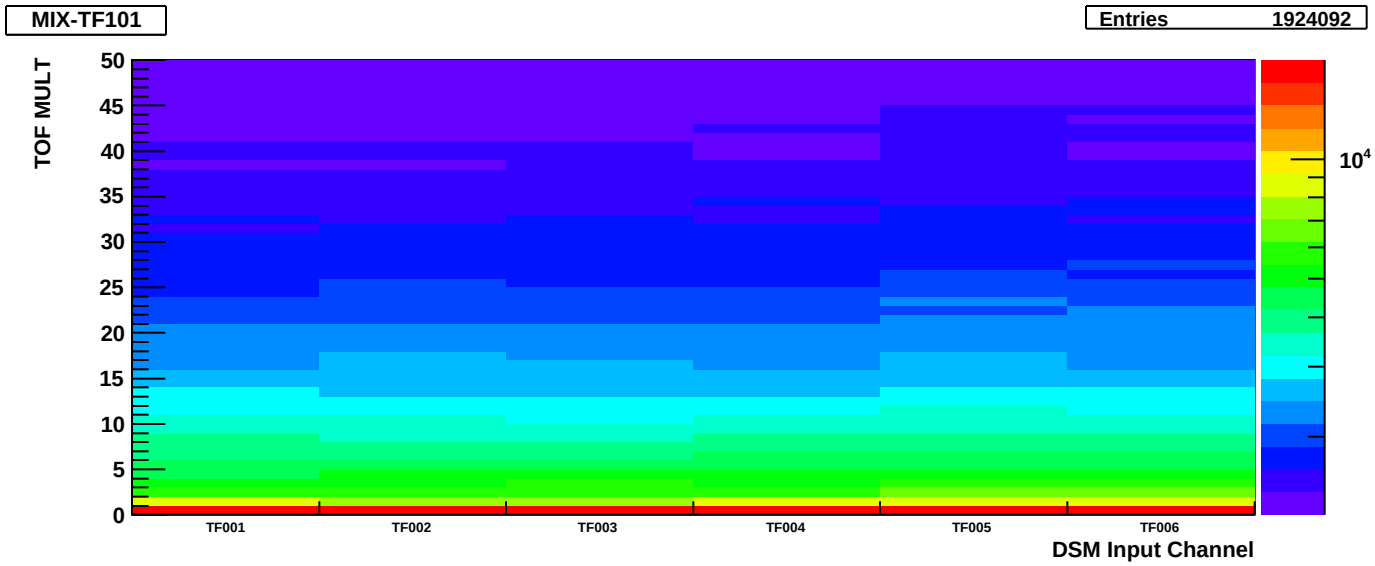

MIX-TF002
Entries 1.346864e+07
TOF MULT
TOF tray

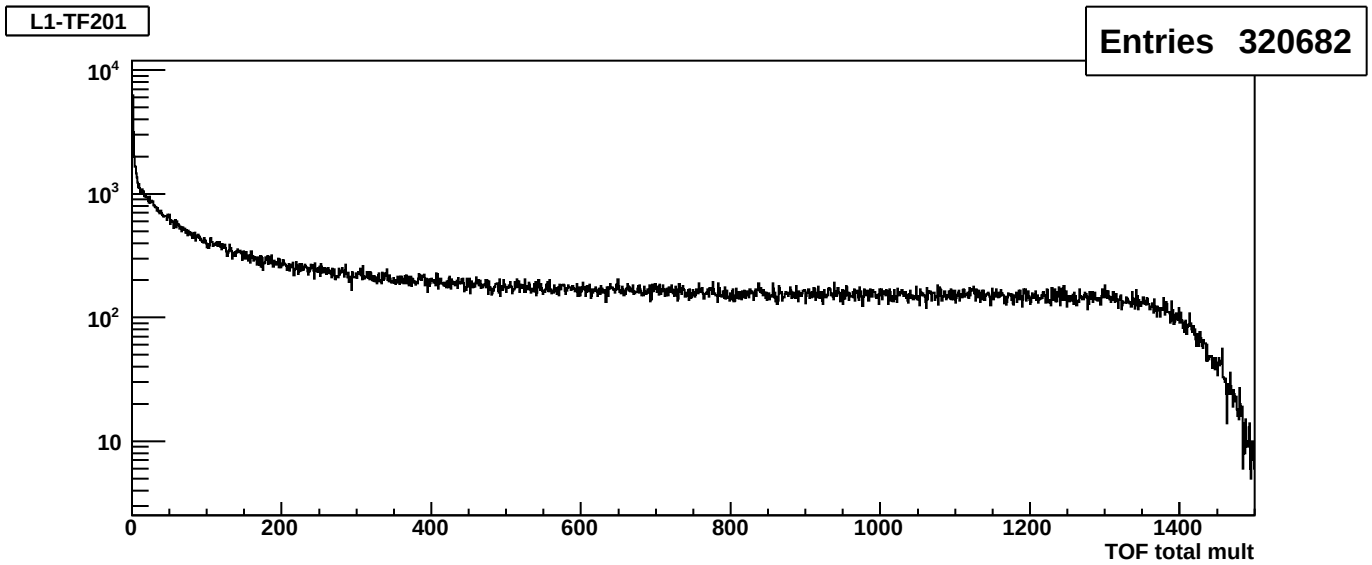

MIX-TF003
Entries 1.346864e+07
TOF MULT
TOF tray

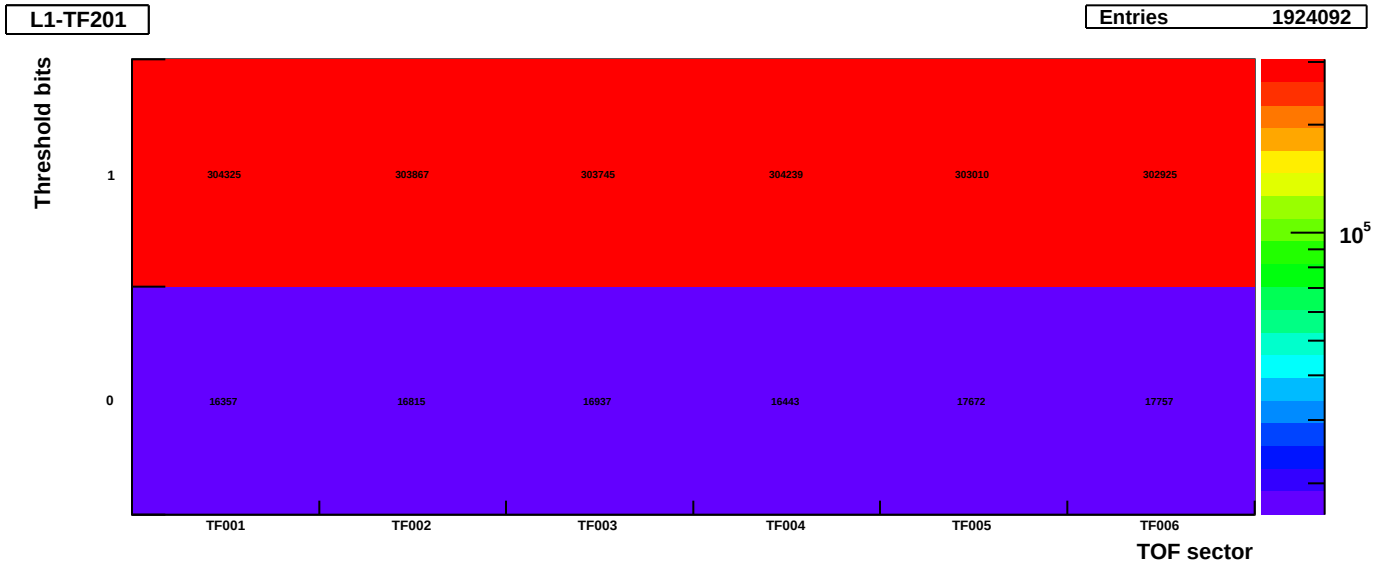

MIX-TF004
Entries 1.346864e+07
TOF MULT
TOF tray

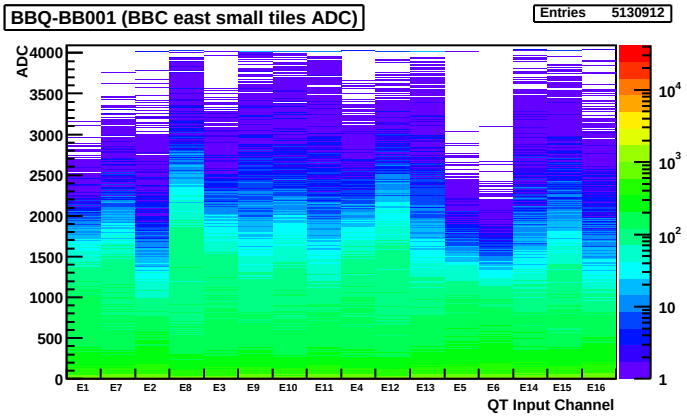

MIX-TF005
Entries 1.346864e+07
TOF MULT
TOF tray

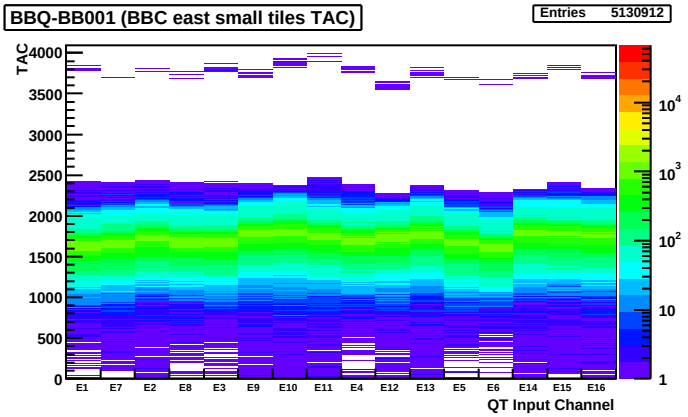

MIX-TF006
Entries 1.346864e+07
TOF MULT
TOF tray

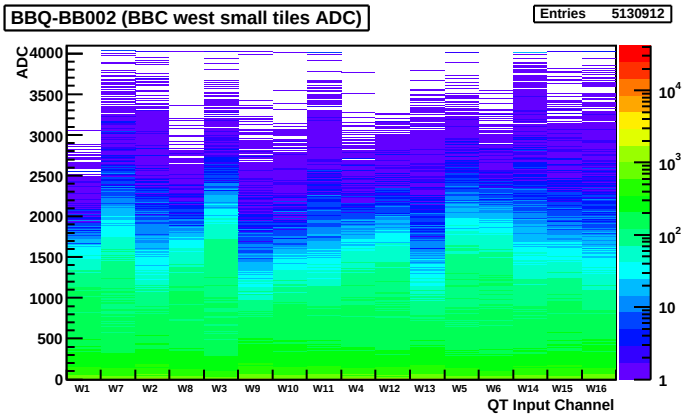

MIX-TF101
Entries 1924092
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

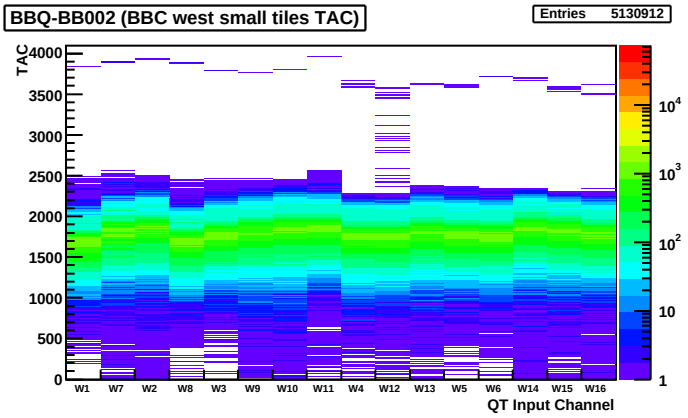

L1-TF201
Entries 320682
10^4
10^3
10^2
10
0
200
400
600
800
1000
1200
1400
TOF total mult

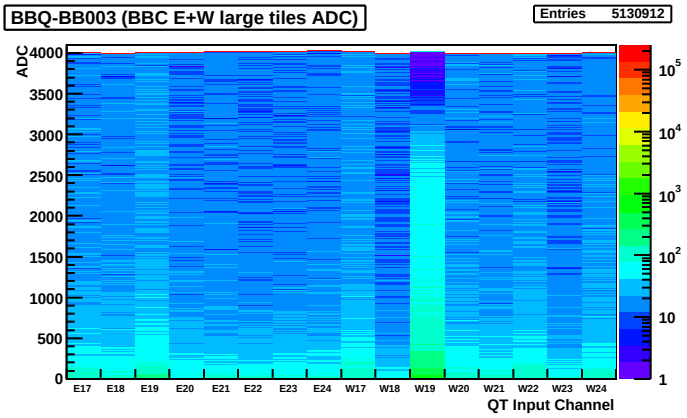

L1-TF201
Entries 1924092
Threshold bits
1
0
304325
303867
303745
304239
303010
302925
16357
16815
16937
16443
17672
17757
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

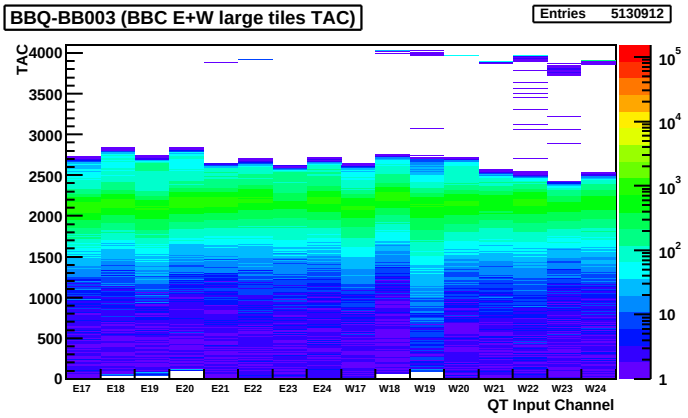

BBQ-BB001 (BBC east small tiles ADC)
Entries 5130912
ADC
QT Input Channel

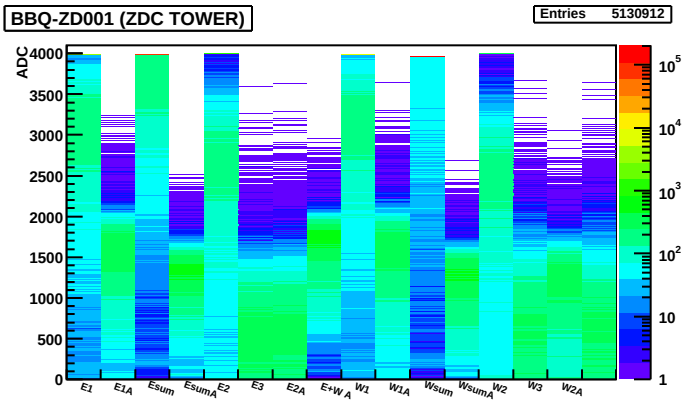

BBQ-BB001 (BBC east small tiles TAC)
Entries 5130912
TAC
QT Input Channel

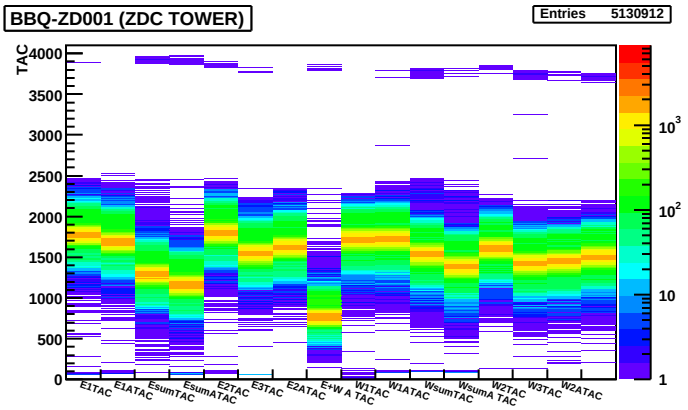

BBQ-BB002 (BBC west small tiles ADC)
Entries 5130912
ADC
QT Input Channel

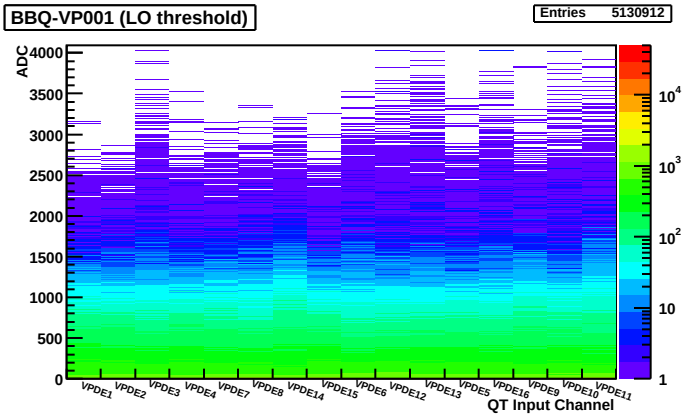

BBQ-BB002 (BBC west small tiles TAC)
Entries 5130912
TAC
QT Input Channel

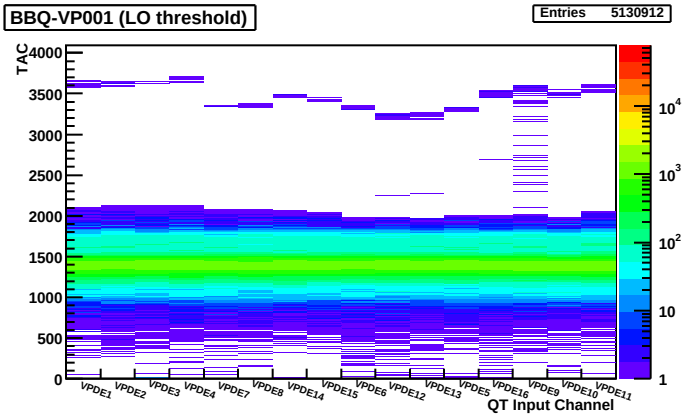

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 5130912
ADC
QT Input Channel

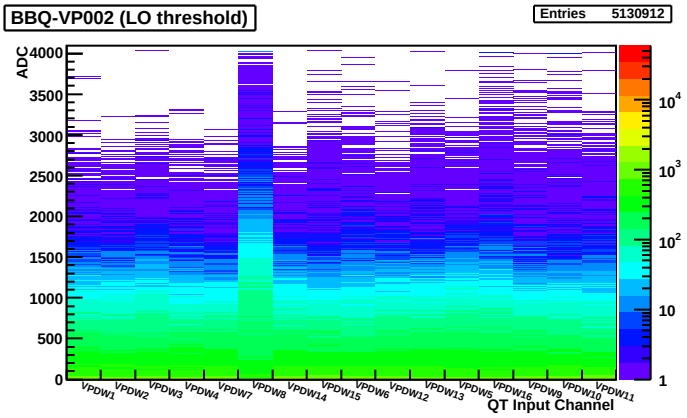

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 5130912
TAC
QT Input Channel

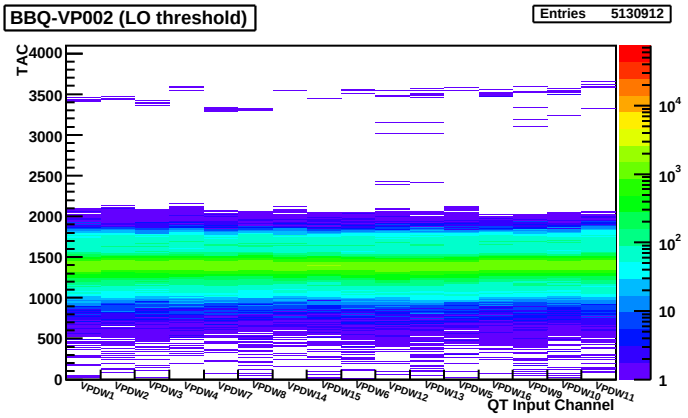

BBQ-ZD001 (ZDC TOWER)
Entries 5130912
ADC

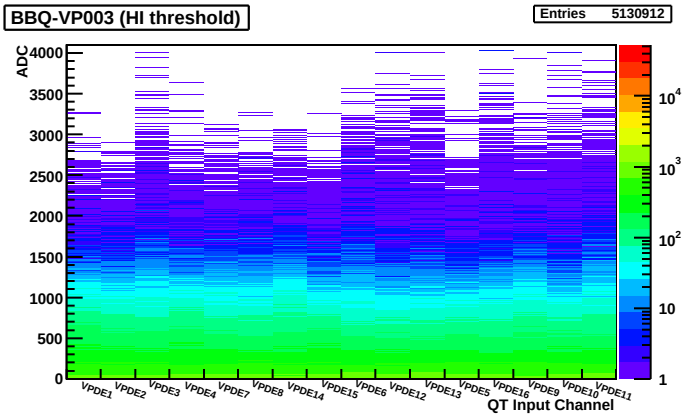

BBQ-ZD001 (ZDC TOWER)
Entries 5130912
TAC

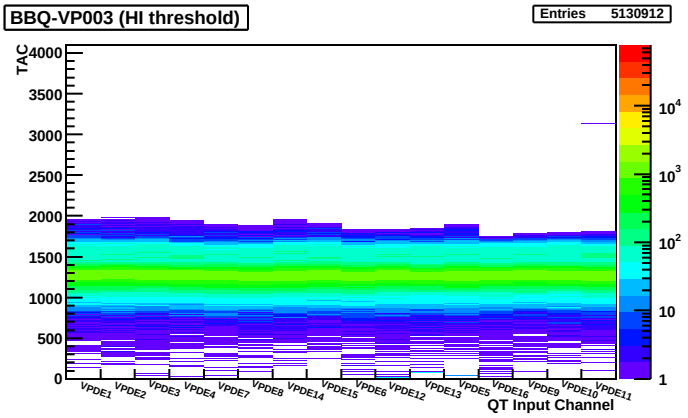

BBQ-VP001 (LO threshold)
Entries 5130912
ADC
QT Input Channel

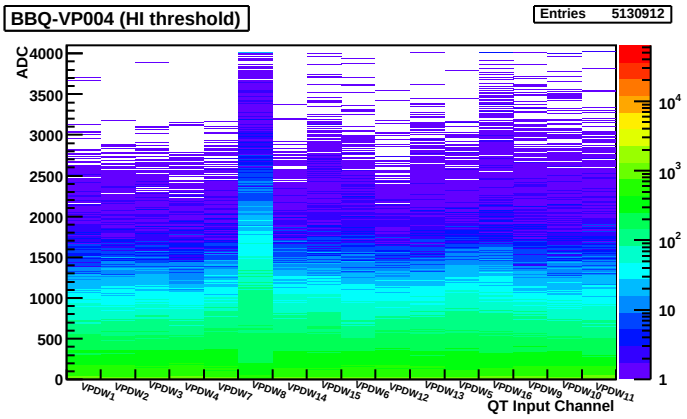

BBQ-VP001 (LO threshold)
Entries 5130912
TAC
QT Input Channel

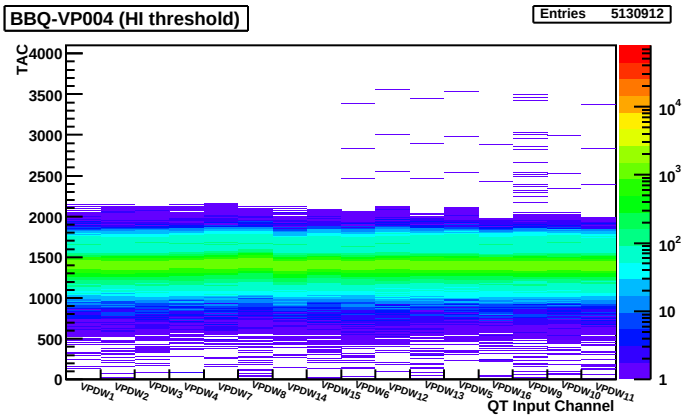

BBQ-VP002 (LO threshold)
Entries 5130912
ADC
QT Input Channel

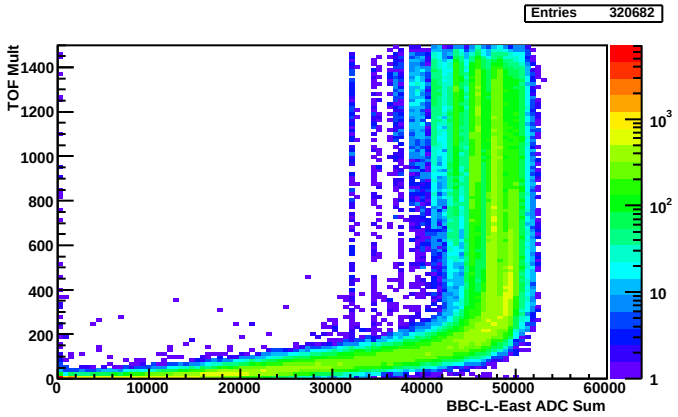

BBQ-VP002 (LO threshold)
Entries 5130912
TAC
QT Input Channel

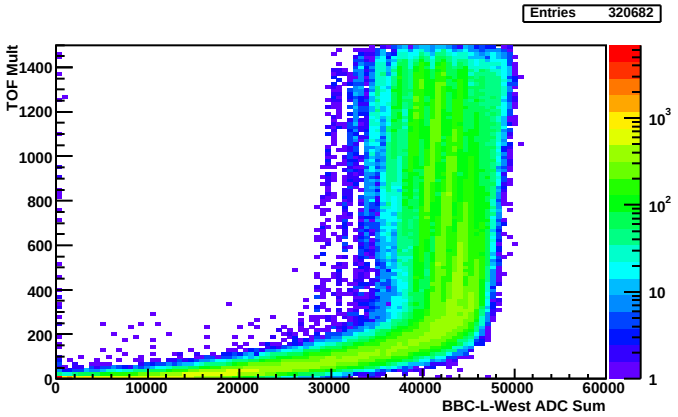

BBQ-VP003 (HI threshold)
Entries 5130912
ADC
QT Input Channel

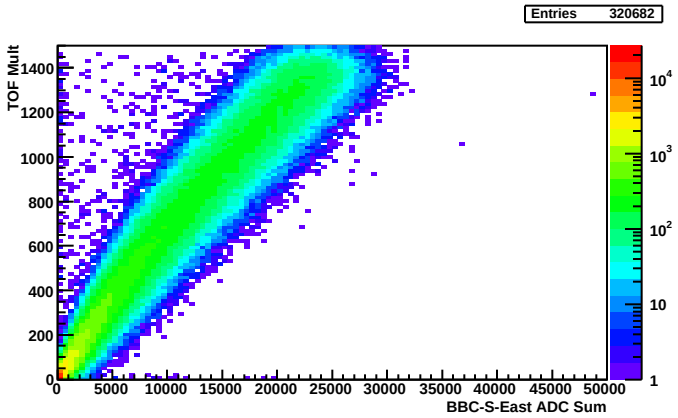

BBQ-VP003 (HI threshold)
Entries 5130912
TAC
QT Input Channel

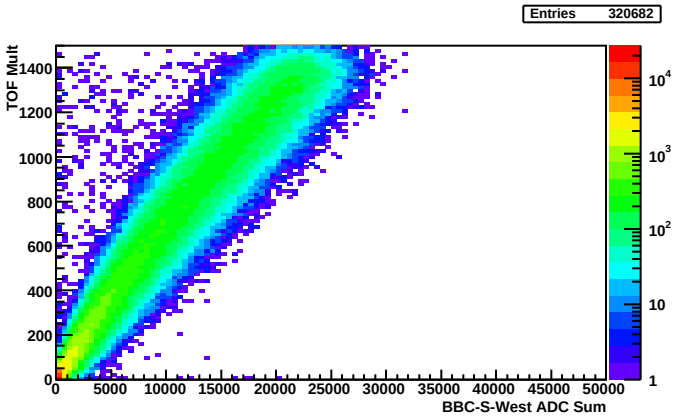

BBQ-VP004 (HI threshold)
Entries 5130912
ADC
QT Input Channel

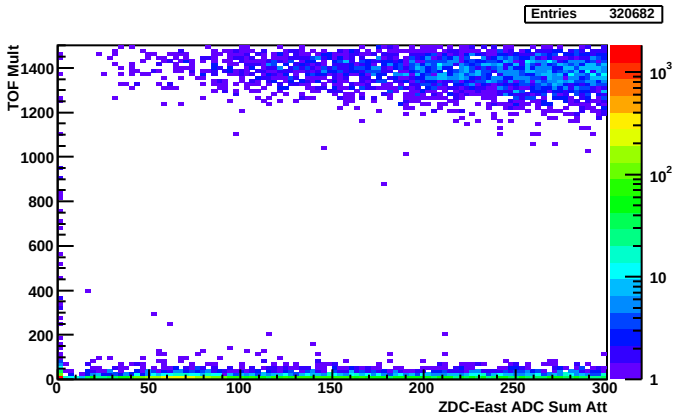

BBQ-VP004 (HI threshold)
Entries 5130912
TAC
QT Input Channel

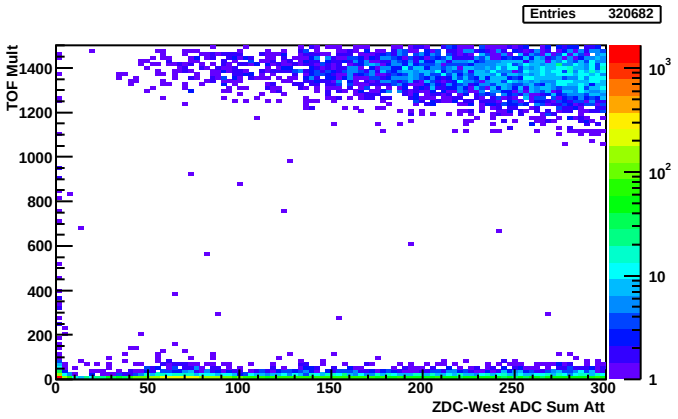

Entries 320682
TOF Mult
BBC-L-East ADC Sum

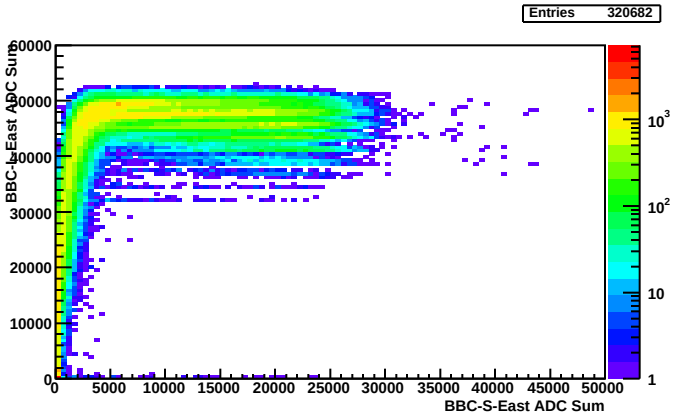

Entries 320682
TOF Mult
BBC-L-West ADC Sum

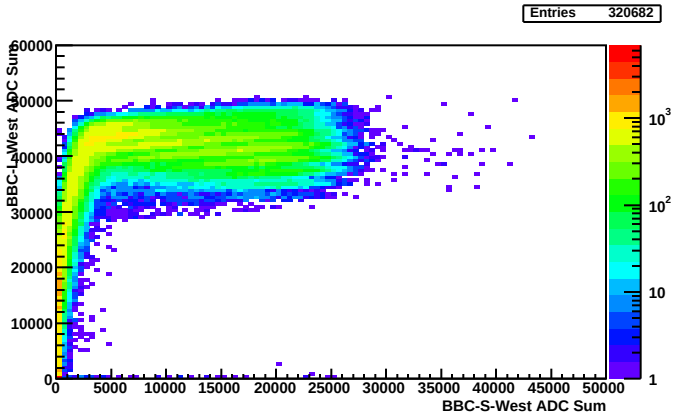

Entries 320682
TOF Mult
BBC-S-East ADC Sum

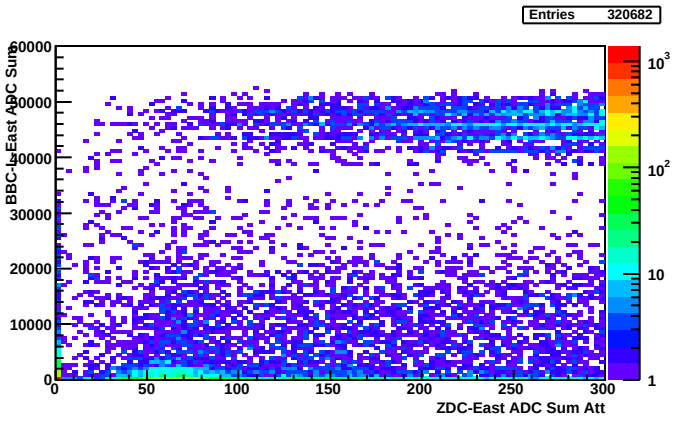

Entries 320682
TOF Mult
BBC-S-West ADC Sum

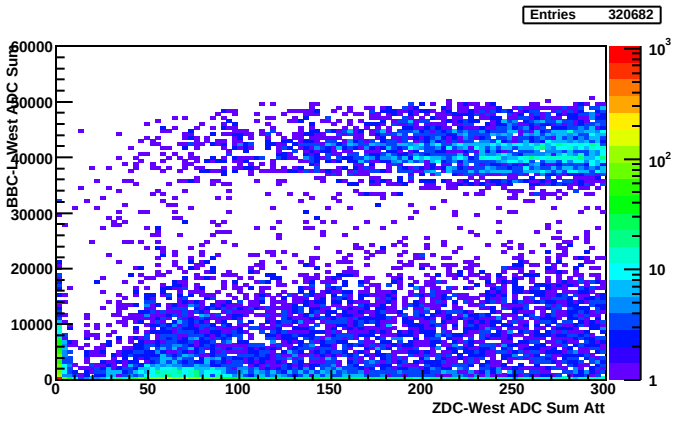

Entries 320682
TOF Mult
ZDC-East ADC Sum Att


Entries 320682
TOF Mult
ZDC-West ADC Sum Att


Entries 320682
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 320682
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 320682
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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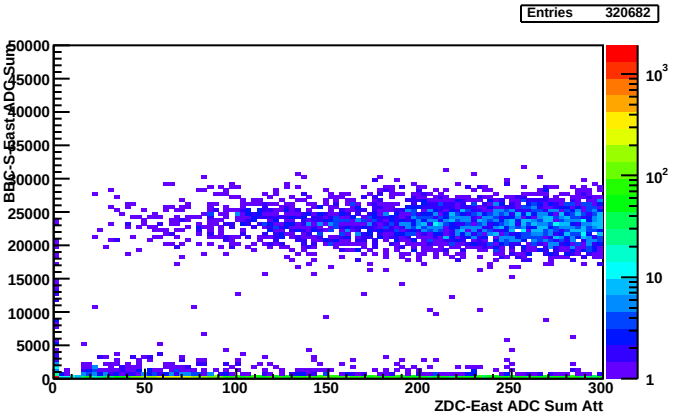
BBC-L-West ADC Sum
ZDC-West ADC Sum Att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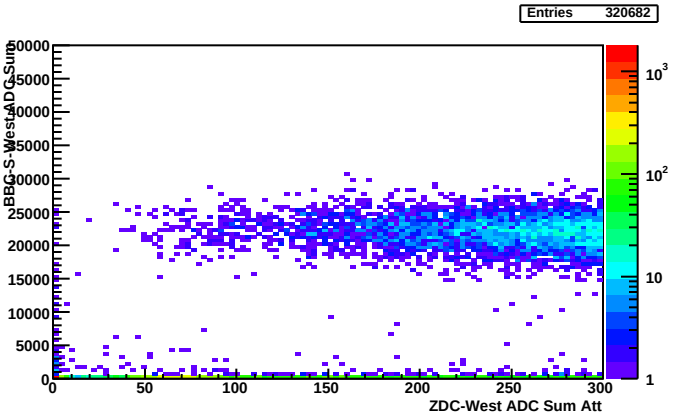
BBC-S-East ADC Sum
ZDC-East ADC Sum Att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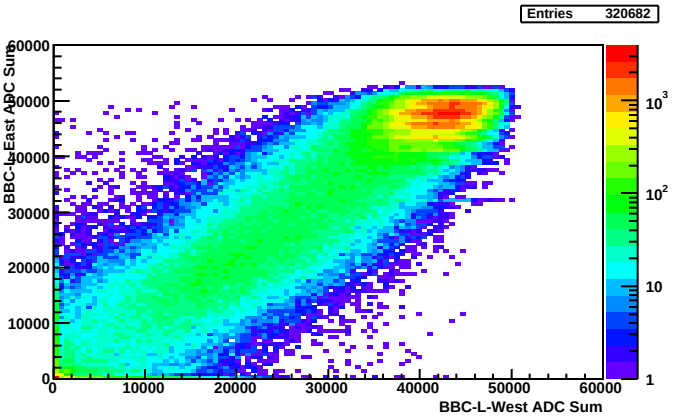
ZDC-West ADC Sum Att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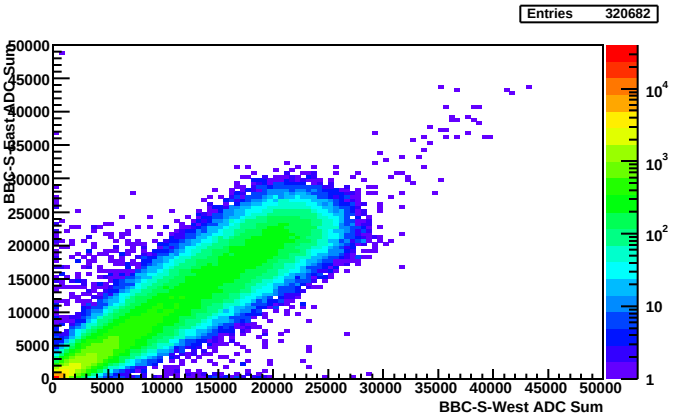
BBC-L-East ADC Sum
BBC-L-West ADC Sum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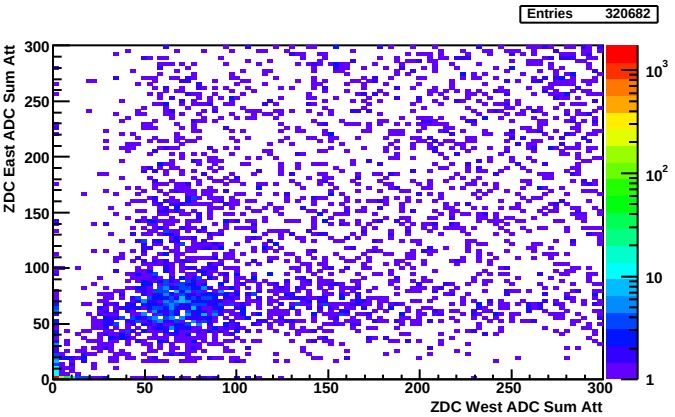
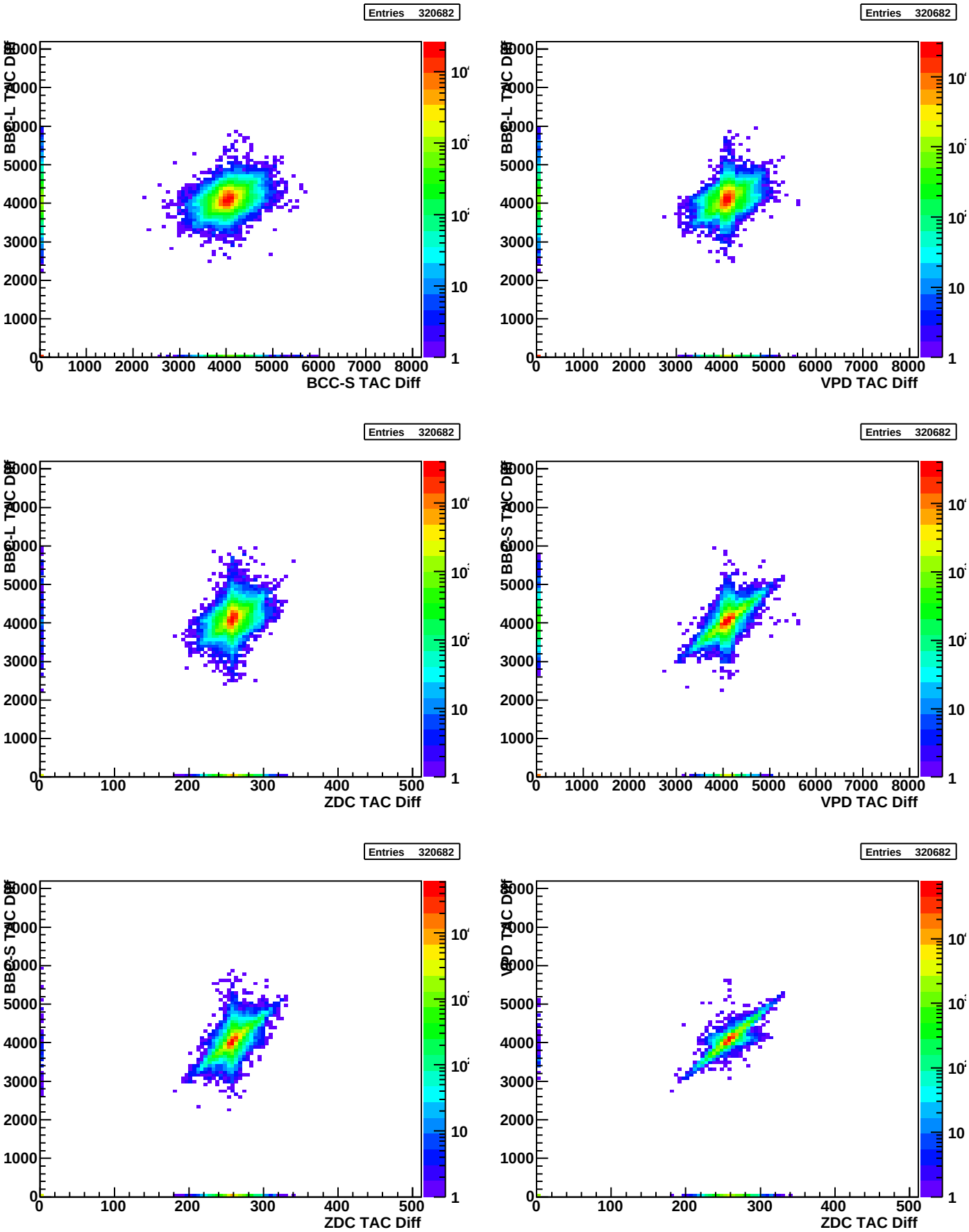
BBC-S-East ADC Sum
BBC-S-West ADC Sum


Entries 320682
ZDC East ADC Sum Att
ZDC West ADC Sum Att


Entries 320682
BBC-L TAC Diff
BCC-S TAC Diff
Entries 320682
BBC-L TAC Diff
VPD TAC Diff
Entries 320682
BBC-L TAC Diff
ZDC TAC Diff
Entries 320682
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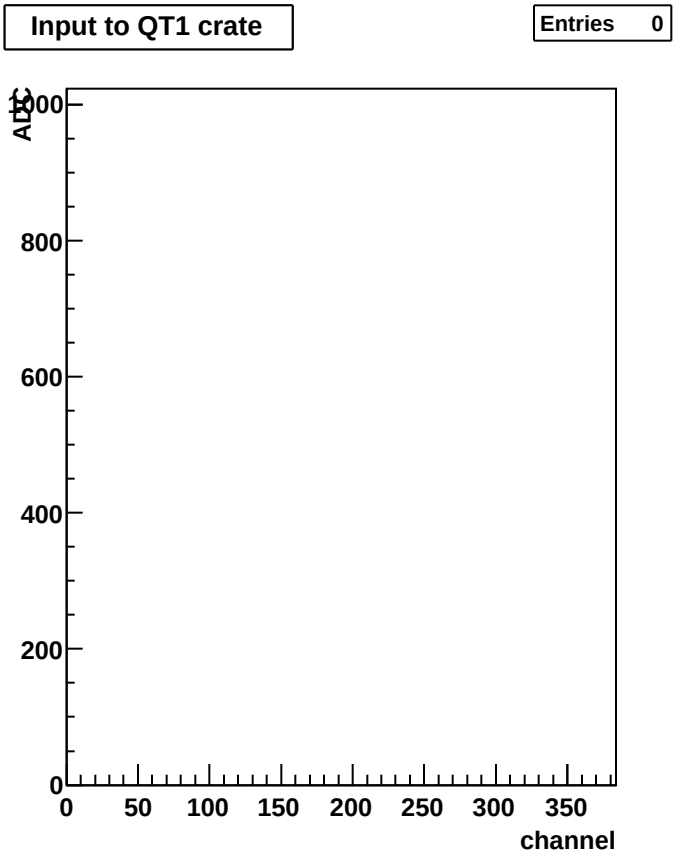
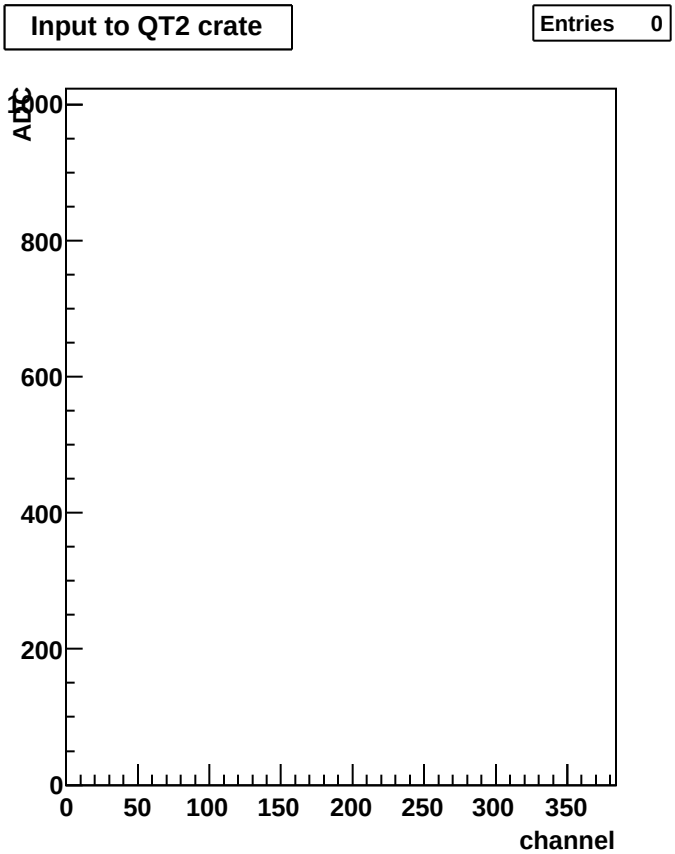
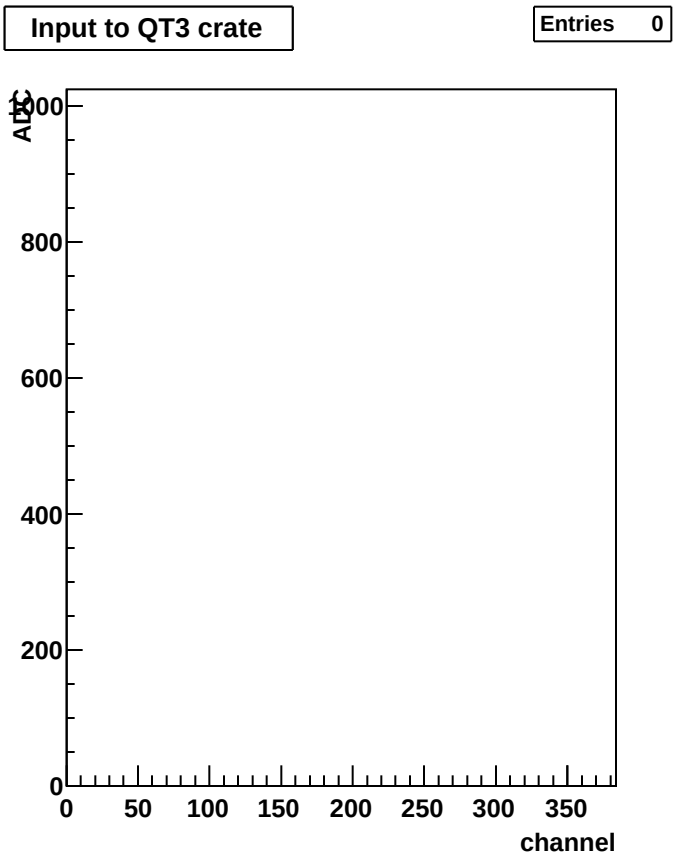
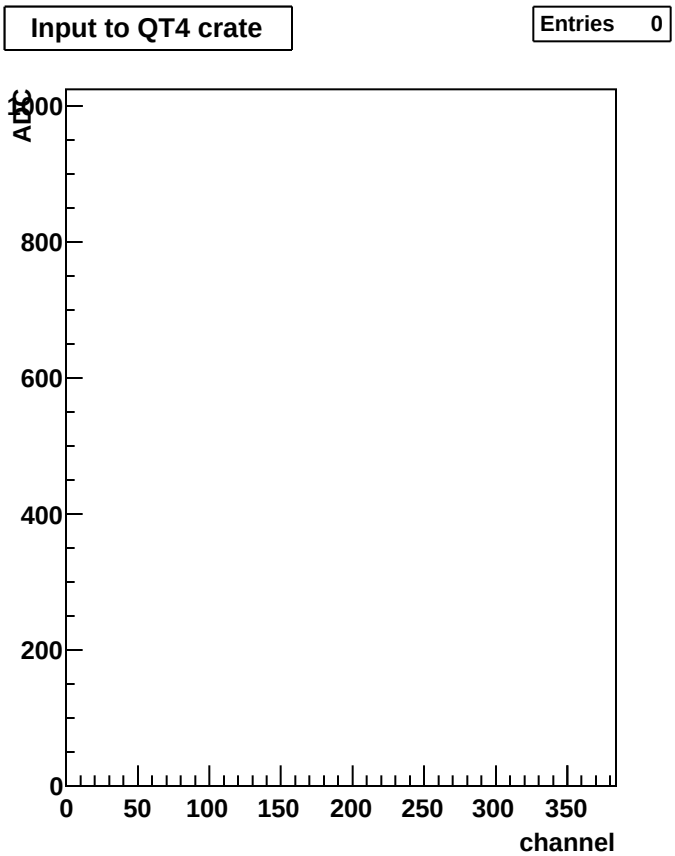
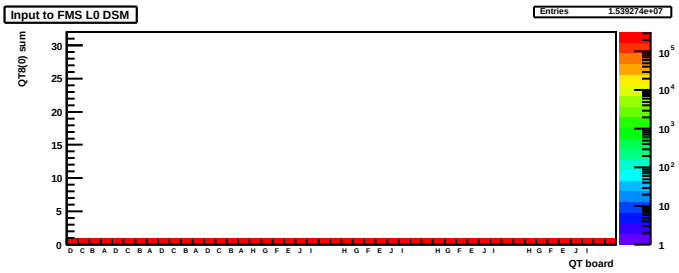
VPD TAC Diff
Entries 320682
BBC-S TAC Diff
ZDC TAC Diff
Entries 320682
VPD TAC Diff
ZDC TAC Diff

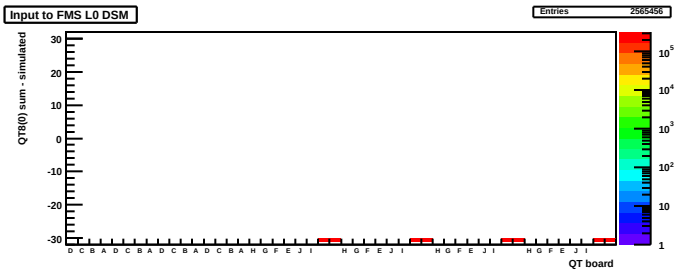

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

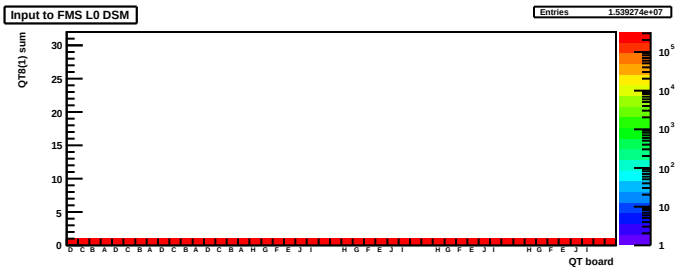

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

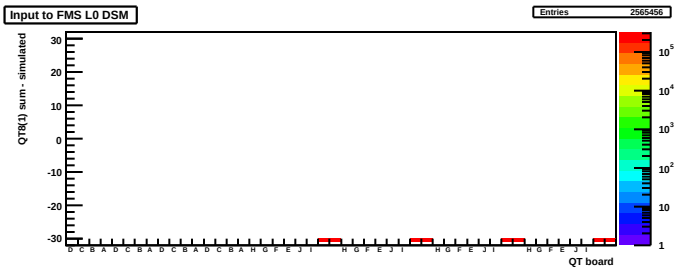

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

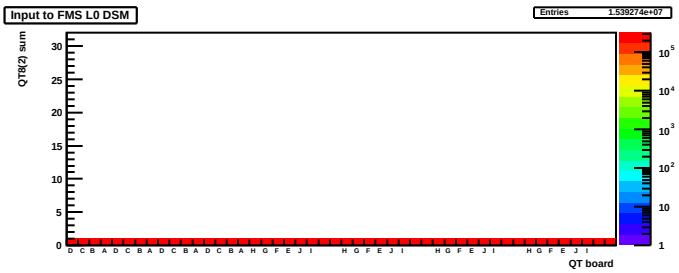

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

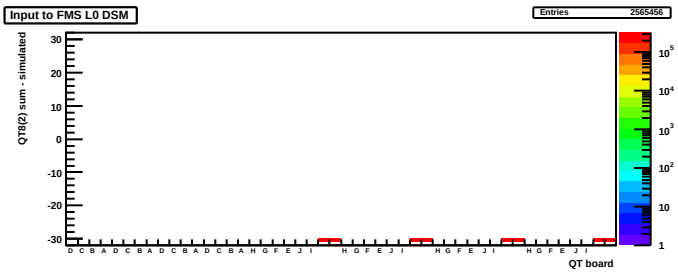

Input to FMS L0 DSM
Entries 1.539274e+07
QT8(0) sum
QT board

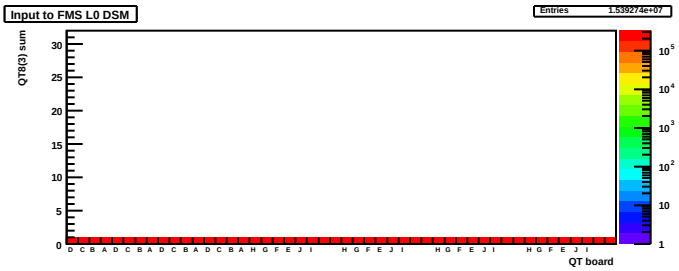

Input to FMS L0 DSM
Entries 2565456
QT8(0) sum - simulated
QT board

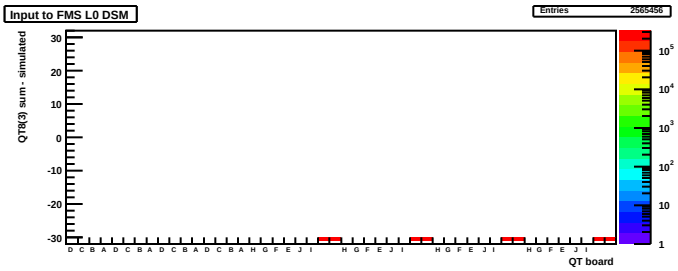

Input to FMS L0 DSM
Entries 1.539274e+07
QT8(1) sum
QT board

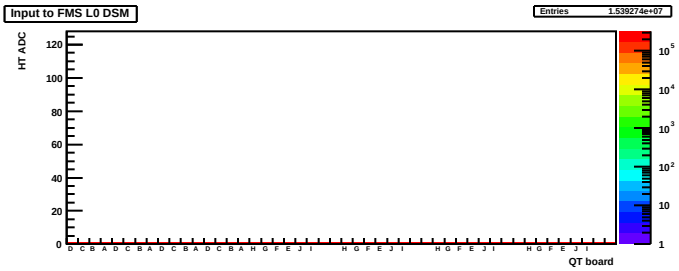

Input to FMS L0 DSM
Entries 2565456
QT8(1) sum - simulated
QT board

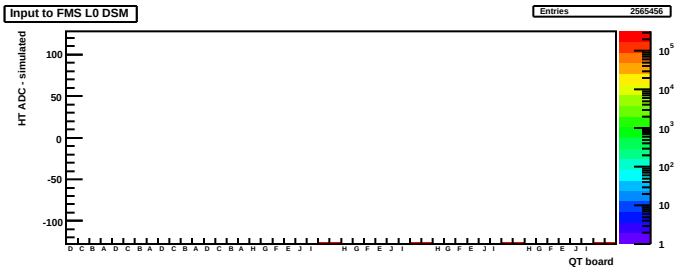

Input to FMS L0 DSM
Entries 1.539274e+07
QT8(2) sum
QT board

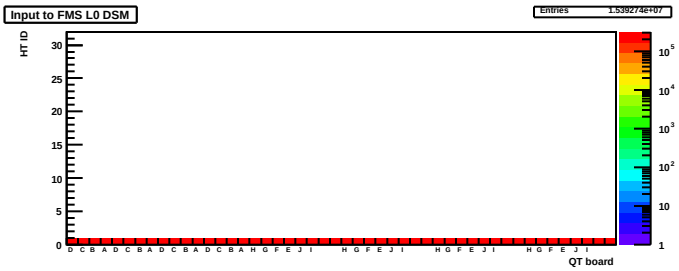

Input to FMS L0 DSM
Entries 2565456
QT8(2) sum - simulated
QT board

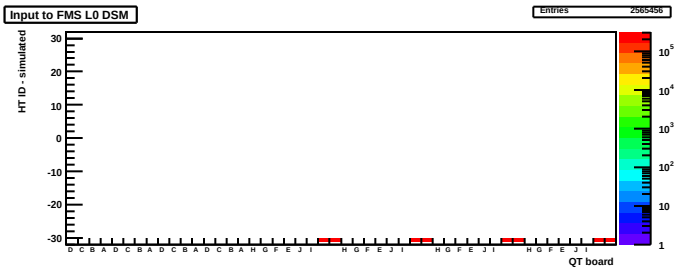

Input to FMS L0 DSM
Entries 1.539274e+07
QT8(3) sum
QT board

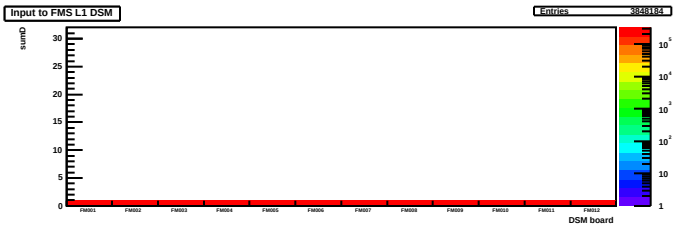

Input to FMS L0 DSM
Entries 2565456
QT8(3) sum - simulated
QT board

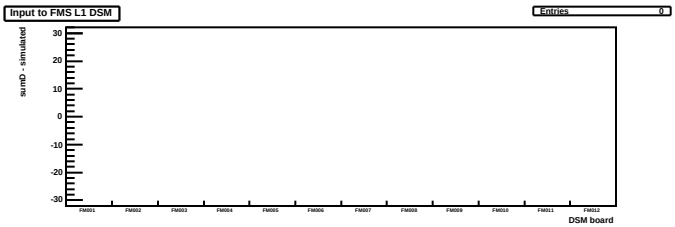

Input to FMS L0 DSM
Entries 1.539274e+07
HT ADC
QT board

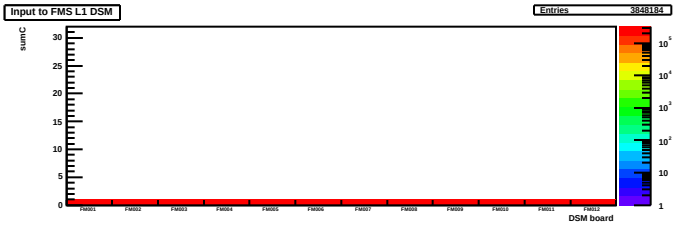

Input to FMS L0 DSM
Entries 2565456
HT ADC - simulated
QT board

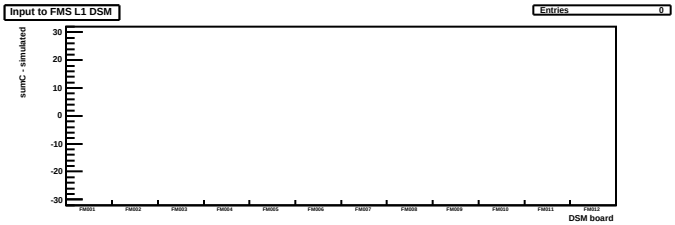

Input to FMS L0 DSM
Entries 1.539274e+07
HT ID
QT board

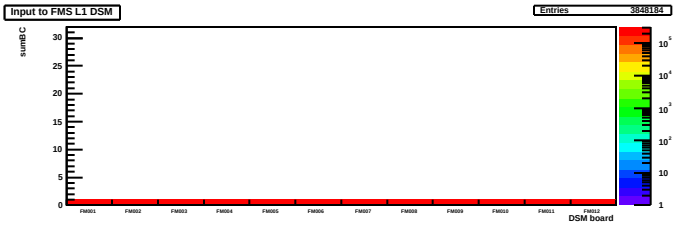

Input to FMS L0 DSM
Entries 2565456
HT ID - simulated
QT board

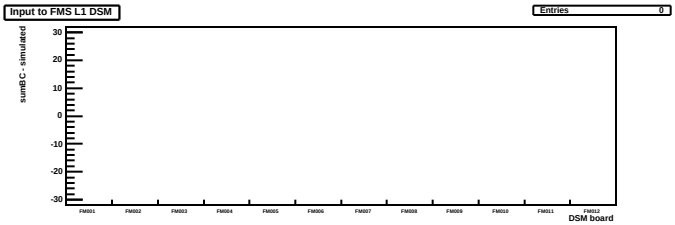

Input to FMS L1 DSM
Entries 3848184
sumD
DSM board

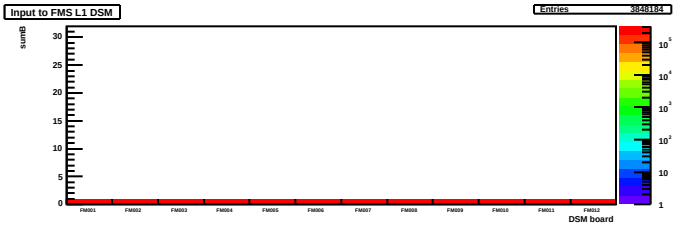

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

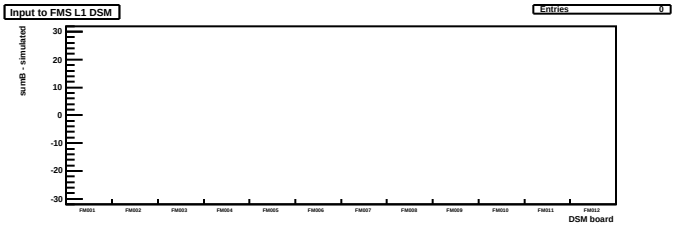

Input to FMS L1 DSM
Entries 3848184
sumC
DSM board

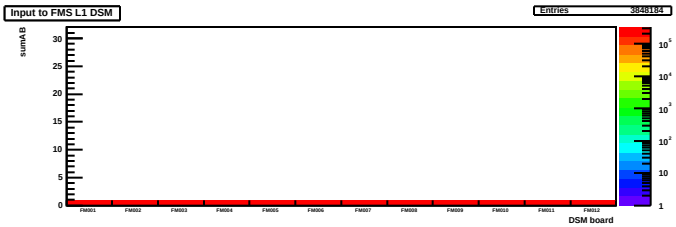

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

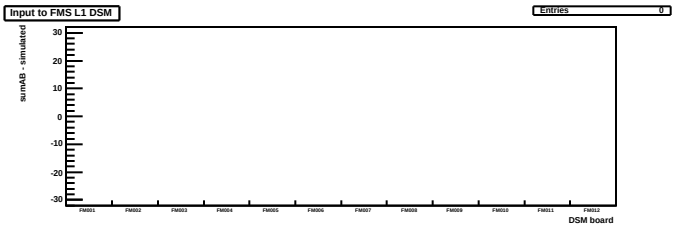

Input to FMS L1 DSM
Entries 3848184
sumBC
DSM board

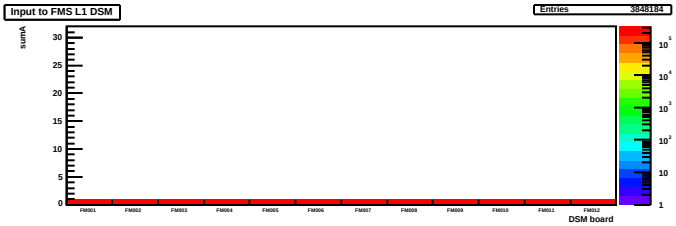

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

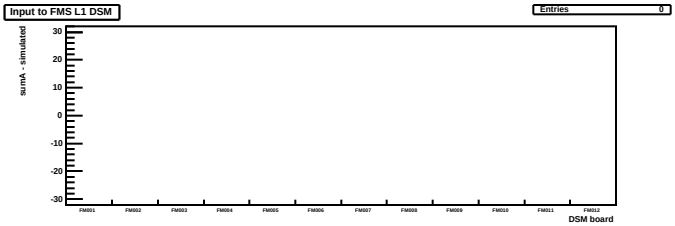

Input to FMS L1 DSM
Entries 3848184
sumB
DSM board

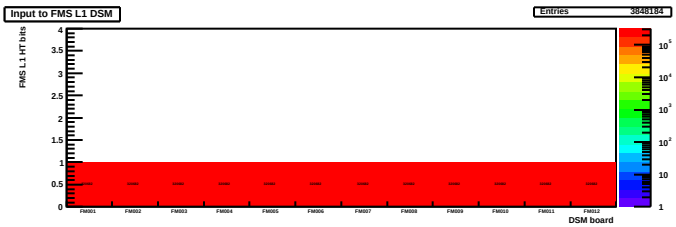

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

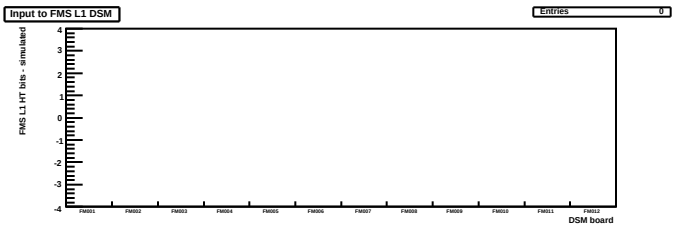

Input to FMS L1 DSM
Entries 3848184
sumAB
DSM board

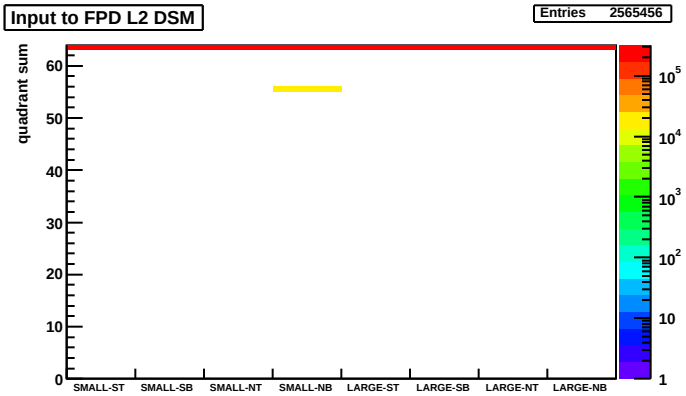

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

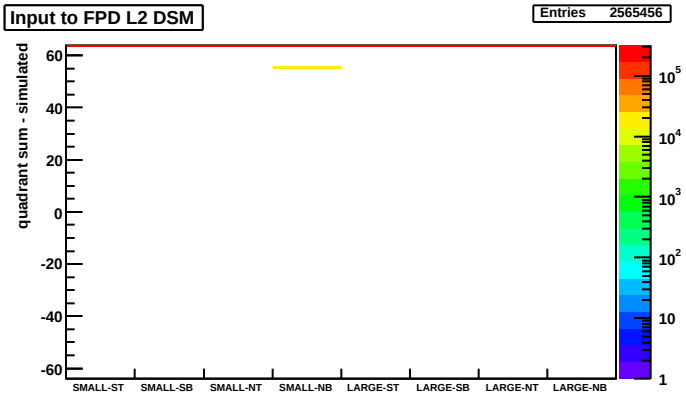

Input to FMS L1 DSM
Entries 3848184
sumA
DSM board

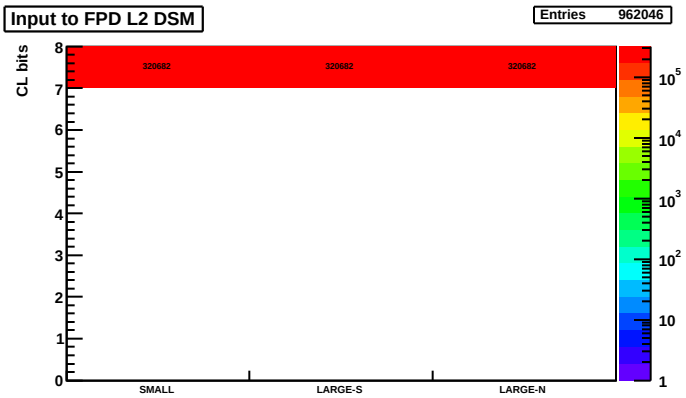

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

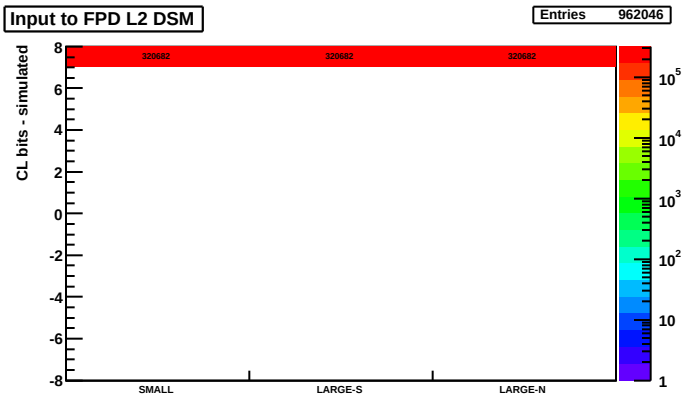

Input to FMS L1 DSM
Entries 3848184
FMS L1 HT bits
DSM board

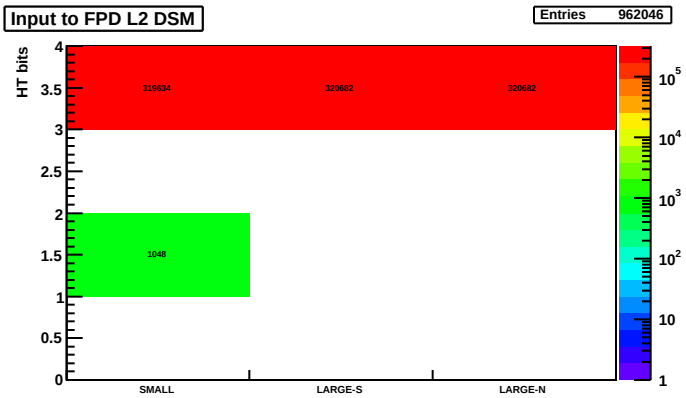

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

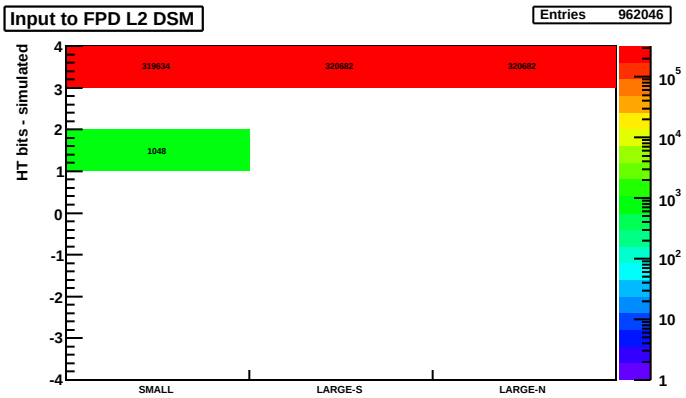

Input to FPD L2 DSM
Entries 2565456
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

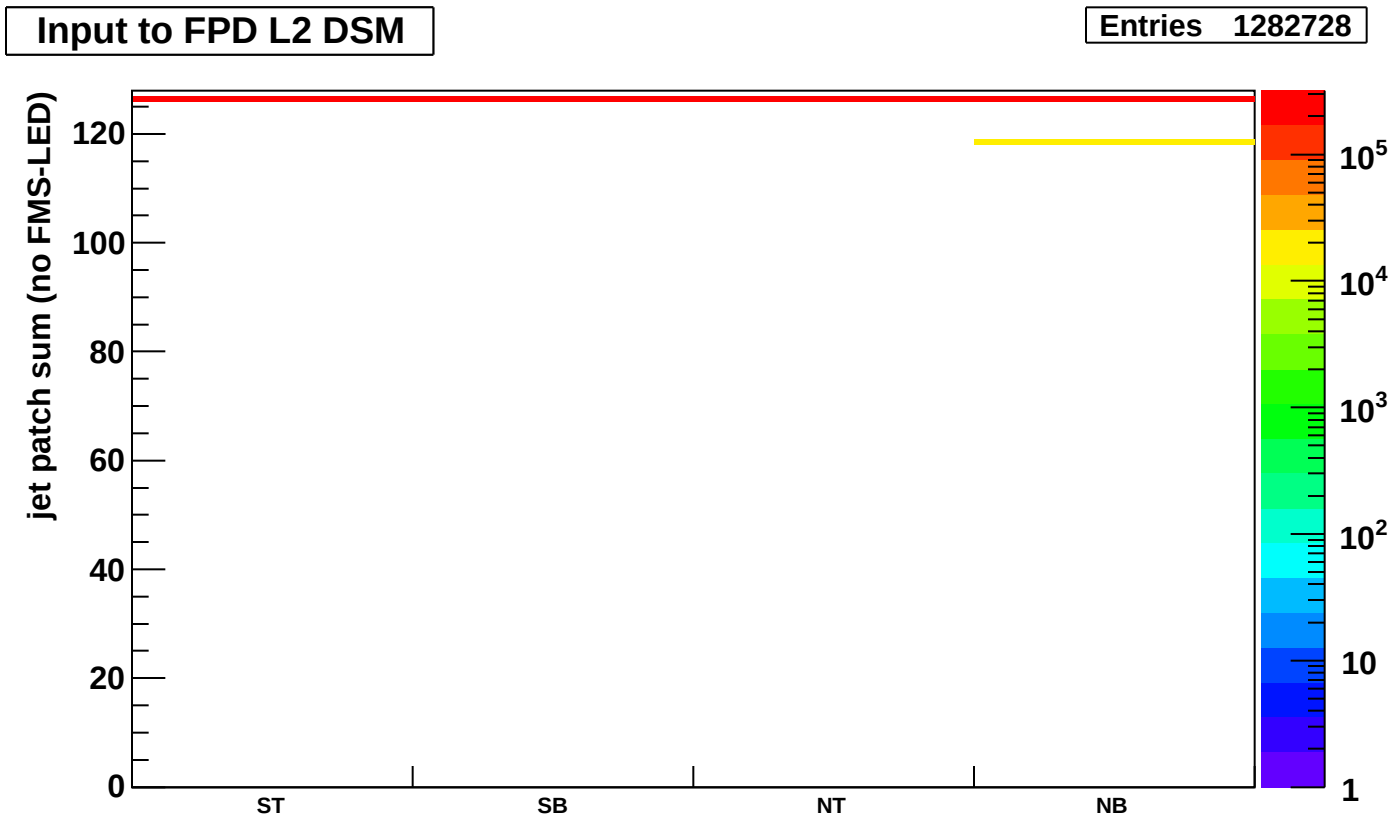

Input to FPD L2 DSM
Entries 2565456
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

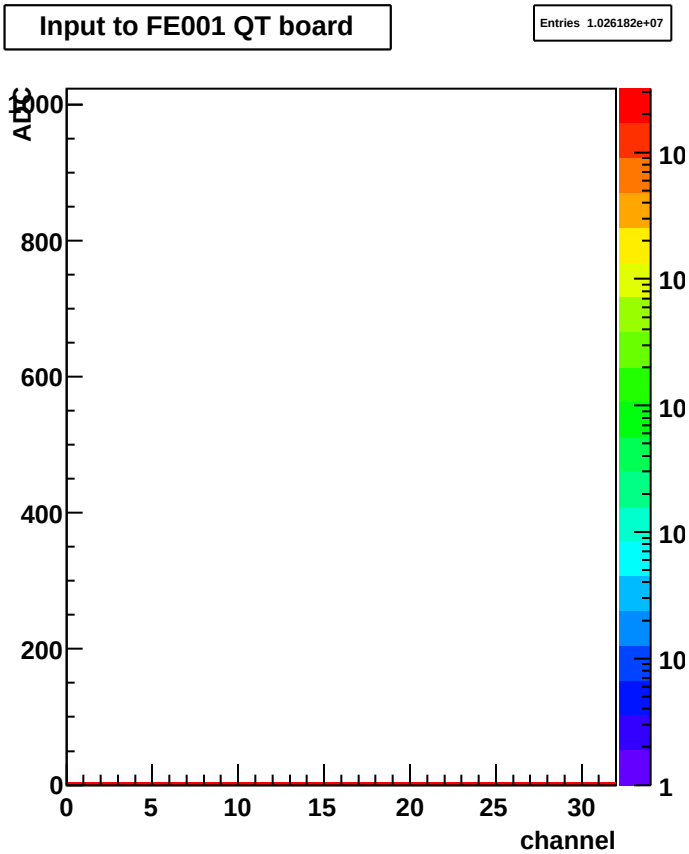

Input to FPD L2 DSM
Entries 962046
CL bits
320682
320682
320682
SMALL
LARGE-S
LARGE-N

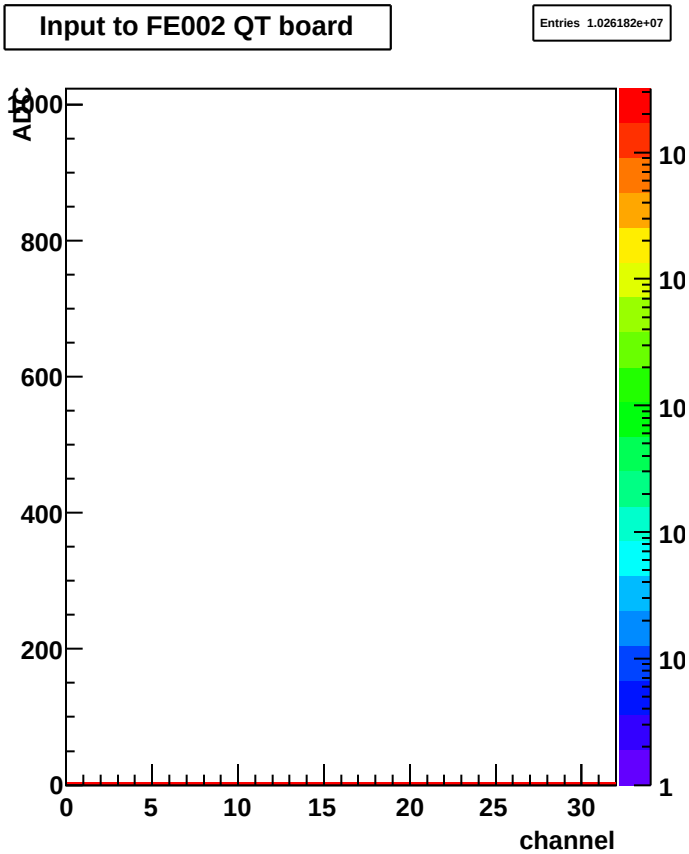

Input to FPD L2 DSM
Entries 962046
CL bits - simulated
320682
320682
320682
SMALL
LARGE-S
LARGE-N

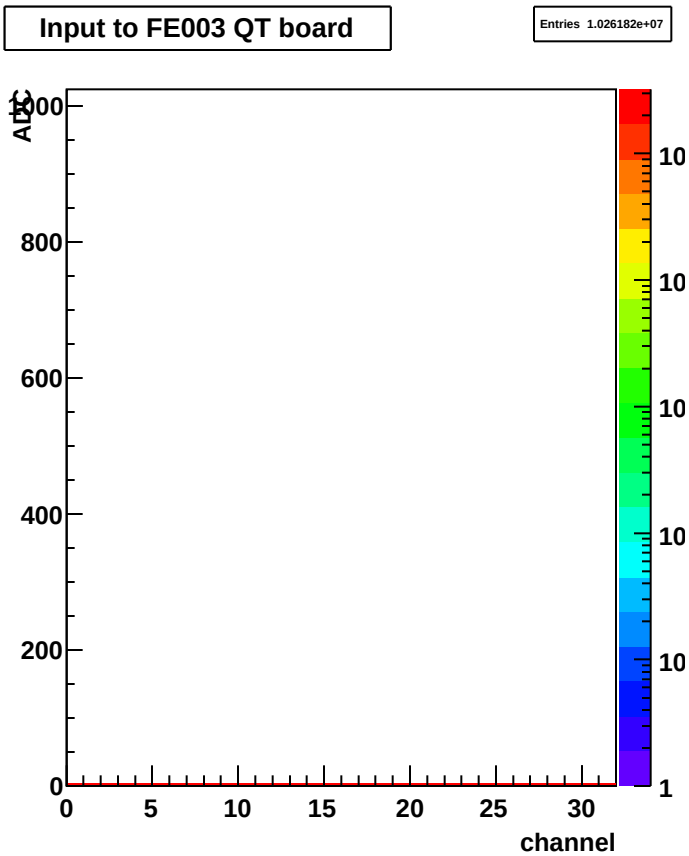

Input to FPD L2 DSM
Entries 962046
HT bits
319634
320682
320682
1048
SMALL
LARGE-S
LARGE-N

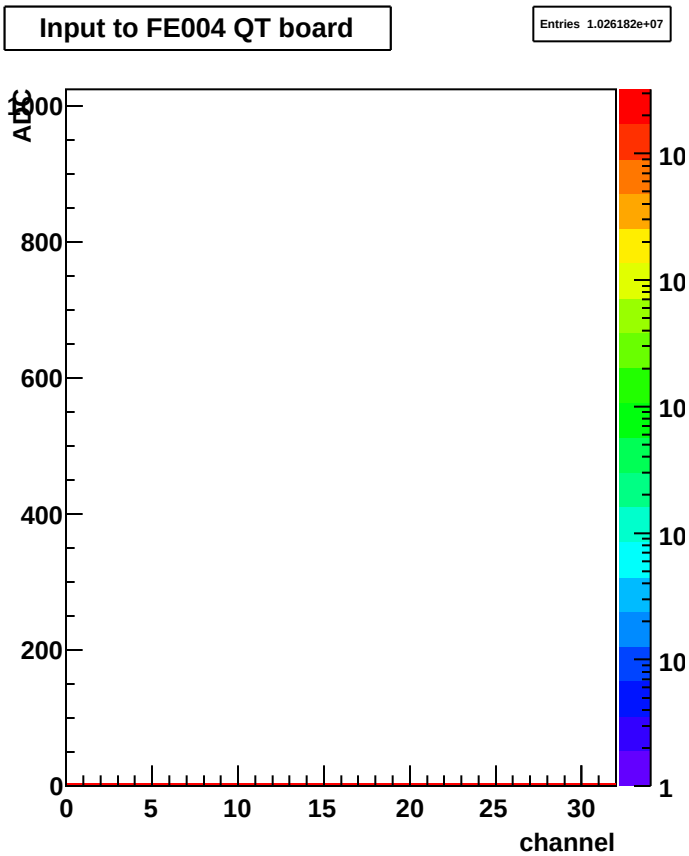

Input to FPD L2 DSM
Entries 962046
HT bits - simulated
319634
320682
320682
1048
SMALL
LARGE-S
LARGE-N


Input to FPD L2 DSM
Entries 1282728
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

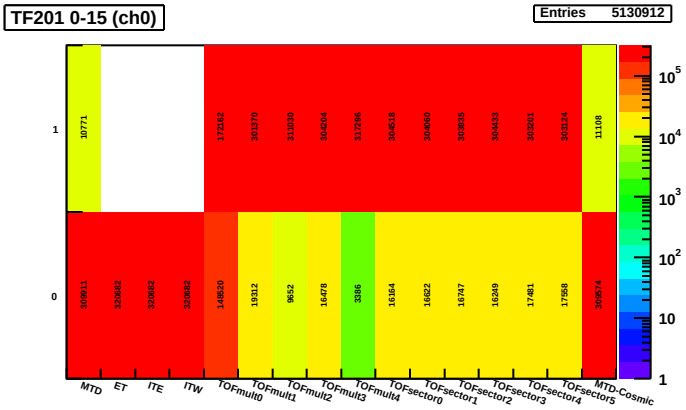

Input to FE001 QT board
Entries 1.026182e+07
ADC
channel


Input to FE002 QT board
Entries 1.026182e+07
ADC
channel

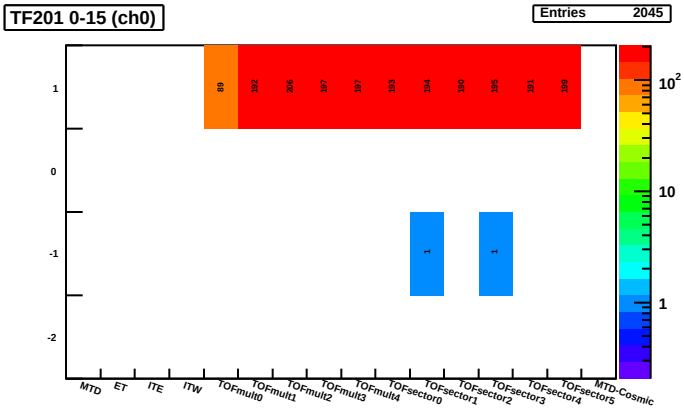

Input to FE003 QT board
Entries 1.026182e+07
ADC
channel


Input to FE004 QT board
Entries 1.026182e+07
ADC
channel

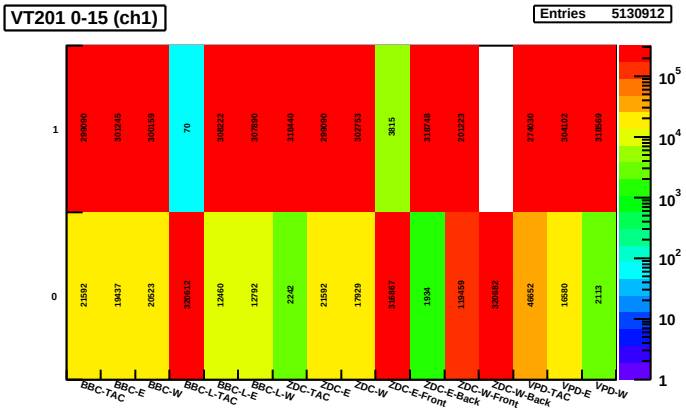
TF201 0-15 (ch0)



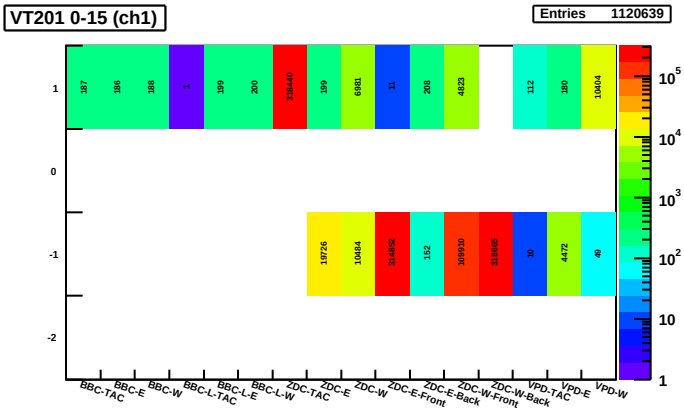
TF201 0-15 (ch0)



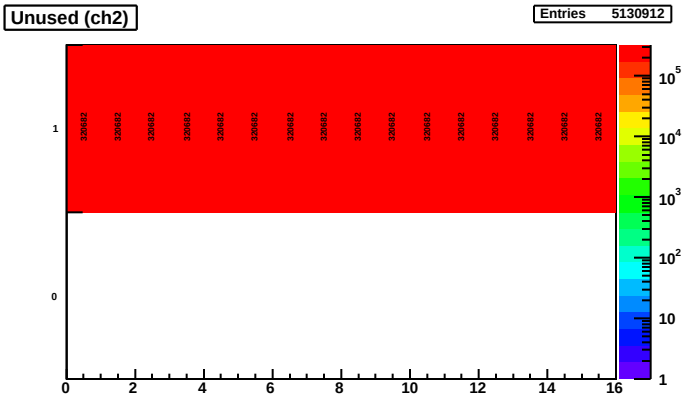
VT201 0-15 (ch1)

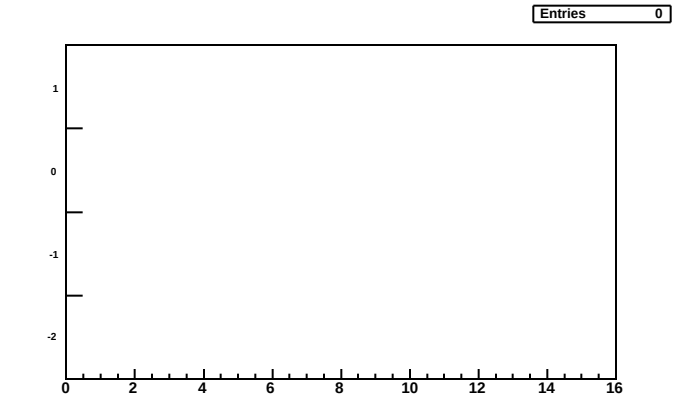


VT201 0-15 (ch1)

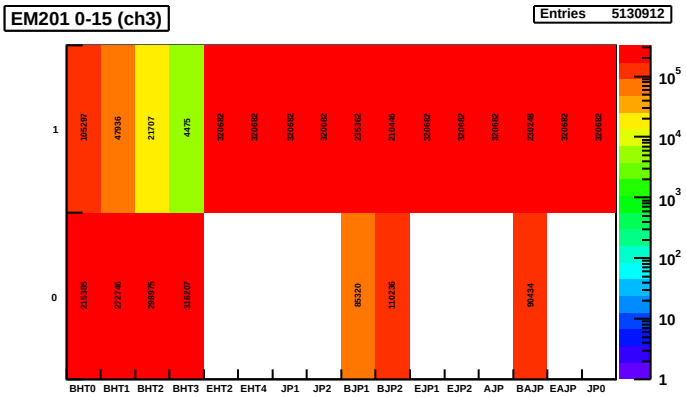


Unused (ch2)

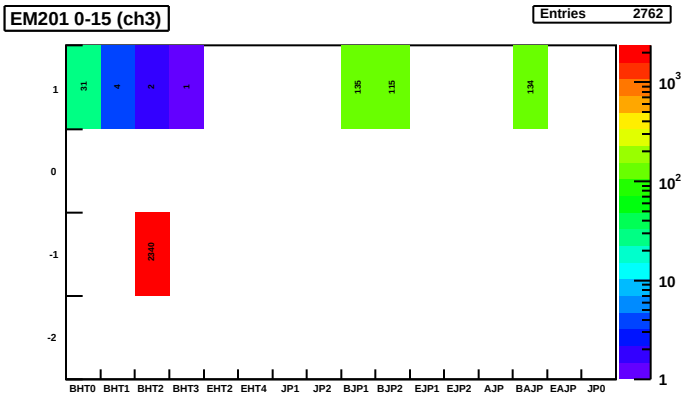




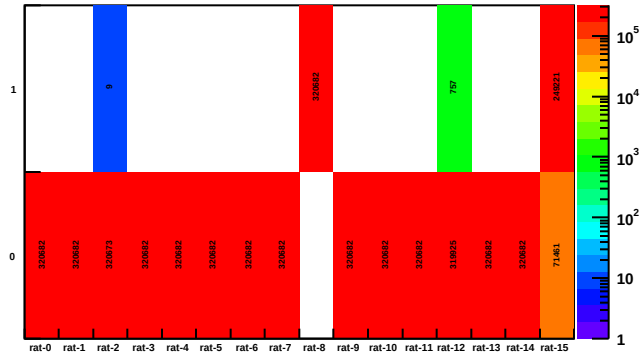
EM201 0-15 (ch3)

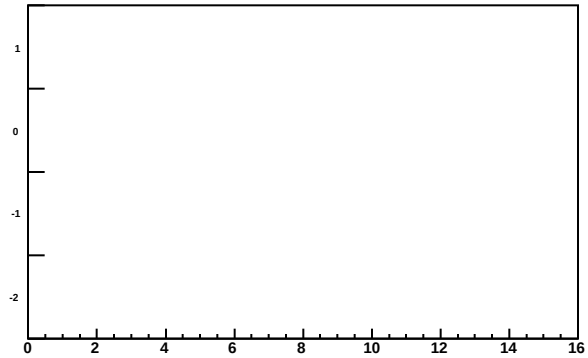


EM201 0-15 (ch3)

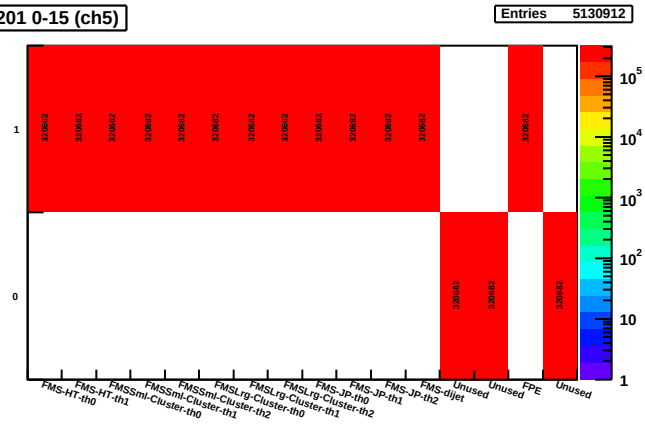


RAT board (ch4)

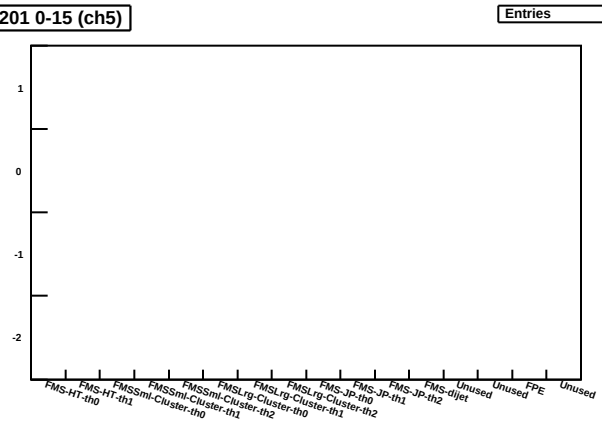




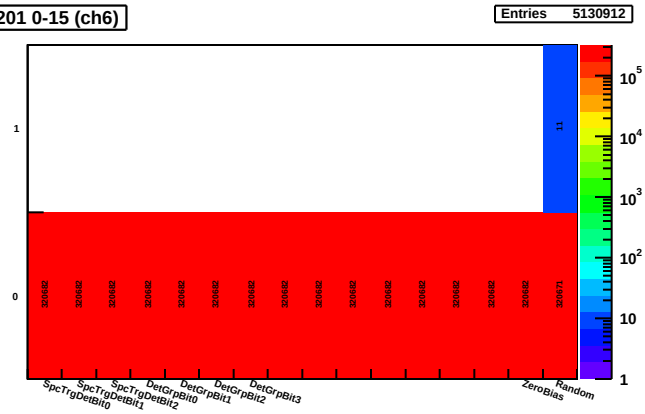
FP201 0-15 (ch5)

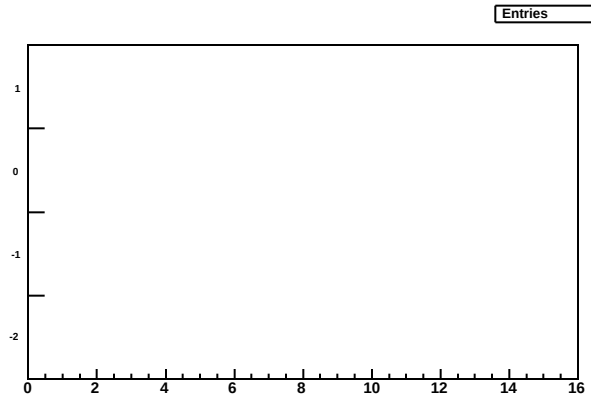
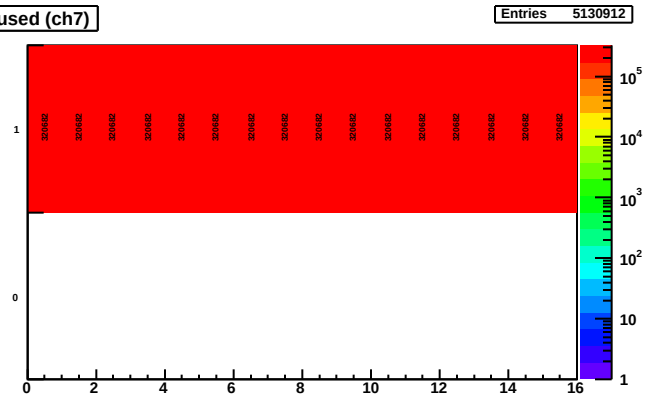
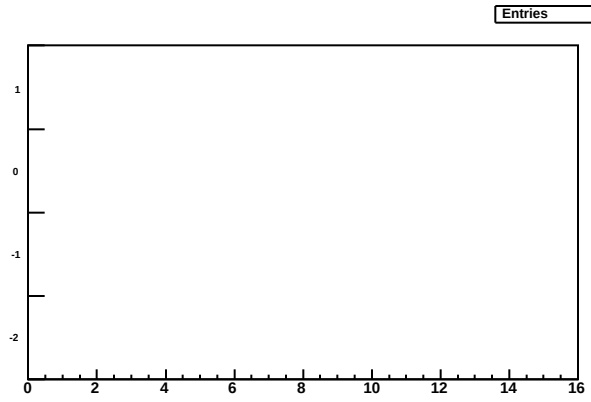


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)