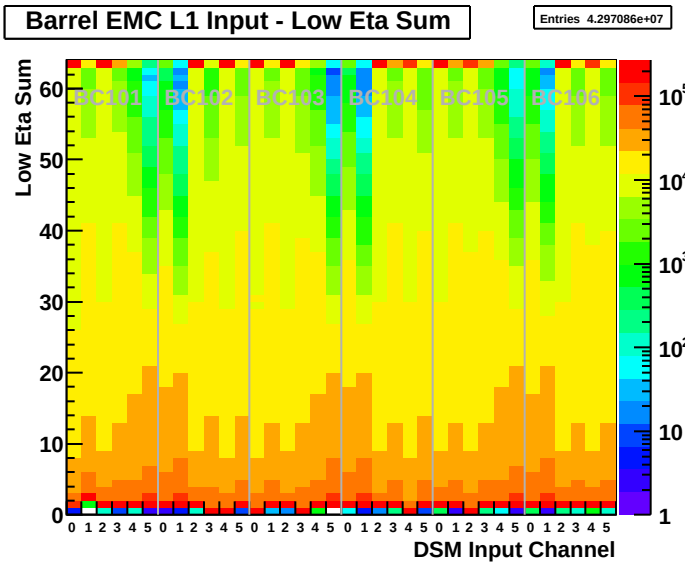
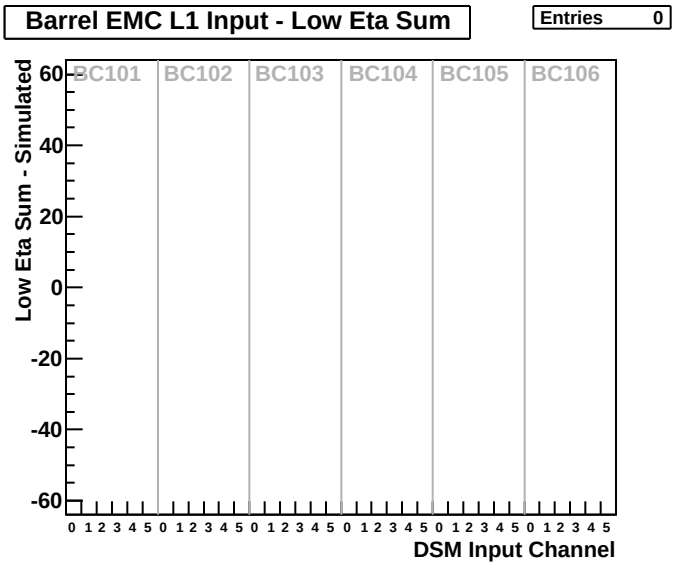
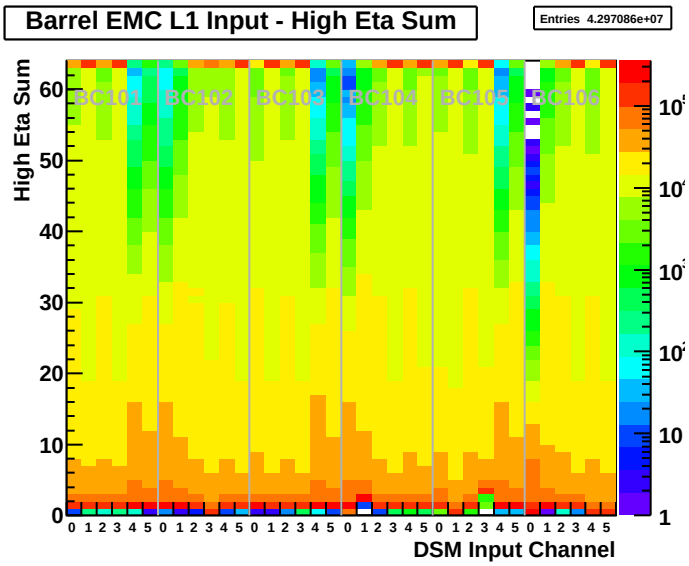
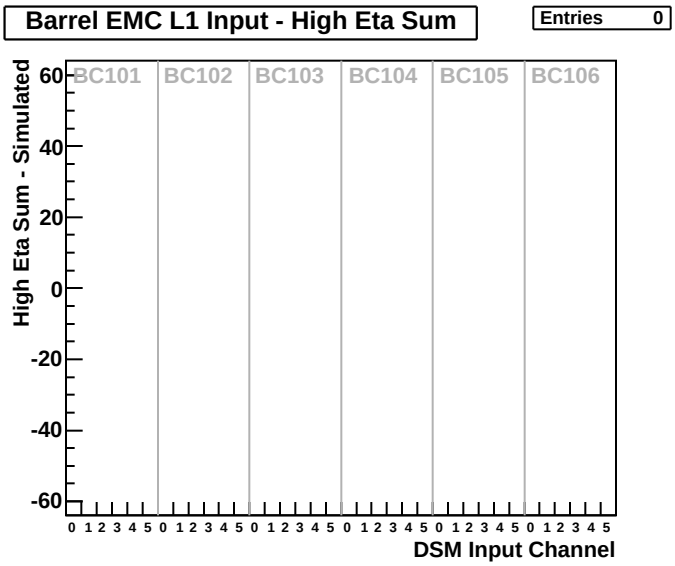
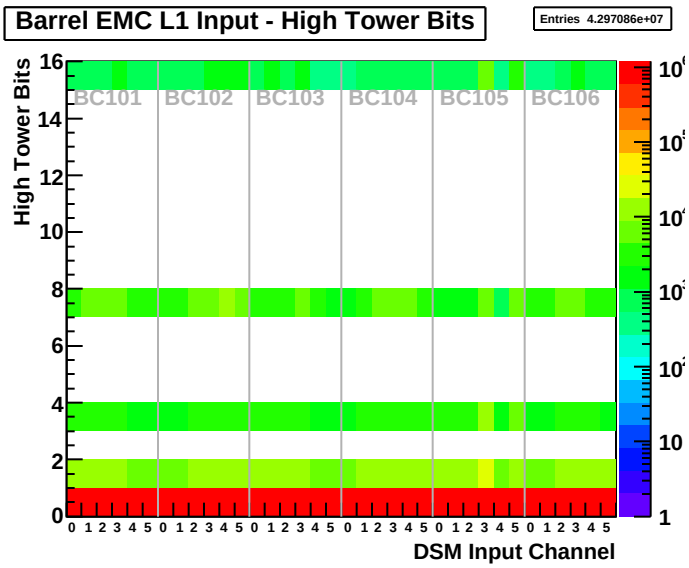
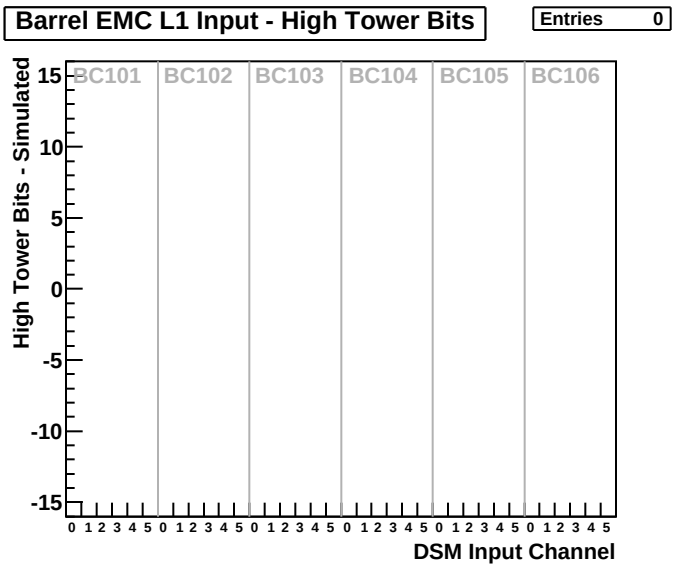

Barrel EMC L1 Input - Low Eta Sum
Entries 4.297086e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

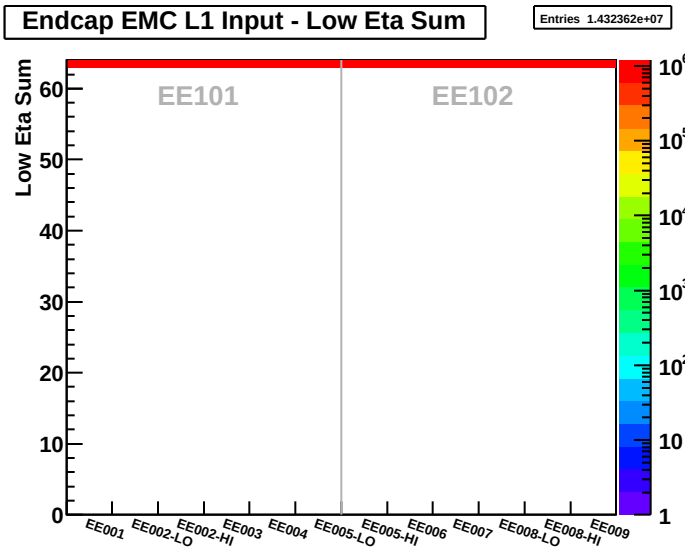

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

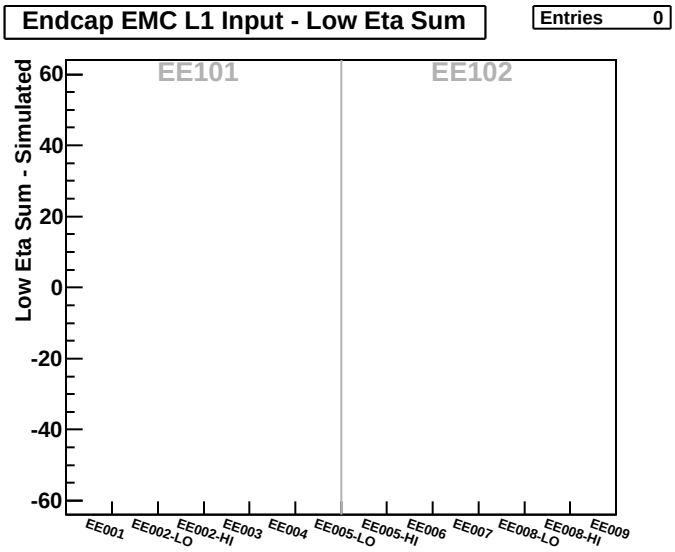

Barrel EMC L1 Input - High Eta Sum
Entries 4.297086e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

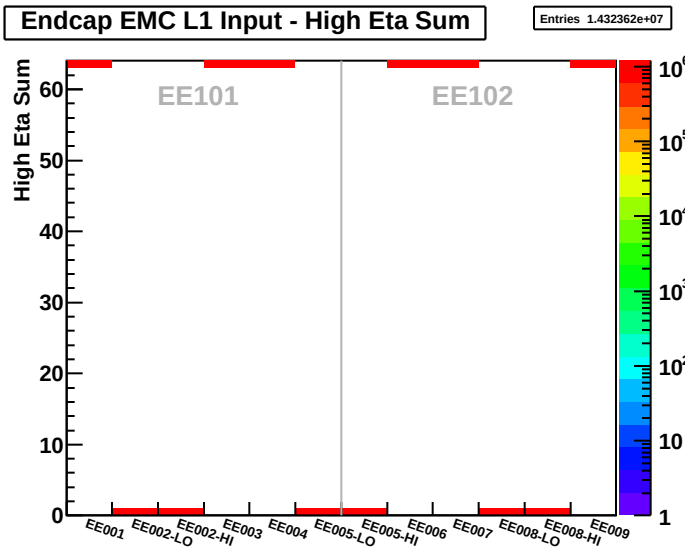

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

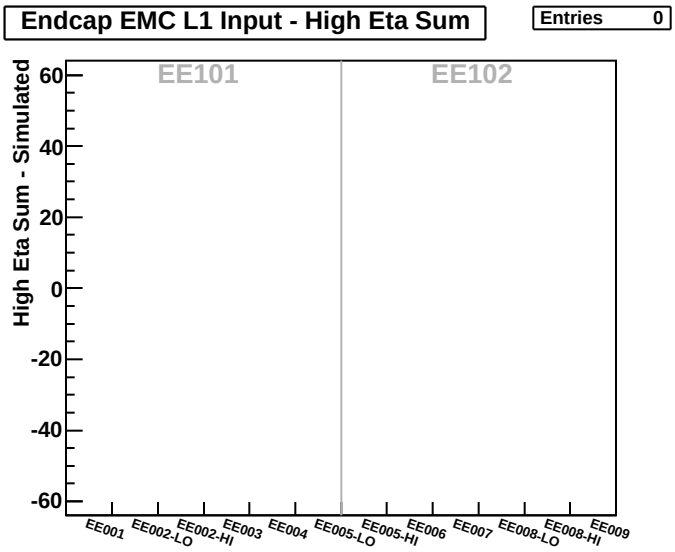

Barrel EMC L1 Input - High Tower Bits
Entries 4.297086e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

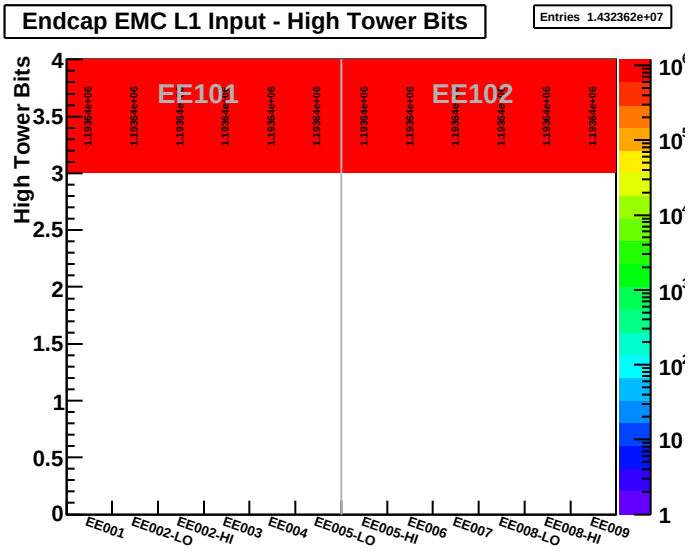

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

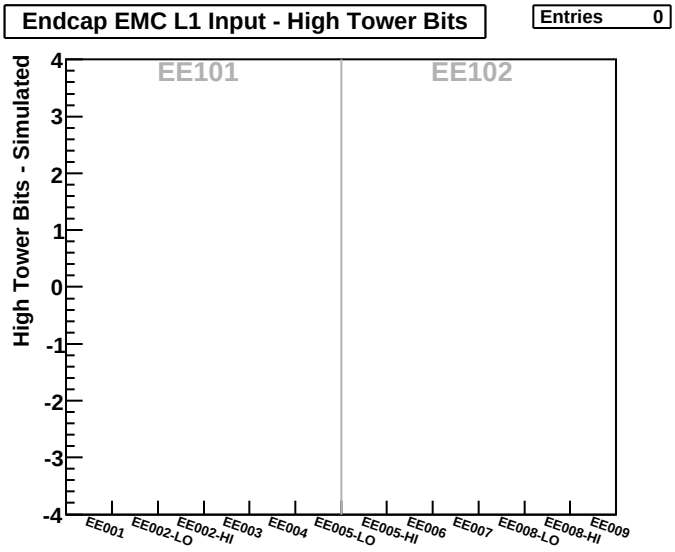

Endcap EMC L1 Input - Low Eta Sum
Entries 1.432362e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

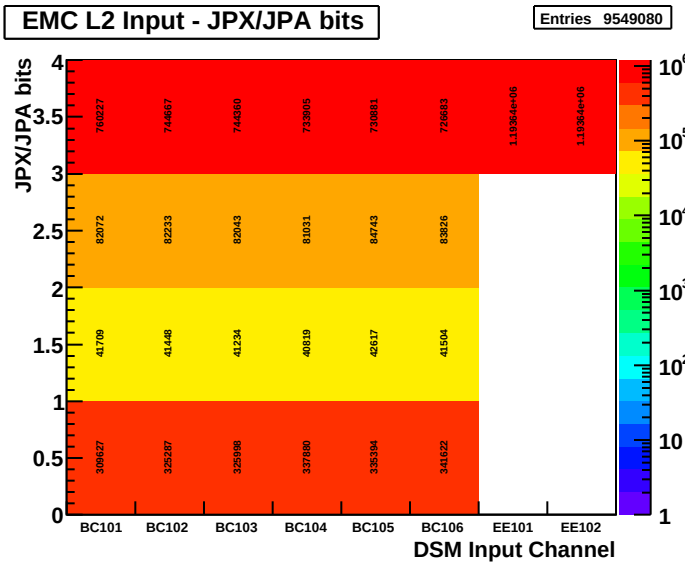

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

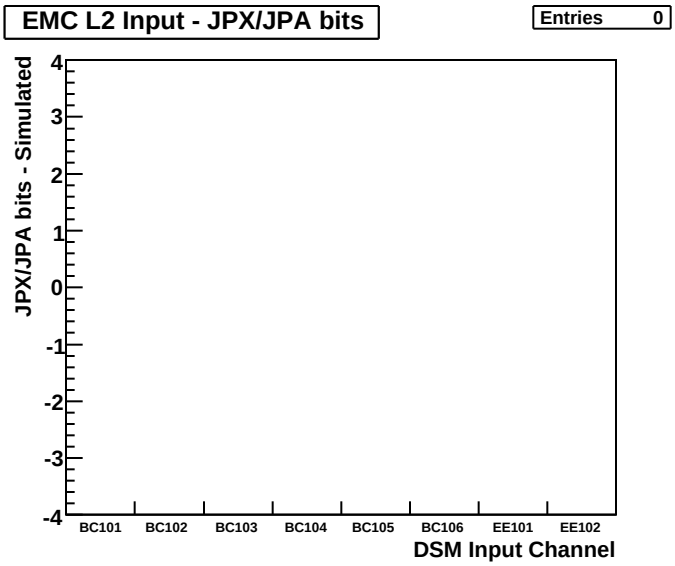

Endcap EMC L1 Input - High Eta Sum
Entries 1.432362e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

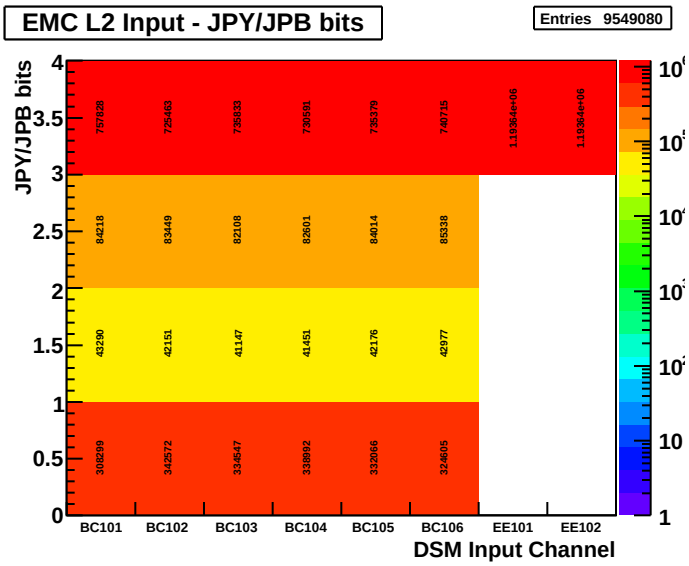

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

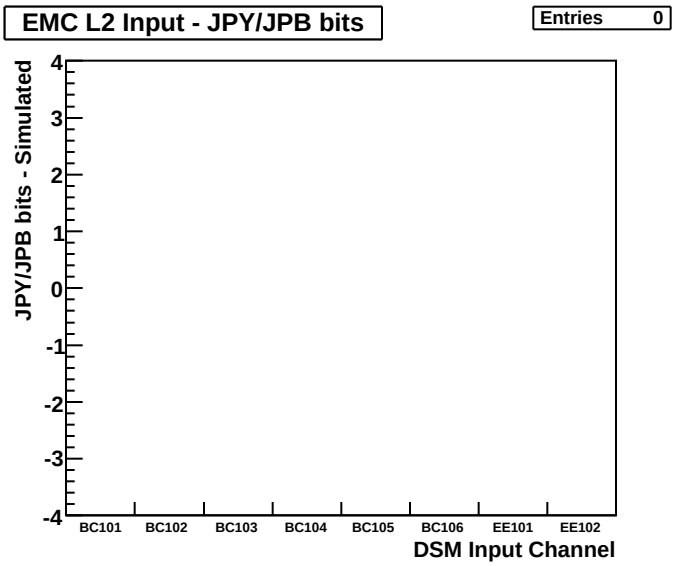

Endcap EMC L1 Input - High Tower Bits
Entries 1.432362e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

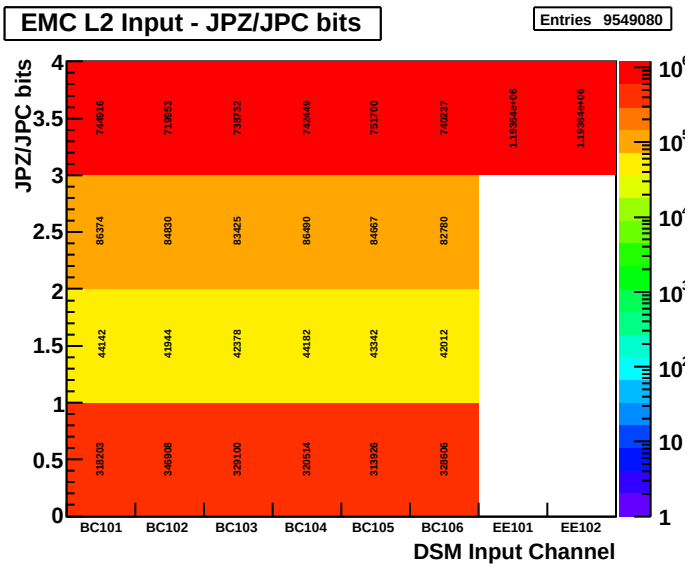

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

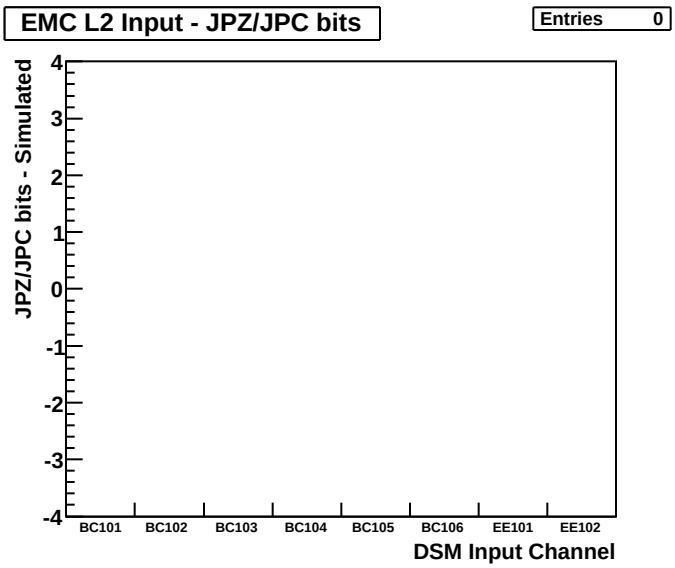

EMC L2 Input - JPX/JPA bits
Entries 9549080
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
760627 744667 744360 733805 730881 726683 1.19364e+06 1.19364e+06
82072 82233 82043 81031 84743 83826
41709 41448 41234 40819 42617 41504
308627 325287 325938 337880 335394 341622

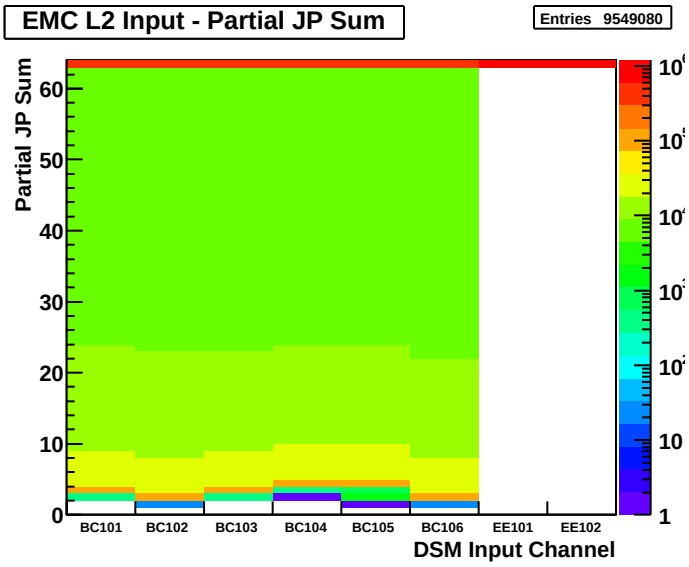

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

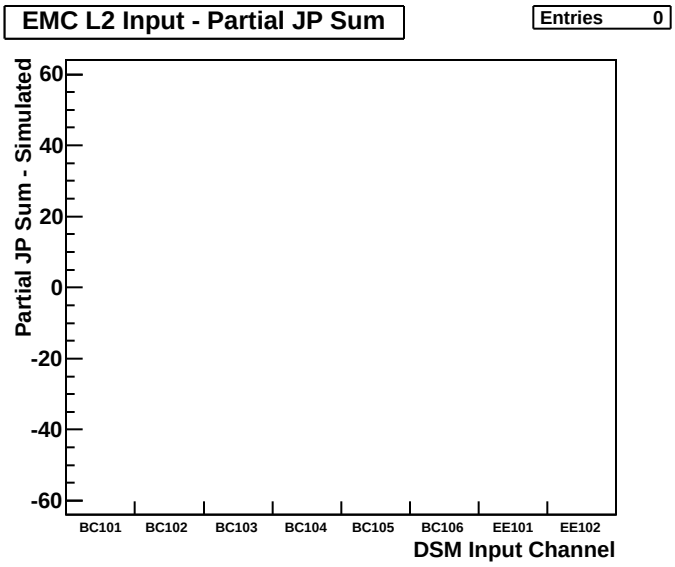

EMC L2 Input - JPY/JPB bits
Entries 9549080
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
757828 725463 735833 730591 735379 740715 1.19364e+06 1.19364e+06
84218 83449 82108 82691 84014 85338
43290 42151 41147 41451 42176 42977
306299 342572 334547 338992 332066 324605

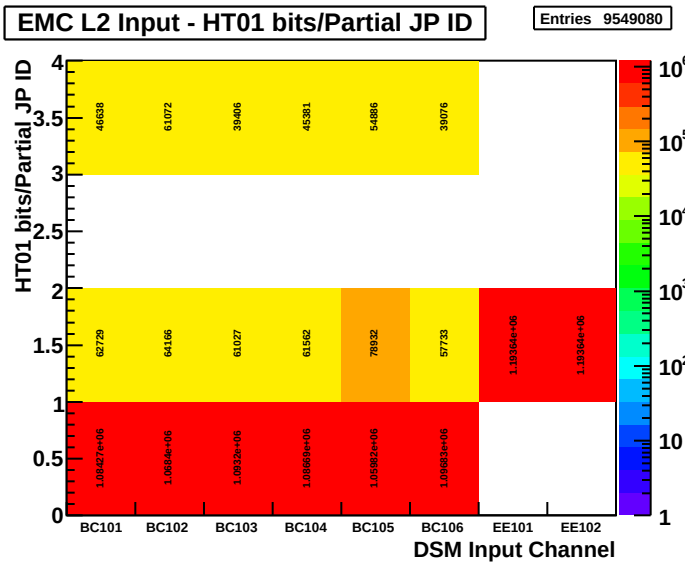

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

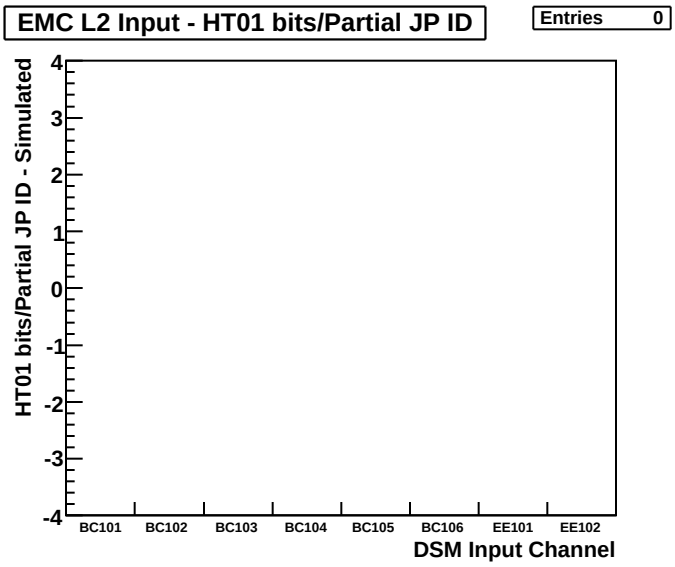

EMC L2 Input - JPZ/JPC bits
Entries 9549080
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
744916 719953 738732 742449 751700 740237 1.19364e+06 1.19364e+06
86374 84830 83425 86490 84667 82780
44142 41944 42378 44182 43342 42012
318203 346908 329100 320514 313926 328606

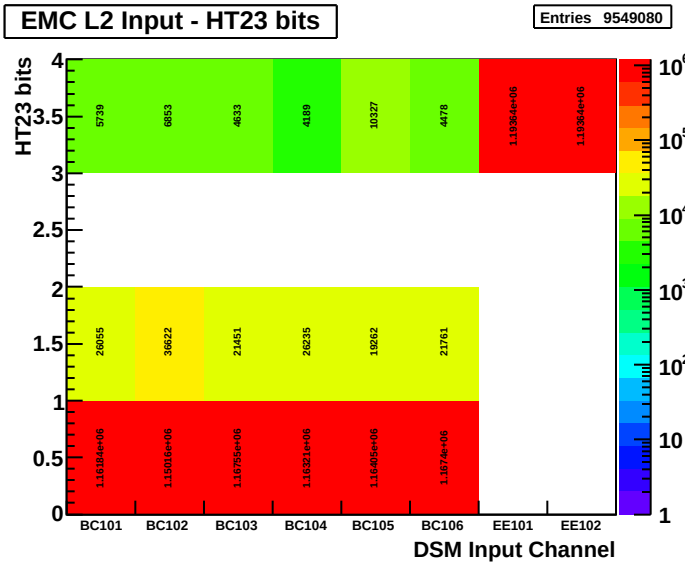

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

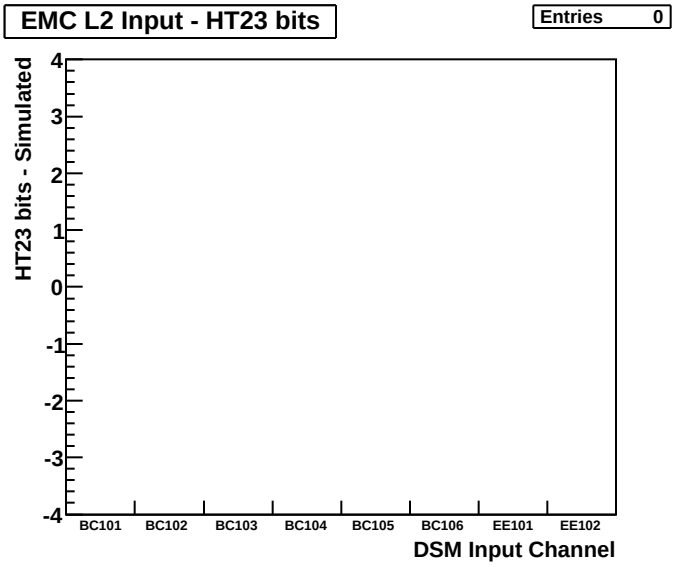

EMC L2 Input - Partial JP Sum
Entries 9549080
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

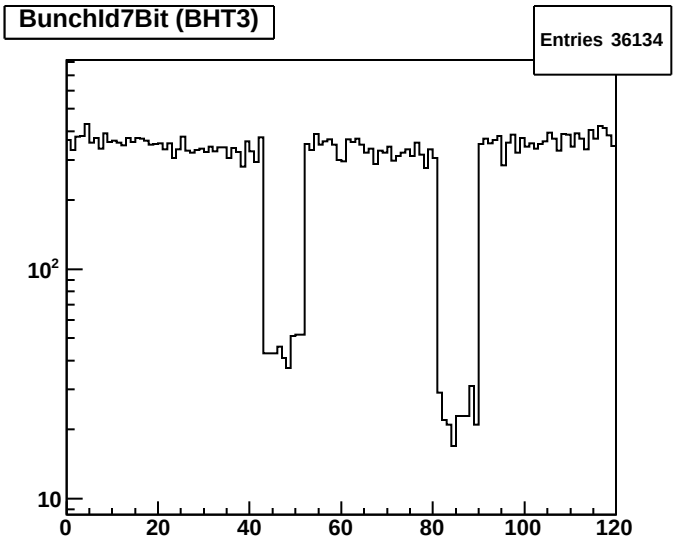

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

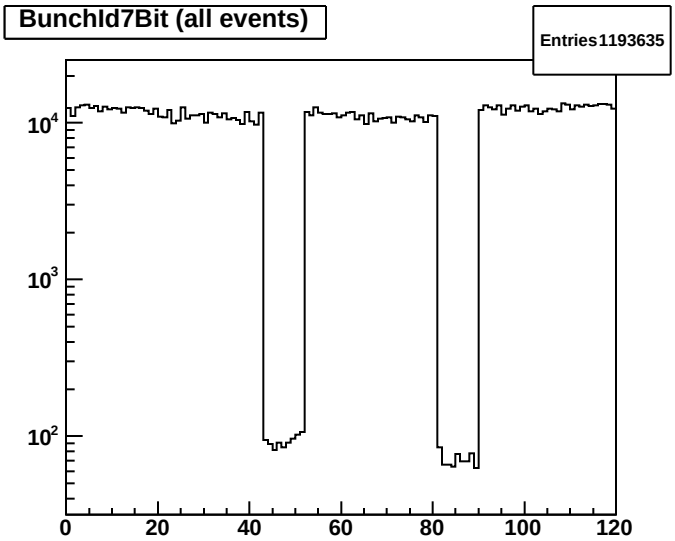

EMC L2 Input - HT01 bits/Partial JP ID
Entries 9549080
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

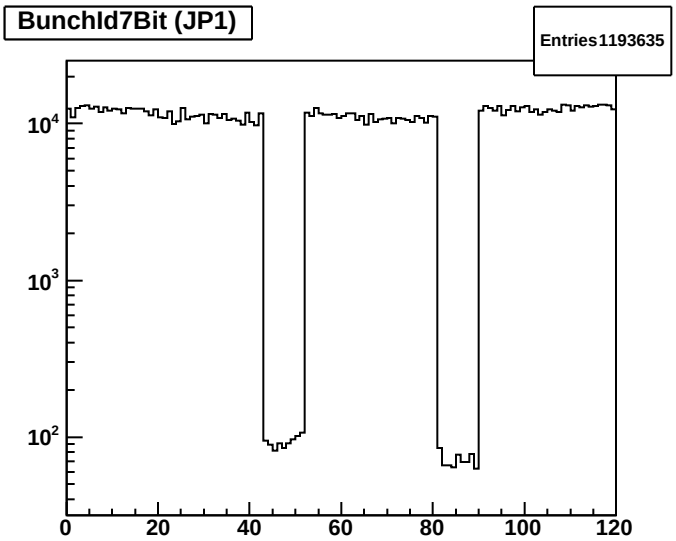

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

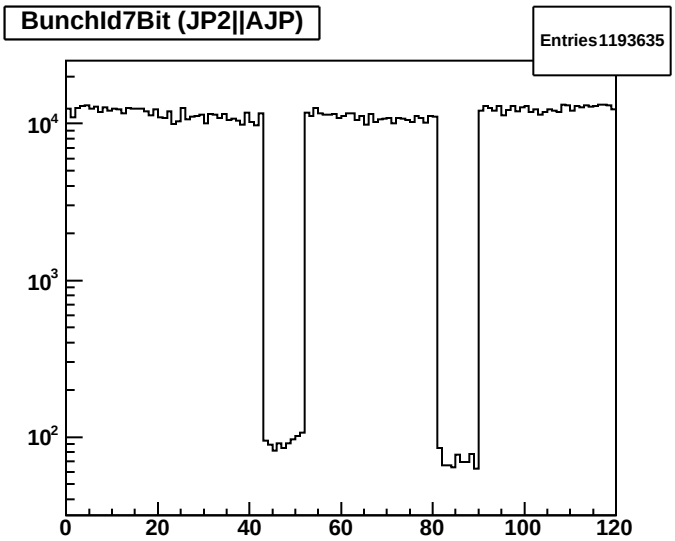

EMC L2 Input - HT23 bits
Entries 9549080
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

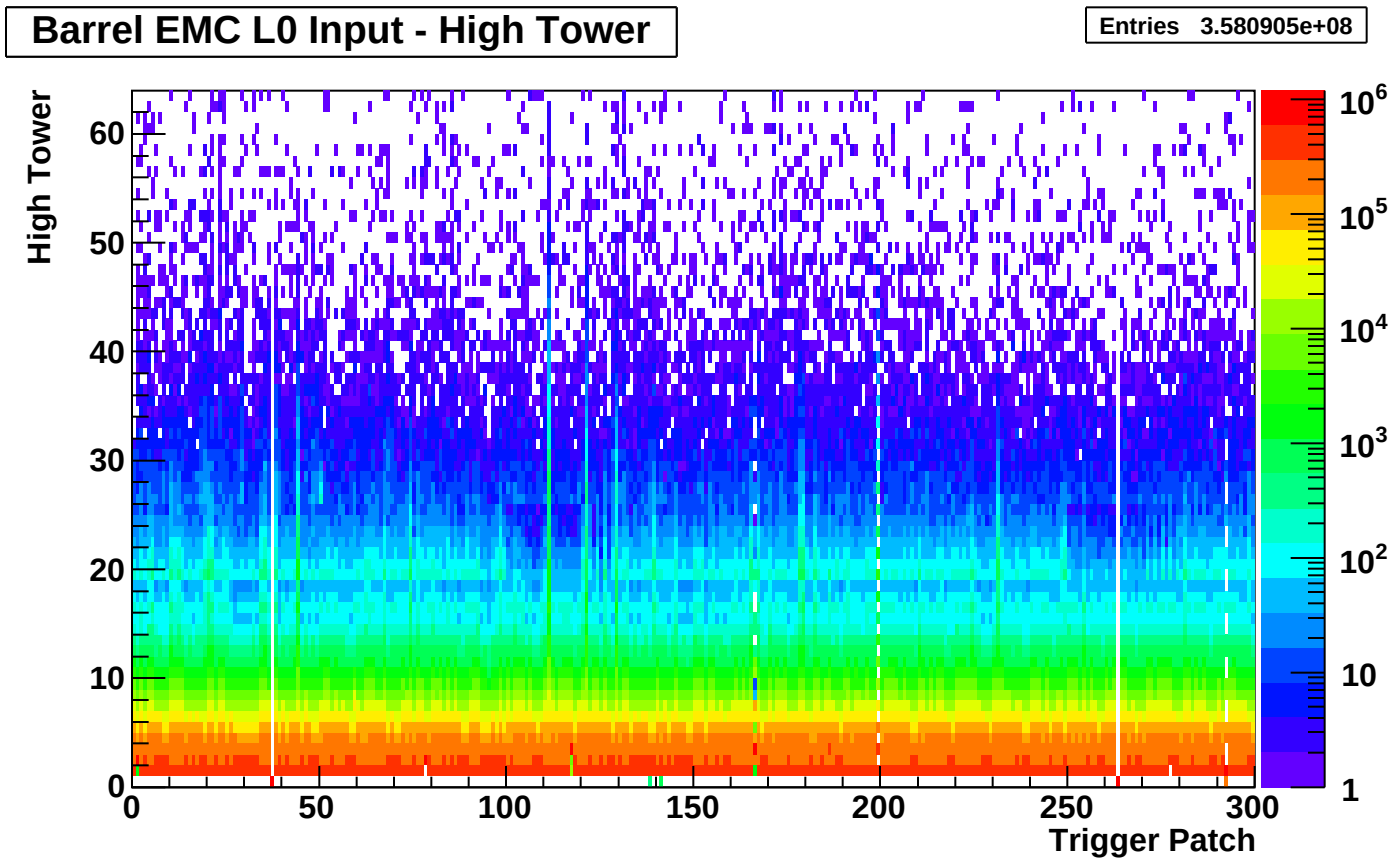

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

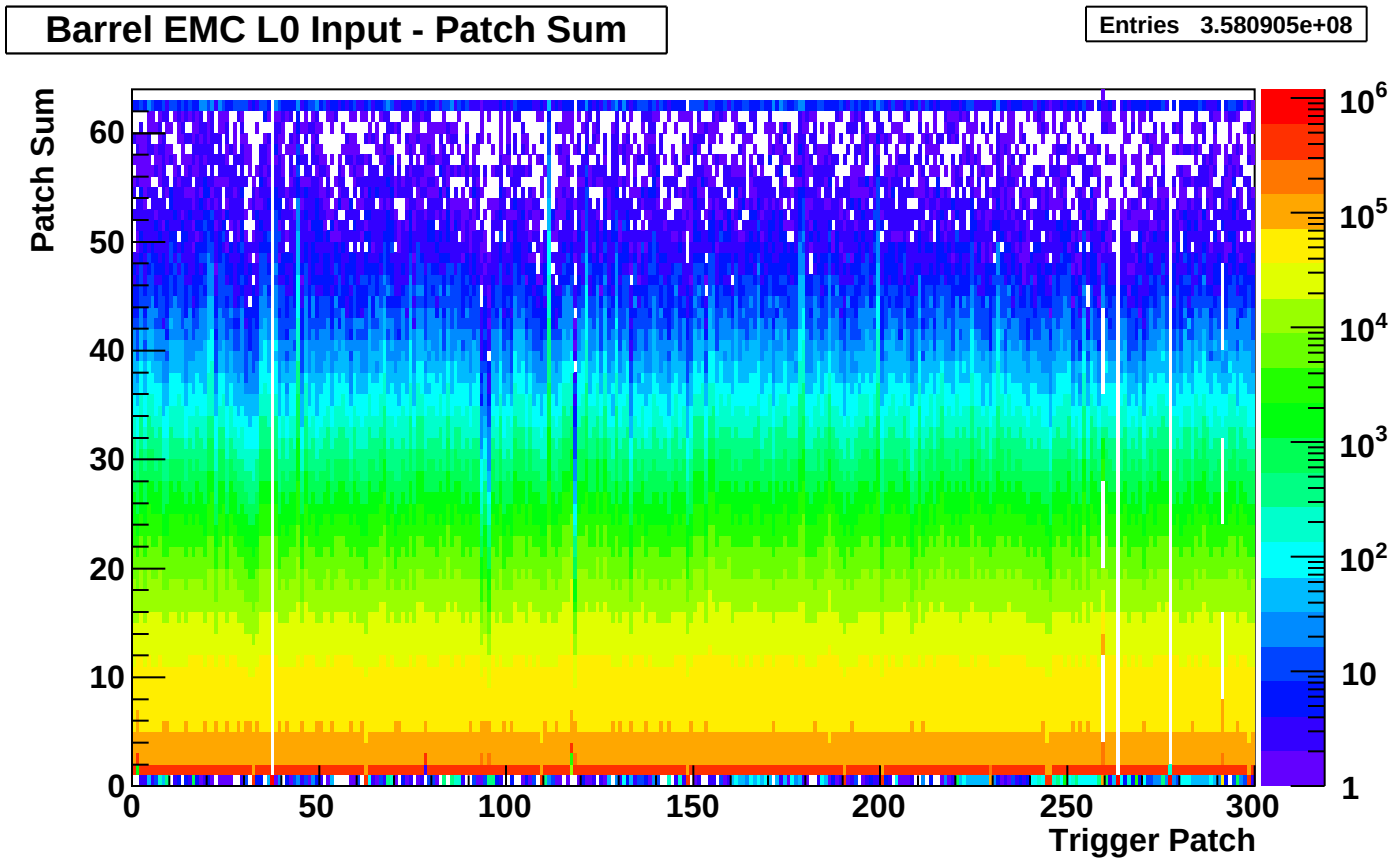

BunchId7Bit (BHT3)
Entries 36134
10
10^2
0
20
40
60
80
100
120

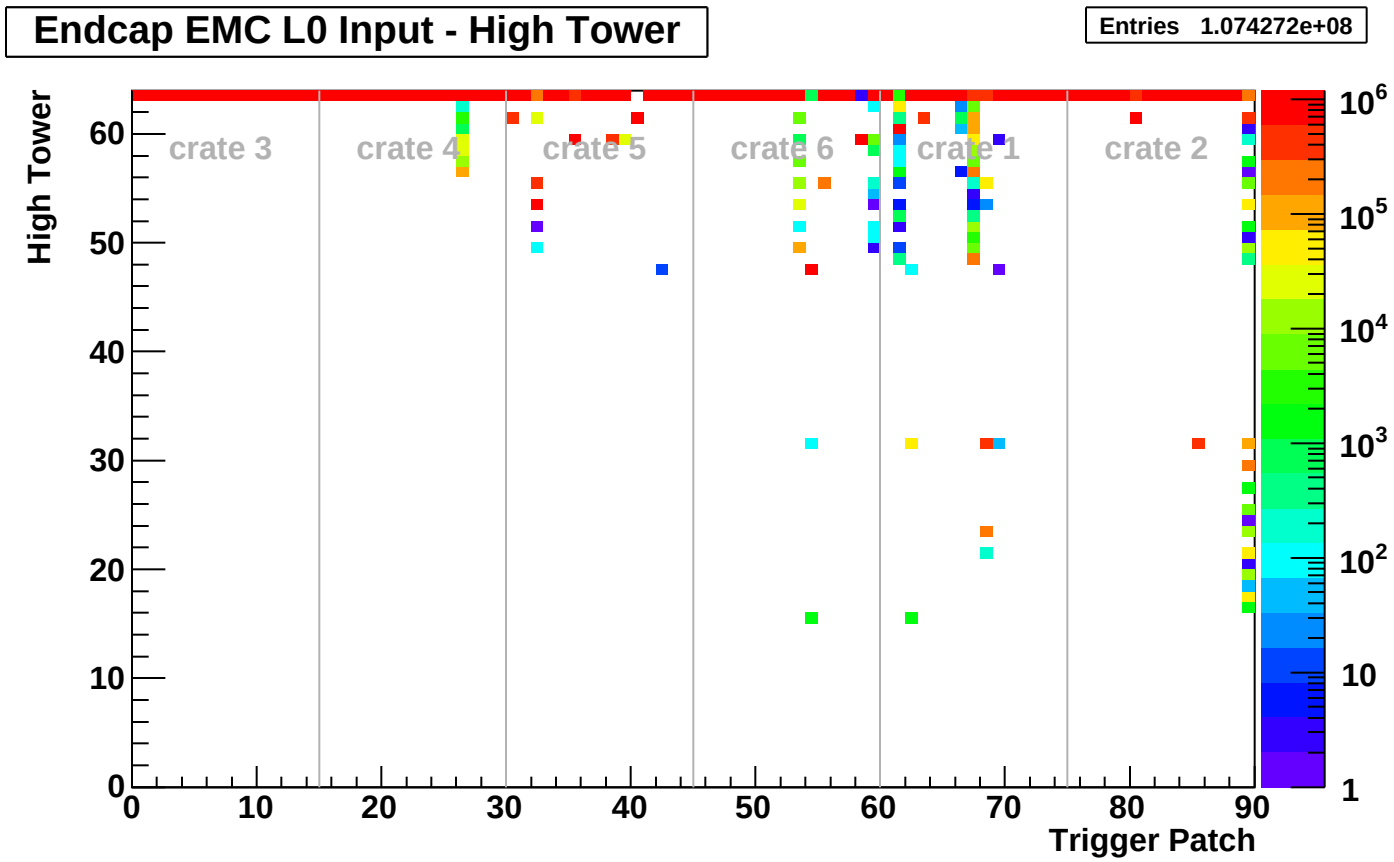

BunchId7Bit (all events)
Entries 1193635
10^2
10^3
10^4
0
20
40
60
80
100
120

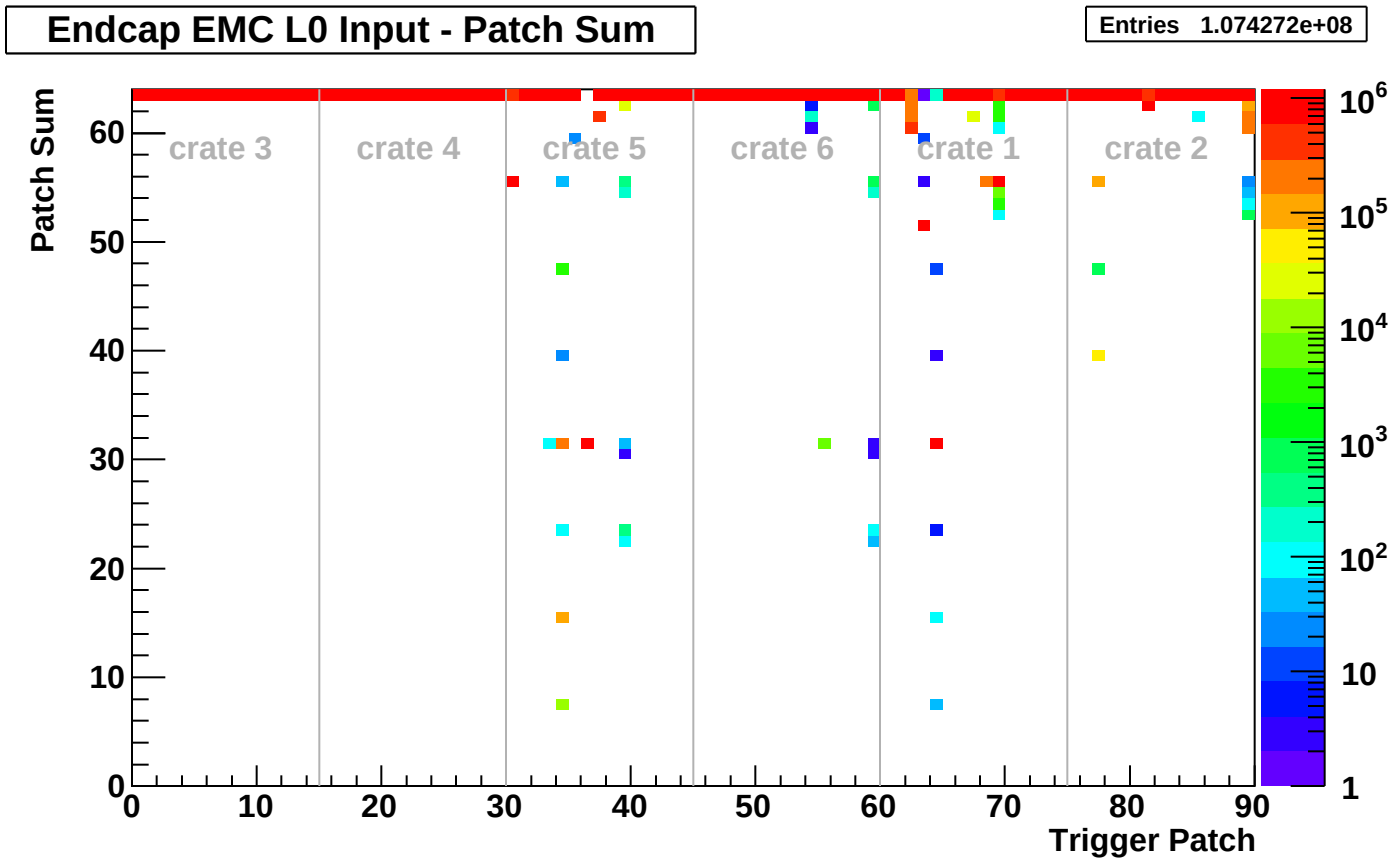

BunchId7Bit (JP1)
Entries 1193635
10^2
10^3
10^4
0
20
40
60
80
100
120

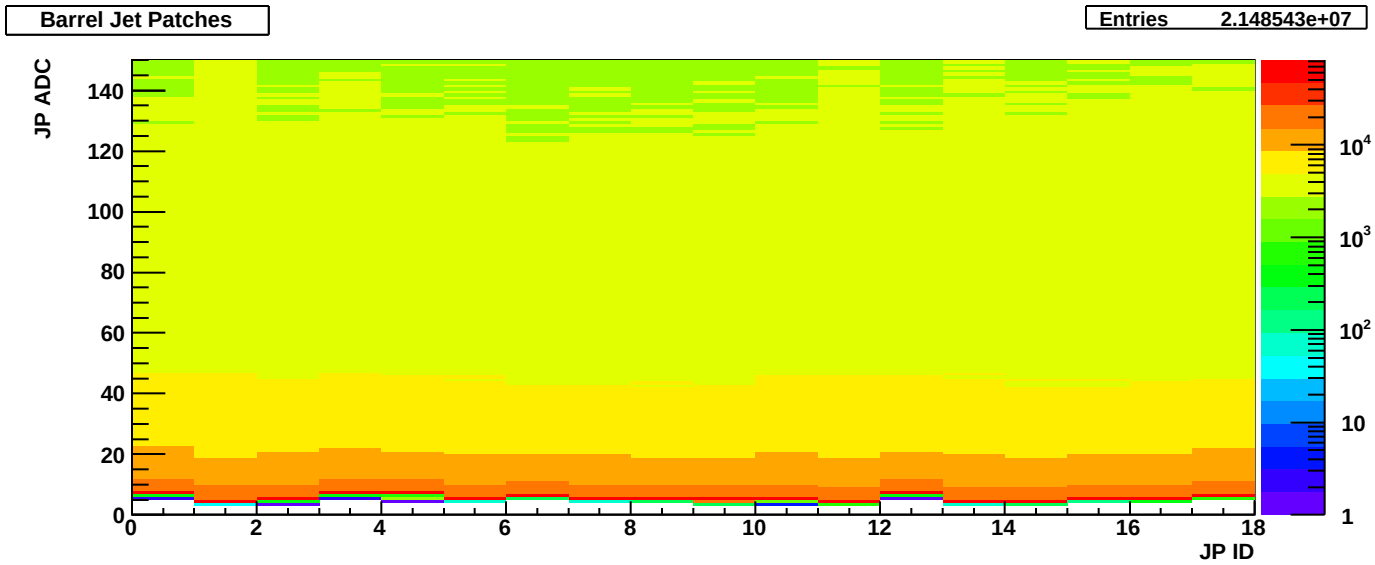

BunchId7Bit (JP2||AJP)
Entries 1193635
10^2
10^3
10^4
0
20
40
60
80
100
120

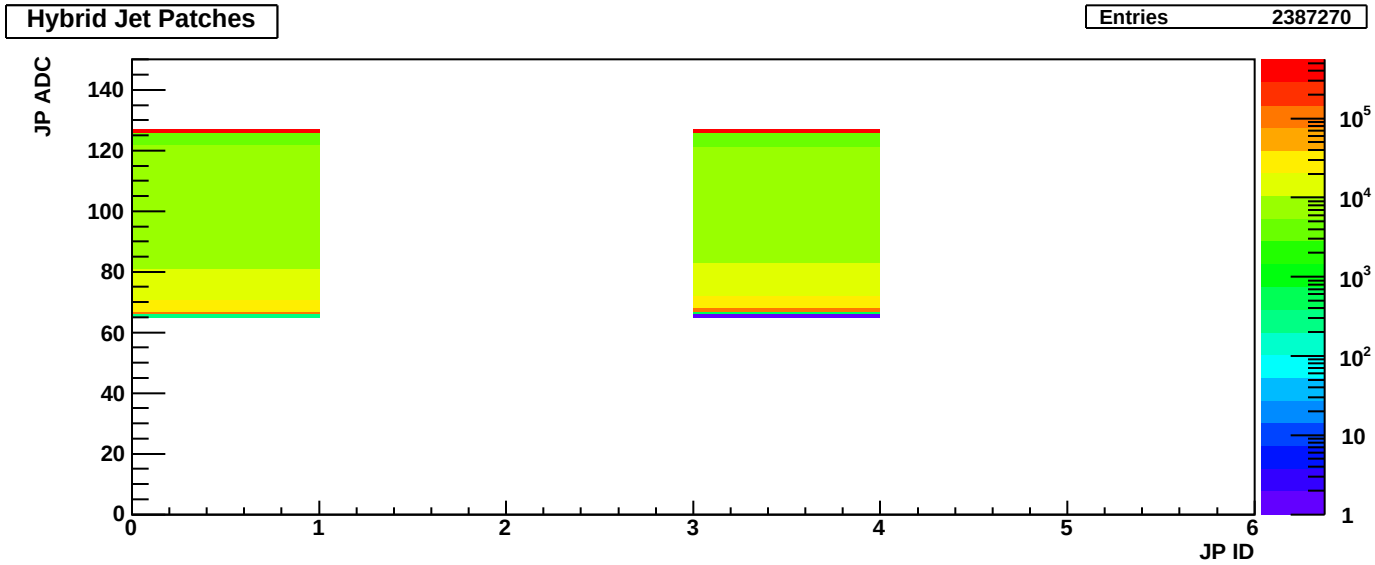

Barrel EMC L0 Input - High Tower
Entries 3.580905e+08
High Tower
Trigger Patch

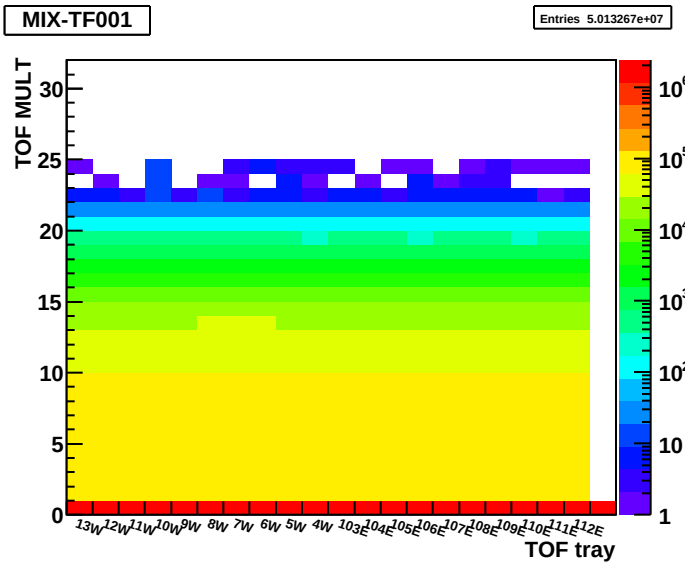

Barrel EMC L0 Input - Patch Sum
Entries 3.580905e+08
Patch Sum
Trigger Patch

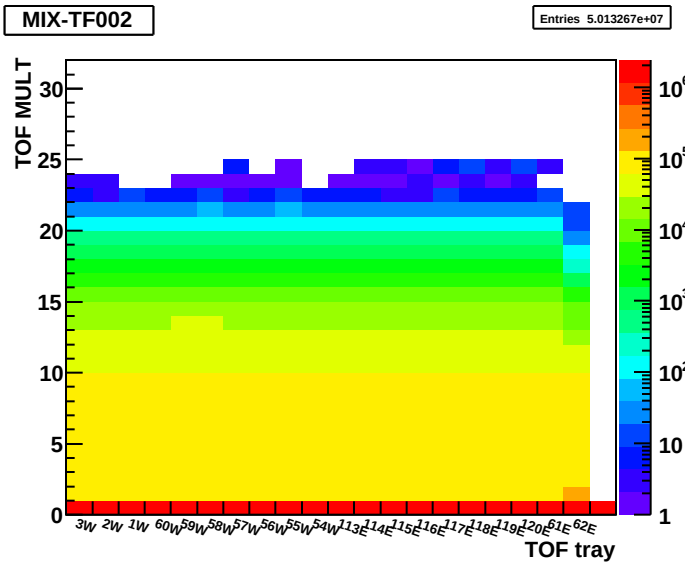

Endcap EMC L0 Input - High Tower
Entries 1.074272e+08
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

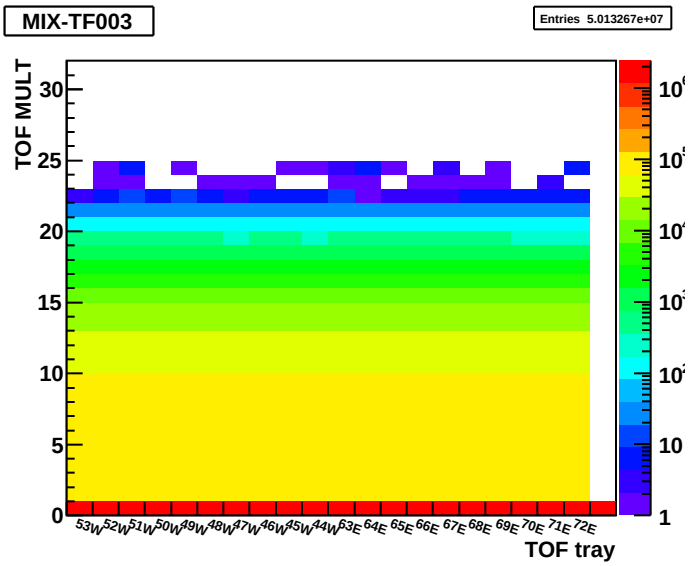

Endcap EMC L0 Input - Patch Sum
Entries 1.074272e+08
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

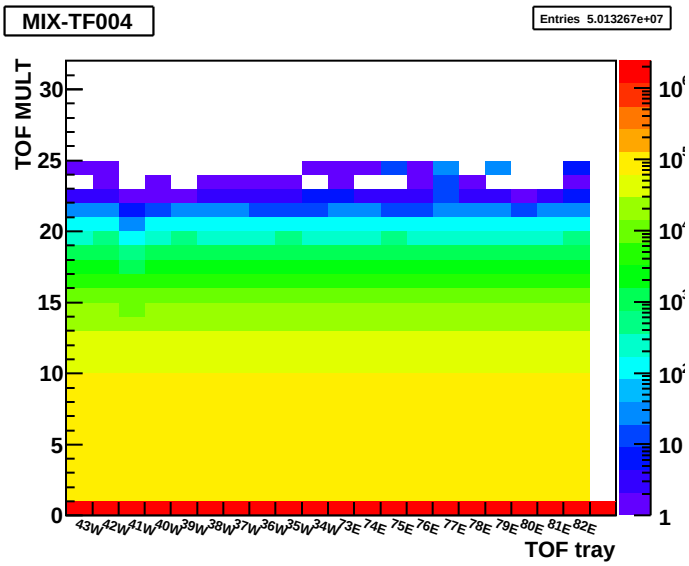

Barrel Jet Patches
Entries 2.148543e+07
JP ADC
JP ID

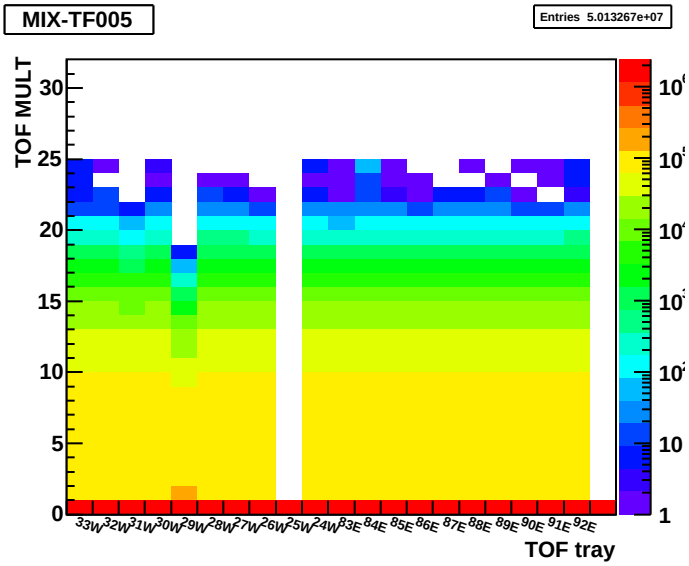

Hybrid Jet Patches
Entries 2387270
JP ADC
JP ID

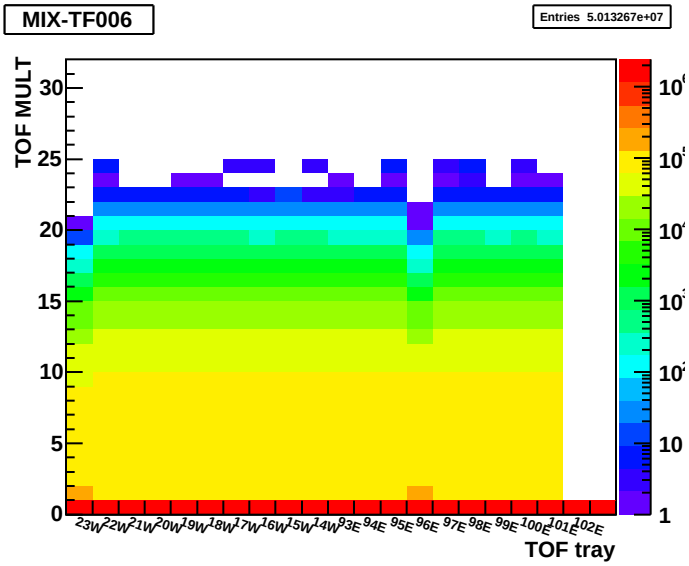

MIX-TF001
Entries 5.013267e+07
TOF MULT
TOF tray

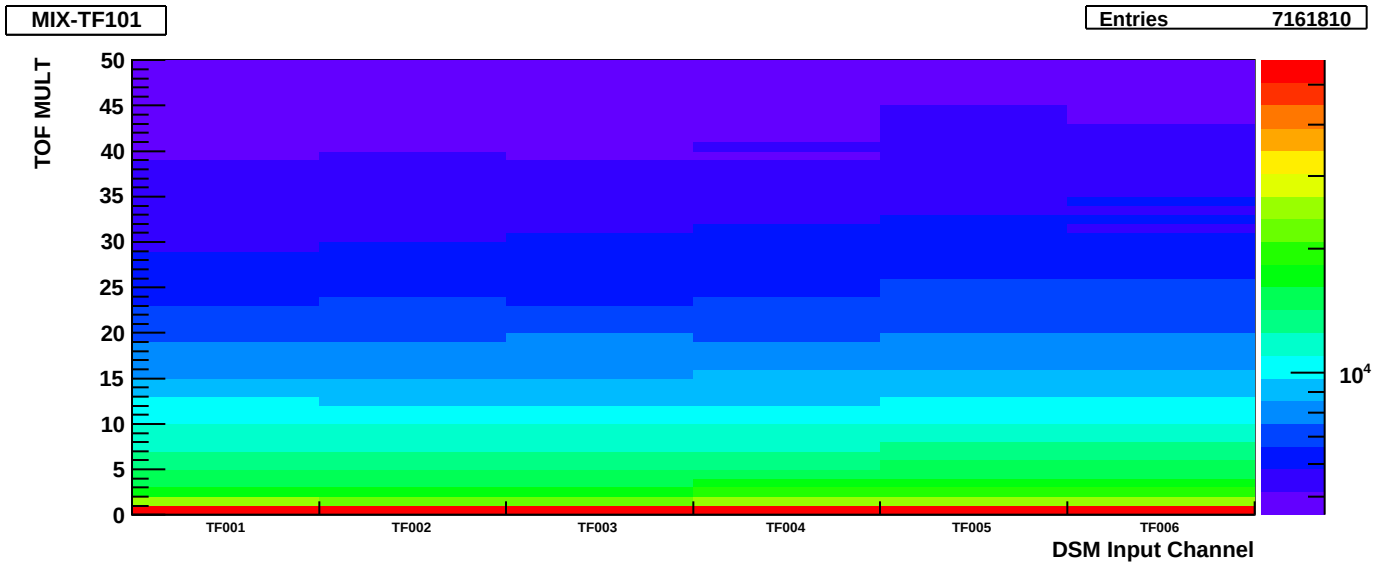

MIX-TF002
Entries 5.013267e+07
TOF MULT
TOF tray

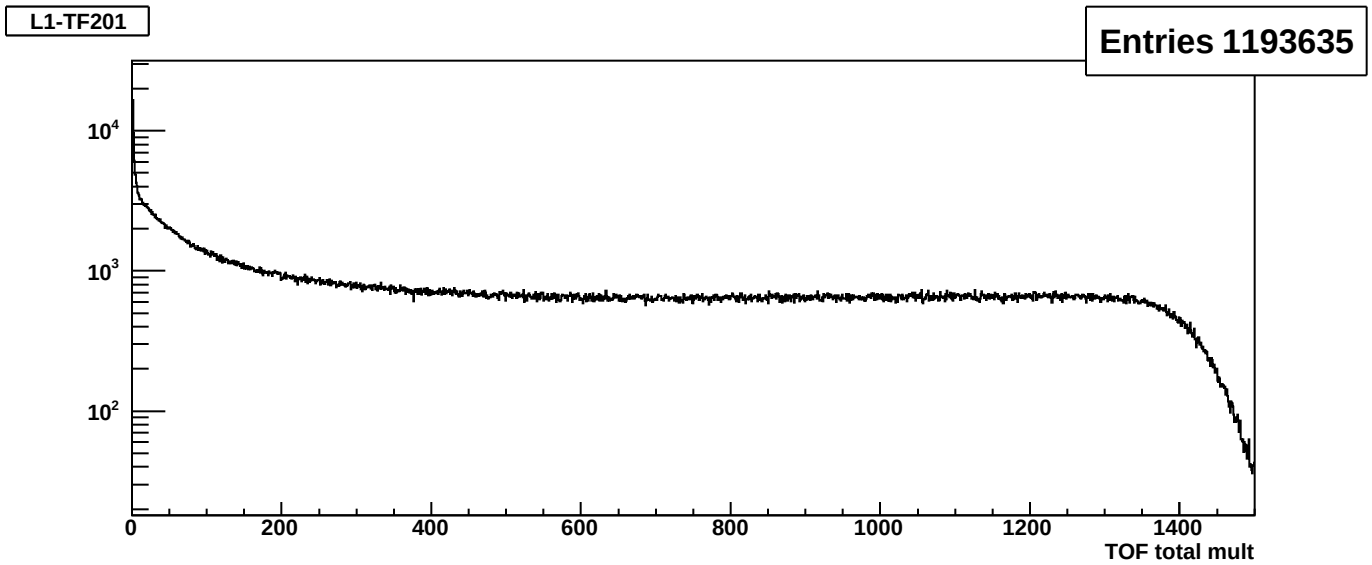

MIX-TF003
Entries 5.013267e+07
TOF MULT
TOF tray

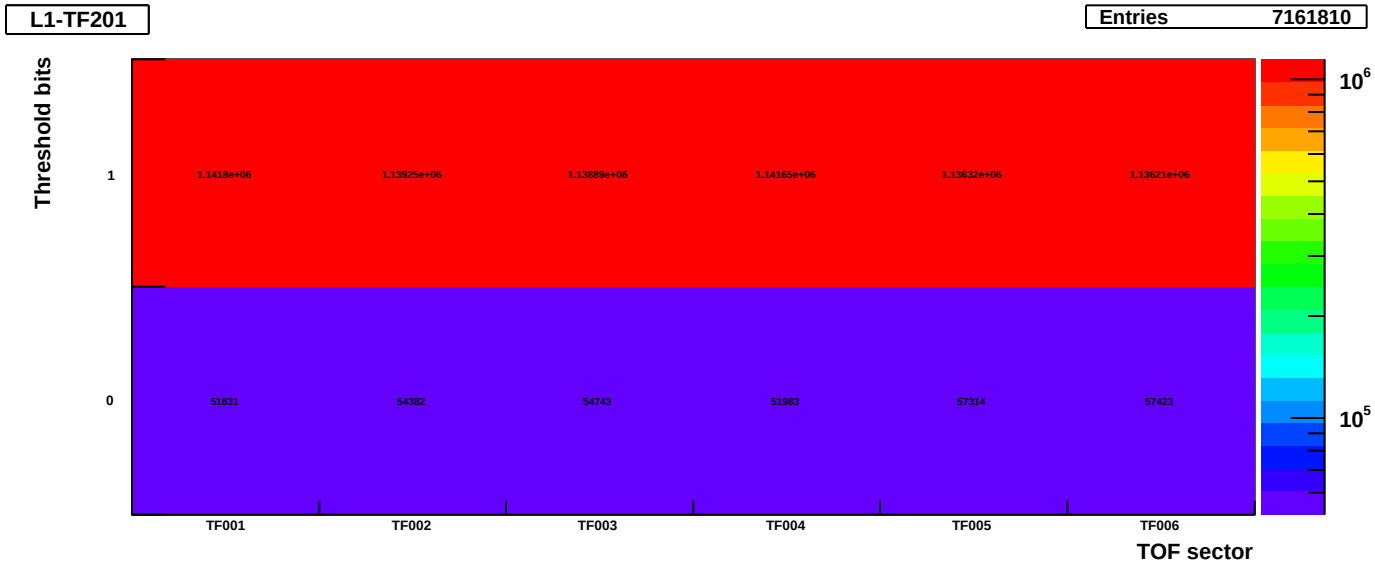

MIX-TF004
Entries 5.013267e+07
TOF MULT
TOF tray

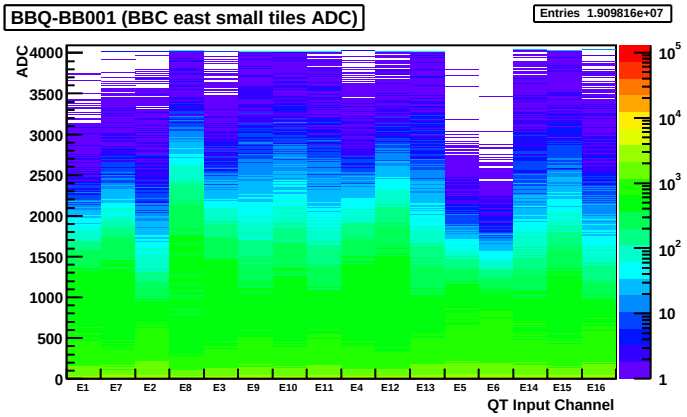

MIX-TF005
Entries 5.013267e+07
TOF MULT
TOF tray

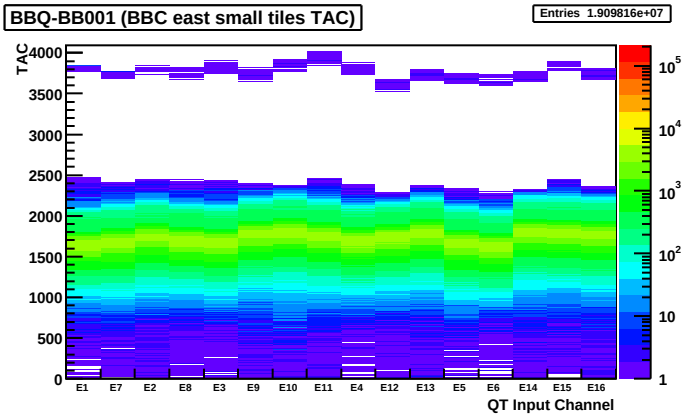

MIX-TF006
Entries 5.013267e+07
TOF MULT
TOF tray

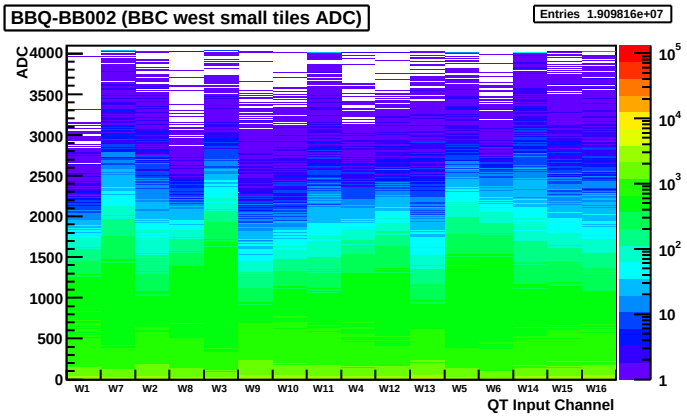

MIX-TF101
Entries 7161810
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

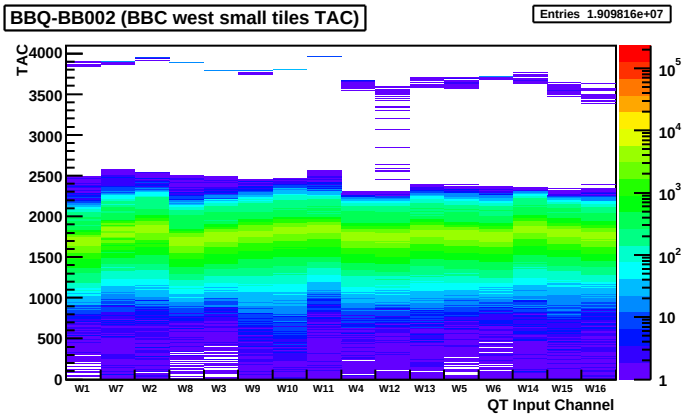

L1-TF201
Entries 1193635
10^4
10^3
10^2
0
200
400
600
800
1000
1200
1400
TOF total mult

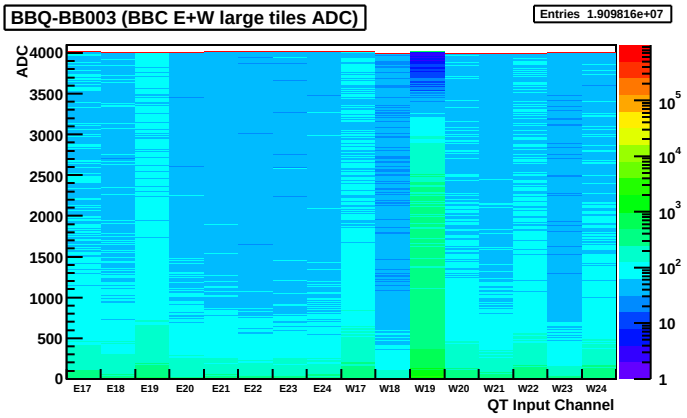

L1-TF201
Entries 7161810
Threshold bits
1
0
1.1418e+06
1.13925e+06
1.13889e+06
1.14165e+06
1.13632e+06
1.13621e+06
51831
54382
54743
51983
57314
57423
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^6
10^5

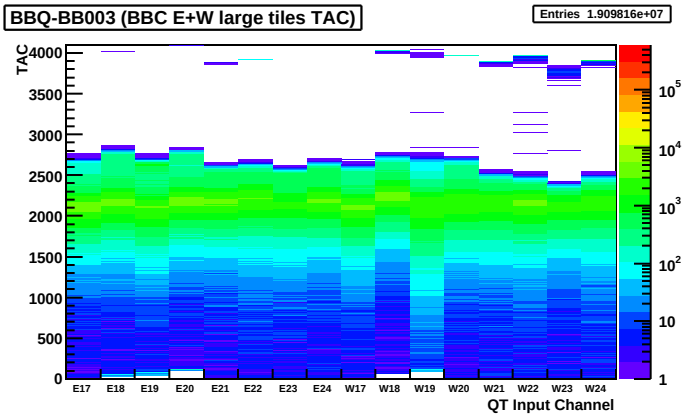

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.909816e+07
ADC
QT Input Channel

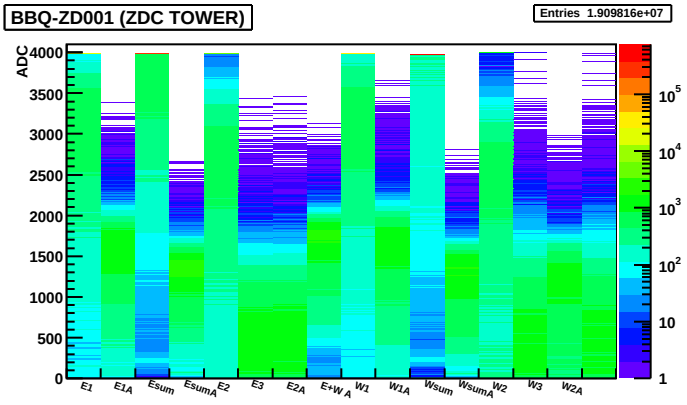

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.909816e+07
TAC
QT Input Channel

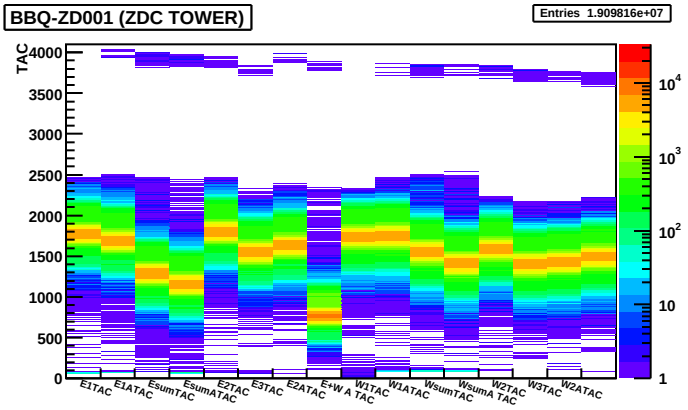

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.909816e+07
ADC
QT Input Channel

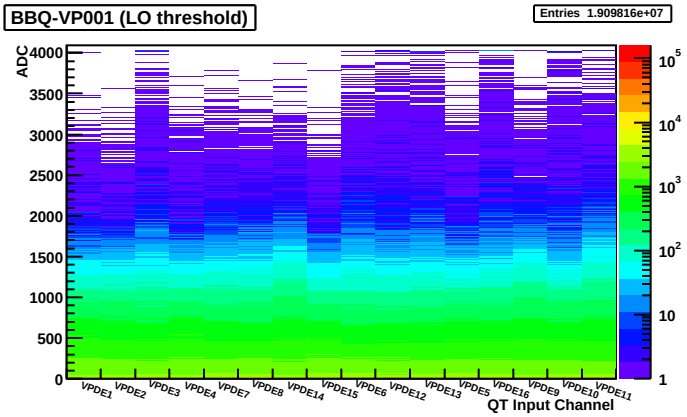

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.909816e+07
TAC
QT Input Channel

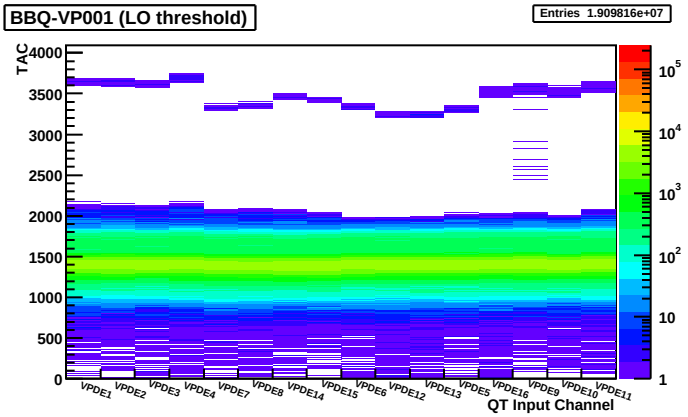

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.909816e+07
ADC
QT Input Channel

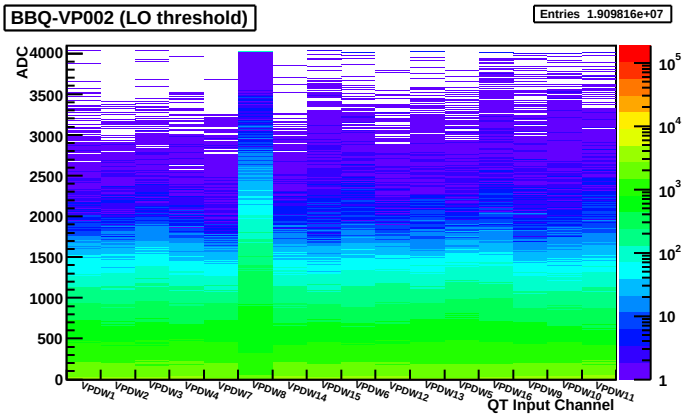

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.909816e+07
TAC
QT Input Channel

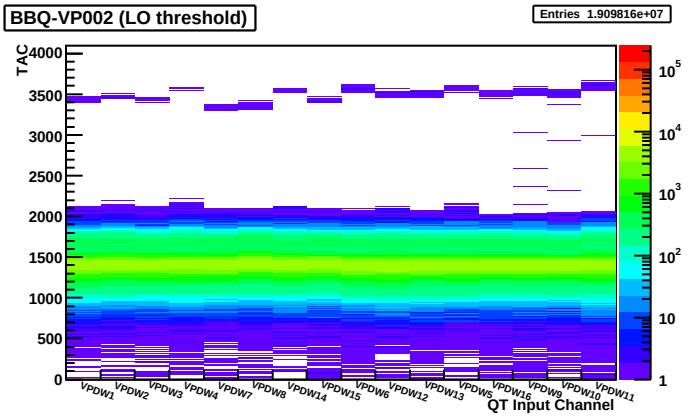

BBQ-ZD001 (ZDC TOWER)
Entries 1.909816e+07
ADC

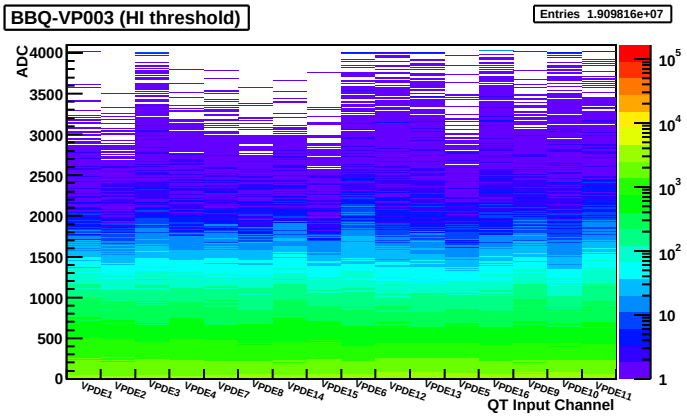

BBQ-ZD001 (ZDC TOWER)
Entries 1.909816e+07
TAC

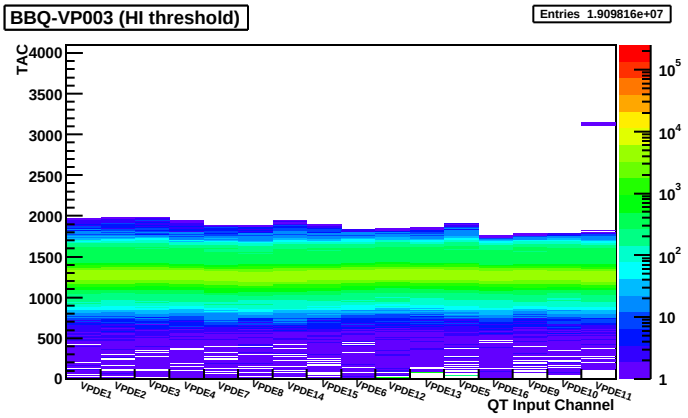

BBQ-VP001 (LO threshold)
Entries 1.909816e+07
ADC
QT Input Channel

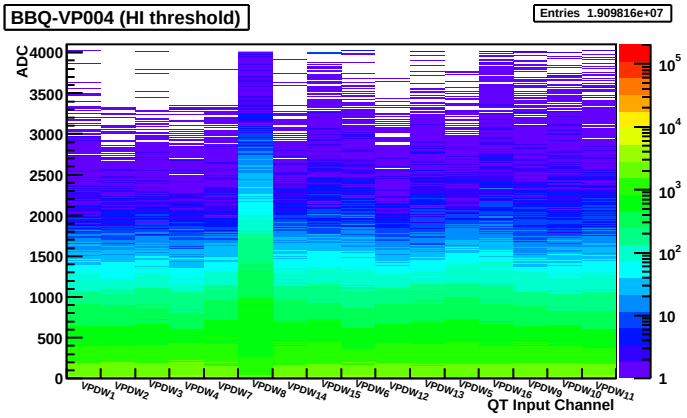

BBQ-VP001 (LO threshold)
Entries 1.909816e+07
TAC
QT Input Channel

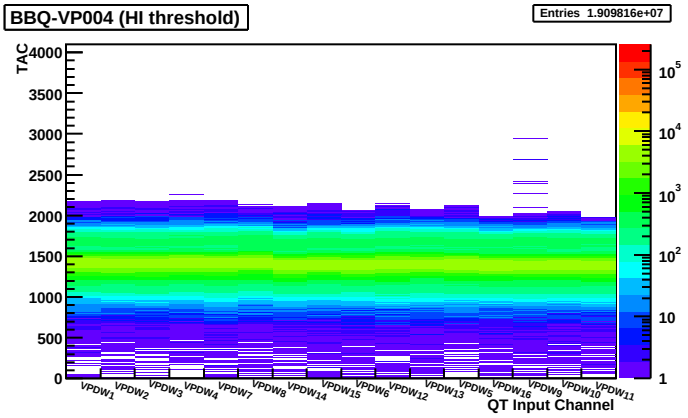

BBQ-VP002 (LO threshold)
Entries 1.909816e+07
ADC
QT Input Channel

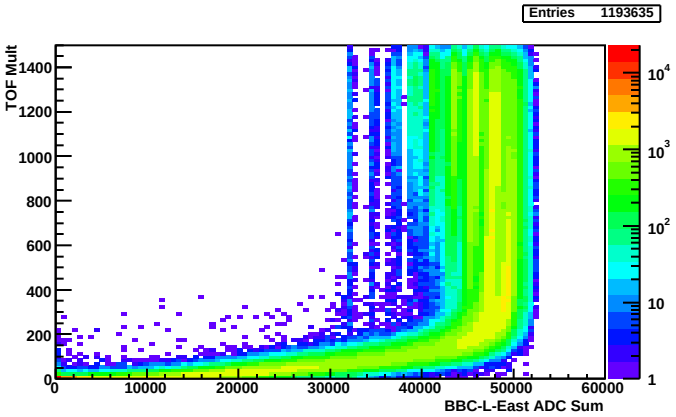

BBQ-VP002 (LO threshold)
Entries 1.909816e+07
TAC
QT Input Channel

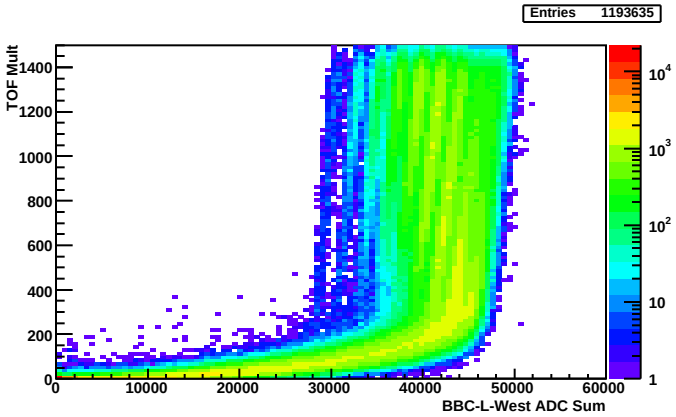

BBQ-VP003 (HI threshold)
Entries 1.909816e+07
ADC
QT Input Channel

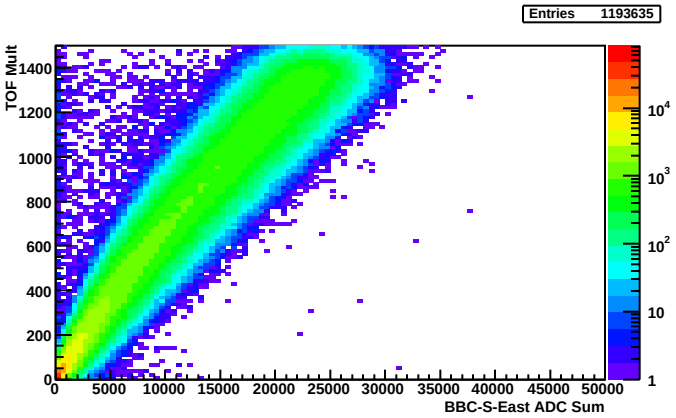

BBQ-VP003 (HI threshold)
Entries 1.909816e+07
TAC
QT Input Channel

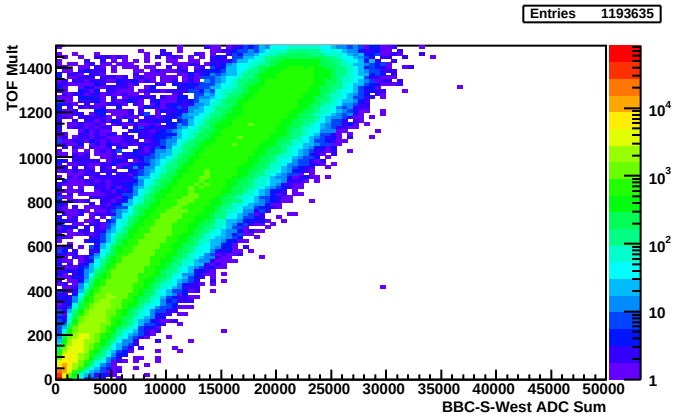

BBQ-VP004 (HI threshold)
Entries 1.909816e+07
ADC
QT Input Channel

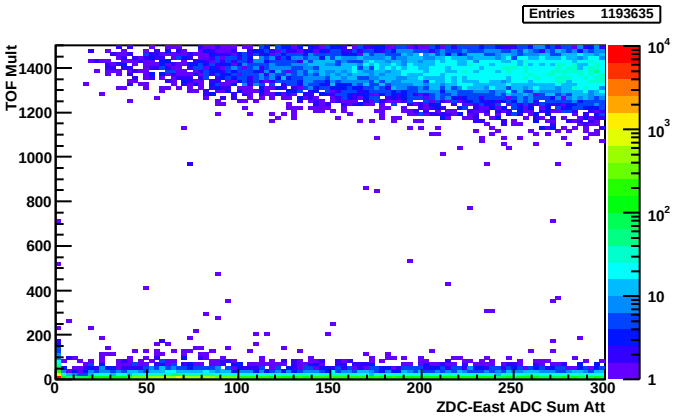

BBQ-VP004 (HI threshold)
Entries 1.909816e+07
TAC
QT Input Channel

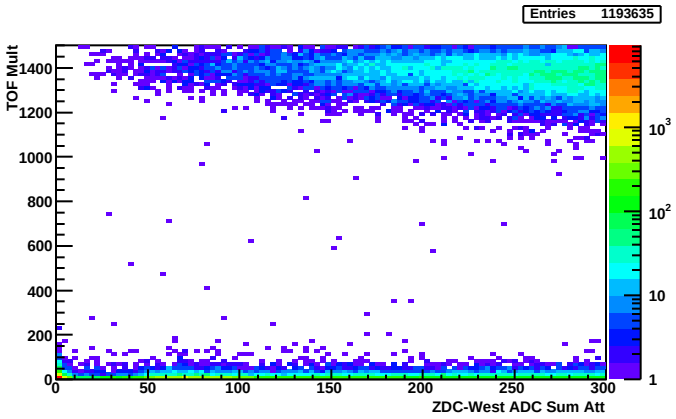

Entries 1193635
TOF Mult
BBC-L-East ADC Sum

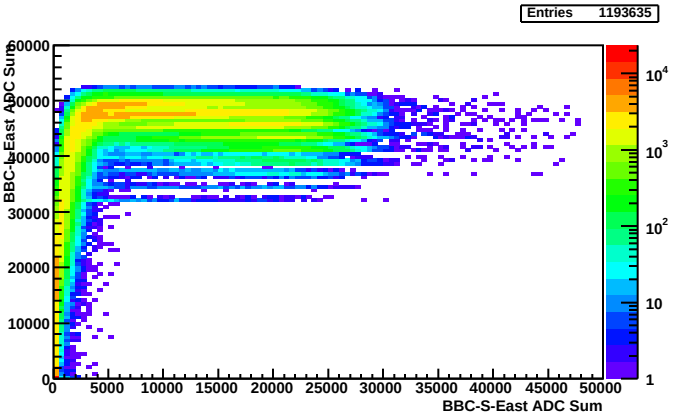

Entries 1193635
TOF Mult
BBC-L-West ADC Sum

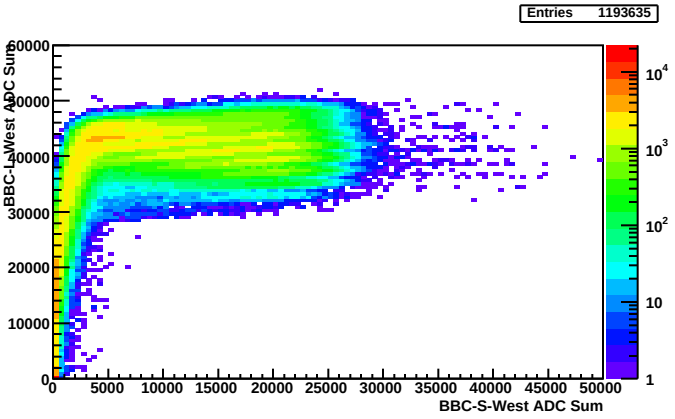

Entries 1193635
TOF Mult
BBC-S-East ADC Sum

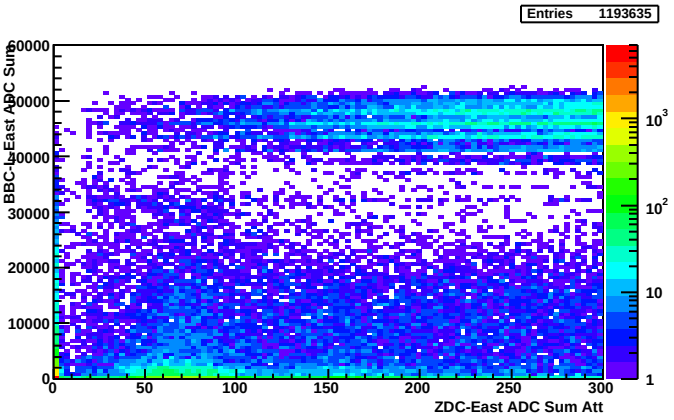

Entries 1193635
TOF Mult
BBC-S-West ADC Sum

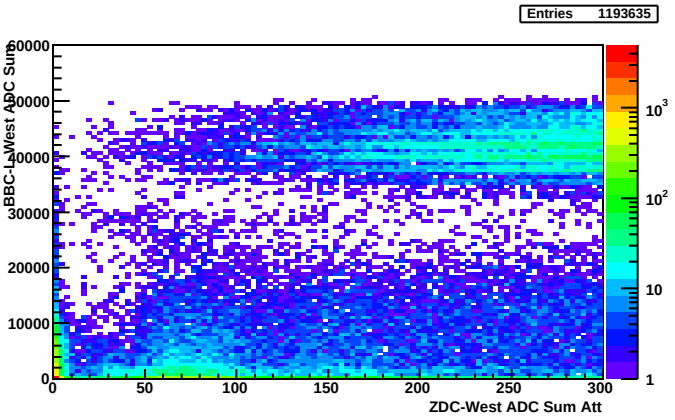

Entries 1193635
TOF Mult
ZDC-East ADC Sum Att


Entries 1193635
TOF Mult
ZDC-West ADC Sum Att


Entries 1193635
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 1193635
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 1193635
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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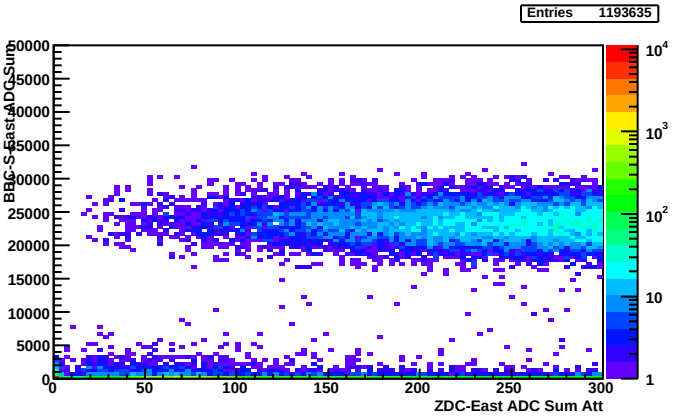
BBC-L-West ADC Sum
ZDC-West ADC Sum Att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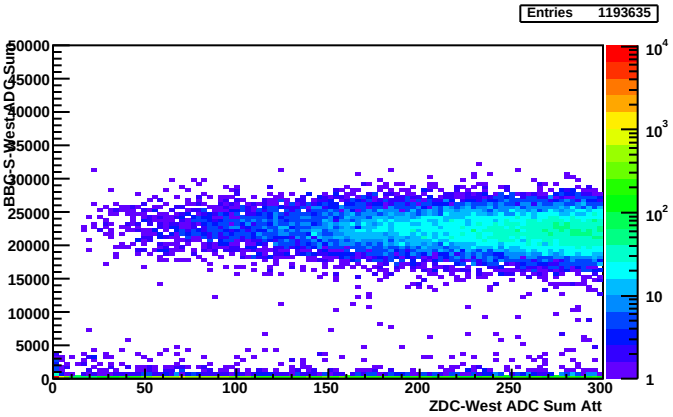
BBC-S-East ADC Sum
ZDC-East ADC Sum Att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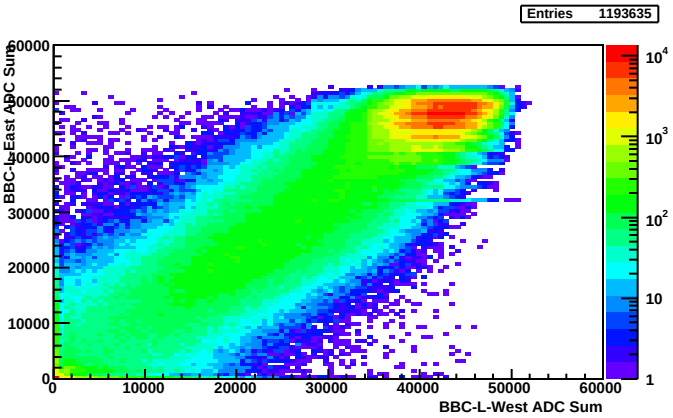
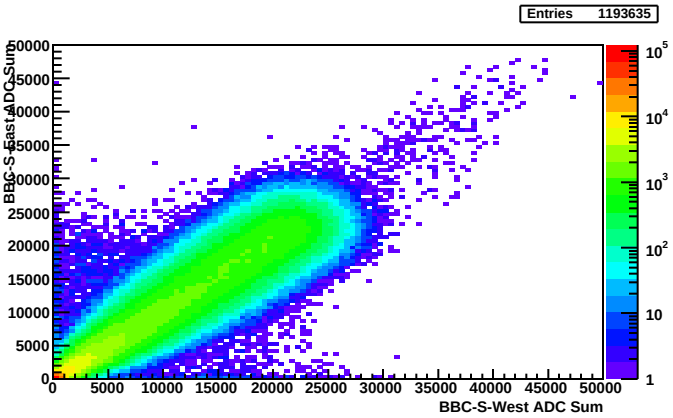
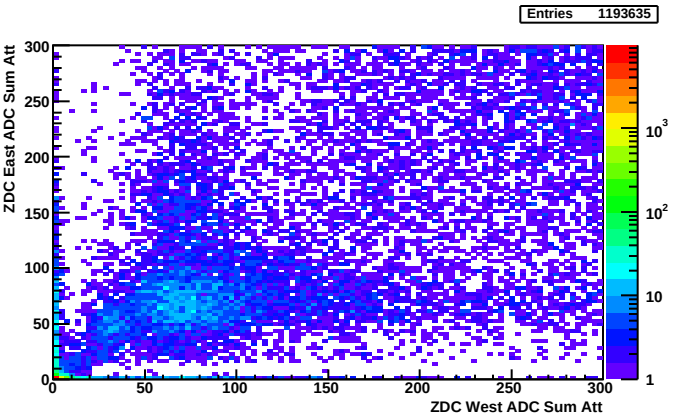
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 1193635
BBC-L-East ADC Sum
BBC-L-West ADC Sum


Entries 1193635
BBC-S-East ADC Sum
BBC-S-West ADC Sum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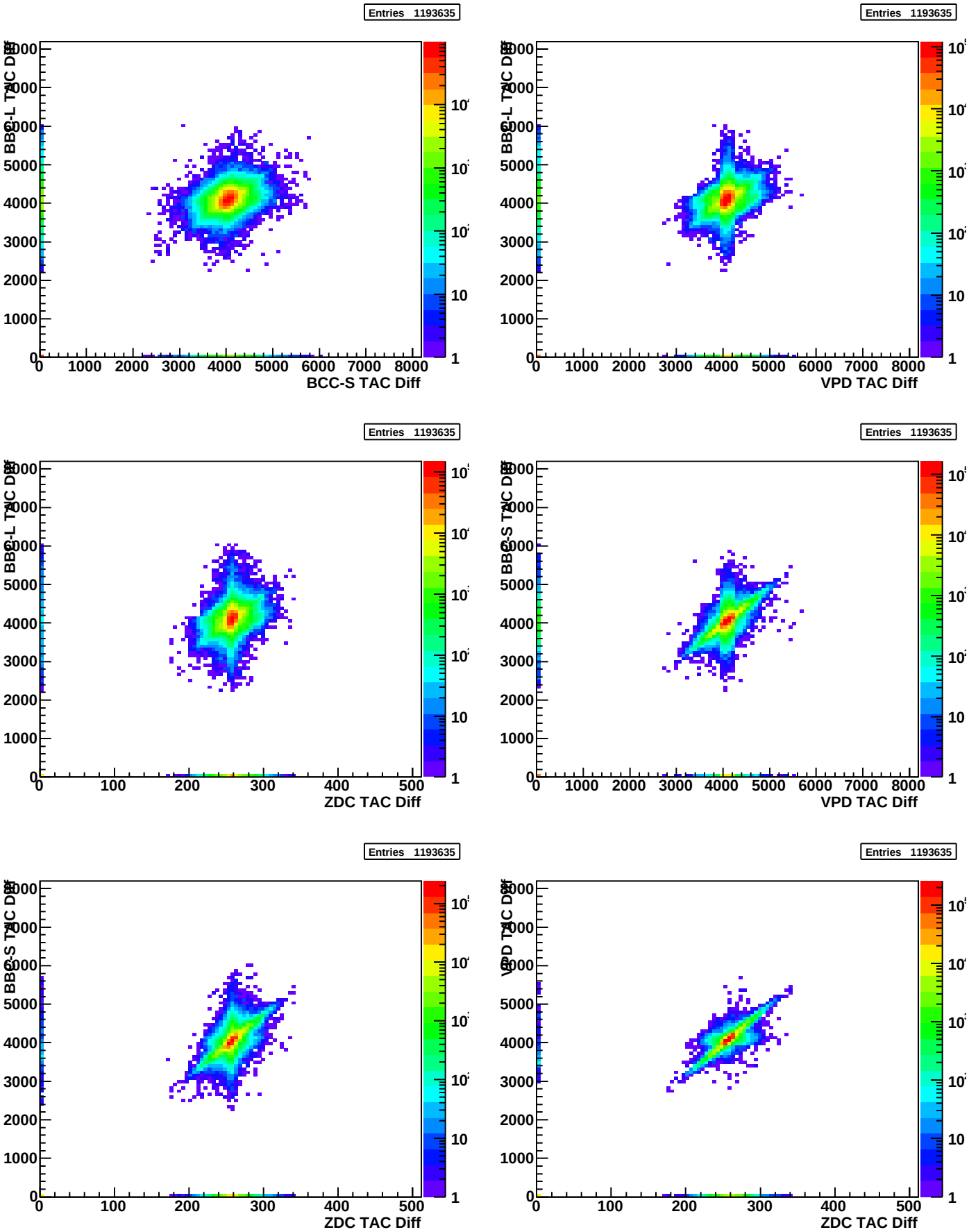
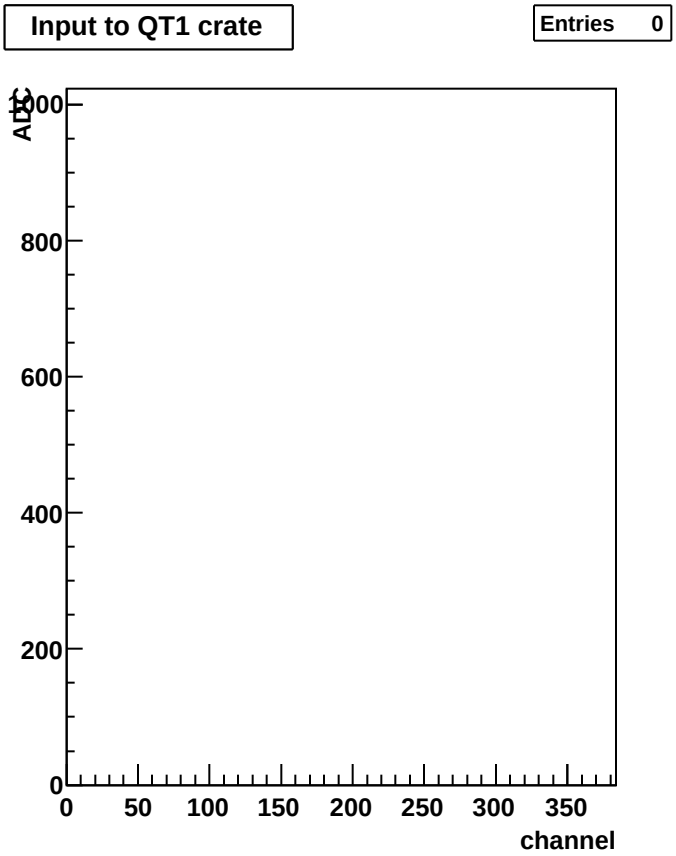
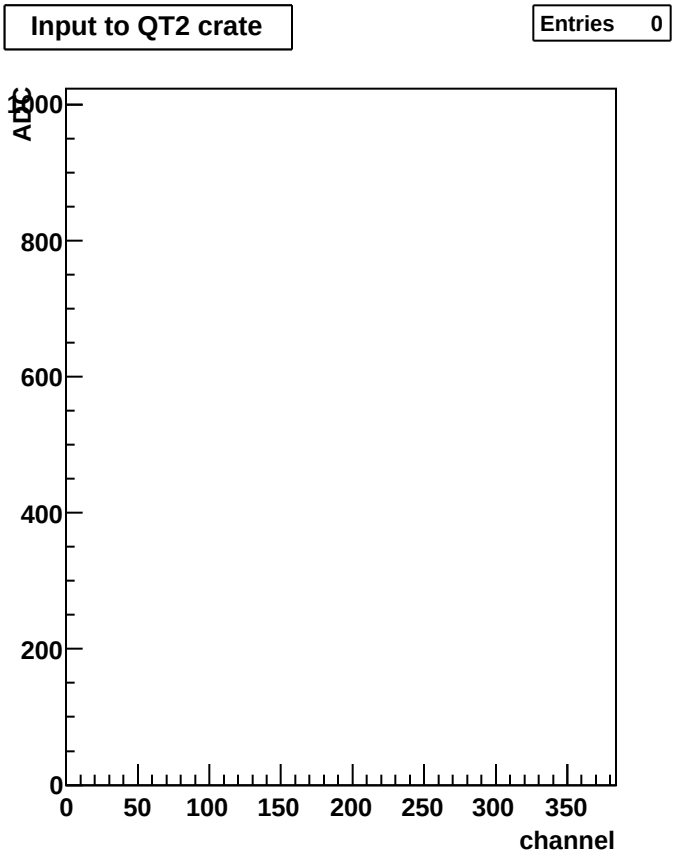
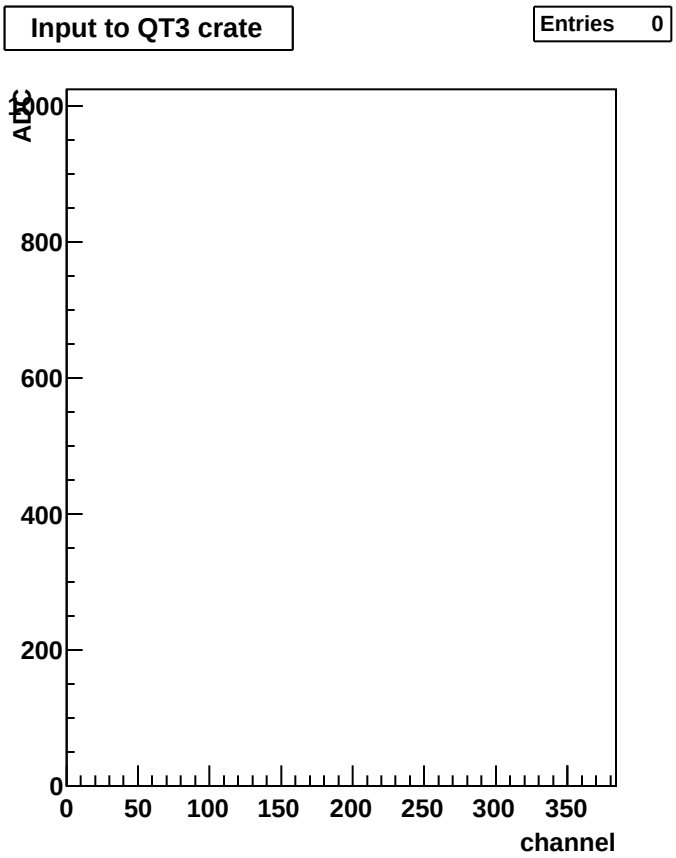
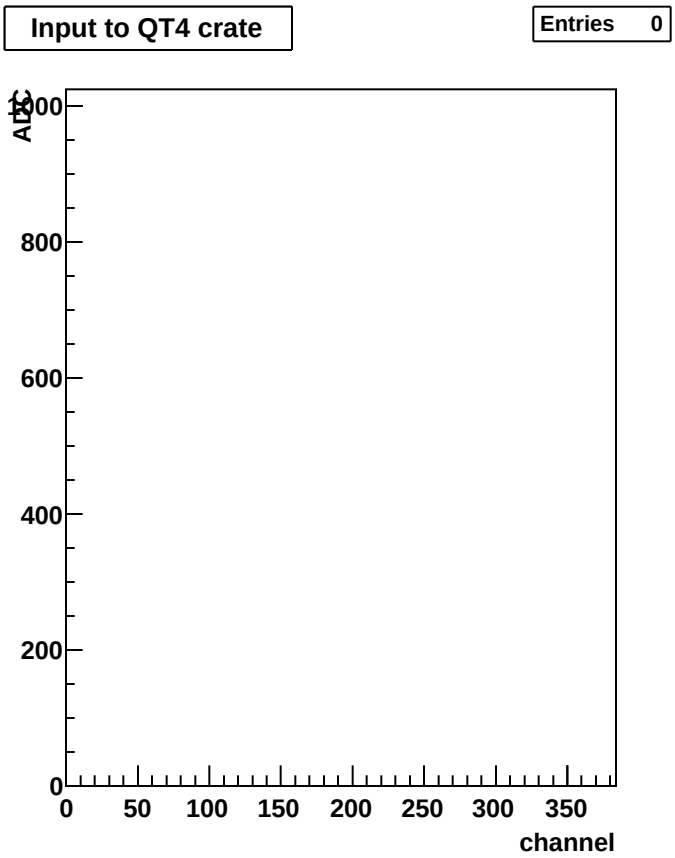
ZDC East ADC Sum Att
ZDC West ADC Sum Att

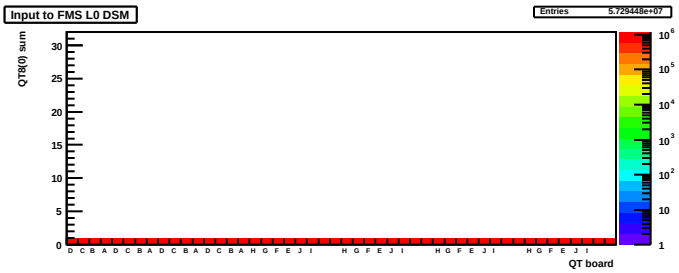

Entries 1193635
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1193635
BBC-L TAC Diff
VPD TAC Diff
Entries 1193635
BBC-L TAC Diff
ZDC TAC Diff
Entries 1193635
BBC-S TAC Diff
VPD TAC Diff
Entries 1193635
BBC-S TAC Diff
ZDC TAC Diff
Entries 1193635
VPD TAC Diff
ZDC TAC Diff

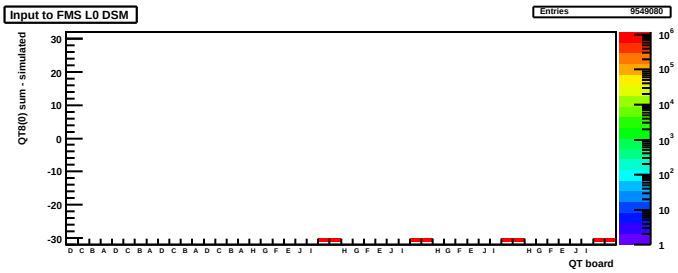

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

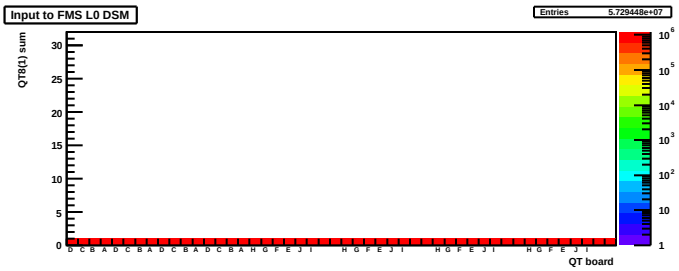

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

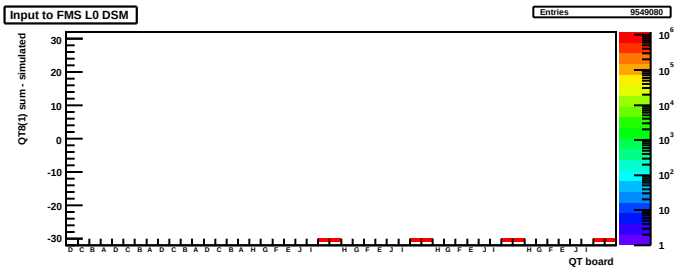

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

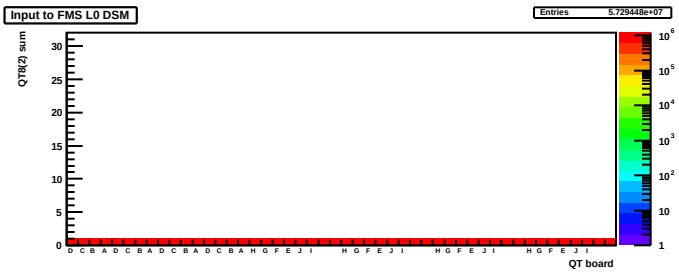

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

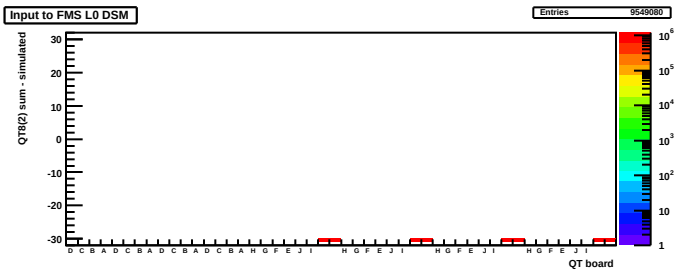

Input to FMS L0 DSM
Entries 5.729448e+07
QT8(0) sum
QT board

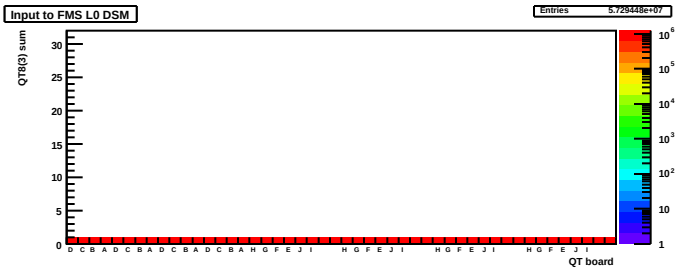

Input to FMS L0 DSM
Entries 9549080
QT8(0) sum - simulated
QT board

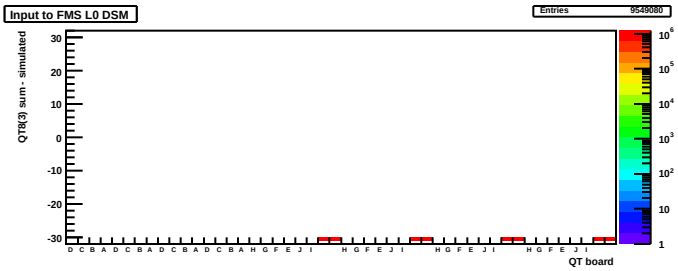

Input to FMS L0 DSM
Entries 5.729448e+07
QT8(1) sum
QT board

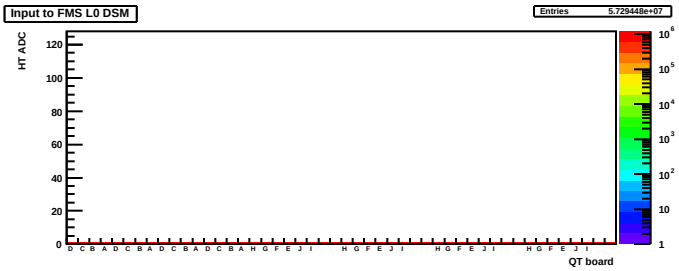

Input to FMS L0 DSM
Entries 9549080
QT8(1) sum - simulated
QT board

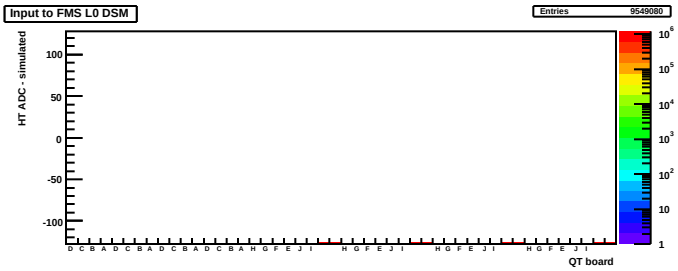

Input to FMS L0 DSM
Entries 5.729448e+07
QT8(2) sum
QT board

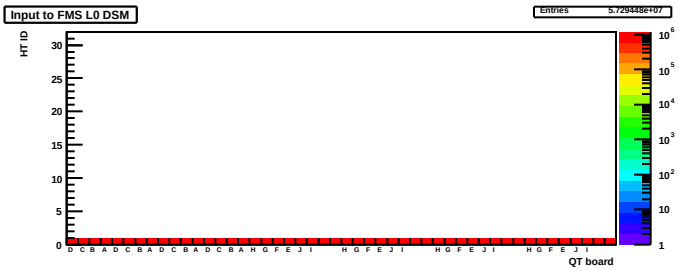

Input to FMS L0 DSM
Entries 9549080
QT8(2) sum - simulated
QT board

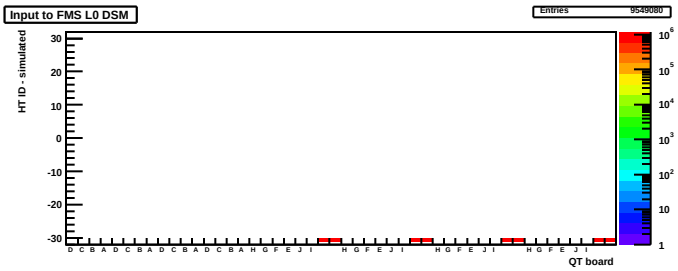

Input to FMS L0 DSM
Entries 5.729448e+07
QT8(3) sum
QT board

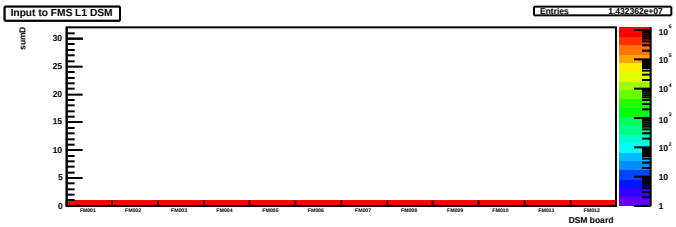

Input to FMS L0 DSM
Entries 9549080
QT8(3) sum - simulated
QT board

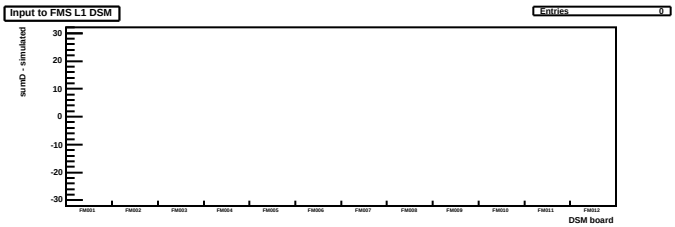

Input to FMS L0 DSM
Entries 5.729448e+07
HT ADC
QT board

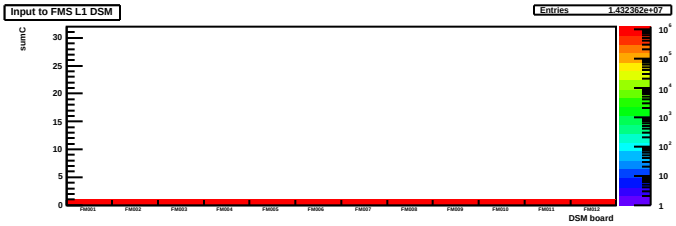

Input to FMS L0 DSM
Entries 9549080
HT ADC - simulated
QT board

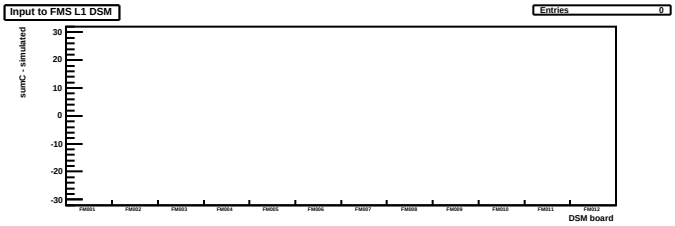

Input to FMS L0 DSM
Entries 5.729448e+07
HT ID
QT board

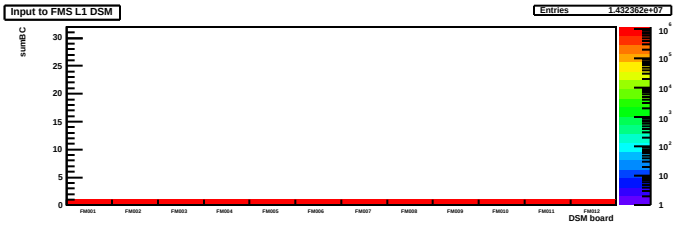

Input to FMS L0 DSM
Entries 9549080
HT ID - simulated
QT board

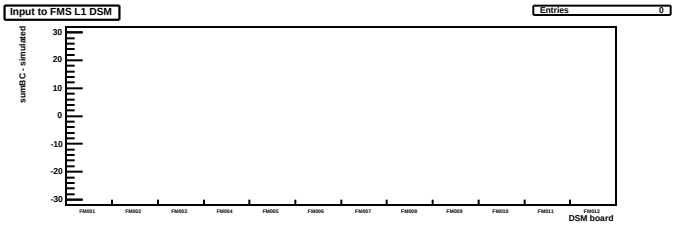

Input to FMS L1 DSM
Entries 1.432362e+07
sumD
DSM board

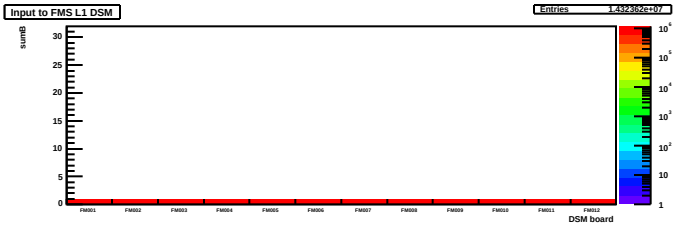

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

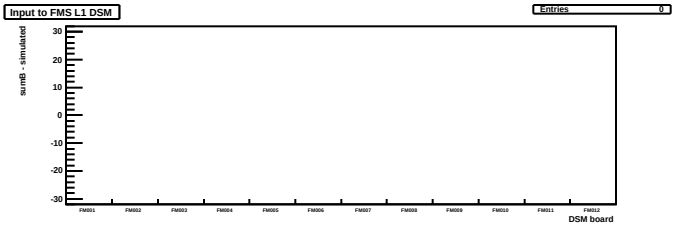

Input to FMS L1 DSM
Entries 1.432362e+07
sumC
DSM board

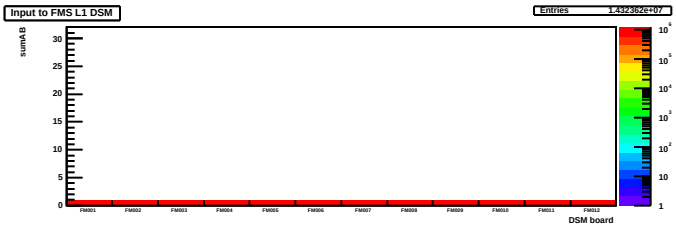

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

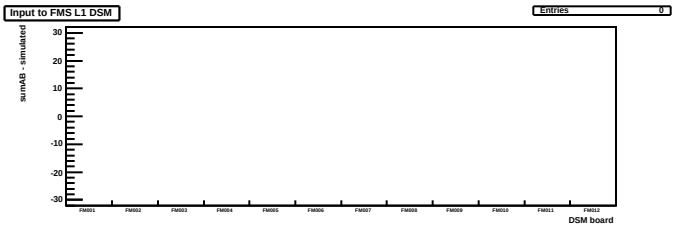

Input to FMS L1 DSM
Entries 1.432362e+07
sumBC
DSM board

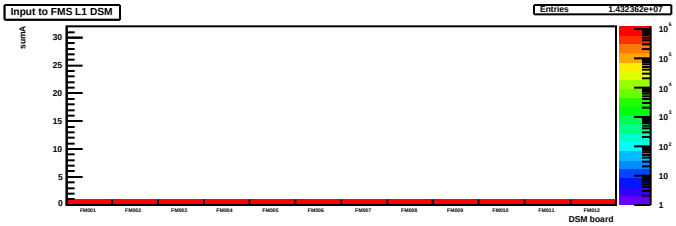

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

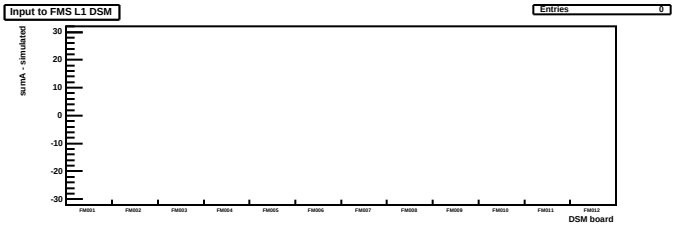

Input to FMS L1 DSM
Entries 1.432362e+07
sumB
DSM board

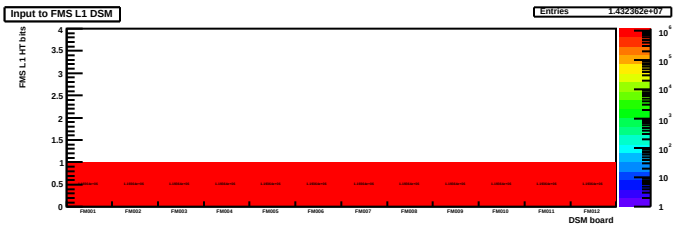

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

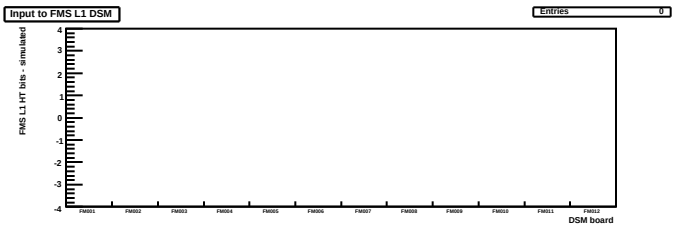

Input to FMS L1 DSM
Entries 1.432362e+07
sumAB
DSM board

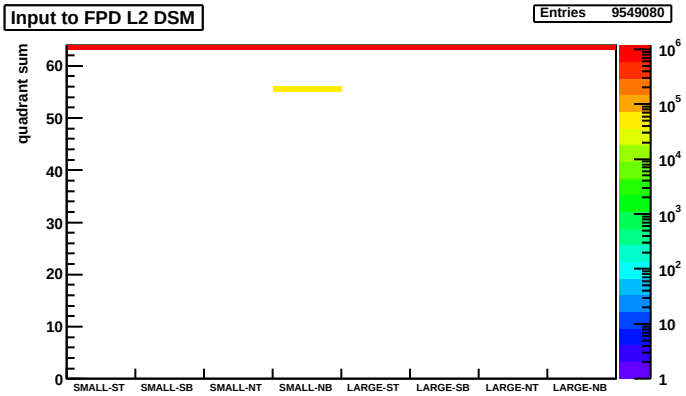

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

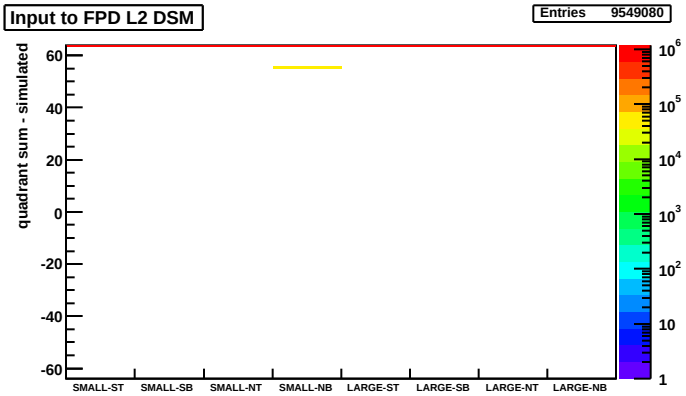

Input to FMS L1 DSM
Entries 1.432362e+07
sumA
DSM board

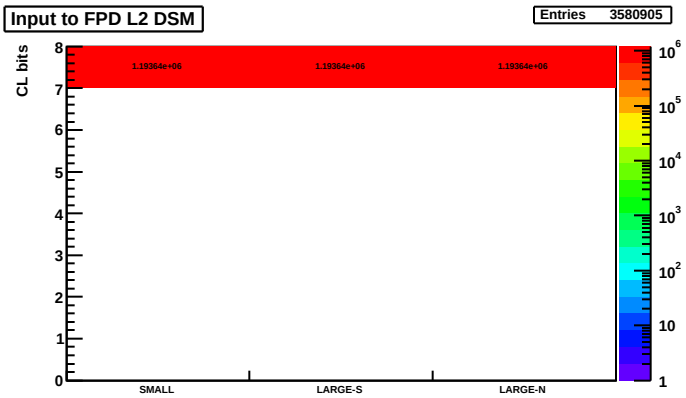

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

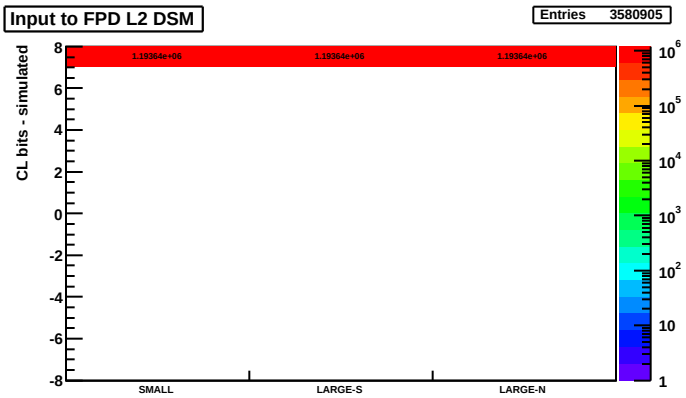

Input to FMS L1 DSM
Entries 1.432362e+07
FMS L1 HT bits
DSM board

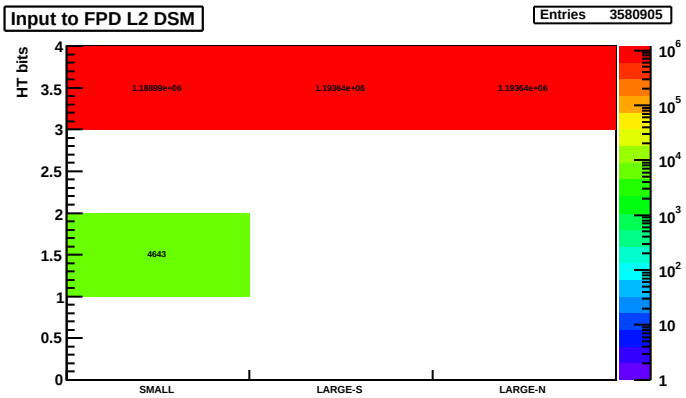

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

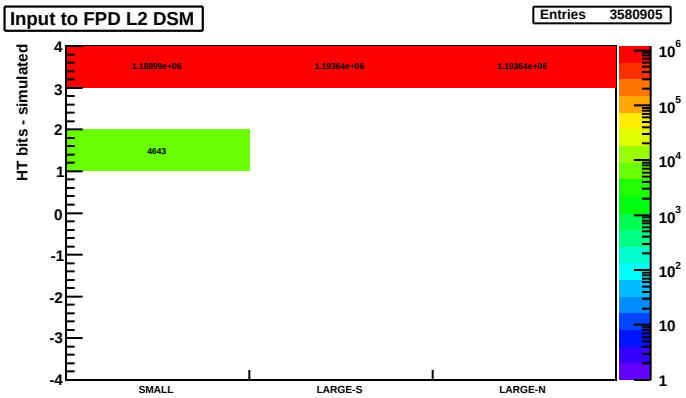

Input to FPD L2 DSM
Entries 9549080
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

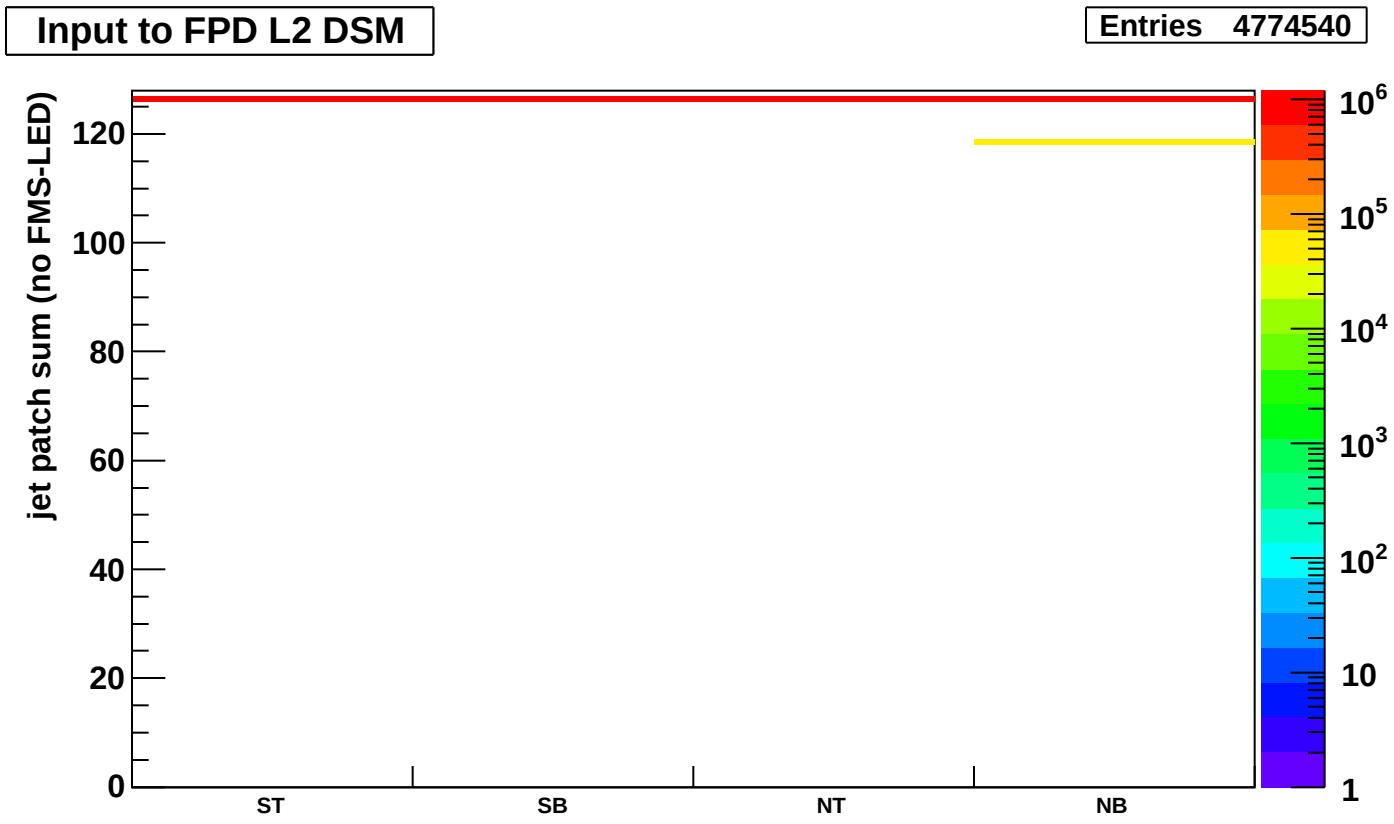

Input to FPD L2 DSM
Entries 9549080
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

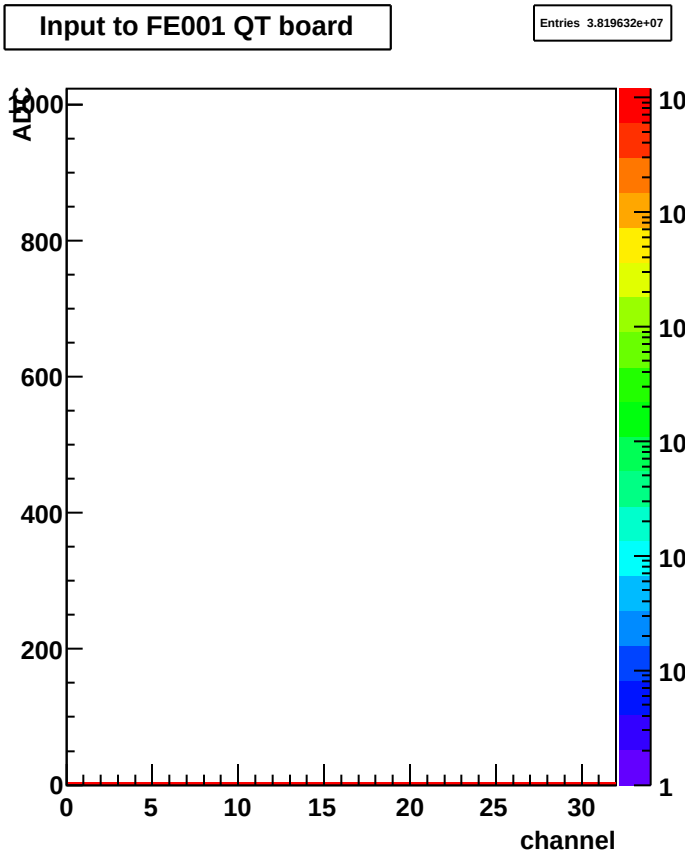

Input to FPD L2 DSM
Entries 3580905
CL bits
1.19364e+06
1.19364e+06
1.19364e+06
SMALL
LARGE-S
LARGE-N

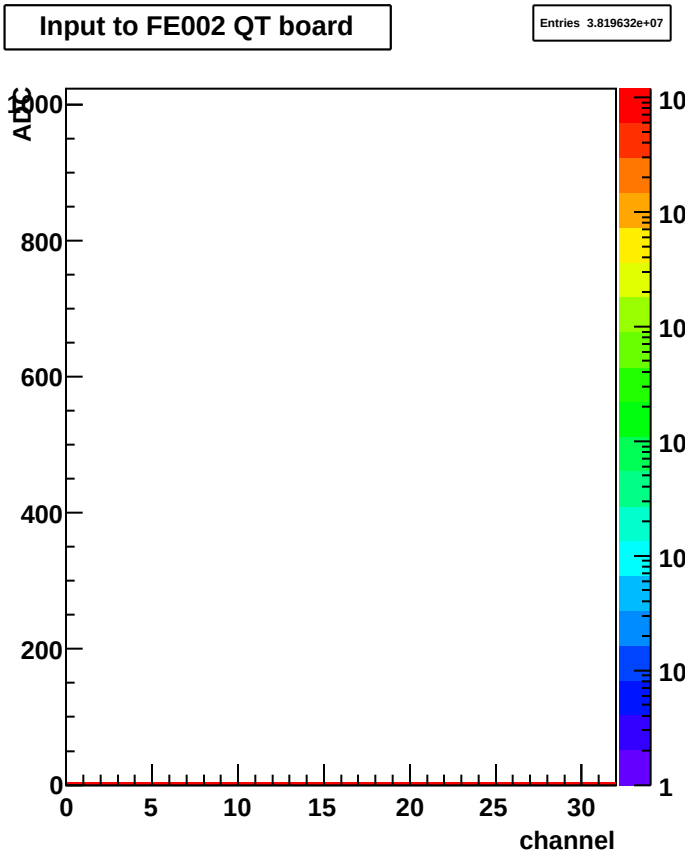

Input to FPD L2 DSM
Entries 3580905
CL bits - simulated
1.19364e+06
1.19364e+06
1.19364e+06
SMALL
LARGE-S
LARGE-N

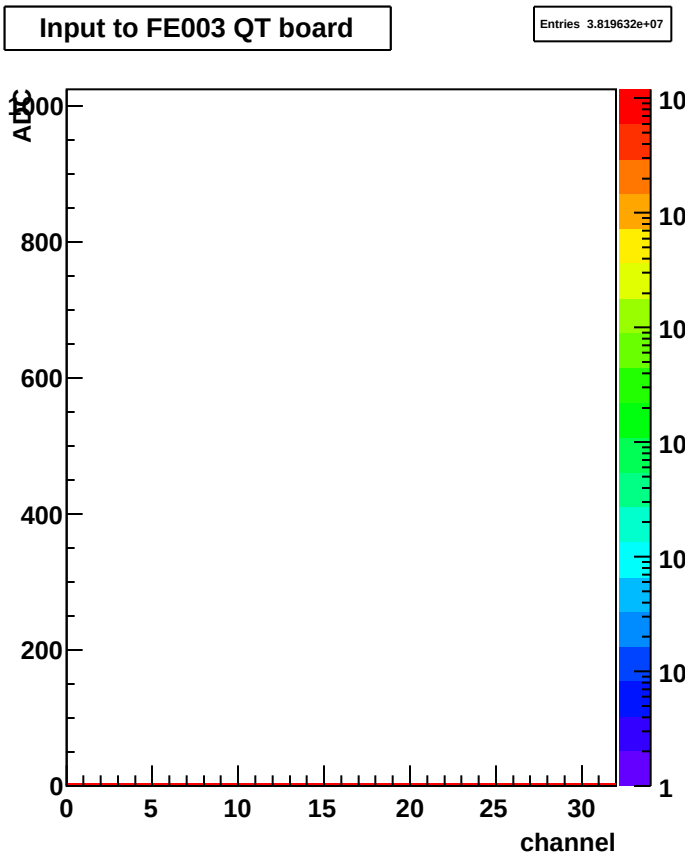

Input to FPD L2 DSM
Entries 3580905
HT bits
1.18899e+06
1.19364e+06
1.19364e+06
4643
SMALL
LARGE-S
LARGE-N

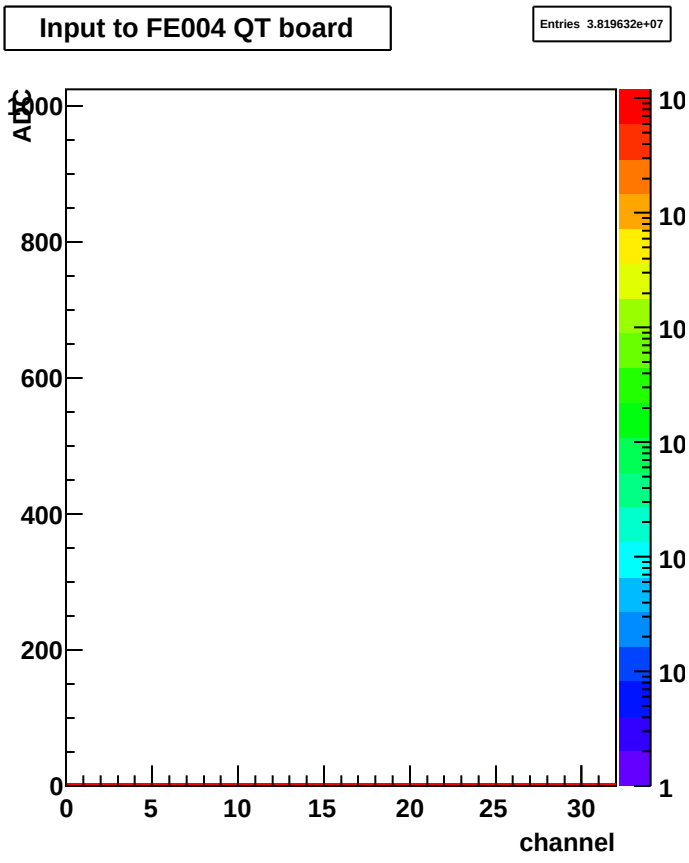

Input to FPD L2 DSM
Entries 3580905
HT bits - simulated
1.18899e+06
1.19364e+06
1.19364e+06
4643
SMALL
LARGE-S
LARGE-N


Input to FPD L2 DSM
Entries 4774540
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5
10^6

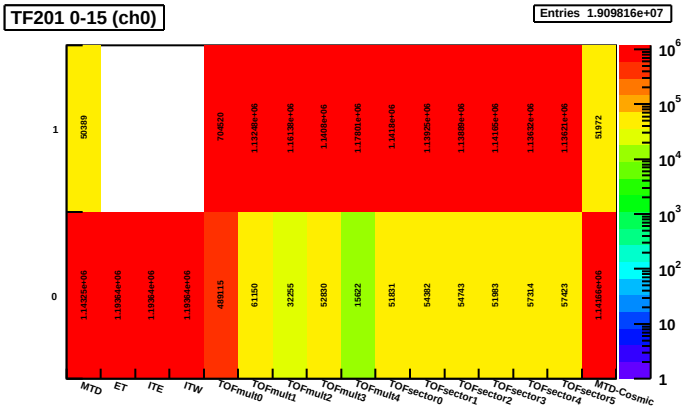

Input to FE001 QT board
Entries 3.819632e+07
ADC
1000
800
600
400
200
0
0
5
10
15
20
25
30
channel
10
10
10
10
10
10
1


Input to FE002 QT board
Entries 3.819632e+07
ADC
1000
800
600
400
200
0
0
5
10
15
20
25
30
channel
10
10
10
10
10
10
1

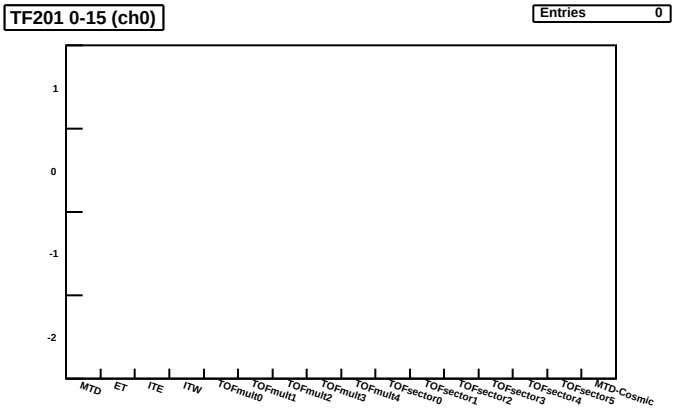

Input to FE003 QT board
Entries 3.819632e+07
ADC
1000
800
600
400
200
0
0
5
10
15
20
25
30
channel
10
10
10
10
10
10
1


Input to FE004 QT board
Entries 3.819632e+07
ADC
1000
800
600
400
200
0
0
5
10
15
20
25
30
channel
10
10
10
10
10
10
1

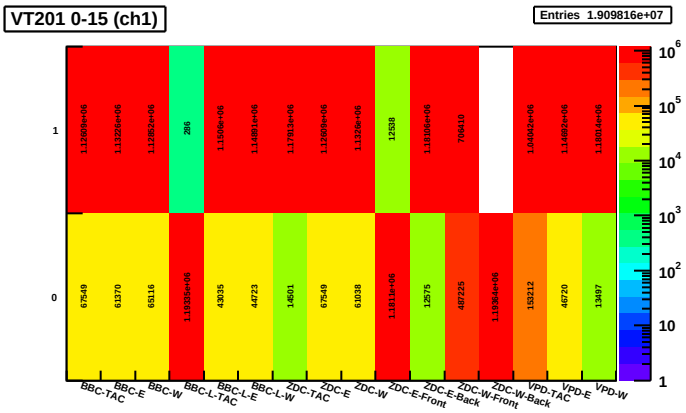
TF201 0-15 (ch0)



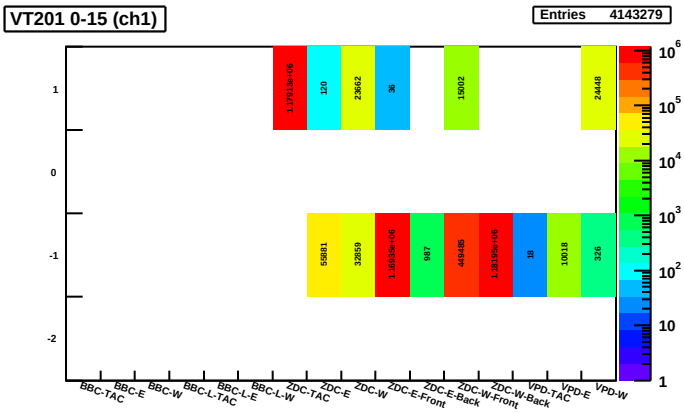
TF201 0-15 (ch0)



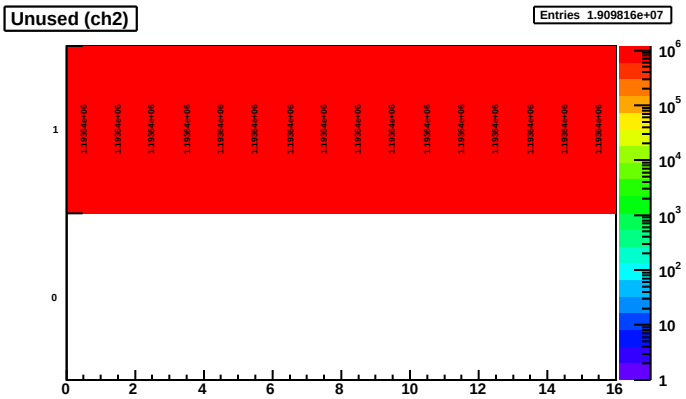
VT201 0-15 (ch1)

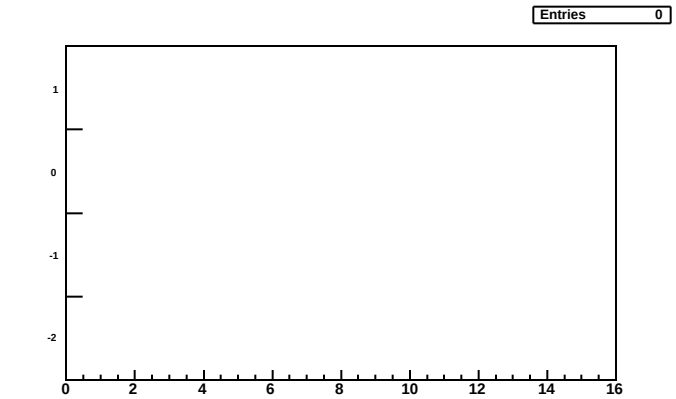


VT201 0-15 (ch1)

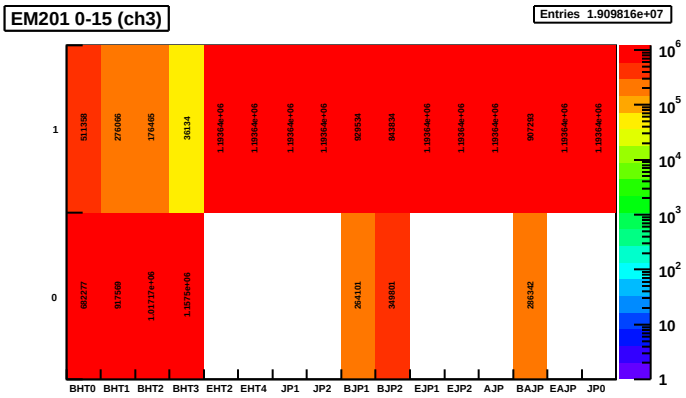


Unused (ch2)

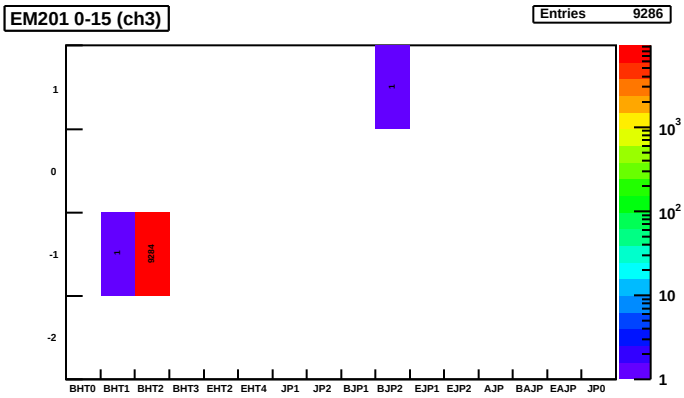




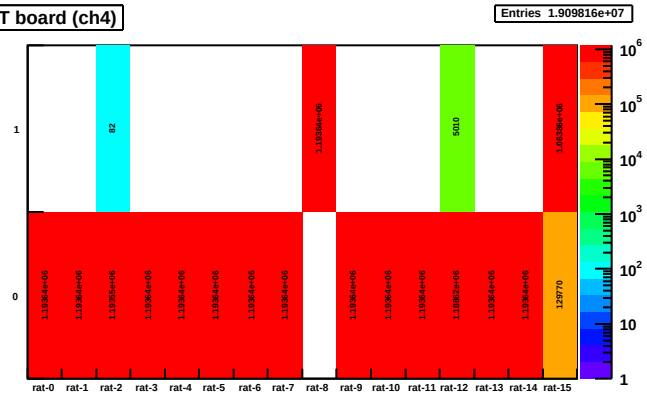
EM201 0-15 (ch3)

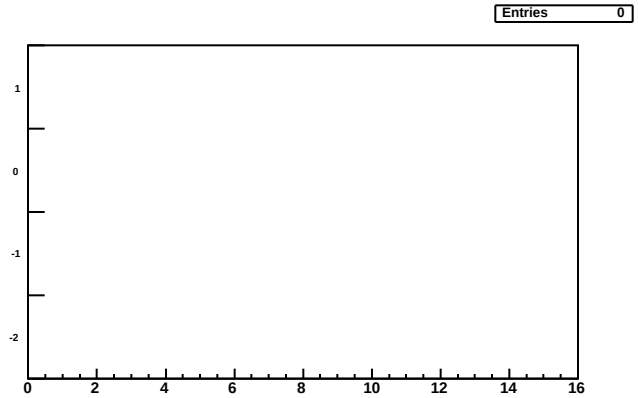


EM201 0-15 (ch3)

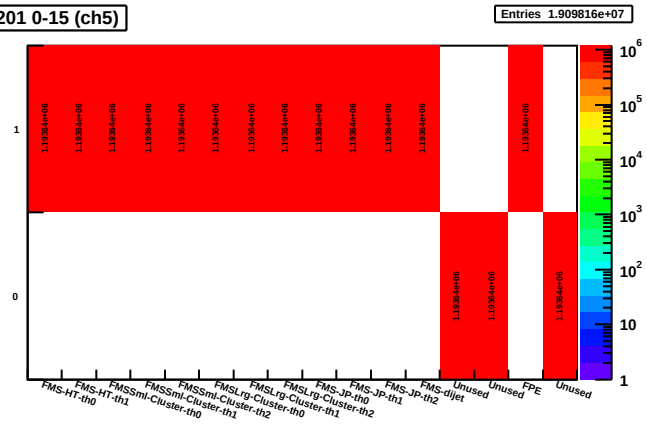


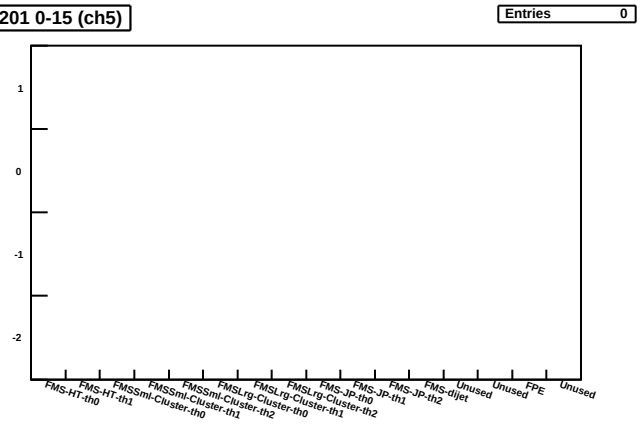
RAT board (ch4)



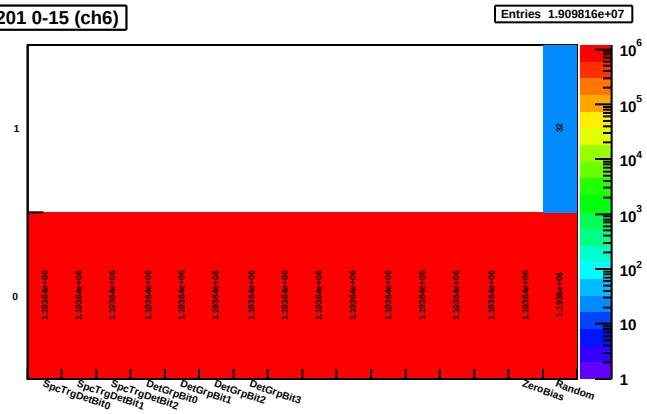


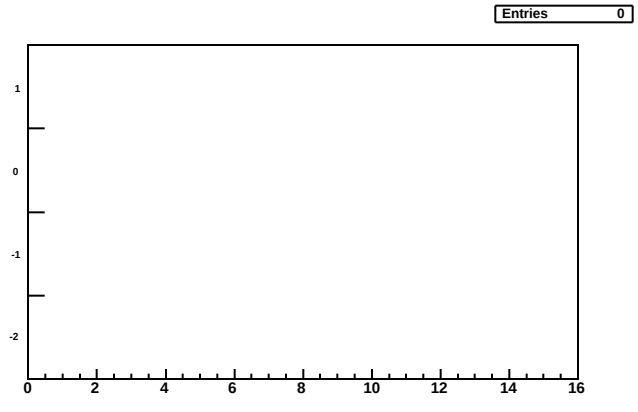
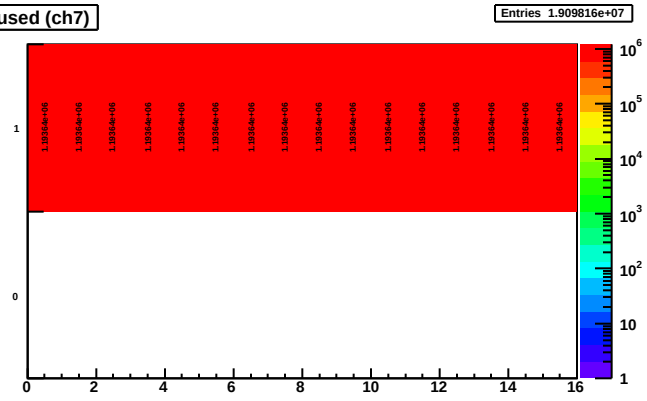
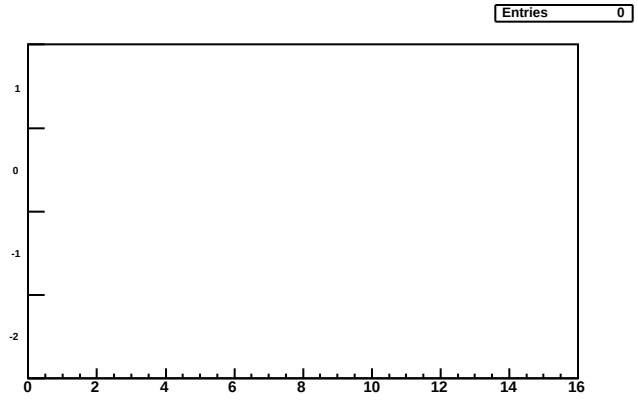
FP201 0-15 (ch5)





ST201 0-15 (ch6)





Unused (ch7)