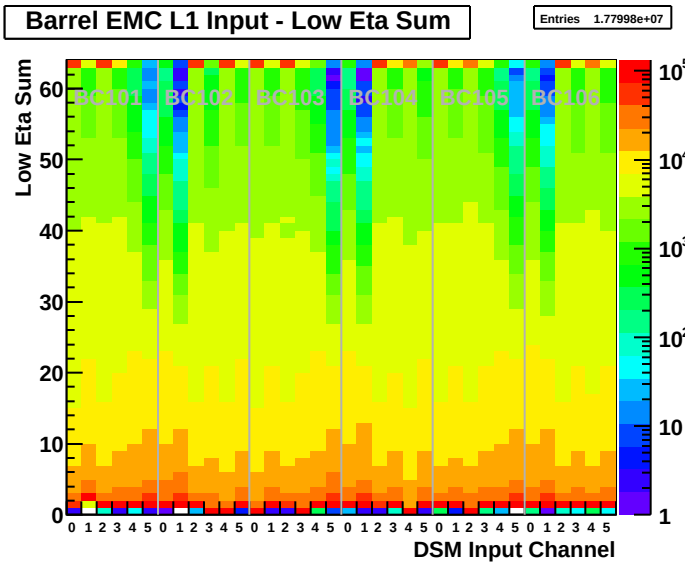
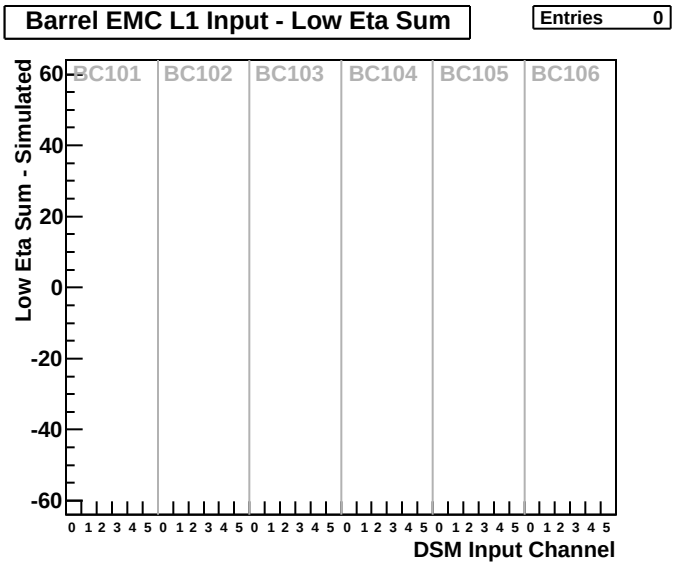
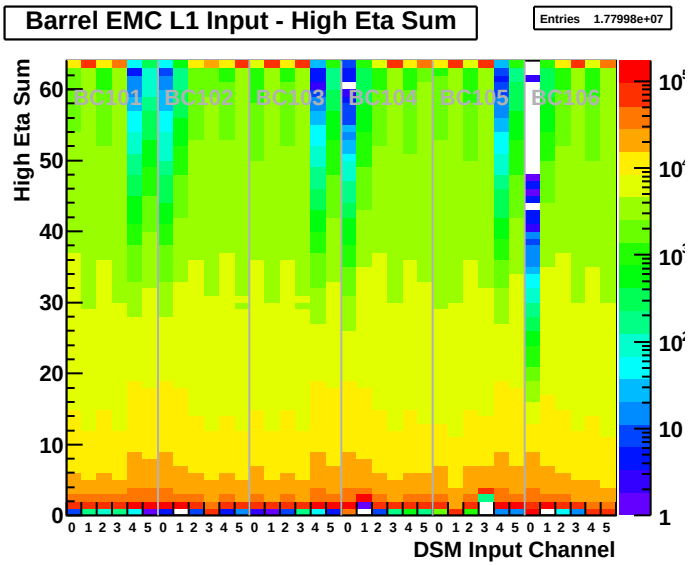
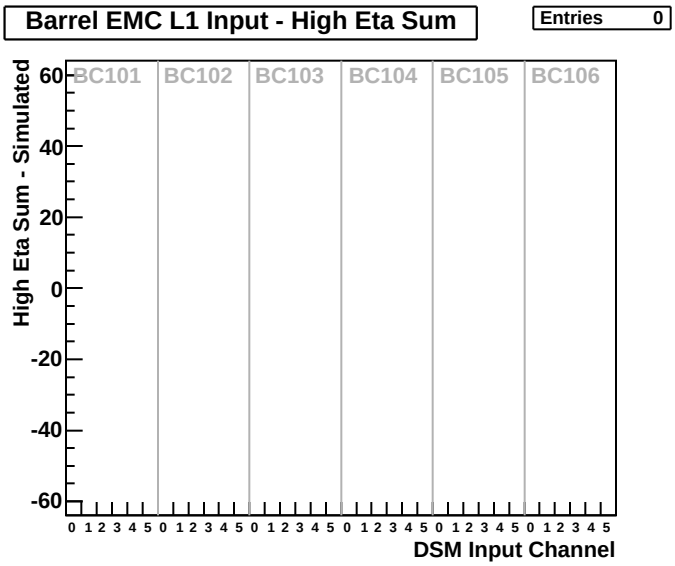
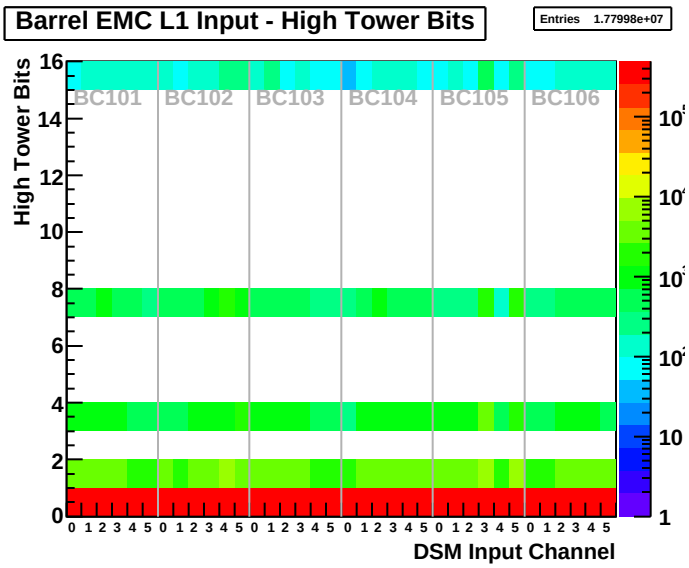
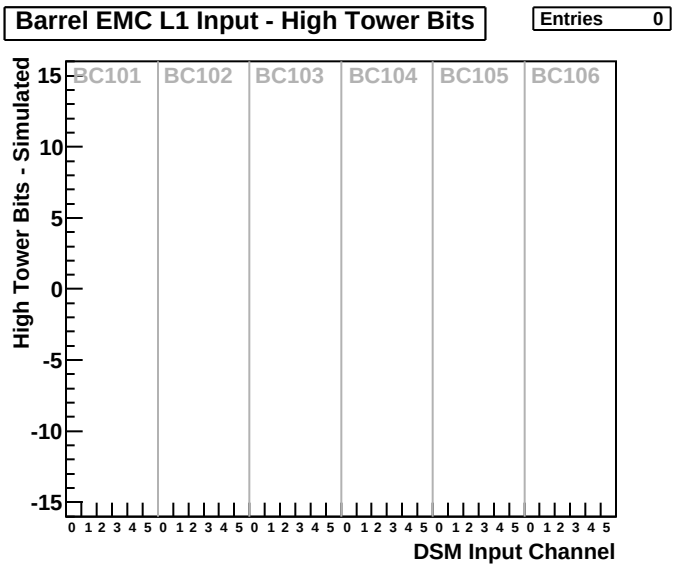

Barrel EMC L1 Input - Low Eta Sum
Entries 1.77998e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

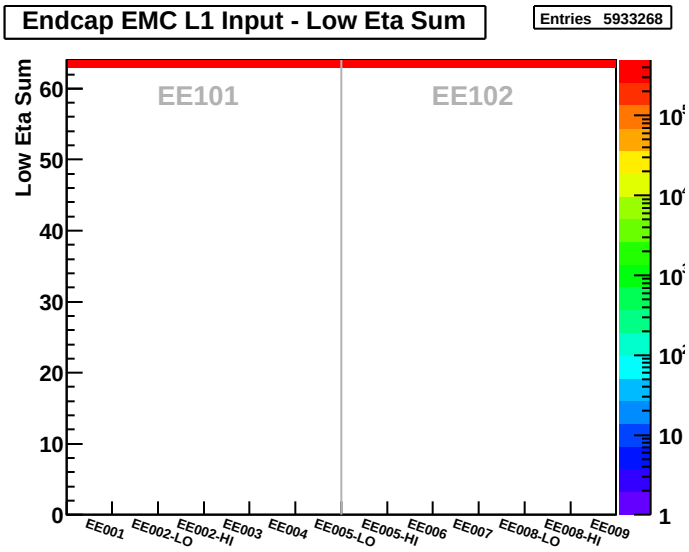

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

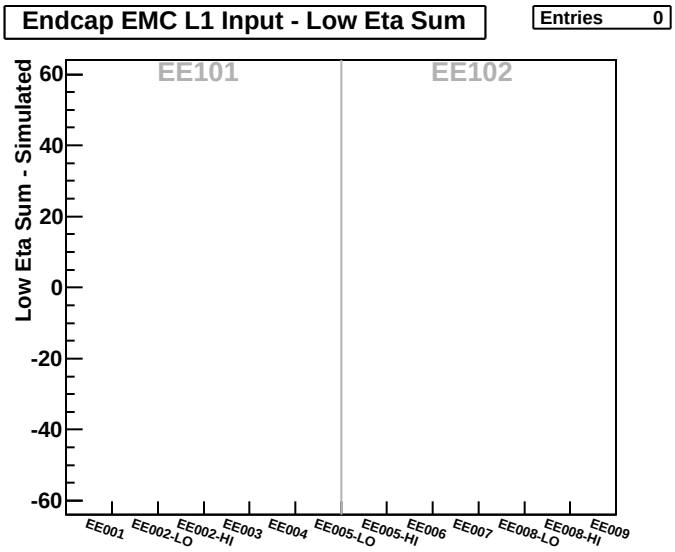

Barrel EMC L1 Input - High Eta Sum
Entries 1.77998e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

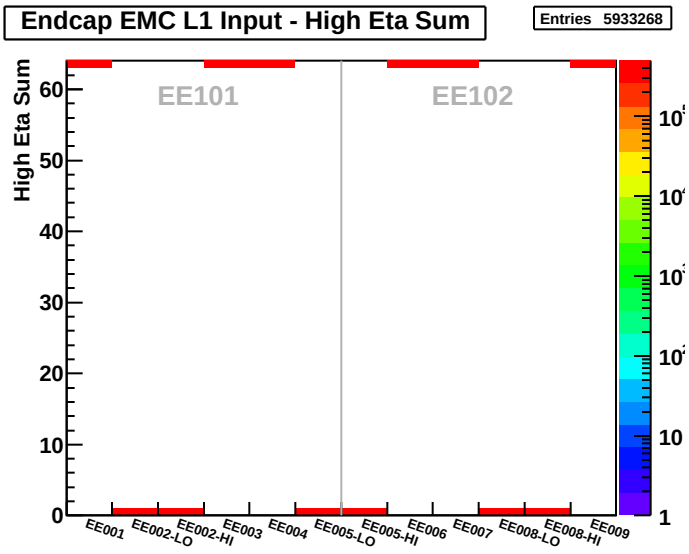

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

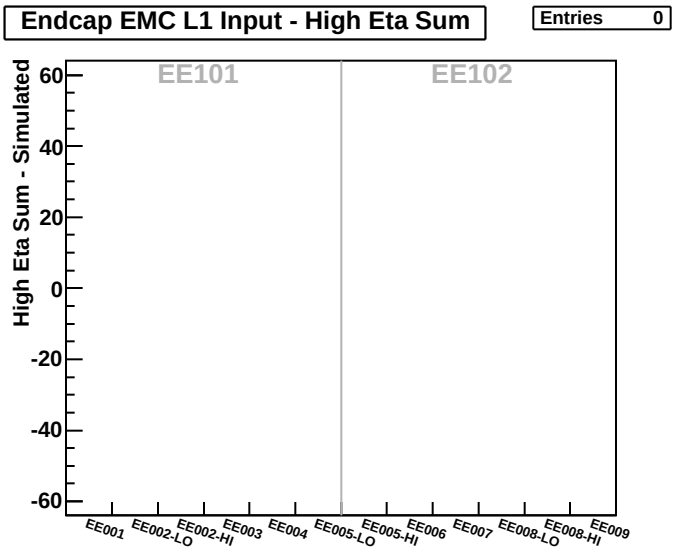

Barrel EMC L1 Input - High Tower Bits
Entries 1.77998e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

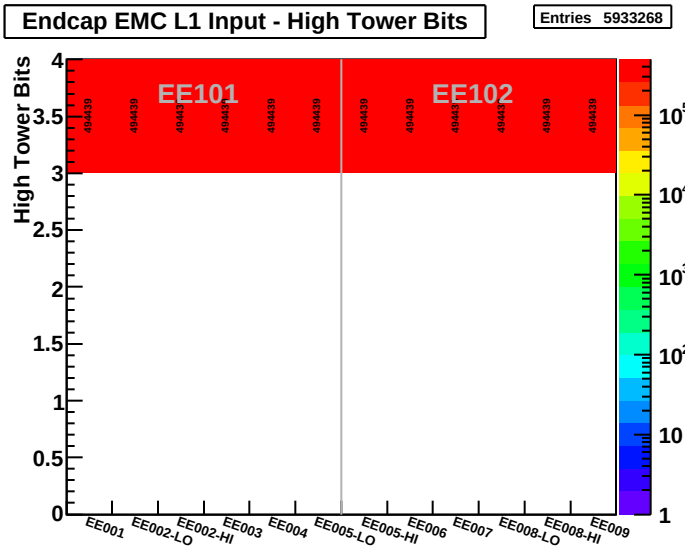

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

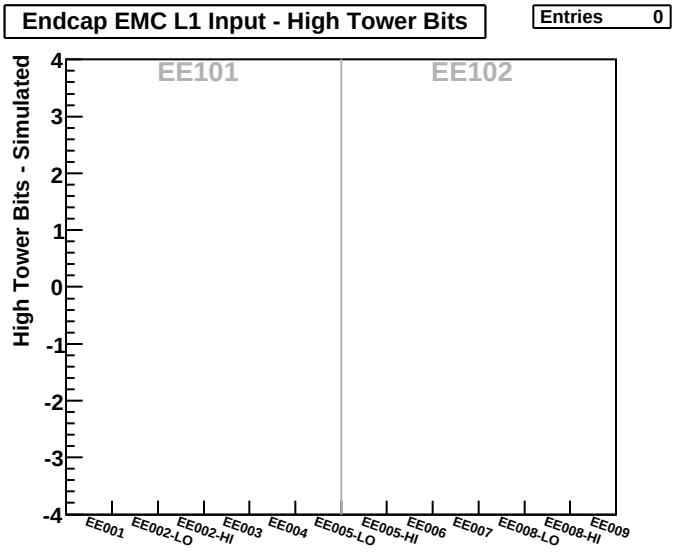

Endcap EMC L1 Input - Low Eta Sum
Entries 5933268
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

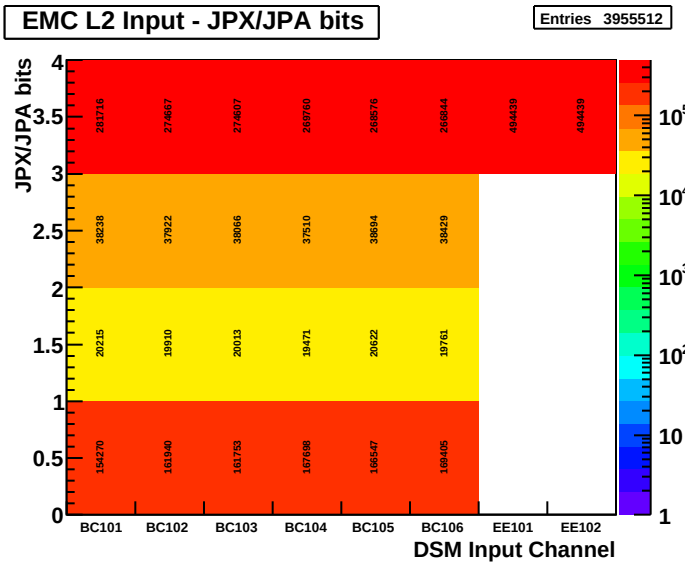

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

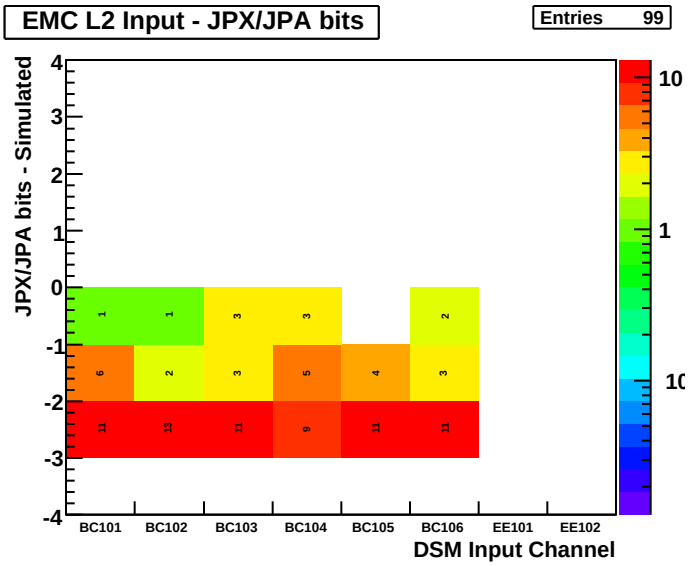

Endcap EMC L1 Input - High Eta Sum
Entries 5933268
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

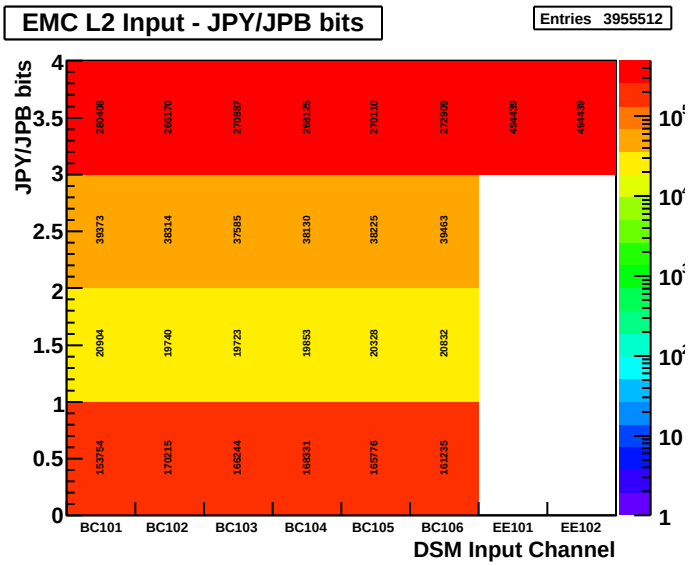

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

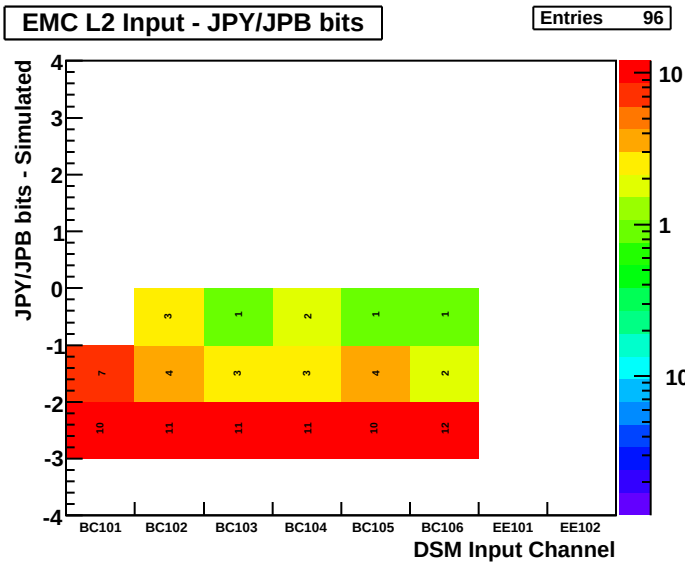

Endcap EMC L1 Input - High Tower Bits
Entries 5933268
High Tower Bits
EE101
EE102
494439
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

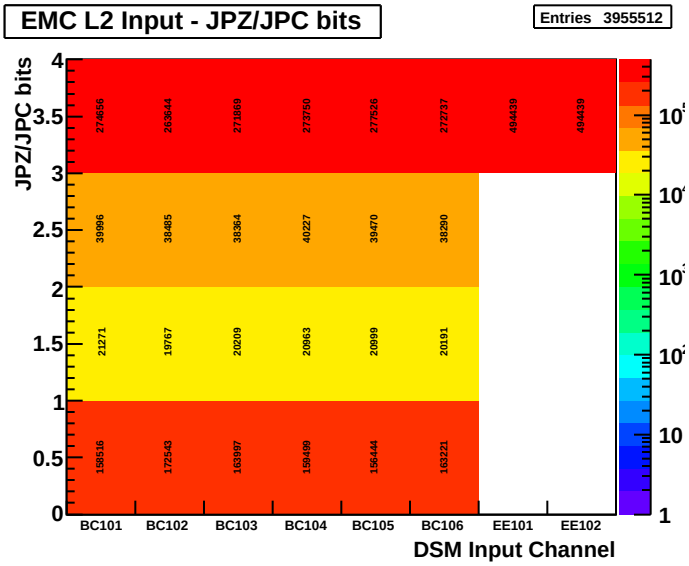

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

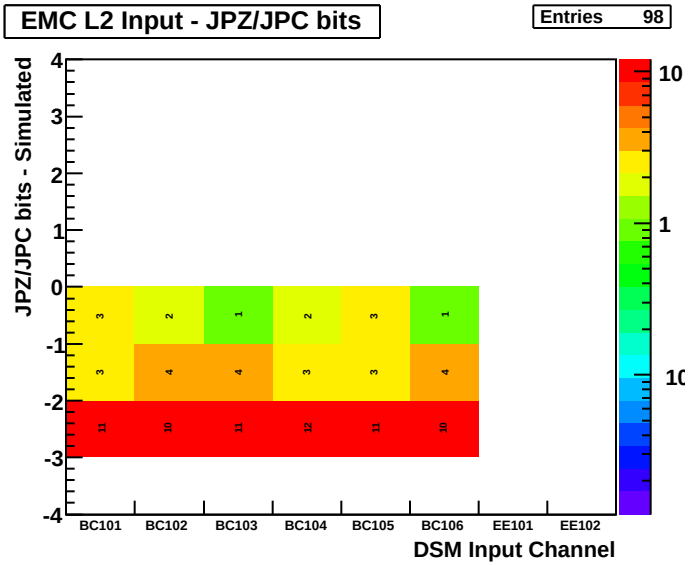

EMC L2 Input - JPX/JPA bits
Entries 3955512
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
281716 274667 274607 269760 268576 268844 494439 494439
38238 37922 38066 37510 38694 38429
20215 19910 20013 19471 20622 19761
154270 161940 161753 167698 166547 169405

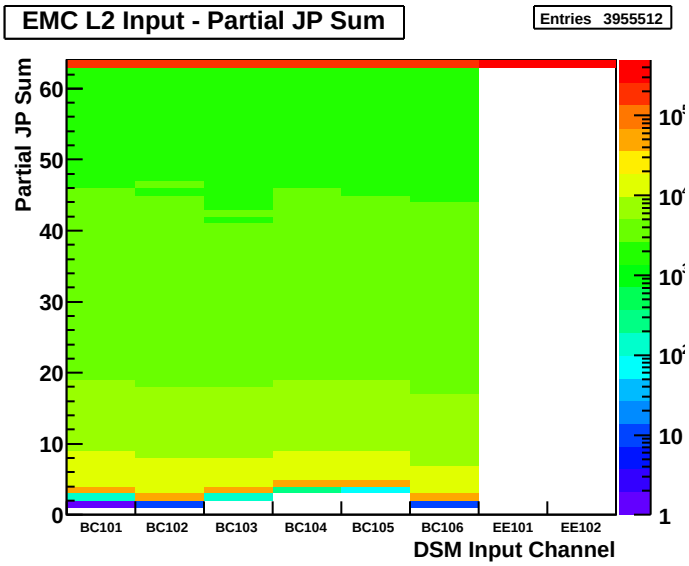

EMC L2 Input - JPX/JPA bits
Entries 99
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1 1 3 3 2
6 2 3 5 4 3
11 13 11 9 11 11

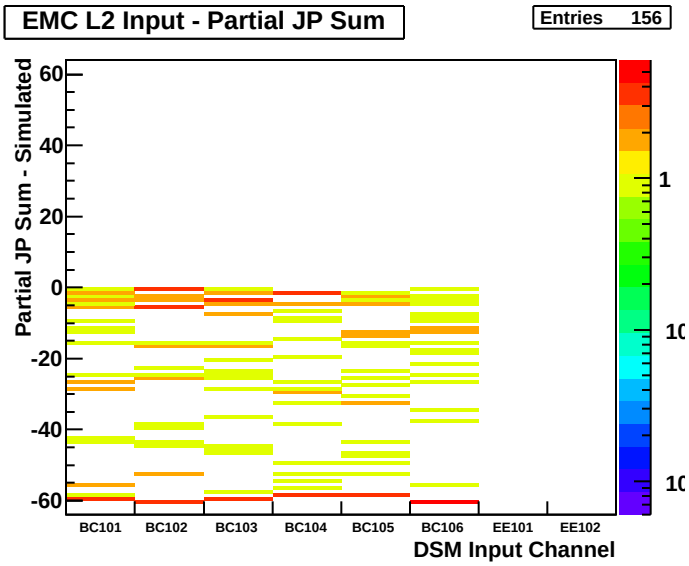

EMC L2 Input - JPY/JPB bits
Entries 3955512
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
280408 266170 270887 268125 270110 272909 494439 494439
38373 38314 37585 38130 38225 39463
20904 19740 19723 19853 20328 20832
155754 170215 166244 168331 165776 161235

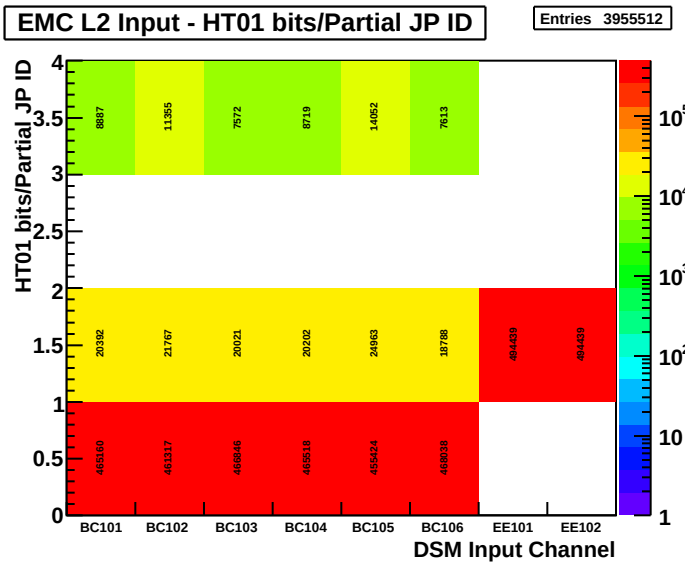

EMC L2 Input - JPY/JPB bits
Entries 96
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3 1 2 1 1
7 4 3 3 4 2
10 11 11 11 10 12

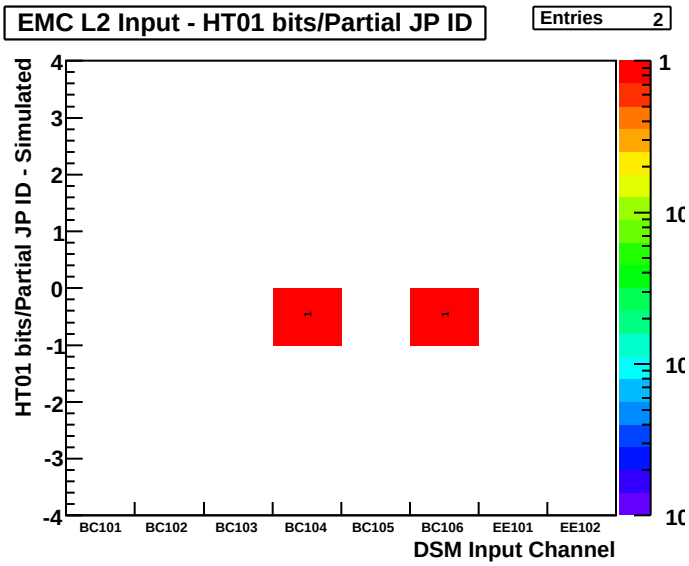

EMC L2 Input - JPZ/JPC bits
Entries 3955512
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
274656 263644 273869 273750 277526 272737 494439 494439
39996 38485 38364 40227 39470 38290
21271 19767 20209 20963 20999 20191
158516 172543 163997 159499 156444 163221

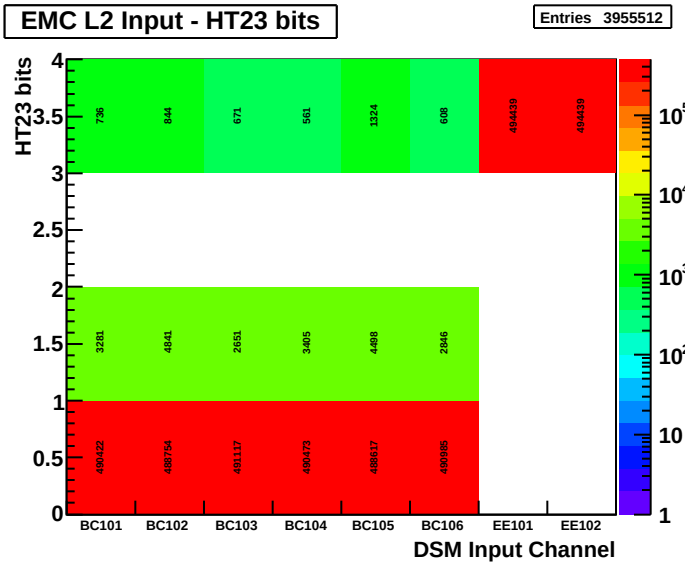

EMC L2 Input - JPZ/JPC bits
Entries 98
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3 2 1 2 3 1
3 4 4 3 3 4
11 10 11 12 11 10

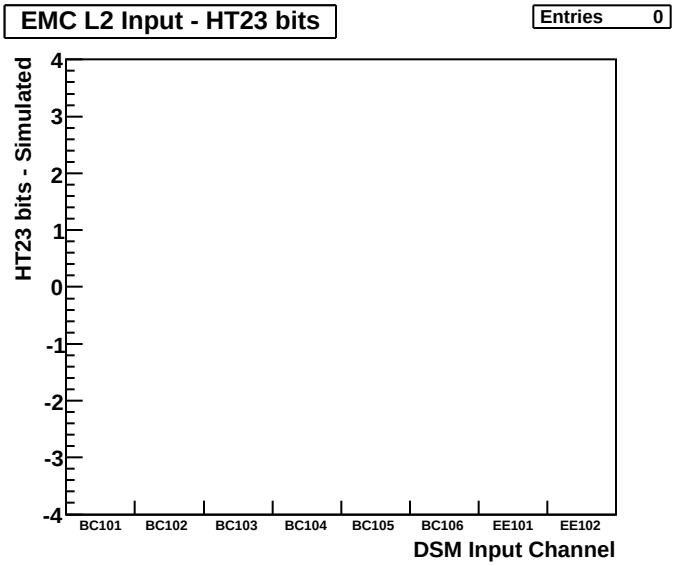

EMC L2 Input - Partial JP Sum
Entries 3955512
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

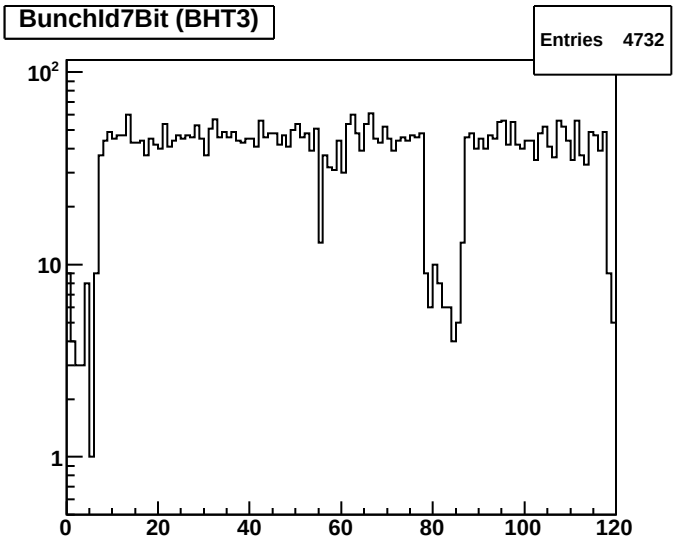

EMC L2 Input - Partial JP Sum
Entries 156
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

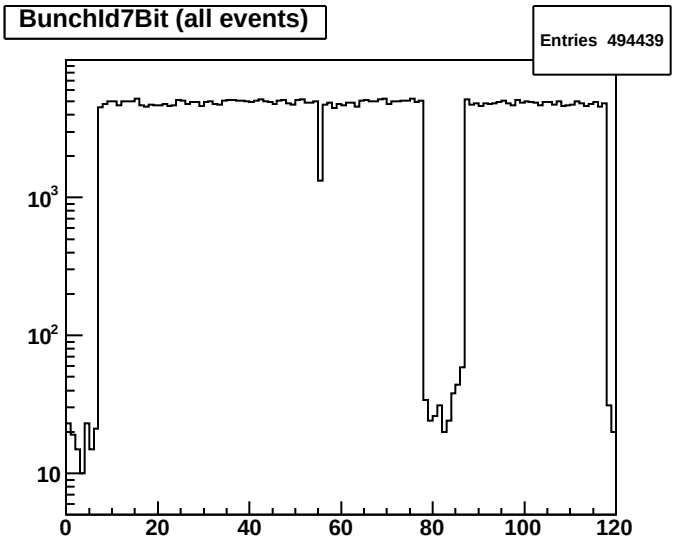

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3955512
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

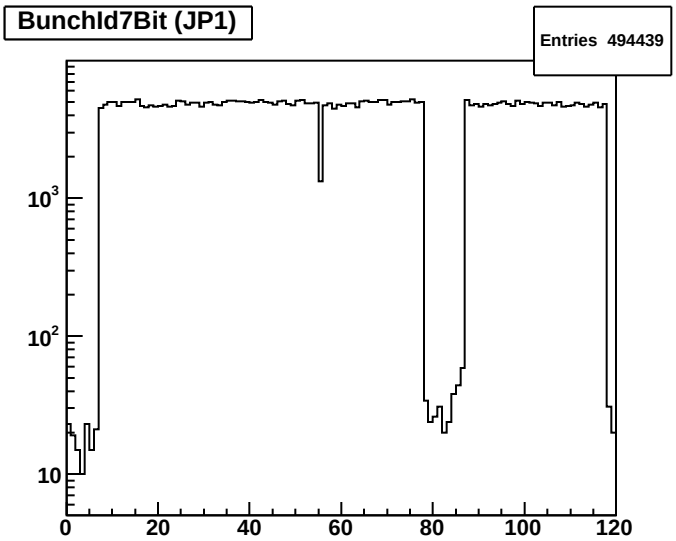

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

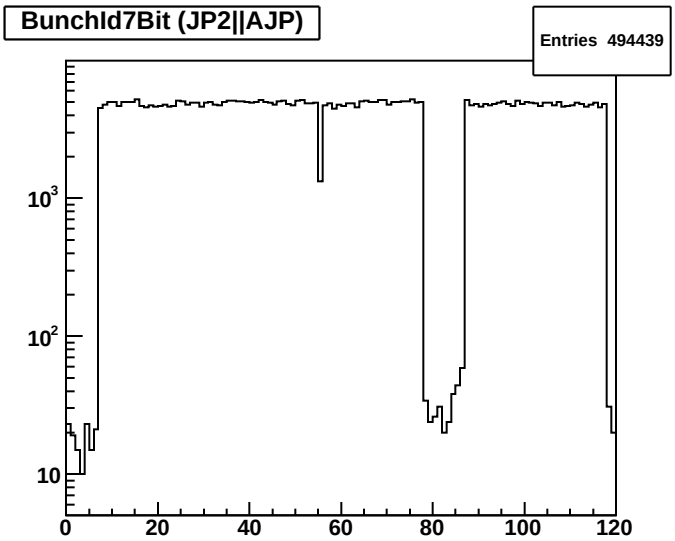

EMC L2 Input - HT23 bits
Entries 3955512
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

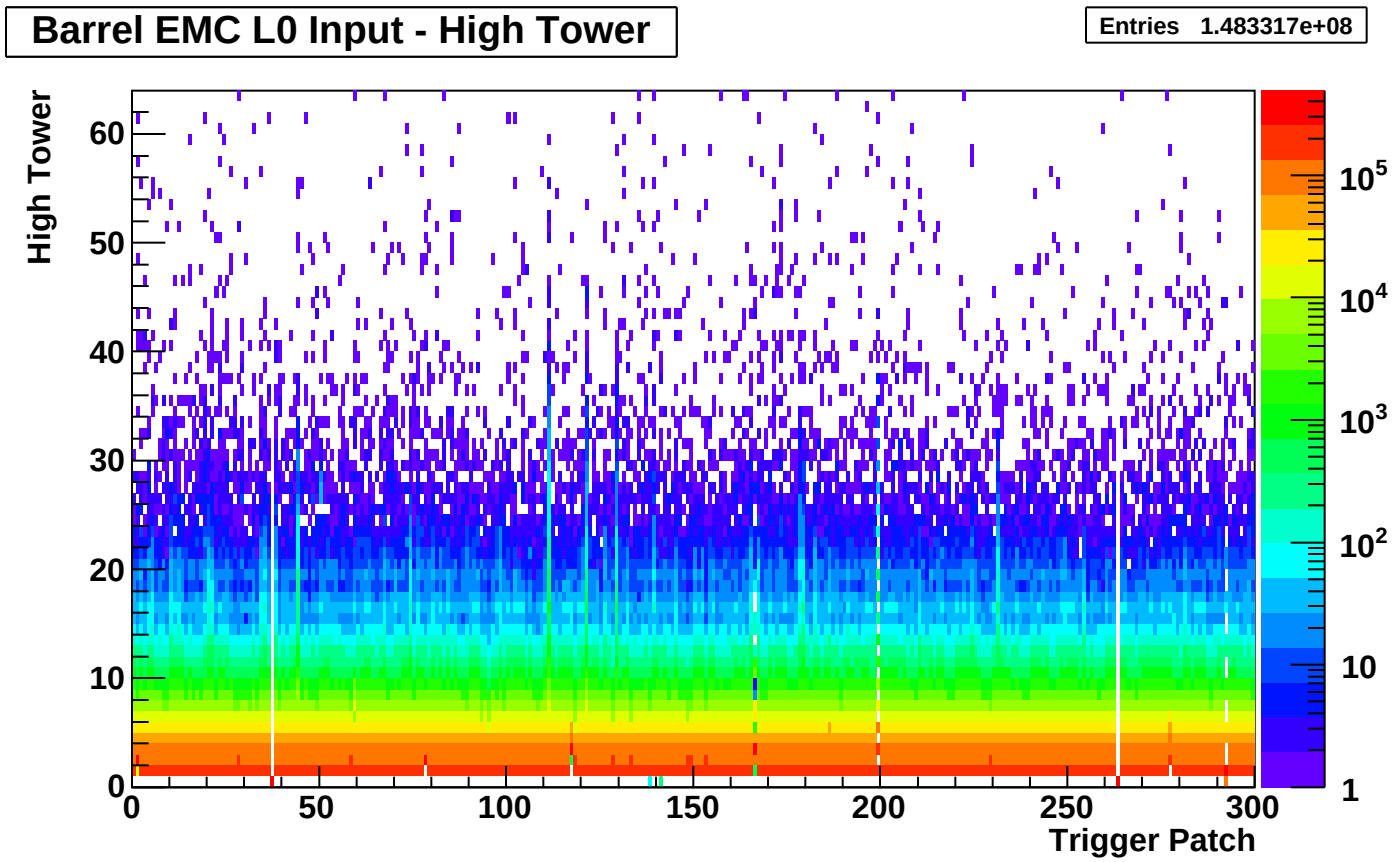

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

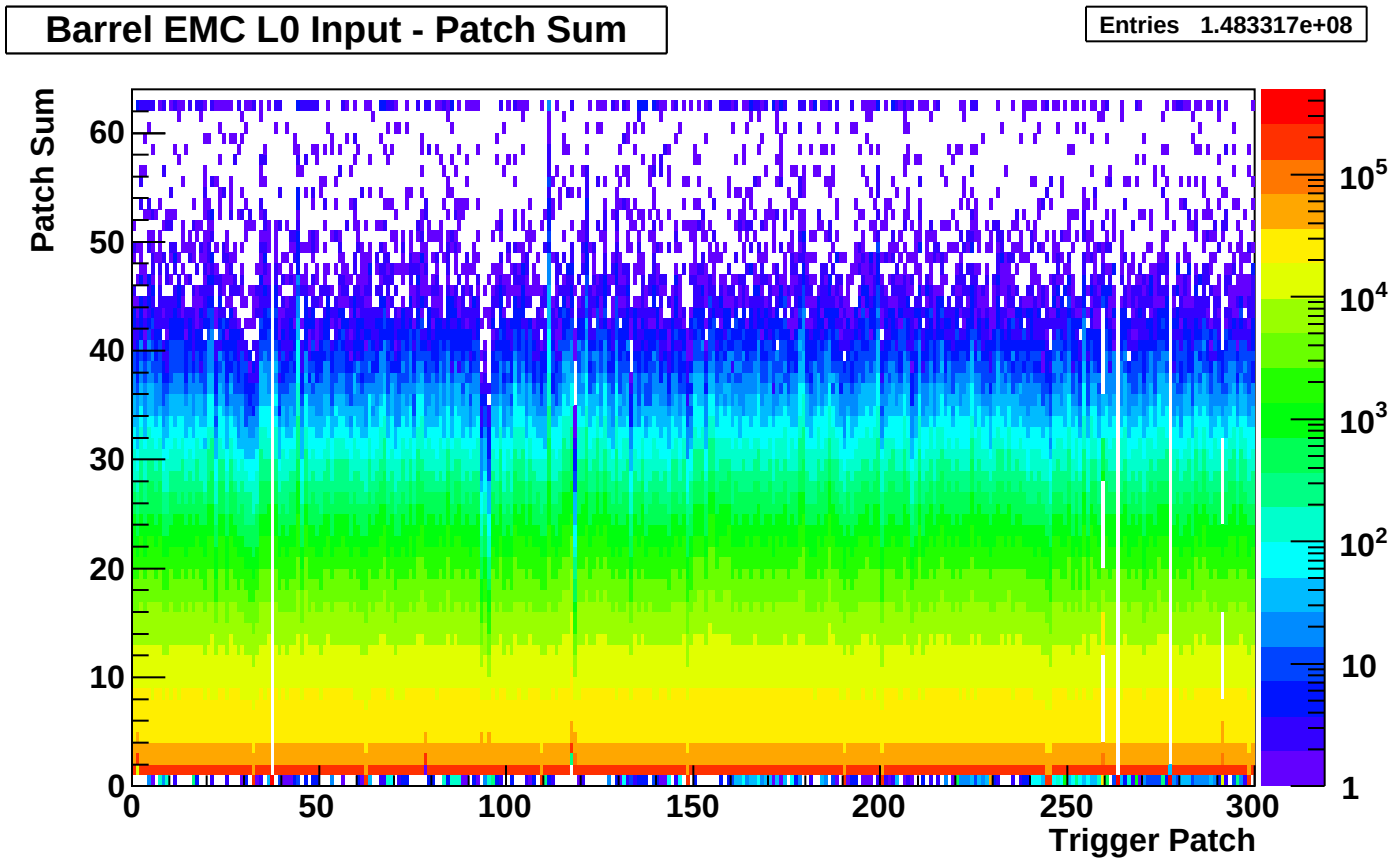

BunchId7Bit (BHT3)
Entries 4732
10^2
10
1
0
20
40
60
80
100
120

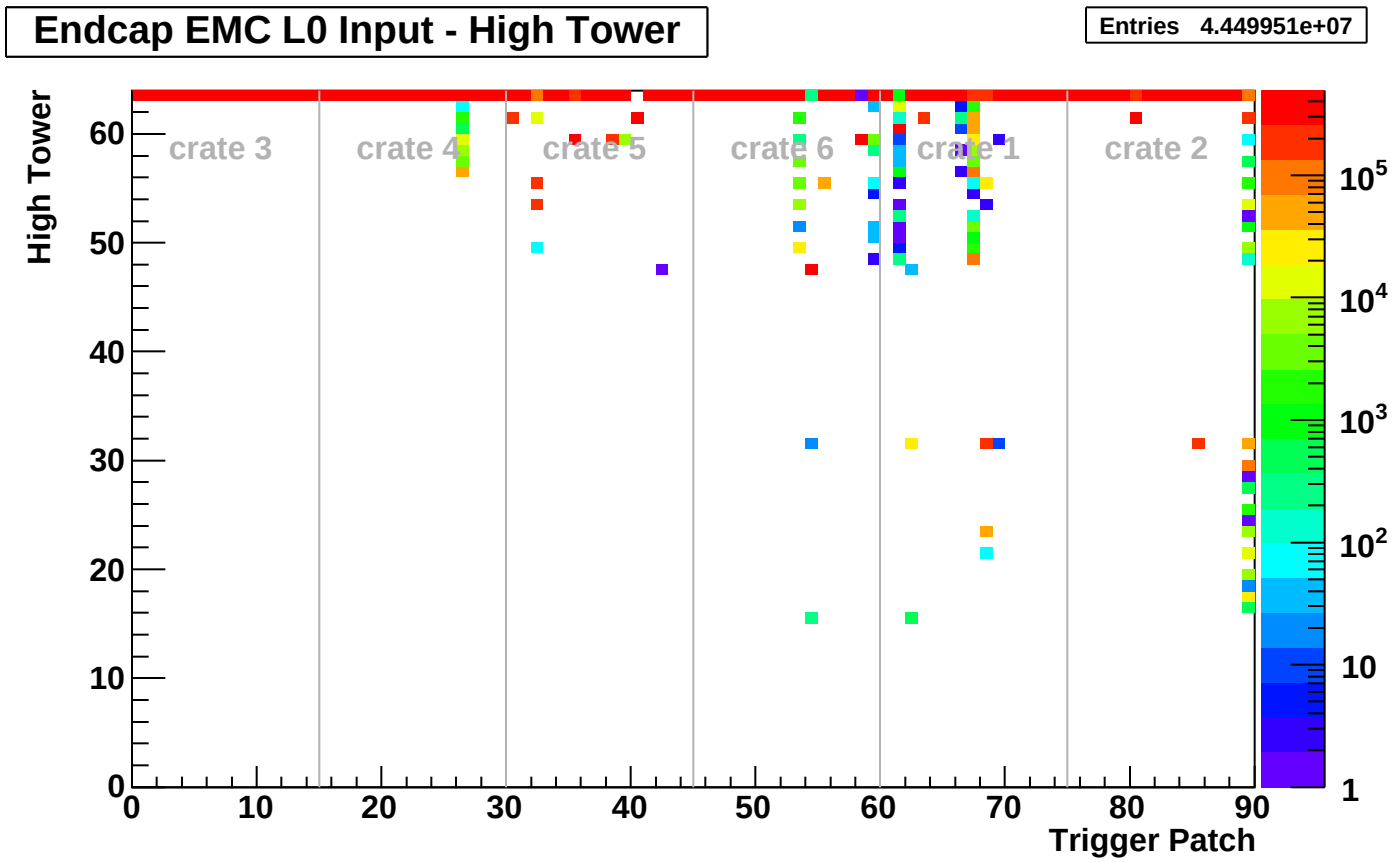

BunchId7Bit (all events)
Entries 494439
10^3
10^2
10
0
20
40
60
80
100
120

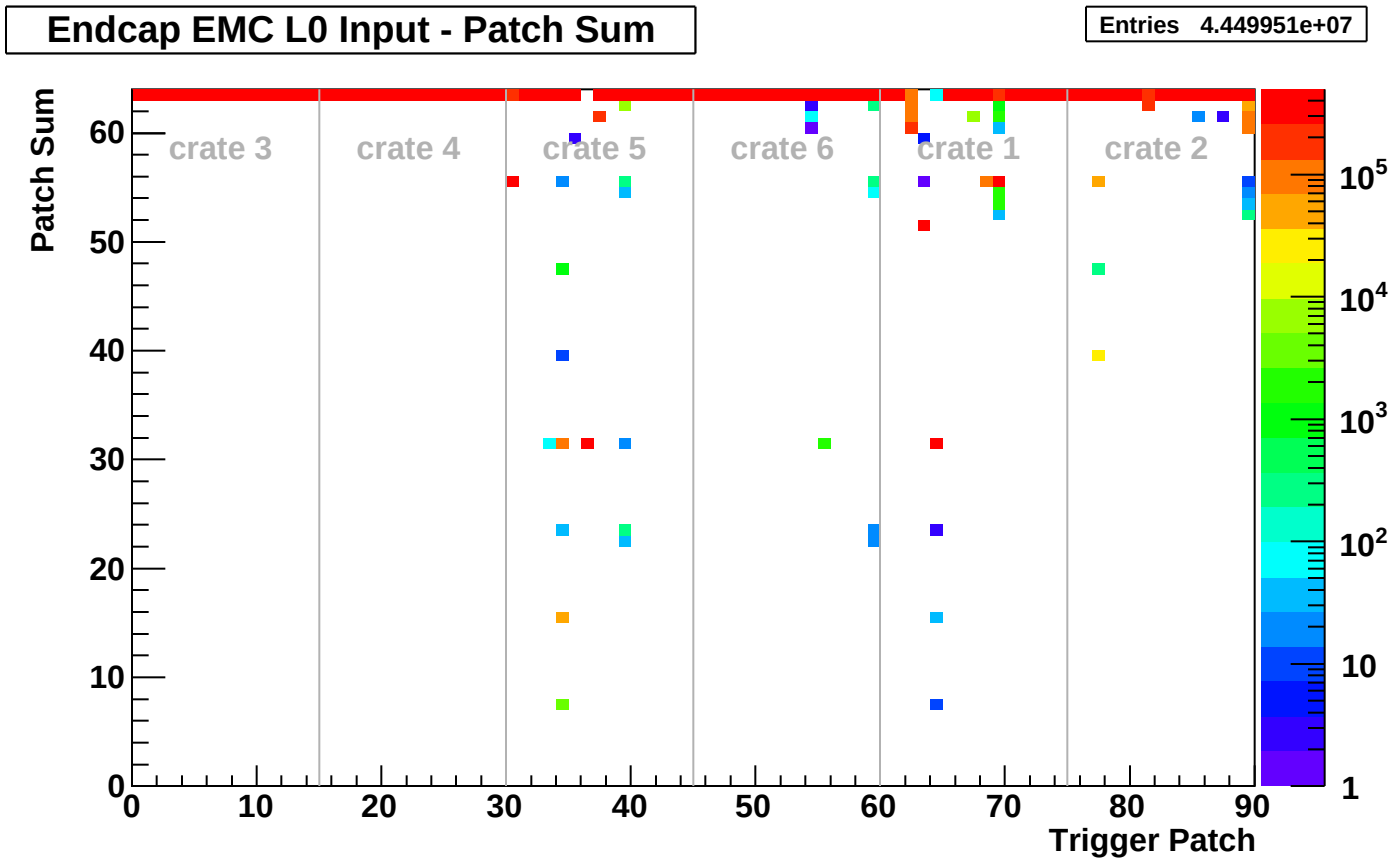

BunchId7Bit (JP1)
Entries 494439
10^3
10^2
10
0
20
40
60
80
100
120

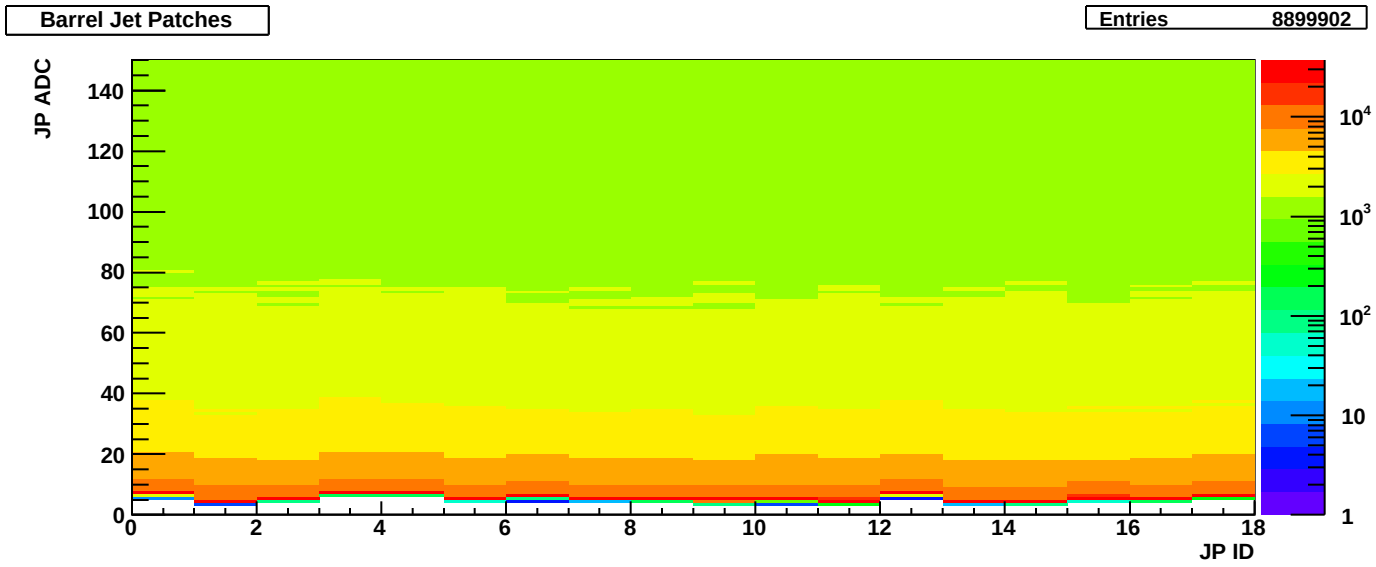

BunchId7Bit (JP2||AJP)
Entries 494439
10^3
10^2
10
0
20
40
60
80
100
120

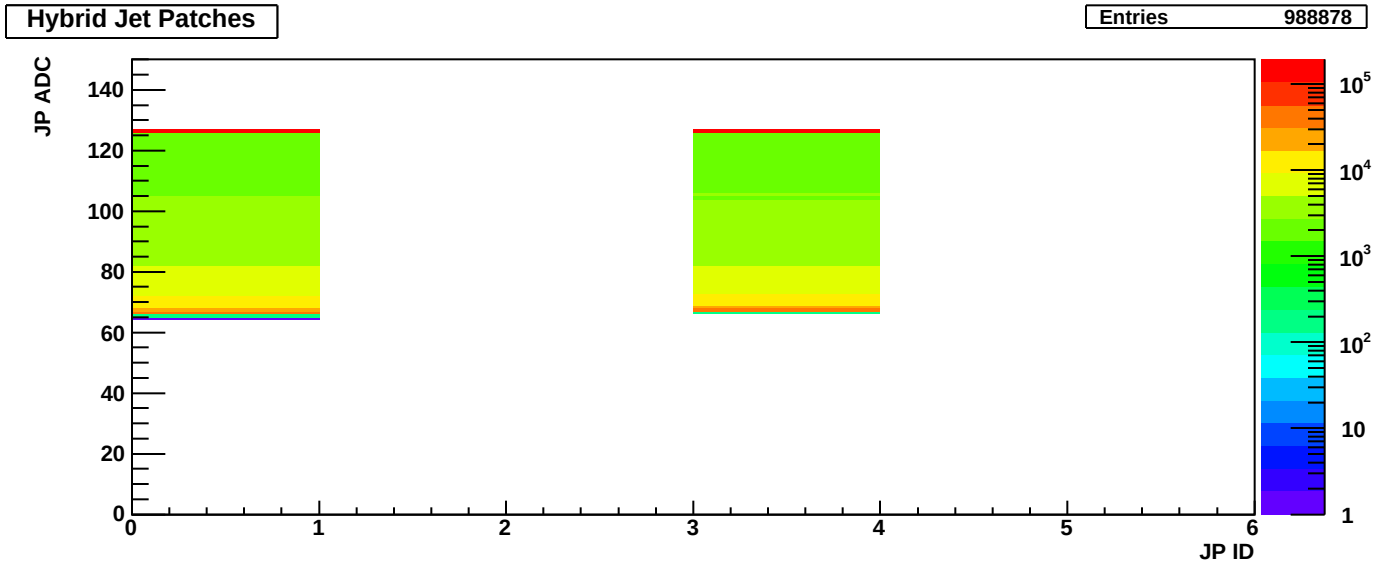

Barrel EMC L0 Input - High Tower
Entries 1.483317e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

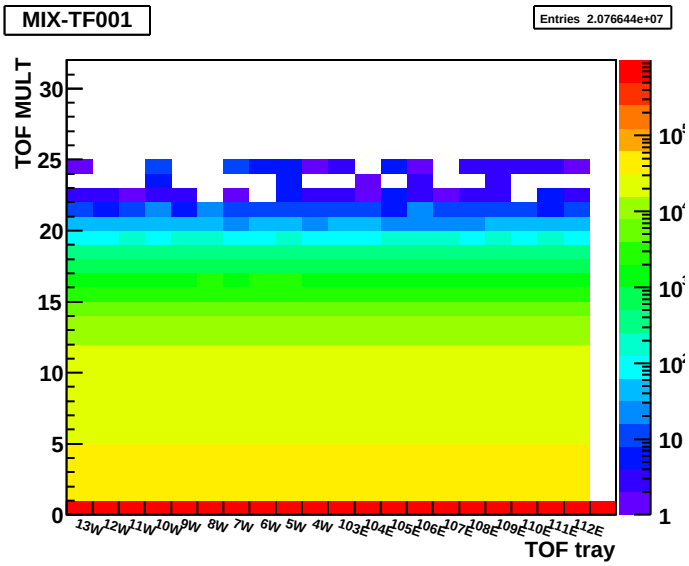

Barrel EMC L0 Input - Patch Sum
Entries 1.483317e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

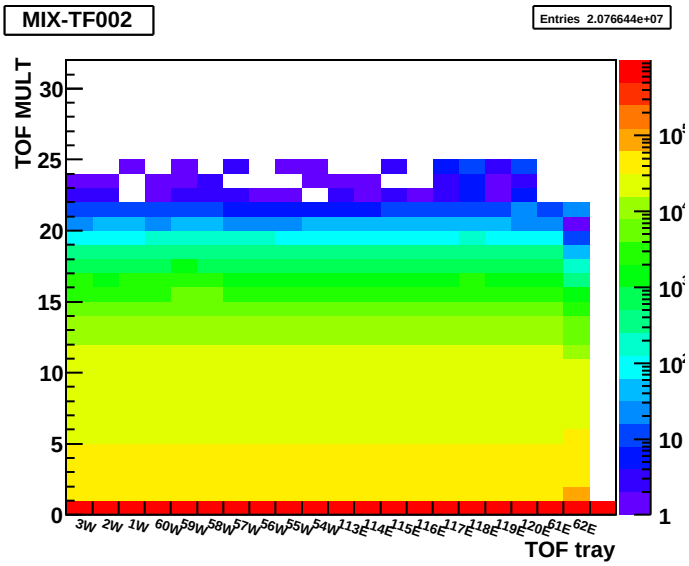

Endcap EMC L0 Input - High Tower
Entries 4.449951e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

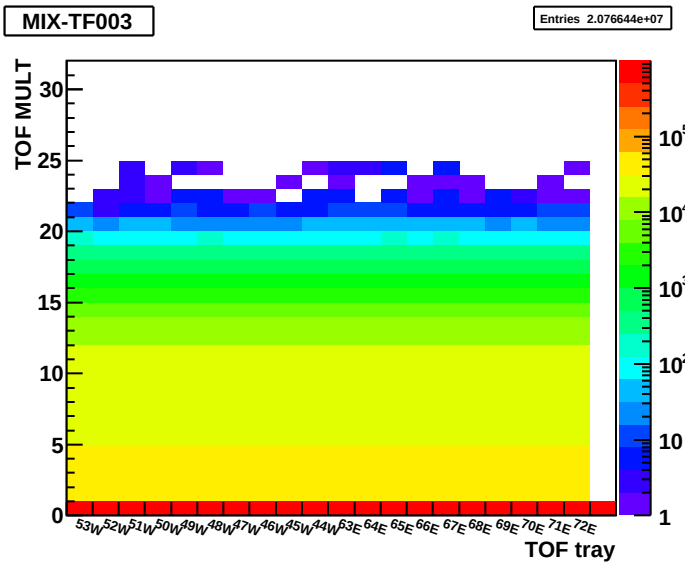

Endcap EMC L0 Input - Patch Sum
Entries 4.449951e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

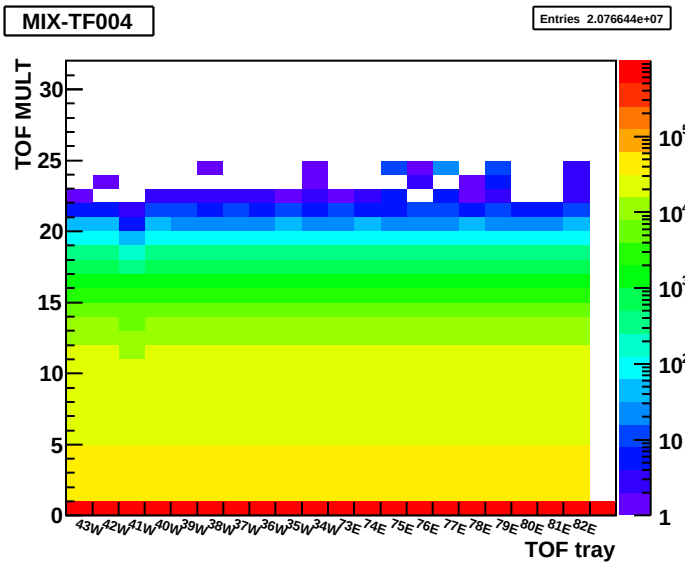

Barrel Jet Patches
Entries 8899902
JP ADC
JP ID

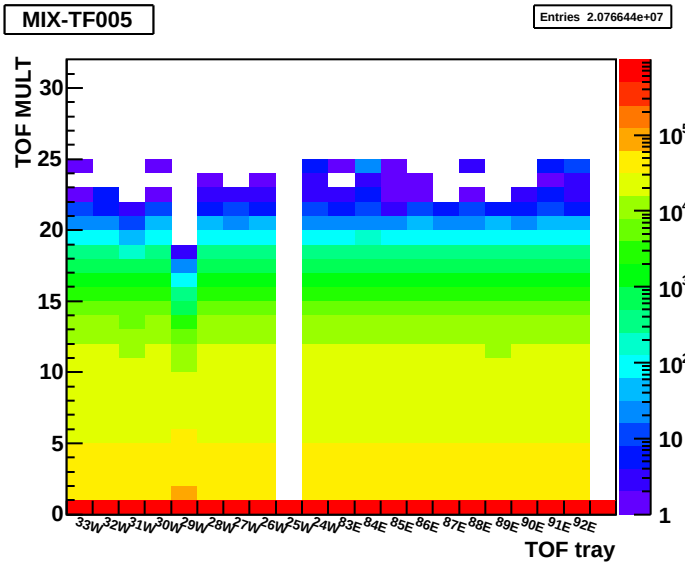

Hybrid Jet Patches
Entries 988878
JP ADC
JP ID

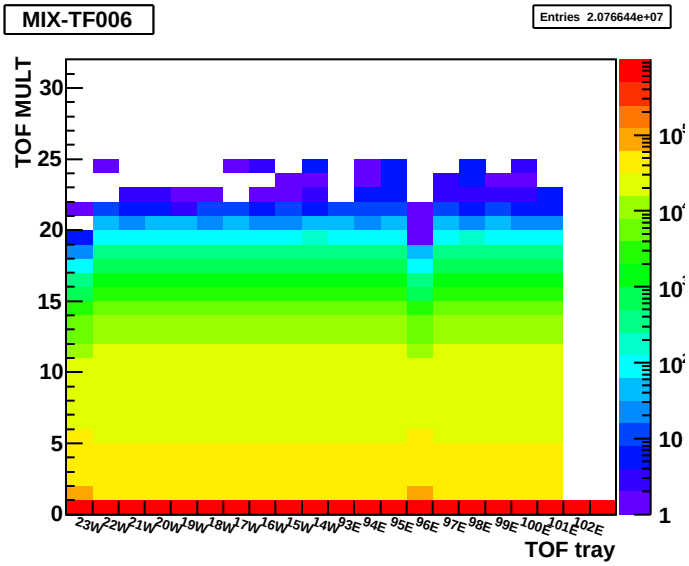

MIX-TF001
Entries 2.076644e+07
TOF MULT
TOF tray

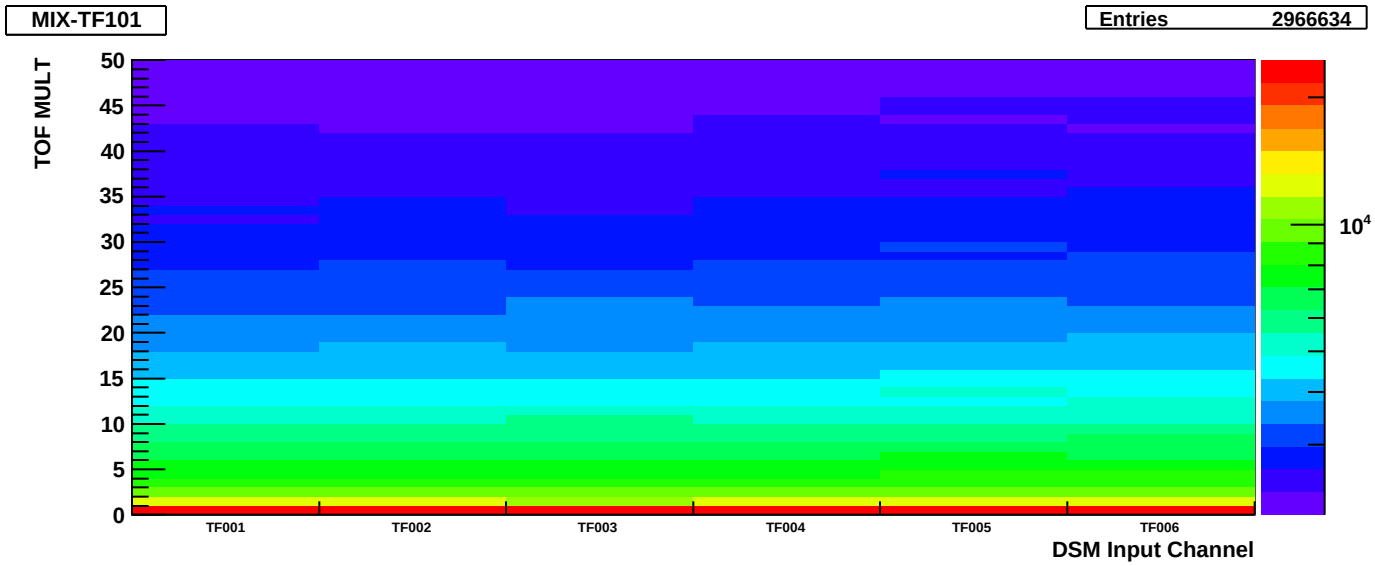

MIX-TF002
Entries 2.076644e+07
TOF MULT
TOF tray

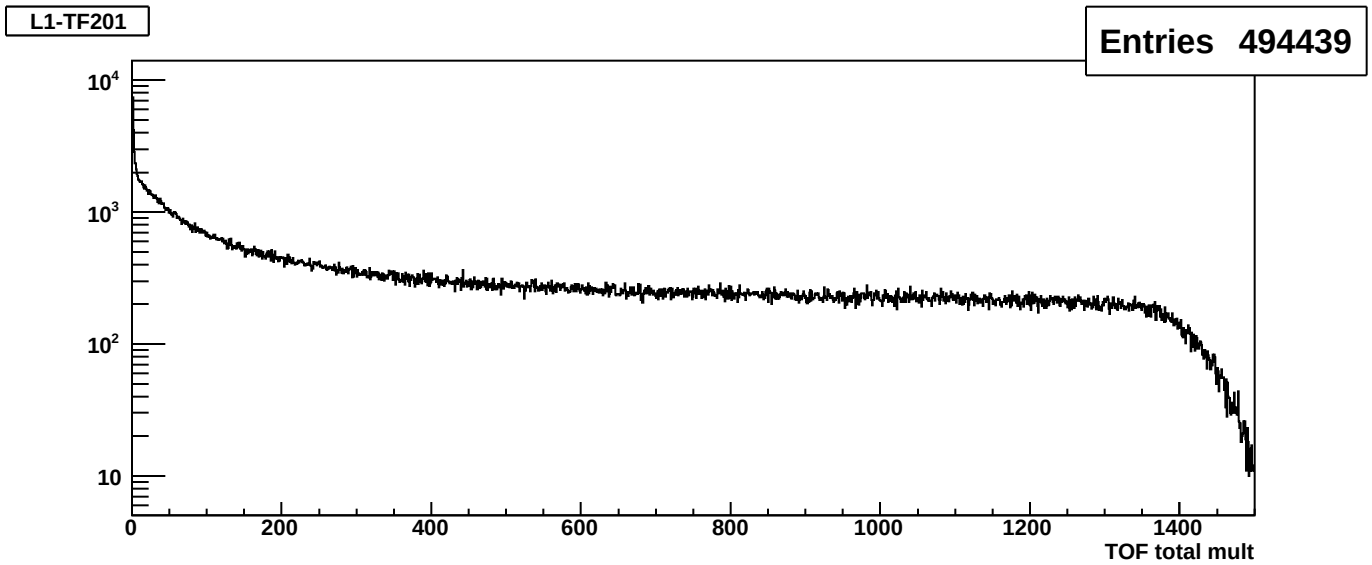

MIX-TF003
Entries 2.076644e+07
TOF MULT
TOF tray

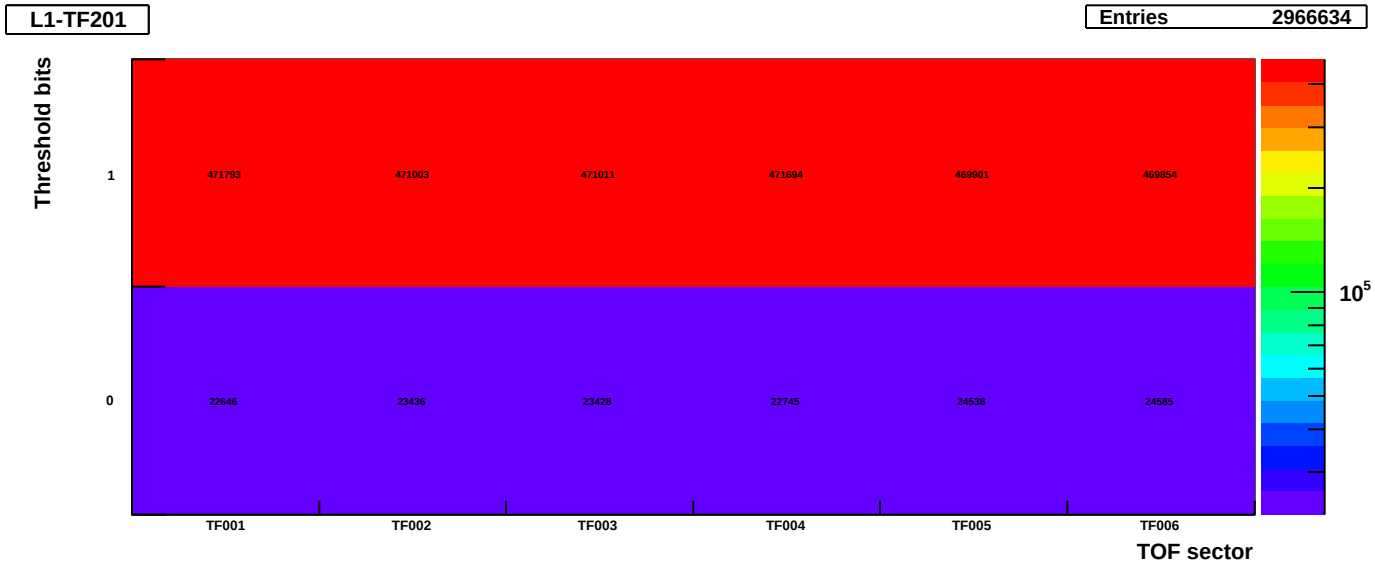

MIX-TF004
Entries 2.076644e+07
TOF MULT
TOF tray

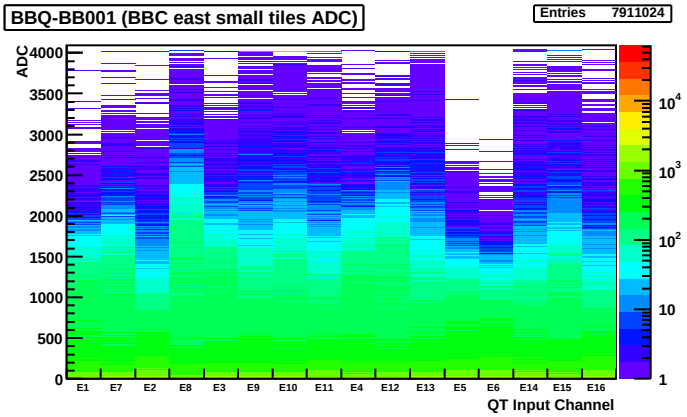

MIX-TF005
Entries 2.076644e+07
TOF MULT
TOF tray

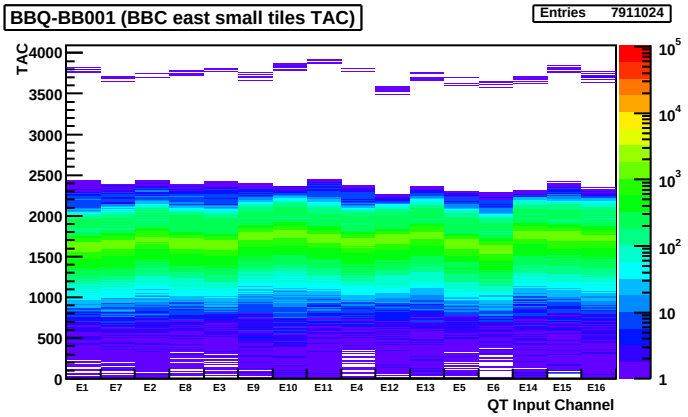

MIX-TF006
Entries 2.076644e+07
TOF MULT
TOF tray

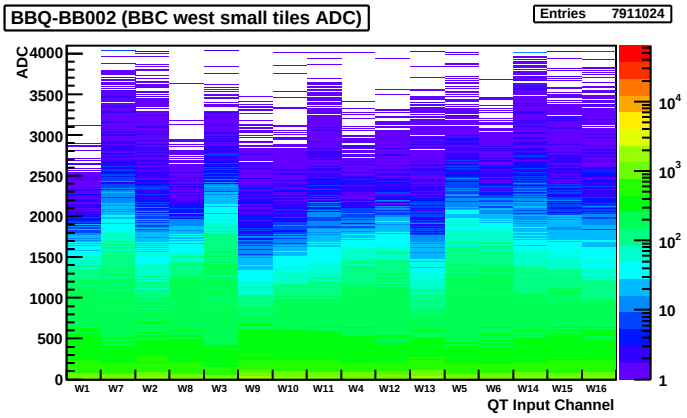

MIX-TF101
Entries 2966634
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

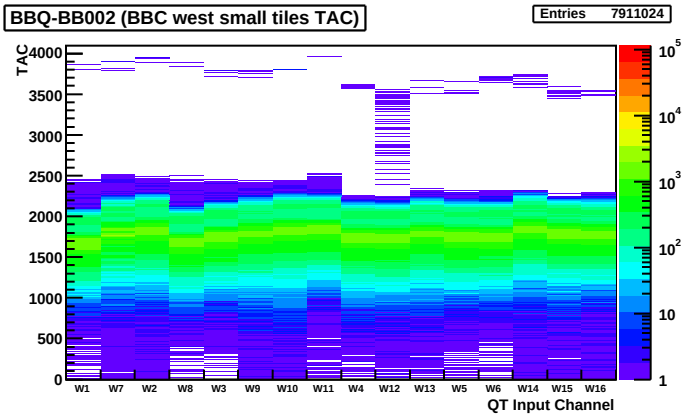

L1-TF201
Entries 494439
10^4
10^3
10^2
10
0
200
400
600
800
1000
1200
1400
TOF total mult

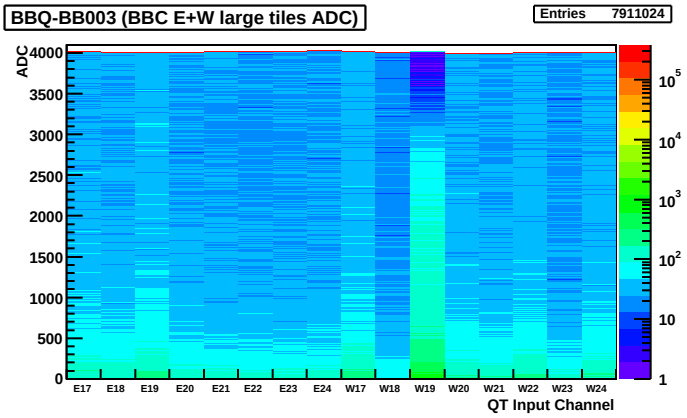

L1-TF201
Entries 2966634
Threshold bits
1
0
471793
471003
471011
471694
469901
469854
22646
23436
23428
22745
24538
24585
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

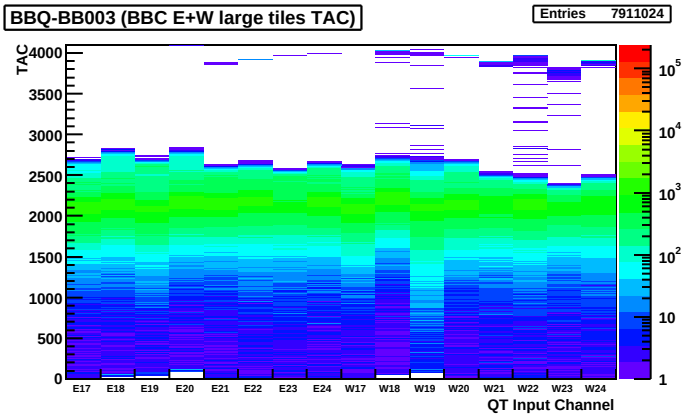

BBQ-BB001 (BBC east small tiles ADC)
Entries 7911024
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

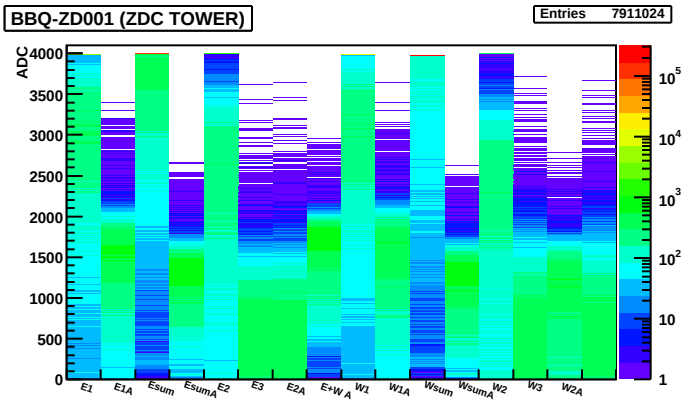

BBQ-BB001 (BBC east small tiles TAC)
Entries 7911024
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

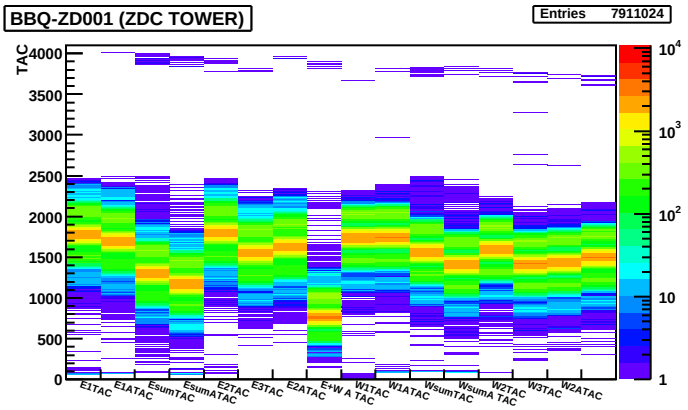

BBQ-BB002 (BBC west small tiles ADC)
Entries 7911024
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

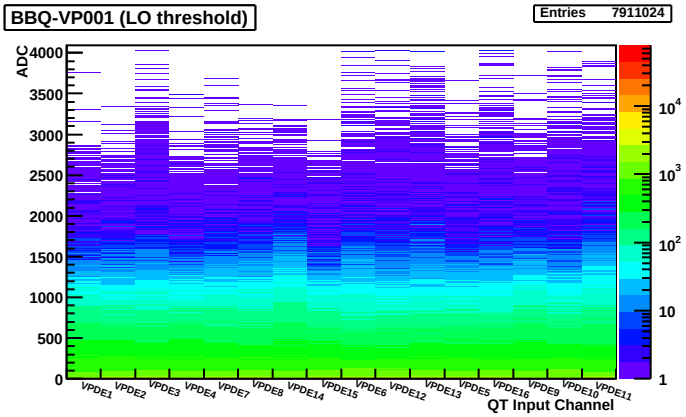

BBQ-BB002 (BBC west small tiles TAC)
Entries 7911024
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

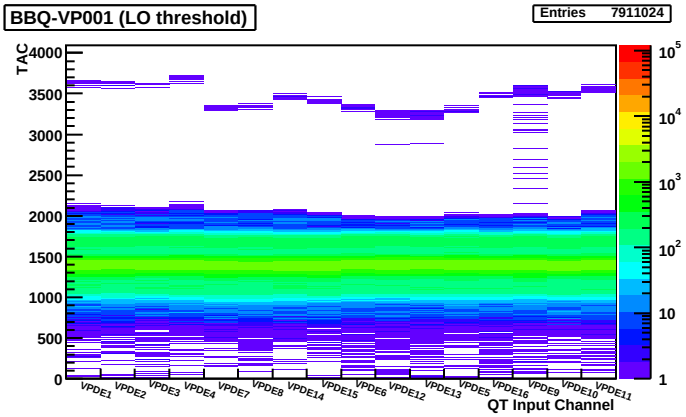

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 7911024
ADC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

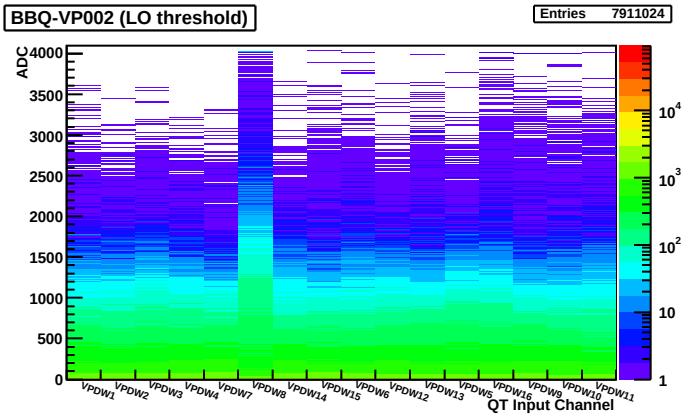

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 7911024
TAC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

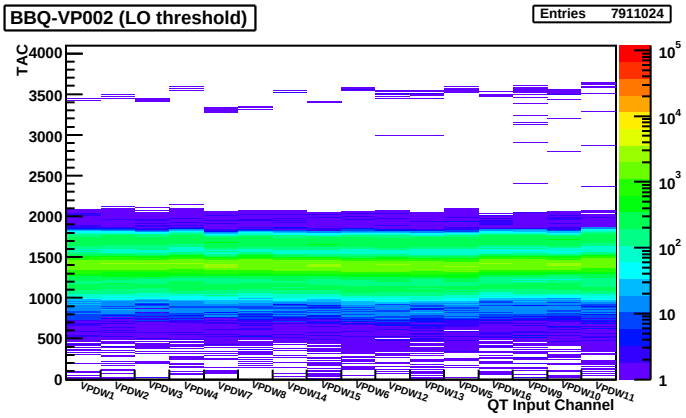

BBQ-ZD001 (ZDC TOWER)
Entries 7911024
ADC
E1 E1A Esum EsumA E2 E3 E2A E+W A W1 W1A Wsum WsumA W2 W3 W2A

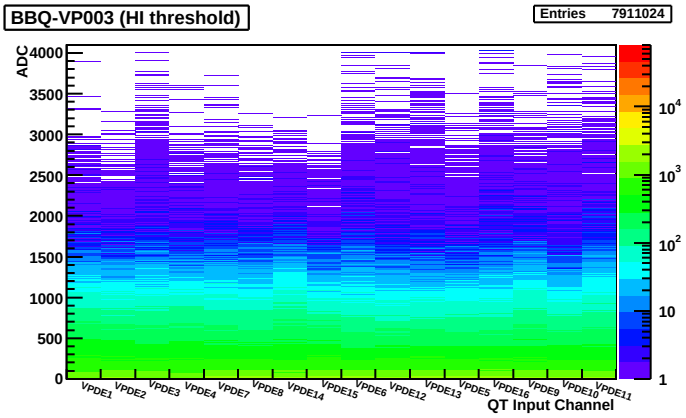

BBQ-ZD001 (ZDC TOWER)
Entries 7911024
TAC
E1TAC E1ATAC EsumTAC EsumATAC E2TAC E3TAC E2ATAC E+W A TAC W1TAC W1ATAC WsumTAC WsumA TAC W2TAC W3TAC W2ATAC

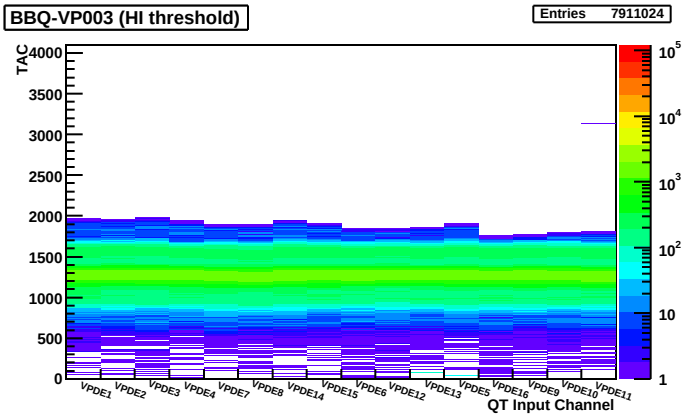

BBQ-VP001 (LO threshold)
Entries 7911024
ADC
QT Input Channel

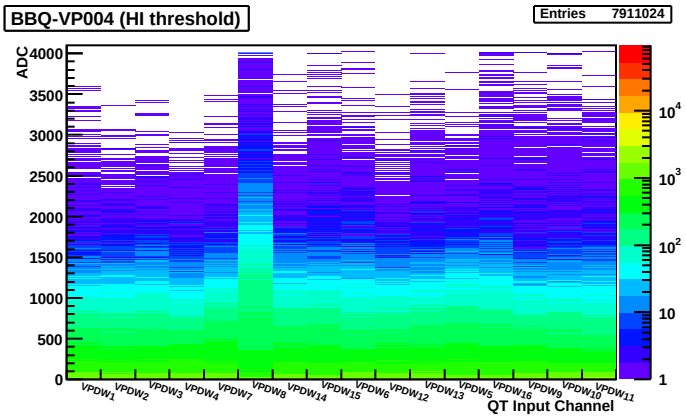

BBQ-VP001 (LO threshold)
Entries 7911024
TAC
QT Input Channel

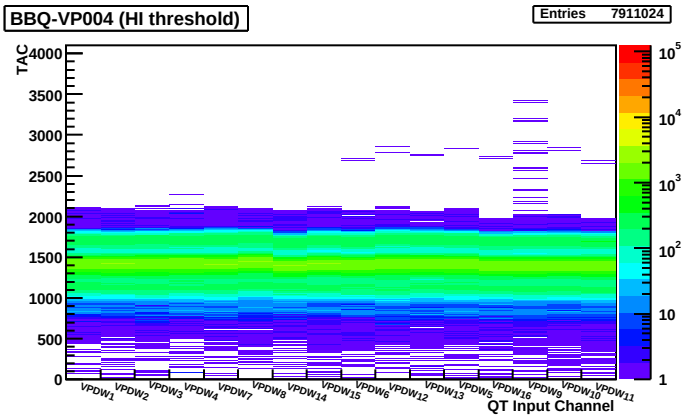

BBQ-VP002 (LO threshold)
Entries 7911024
ADC
QT Input Channel

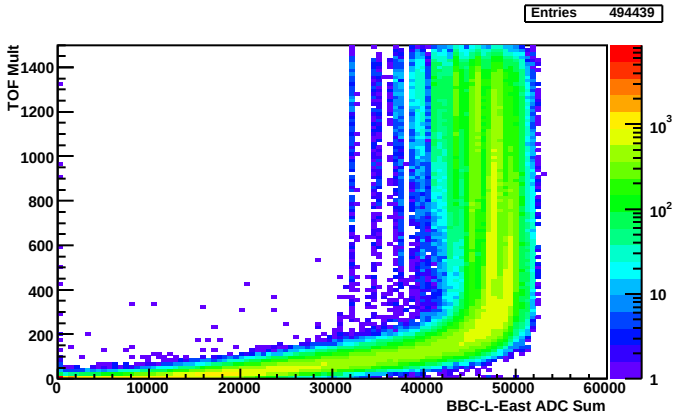

BBQ-VP002 (LO threshold)
Entries 7911024
TAC
QT Input Channel

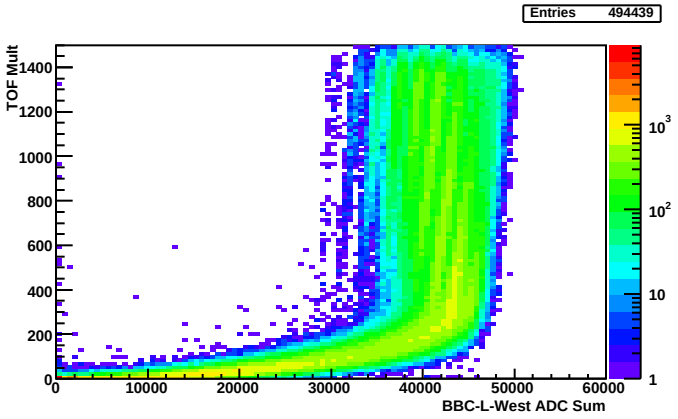

BBQ-VP003 (HI threshold)
Entries 7911024
ADC
QT Input Channel

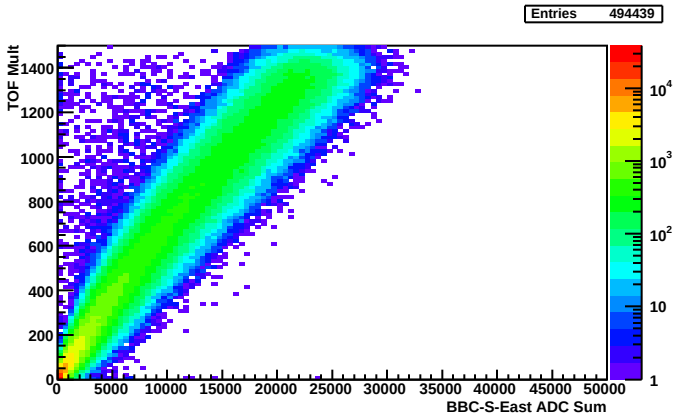

BBQ-VP003 (HI threshold)
Entries 7911024
TAC
QT Input Channel

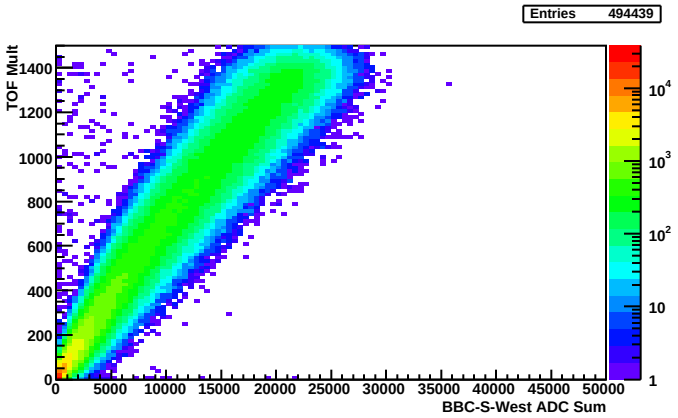

BBQ-VP004 (HI threshold)
Entries 7911024
ADC
QT Input Channel

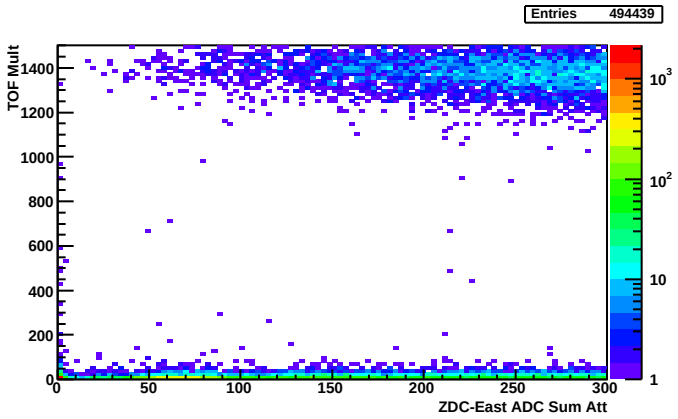

BBQ-VP004 (HI threshold)
Entries 7911024
TAC
QT Input Channel

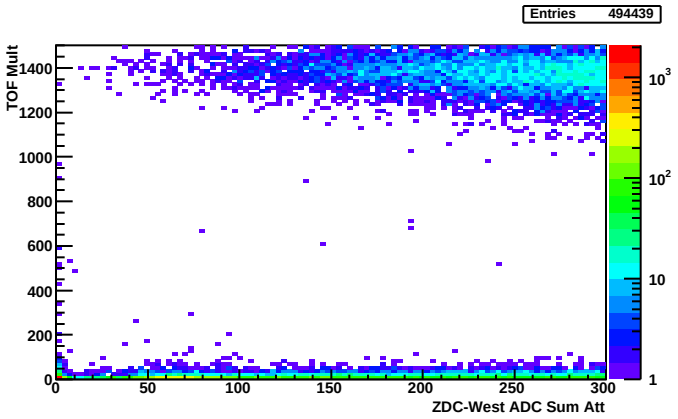

Entries 494439
TOF Mult
BBC-L-East ADC Sum

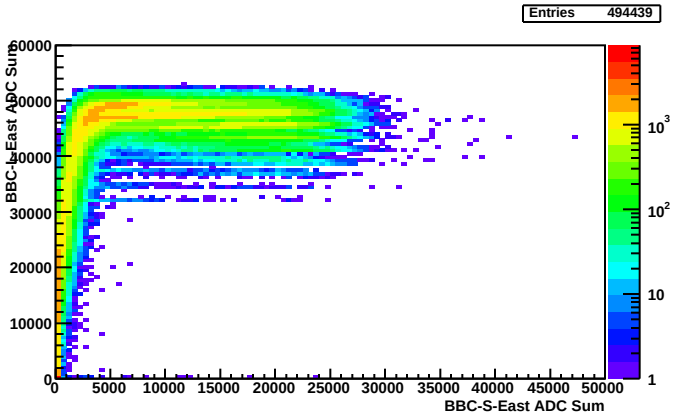

Entries 494439
TOF Mult
BBC-L-West ADC Sum

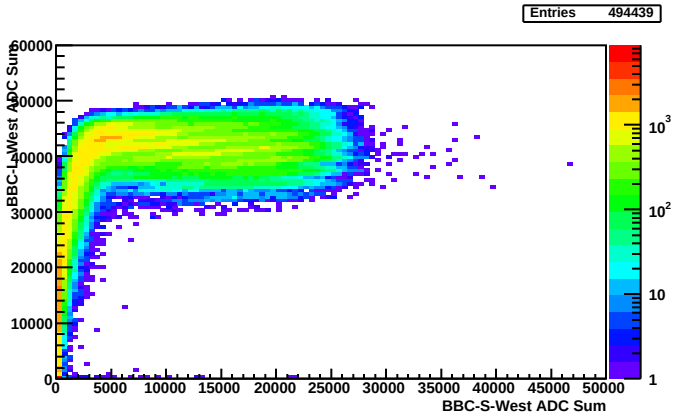

Entries 494439
TOF Mult
BBC-S-East ADC Sum

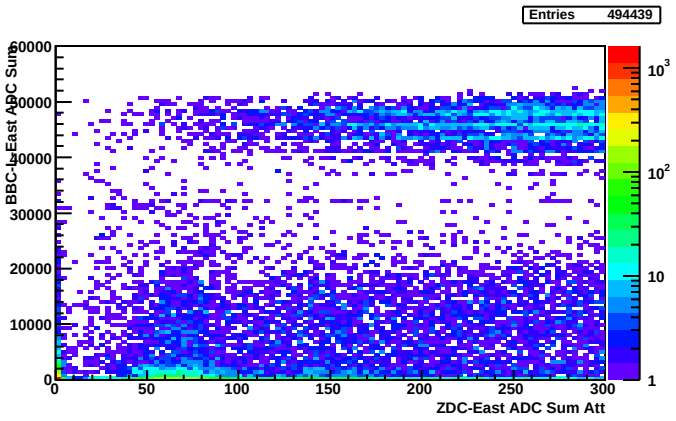

Entries 494439
TOF Mult
BBC-S-West ADC Sum

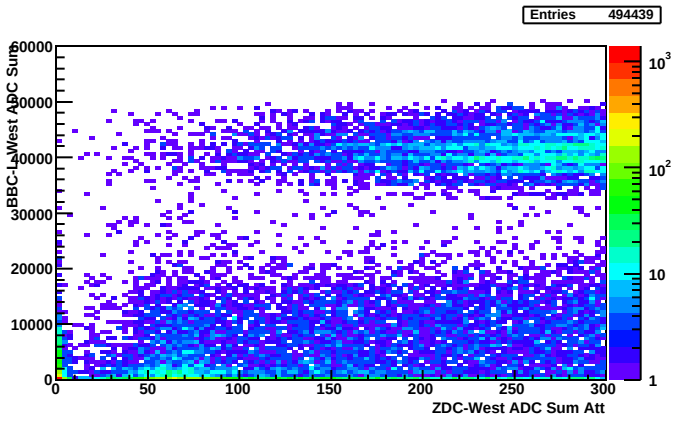

Entries 494439
TOF Mult
ZDC-East ADC Sum Att

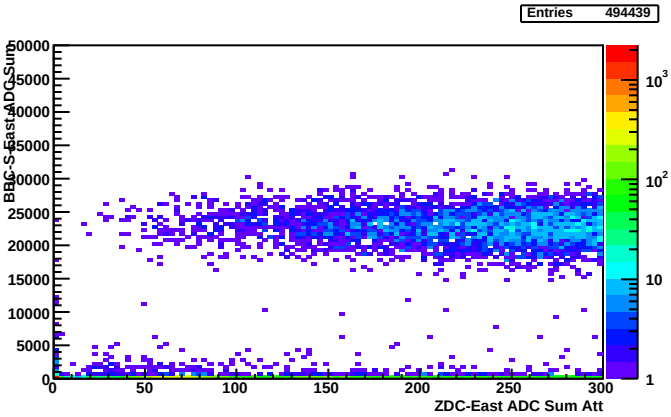

Entries 494439
TOF Mult
ZDC-West ADC Sum Att

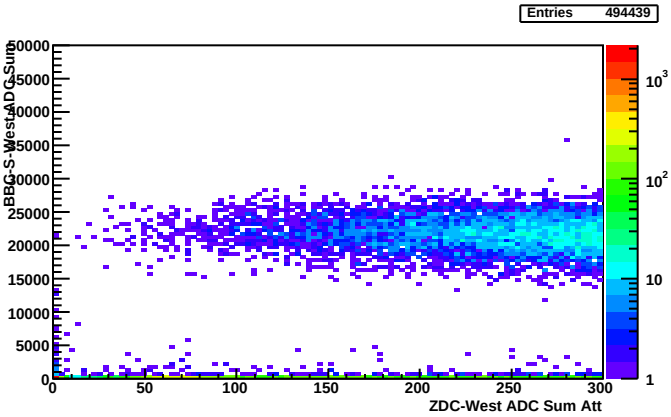

Entries 494439
BBC-L-East ADC Sum
BBC-S-East ADC Sum

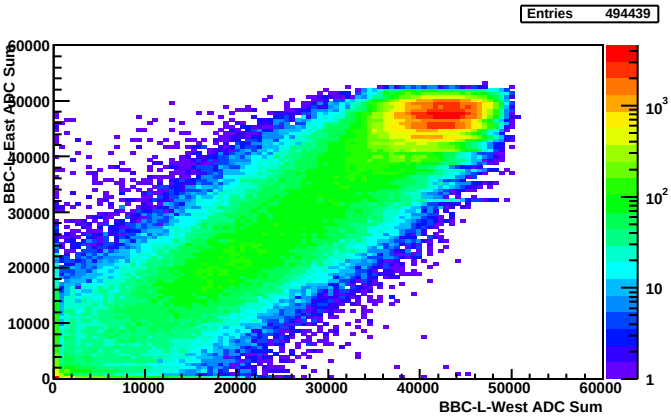

Entries 494439
BBC-L-West ADC Sum
BBC-S-West ADC Sum

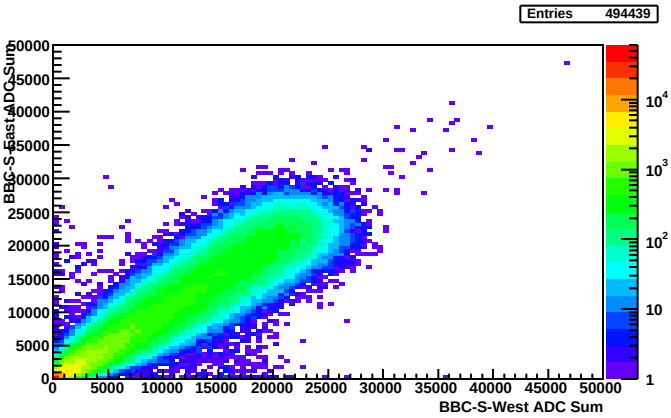

Entries 494439
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

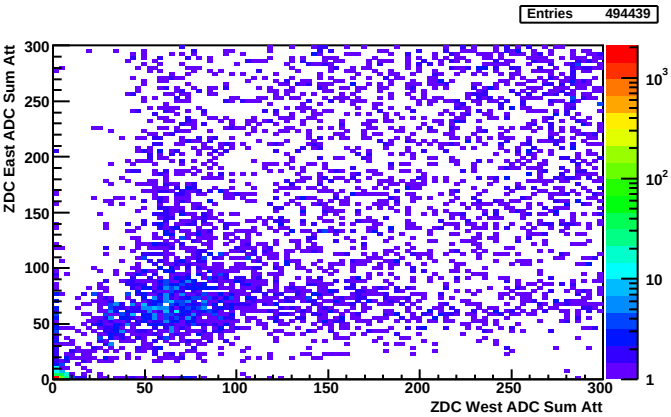

Entries 494439
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

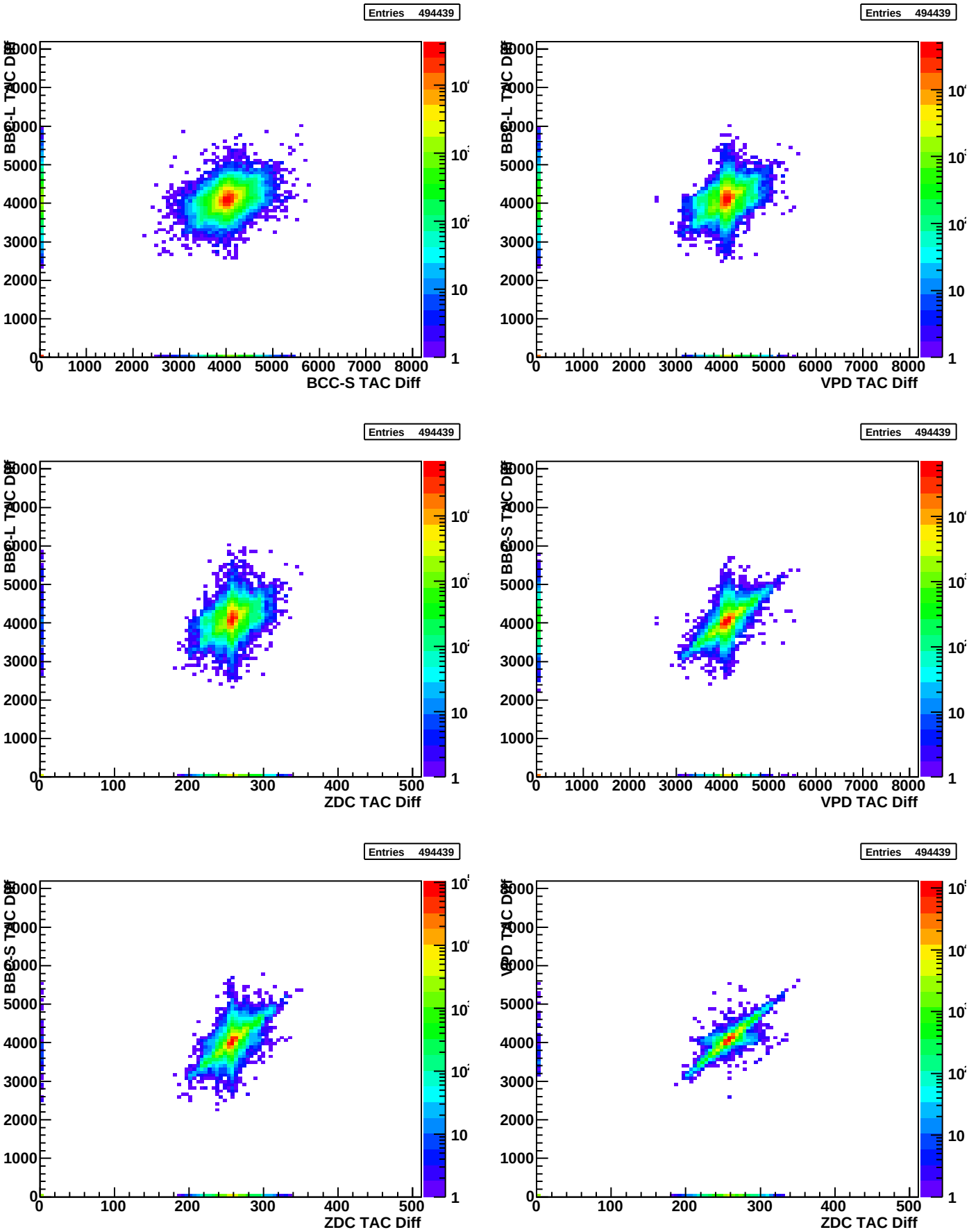

Entries 494439
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

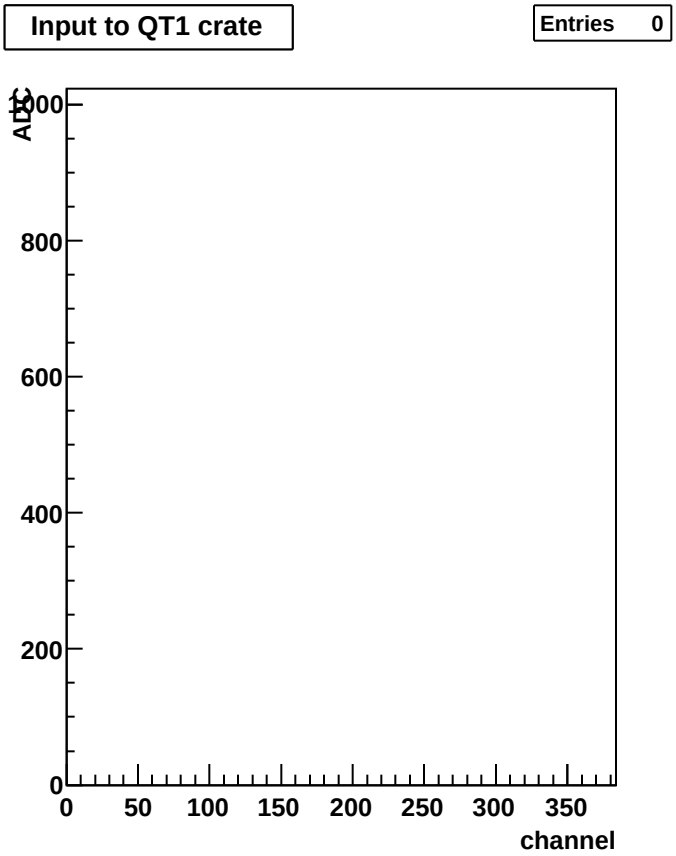

Entries 494439
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

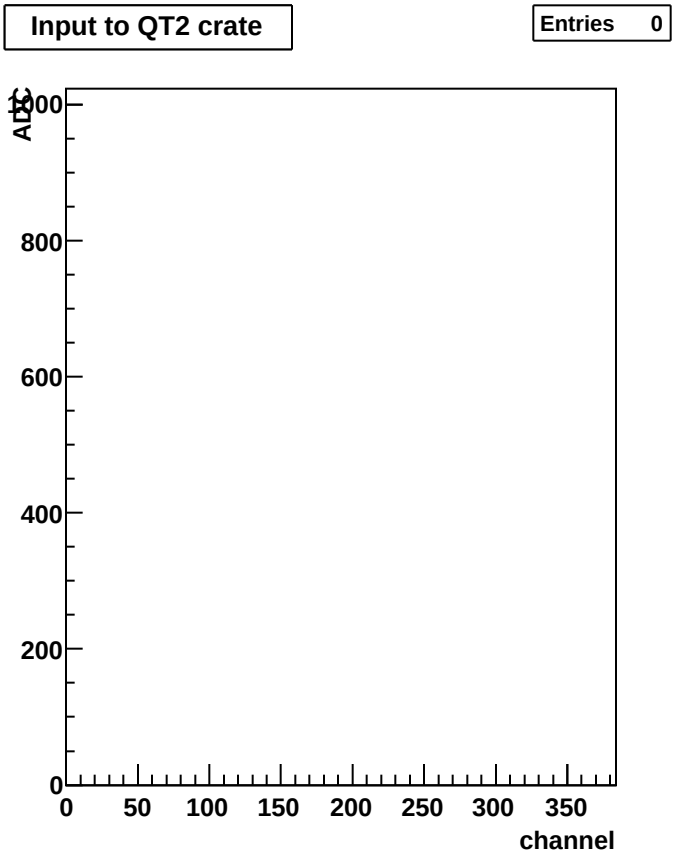

Entries 494439
BBC-L-East ADC Sum
BBC-L-West ADC Sum

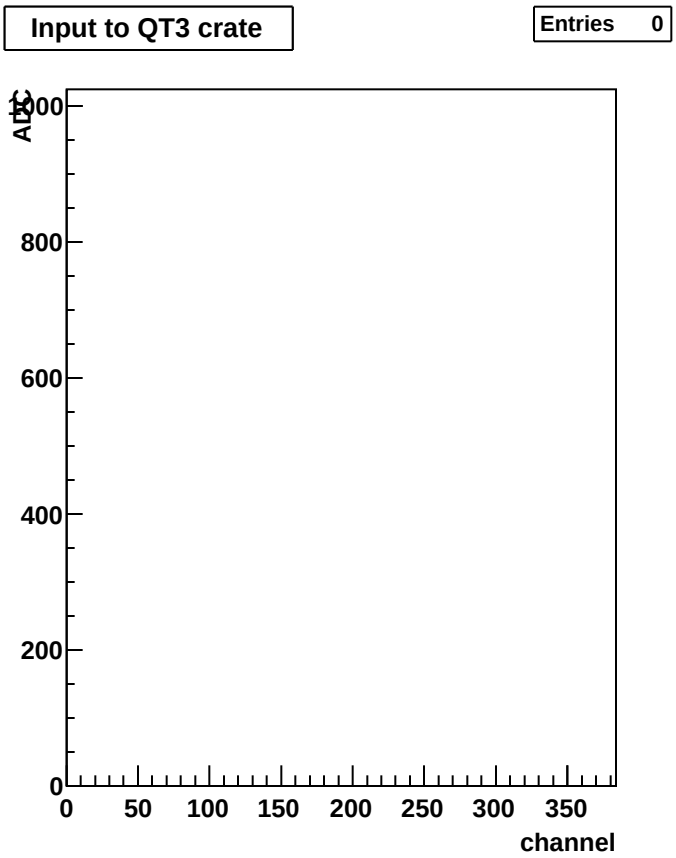

Entries 494439
BBC-S-East ADC Sum
BBC-S-West ADC Sum

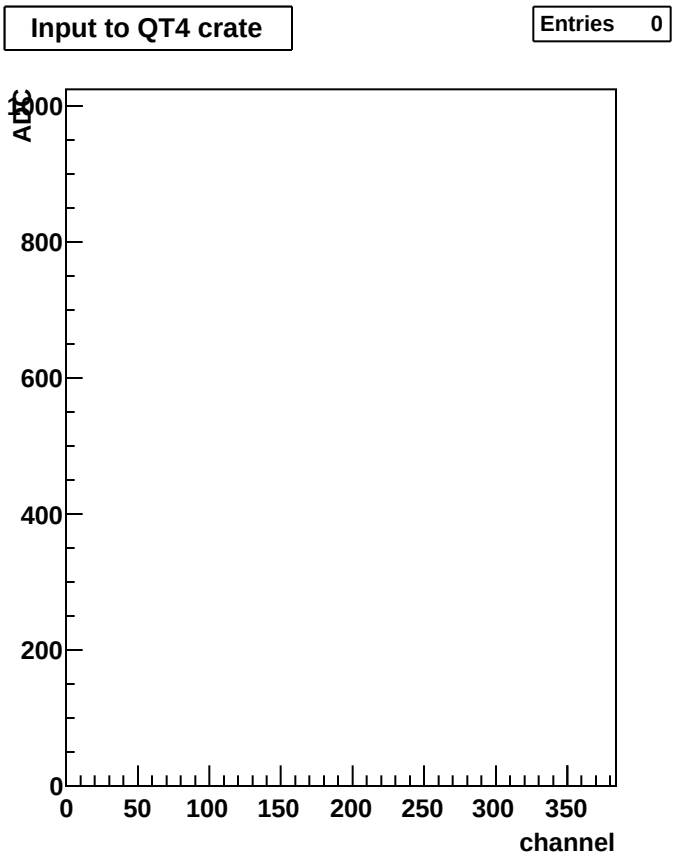

Entries 494439
ZDC East ADC Sum Att
ZDC West ADC Sum Att

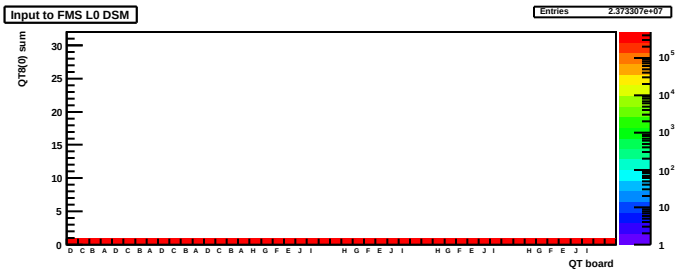

Entries 494439
BBC-L TAC Diff
BCC-S TAC Diff
Entries 494439
BBC-L TAC Diff
VPD TAC Diff
Entries 494439
BBC-L TAC Diff
ZDC TAC Diff
Entries 494439
BBC-S TAC Diff
VPD TAC Diff
Entries 494439
BBC-S TAC Diff
ZDC TAC Diff
Entries 494439
VPD TAC Diff
ZDC TAC Diff

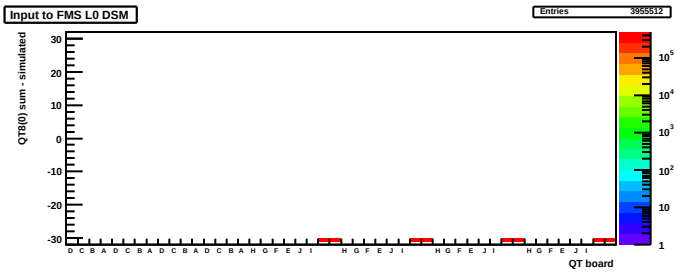

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

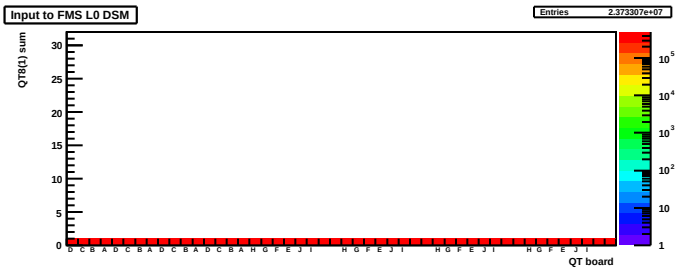

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

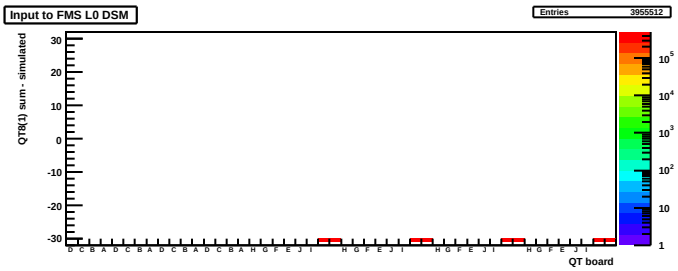

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

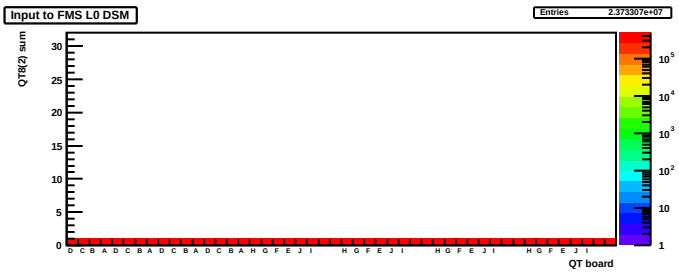

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

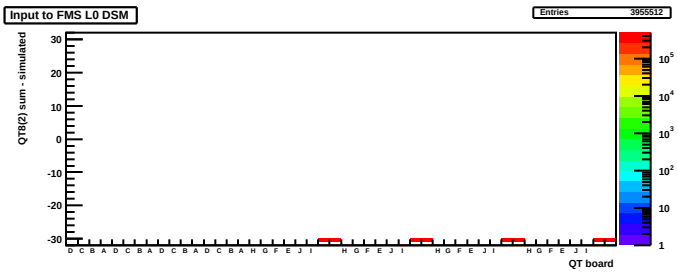

Input to FMS L0 DSM
Entries 2.373307e+07
QT8(0) sum
QT board

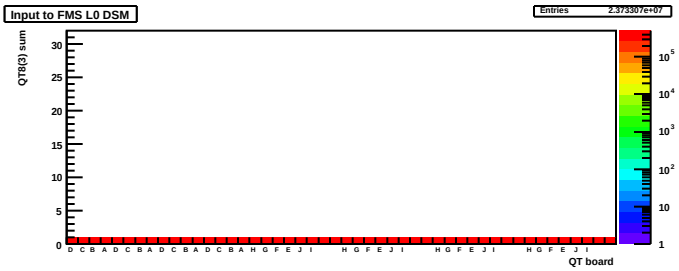

Input to FMS L0 DSM
Entries 3955512
QT8(0) sum - simulated
QT board

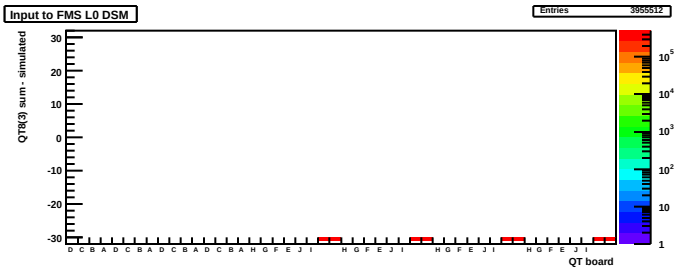

Input to FMS L0 DSM
Entries 2.373307e+07
QT8(1) sum
QT board

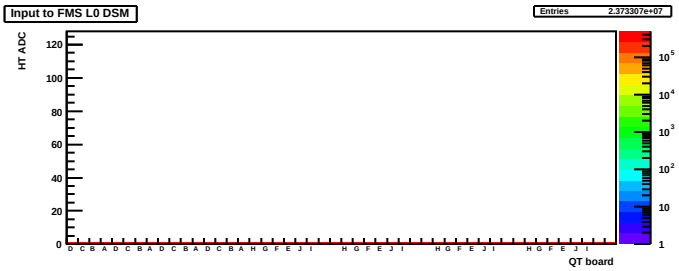

Input to FMS L0 DSM
Entries 3955512
QT8(1) sum - simulated
QT board

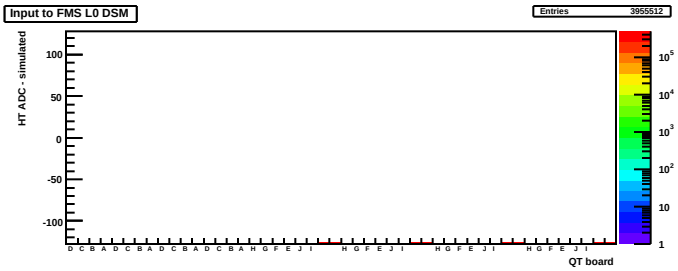

Input to FMS L0 DSM
Entries 2.373307e+07
QT8(2) sum
QT board

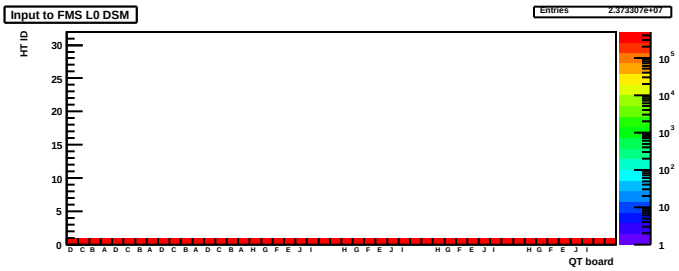

Input to FMS L0 DSM
Entries 3955512
QT8(2) sum - simulated
QT board

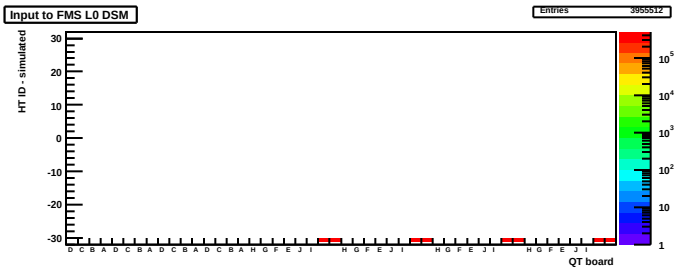

Input to FMS L0 DSM
Entries 2.373307e+07
QT8(3) sum
QT board

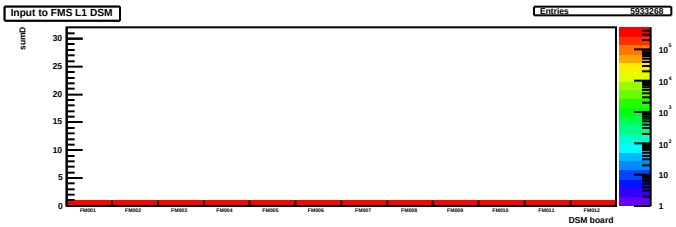

Input to FMS L0 DSM
Entries 3955512
QT8(3) sum - simulated
QT board

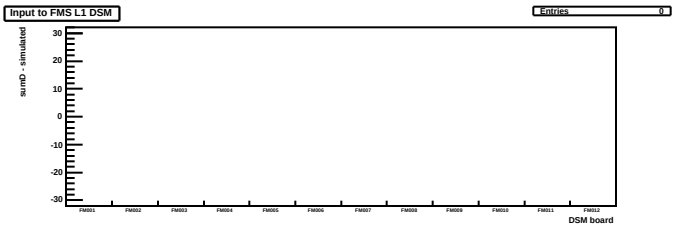

Input to FMS L0 DSM
Entries 2.373307e+07
HT ADC
QT board

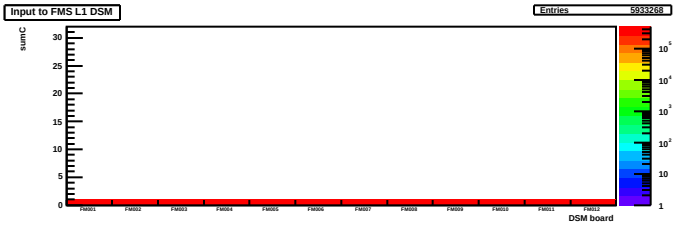

Input to FMS L0 DSM
Entries 3955512
HT ADC - simulated
QT board

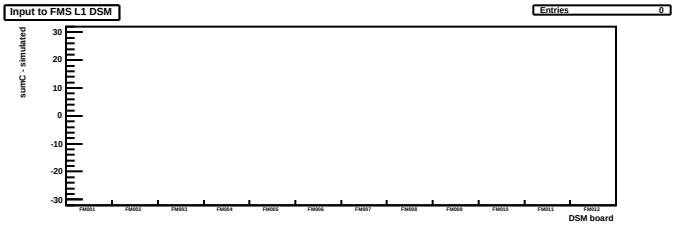

Input to FMS L0 DSM
Entries 2.373307e+07
HT ID
QT board

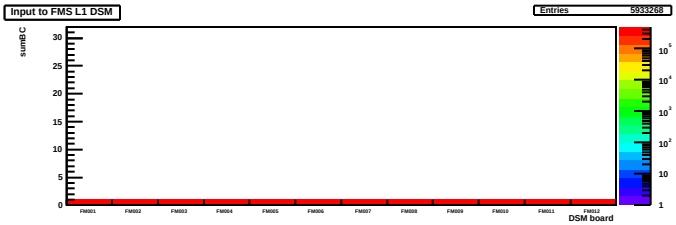

Input to FMS L0 DSM
Entries 3955512
HT ID - simulated
QT board

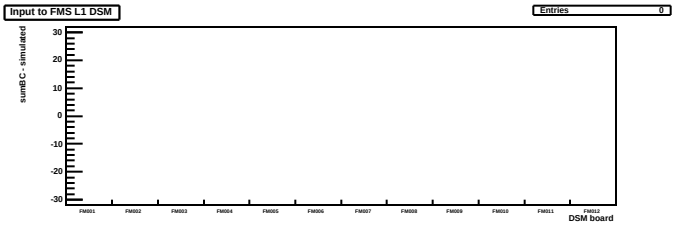

Input to FMS L1 DSM
Entries 5933268
sumD
DSM board

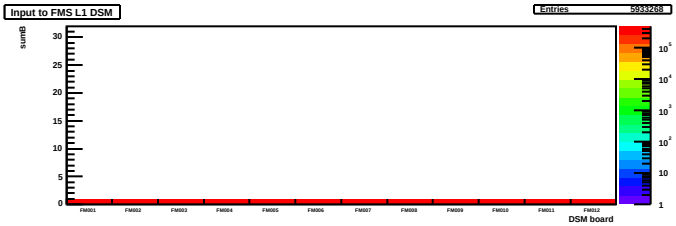

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

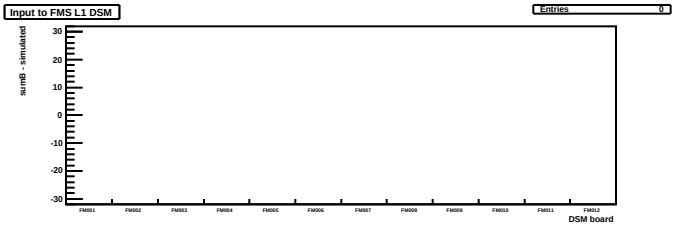

Input to FMS L1 DSM
Entries 5933268
sumC
DSM board

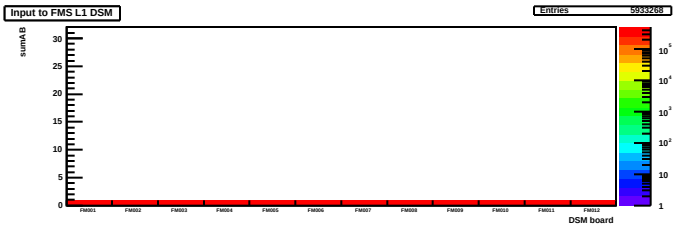

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

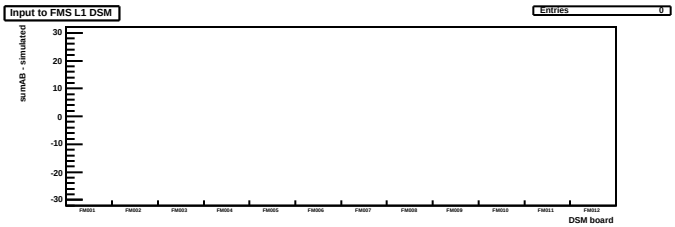

Input to FMS L1 DSM
Entries 5933268
sumBC
DSM board

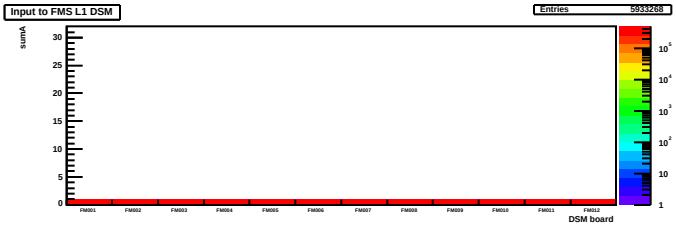

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

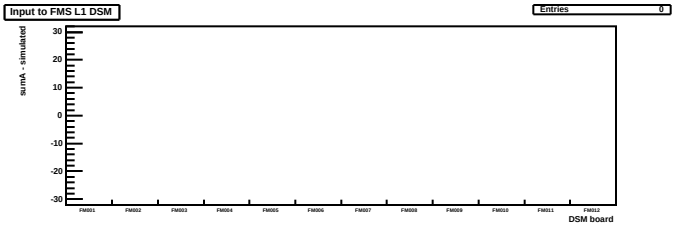

Input to FMS L1 DSM
Entries 5933268
sumB
DSM board

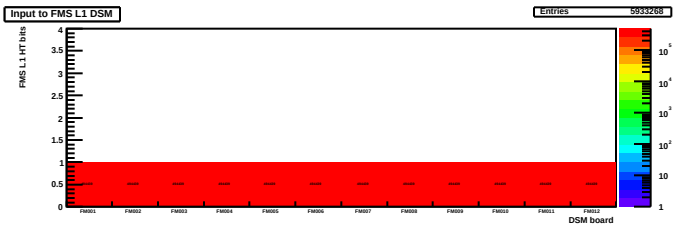

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

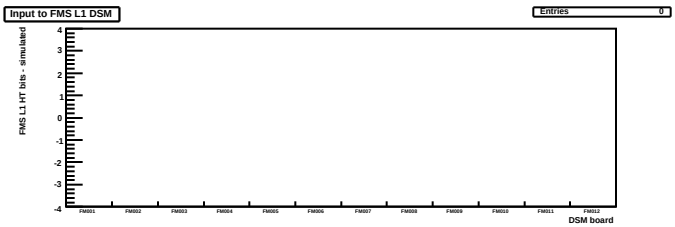

Input to FMS L1 DSM
Entries 5933268
sumAB
DSM board

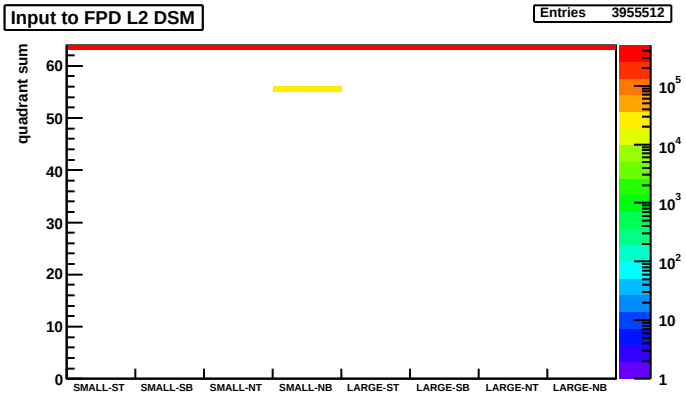

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

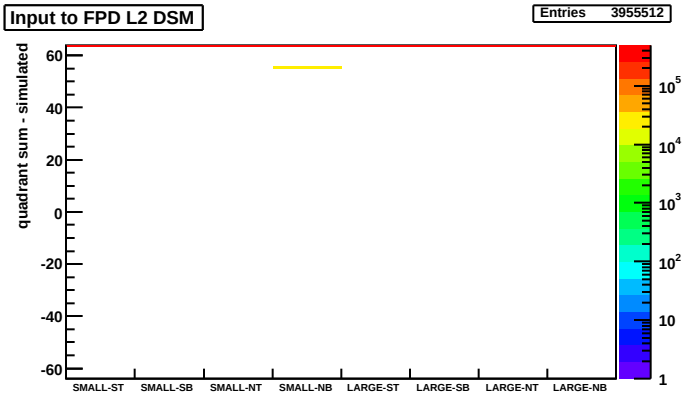

Input to FMS L1 DSM
Entries 5933268
sumA
DSM board

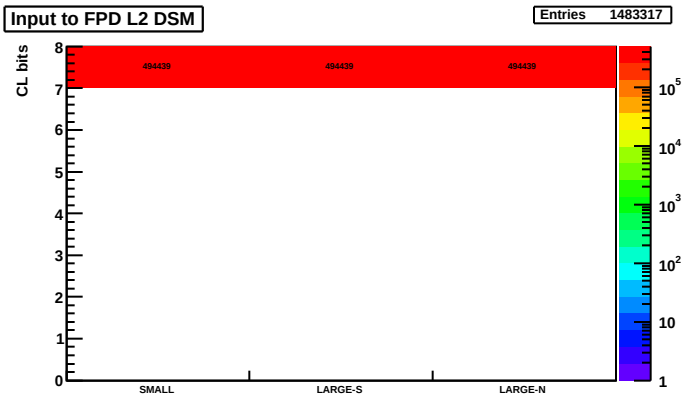

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

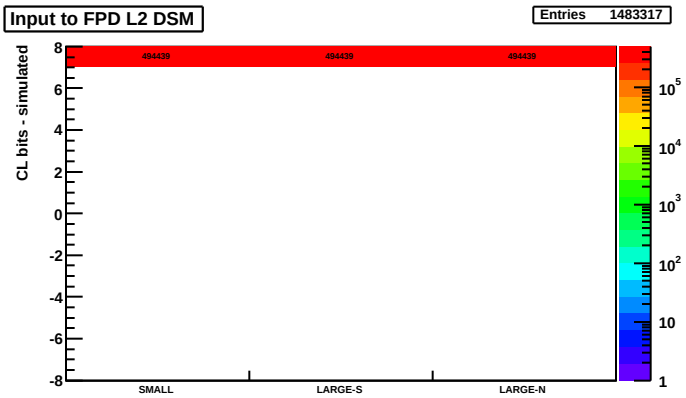

Input to FMS L1 DSM
Entries 5933268
FMS L1 HT bits
DSM board

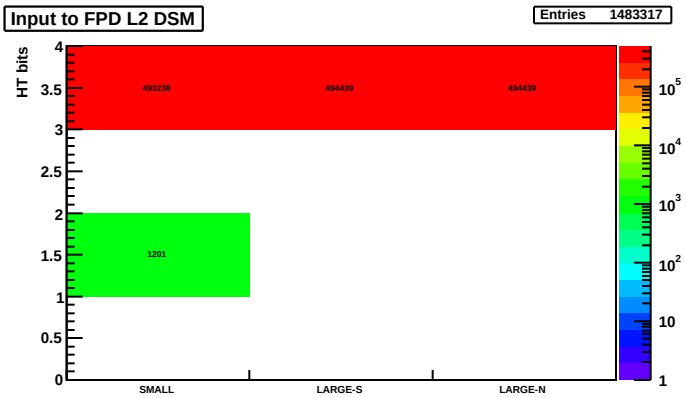

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

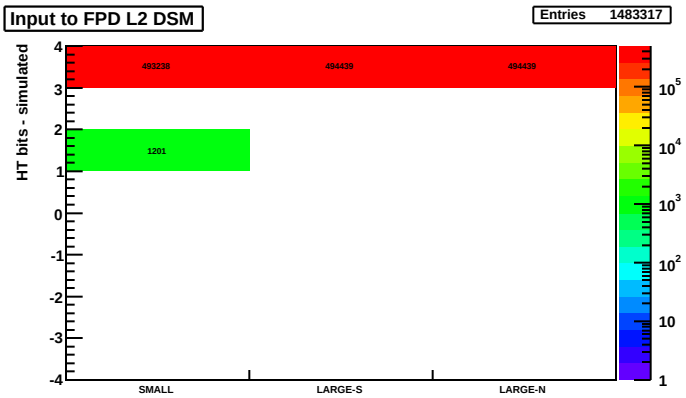

Input to FPD L2 DSM
Entries 3955512
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

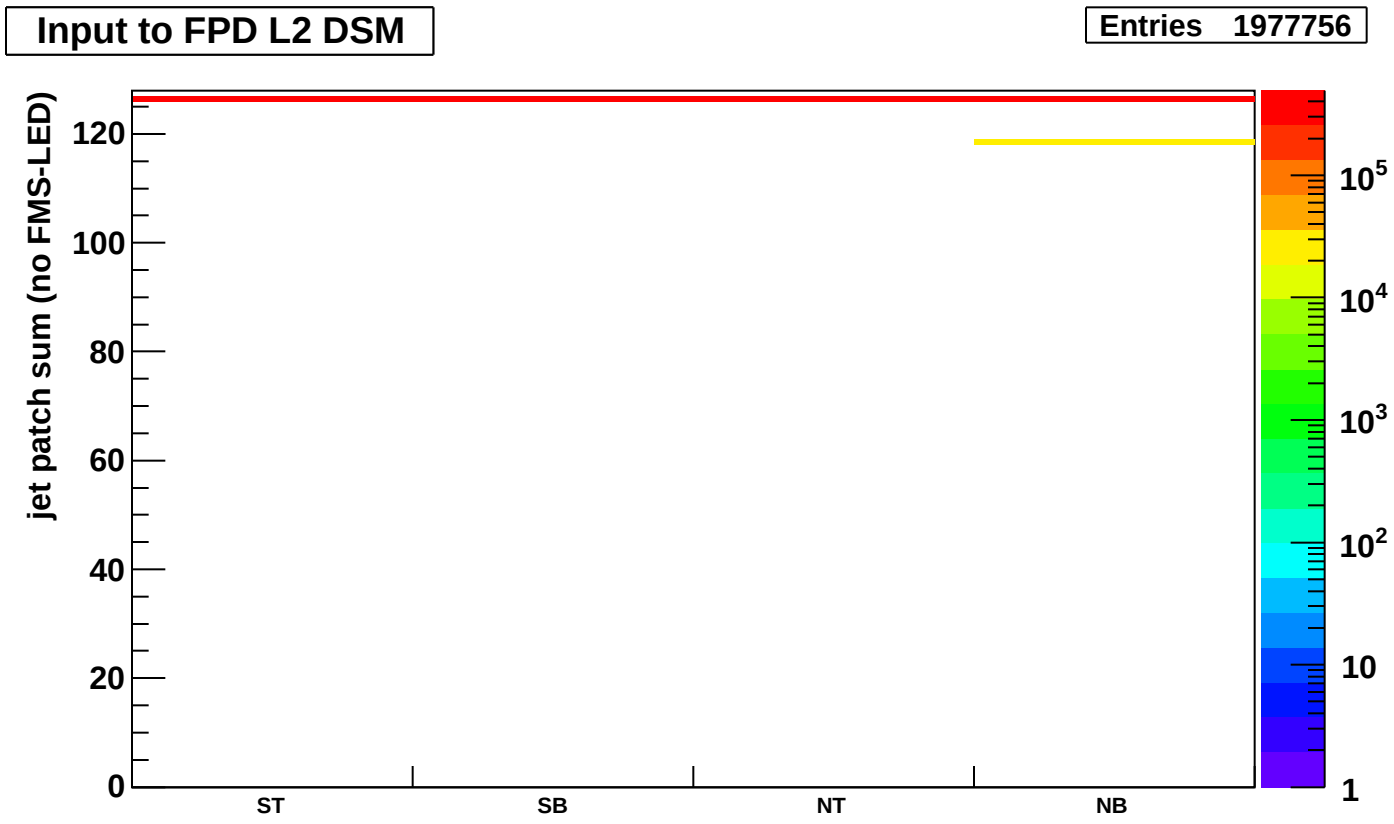

Input to FPD L2 DSM
Entries 3955512
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

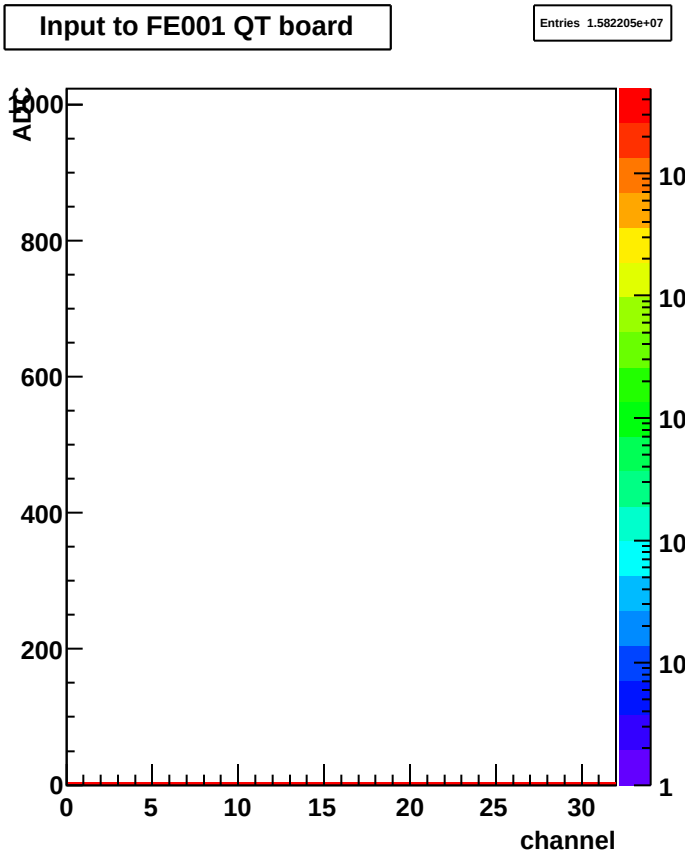

Input to FPD L2 DSM
Entries 1483317
CL bits
494439
494439
494439
SMALL
LARGE-S
LARGE-N

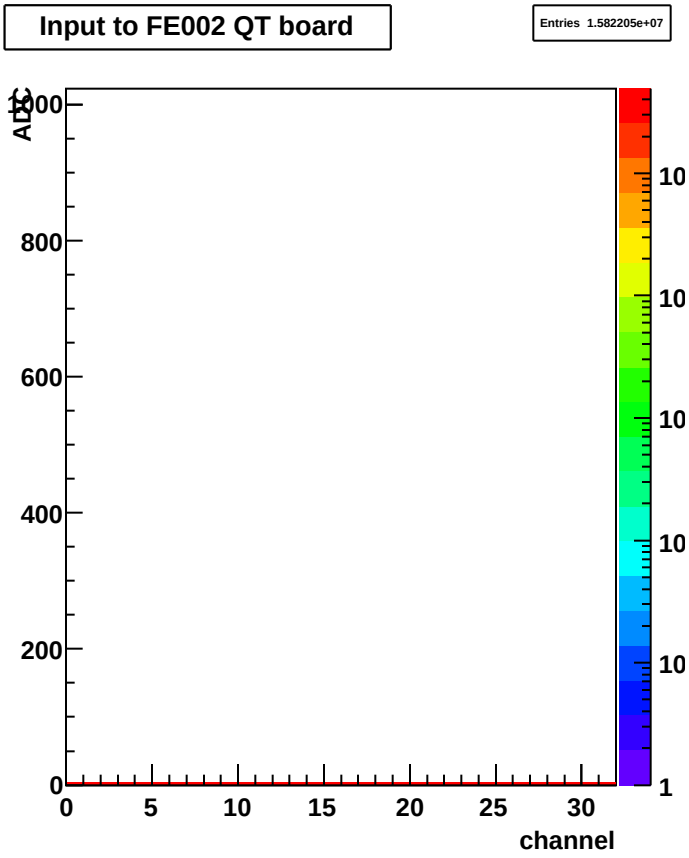

Input to FPD L2 DSM
Entries 1483317
CL bits - simulated
494439
494439
494439
SMALL
LARGE-S
LARGE-N

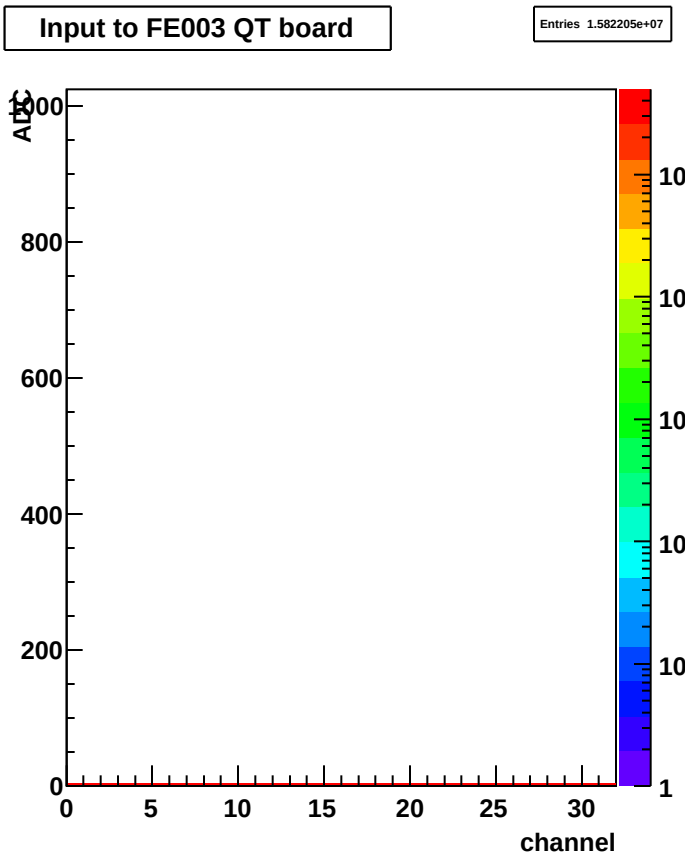

Input to FPD L2 DSM
Entries 1483317
HT bits
493238
494439
494439
1201
SMALL
LARGE-S
LARGE-N

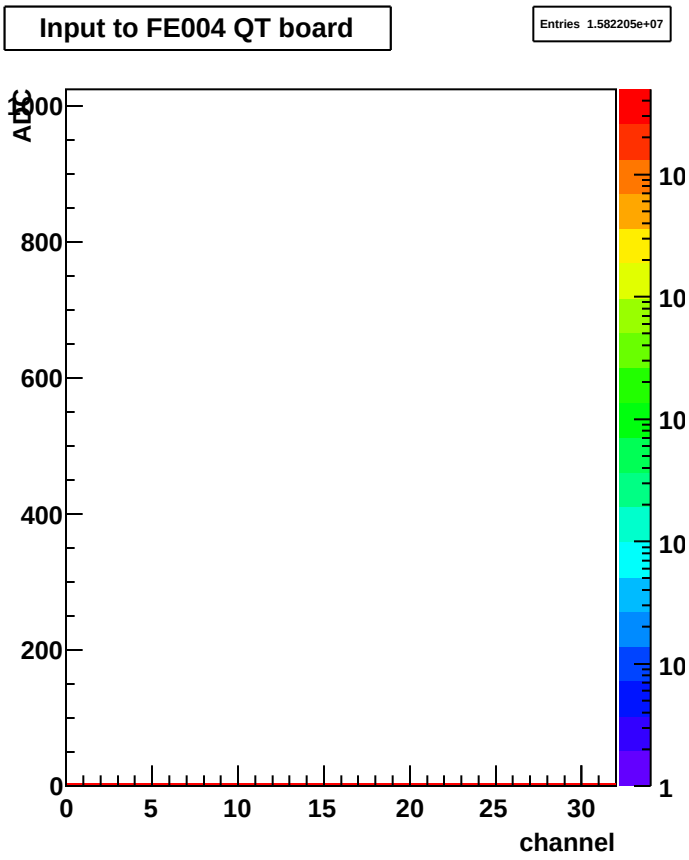

Input to FPD L2 DSM
Entries 1483317
HT bits - simulated
493238
494439
494439
1201
SMALL
LARGE-S
LARGE-N

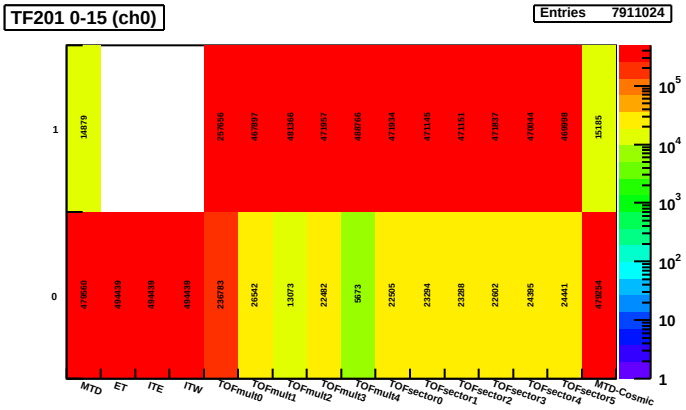

Input to FPD L2 DSM
Entries 1977756
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

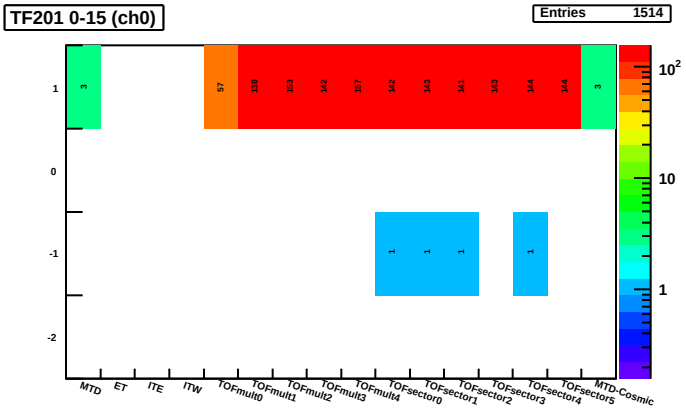

Input to FE001 QT board
Entries 1.582205e+07
ADC
channel

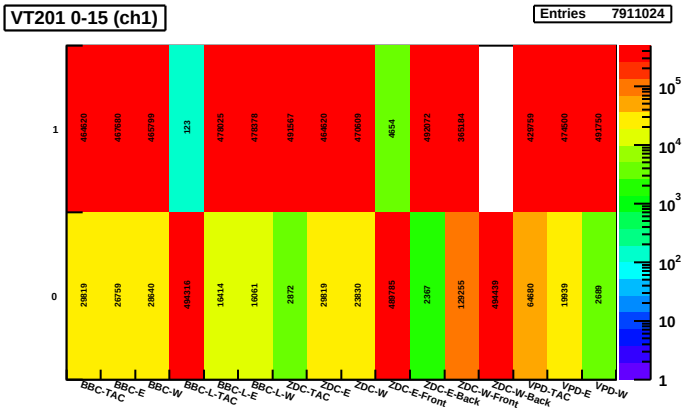

Input to FE002 QT board
Entries 1.582205e+07
ADC
channel

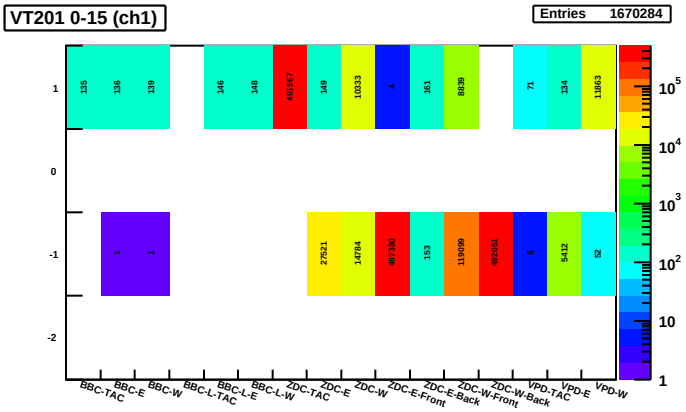

Input to FE003 QT board
Entries 1.582205e+07
ADC
channel

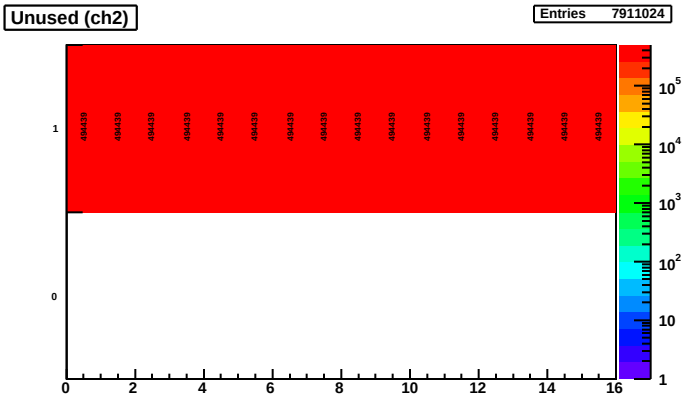

Input to FE004 QT board
Entries 1.582205e+07
ADC
channel

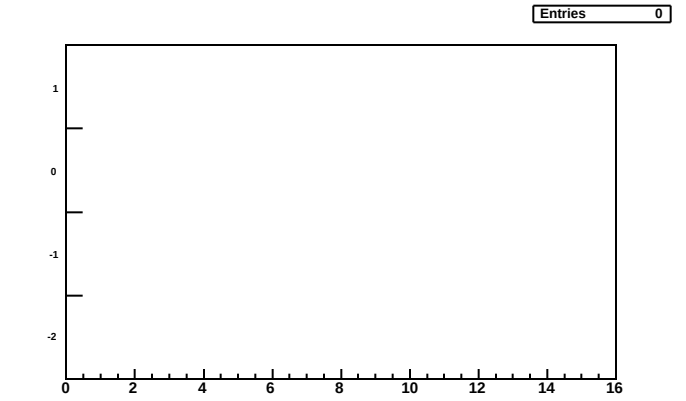

TF201 0-15 (ch0)
Entries 7911024
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

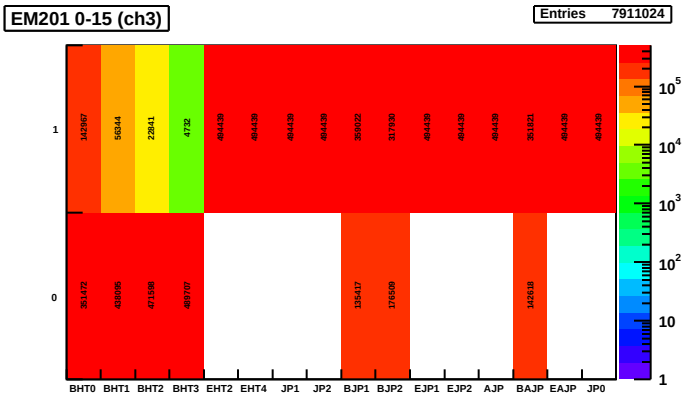

TF201 0-15 (ch0)
Entries 1514
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

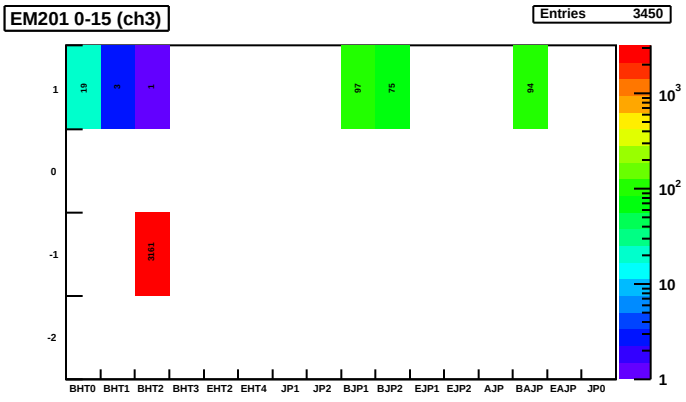

VT201 0-15 (ch1)
Entries 7911024
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 1670284
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

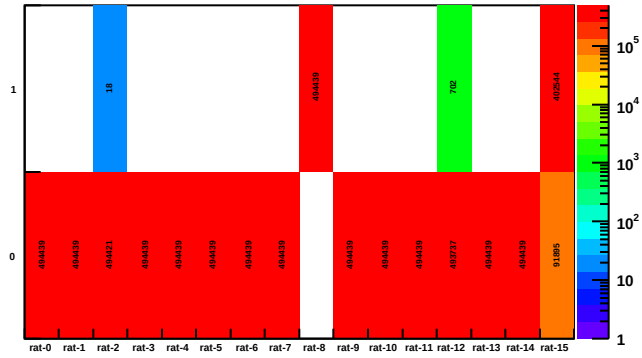

Unused (ch2)
Entries 7911024

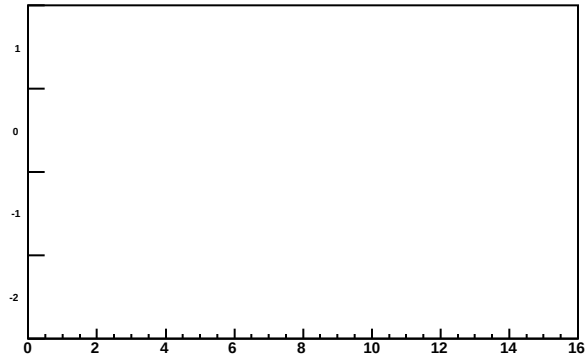

Entries 0


EM201 0-15 (ch3)
Entries 7911024
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

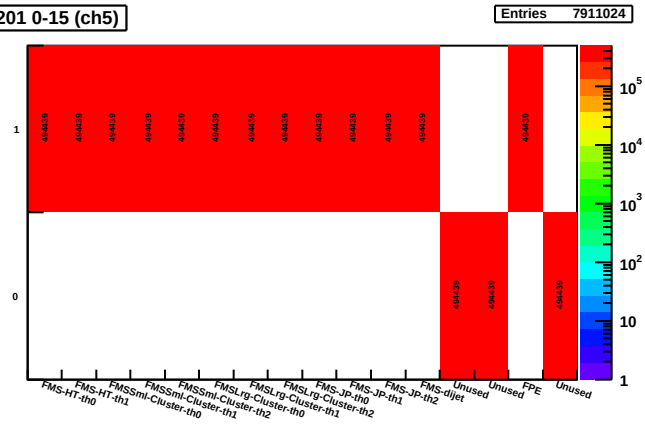

EM201 0-15 (ch3)
Entries 3450
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

RAT board (ch4)

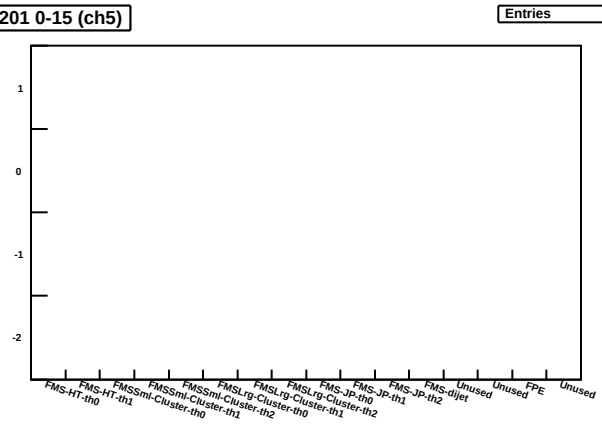




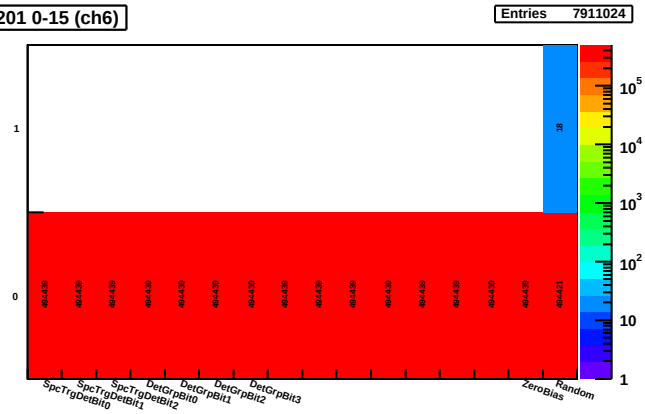
FP201 0-15 (ch5)

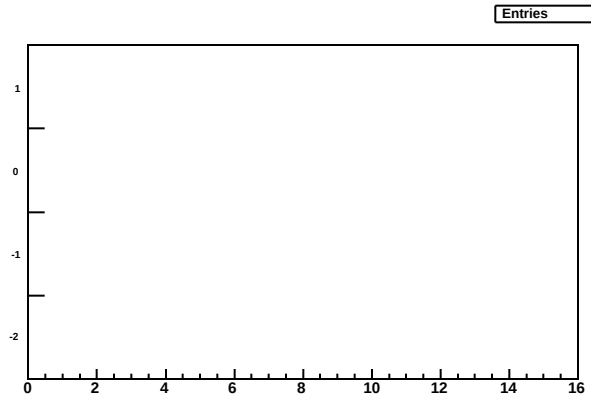
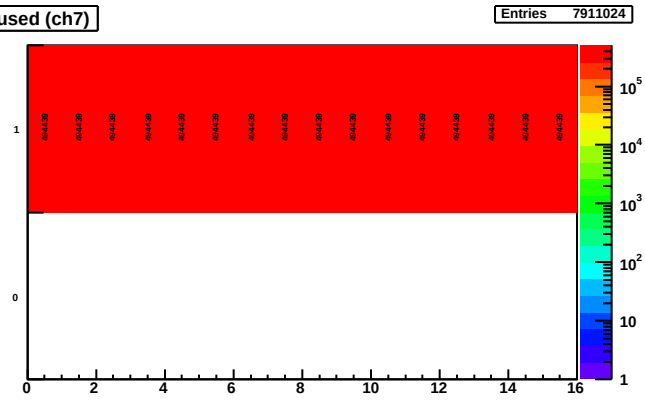
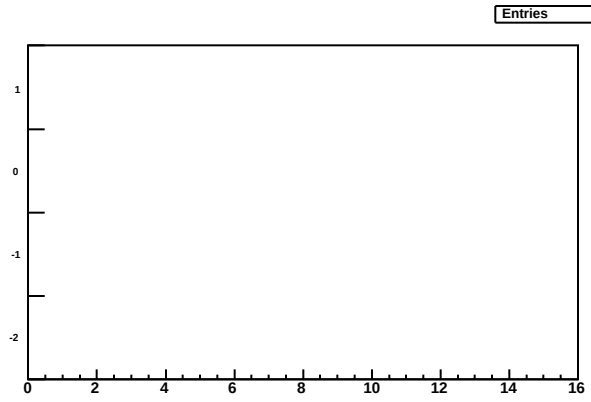


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)