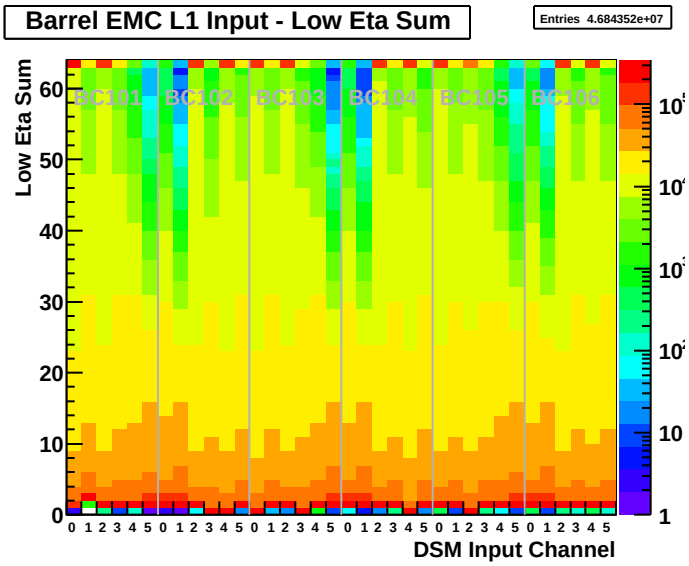
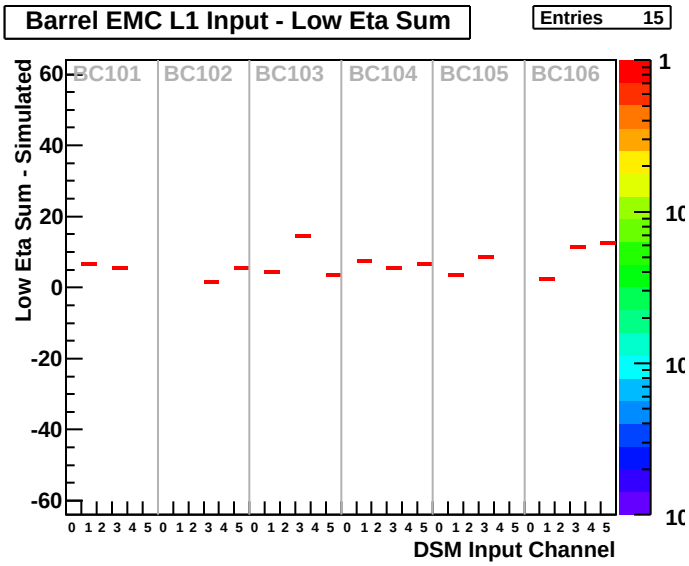
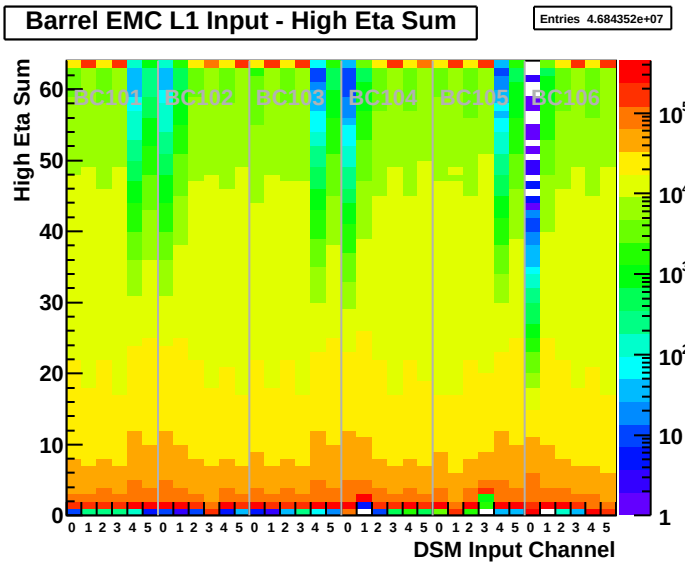
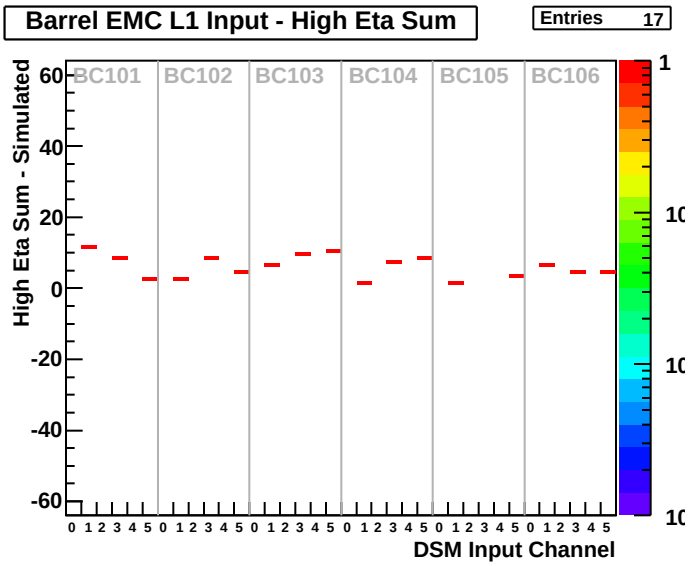
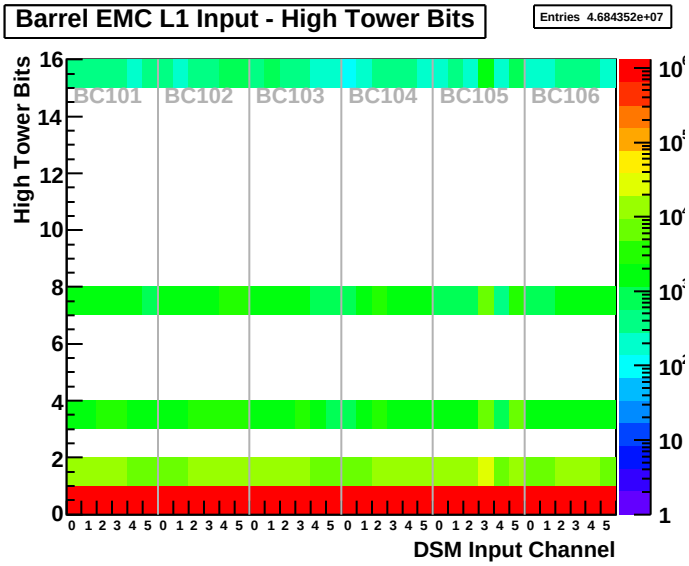
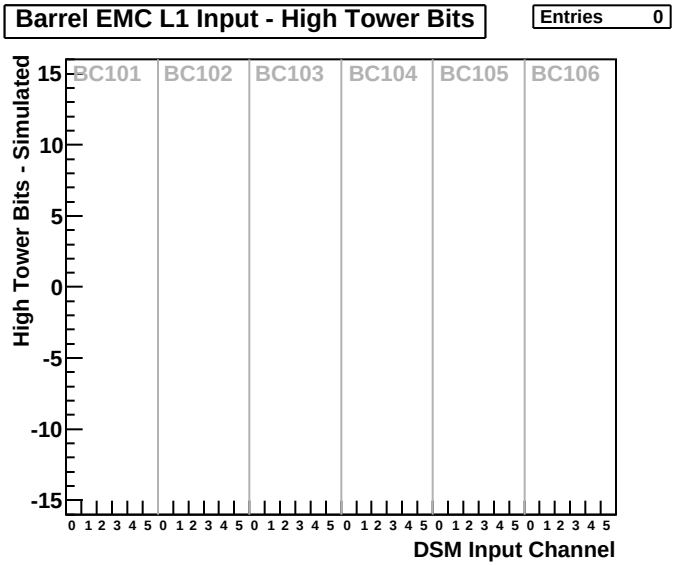

Barrel EMC L1 Input - Low Eta Sum
Entries 4.684352e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

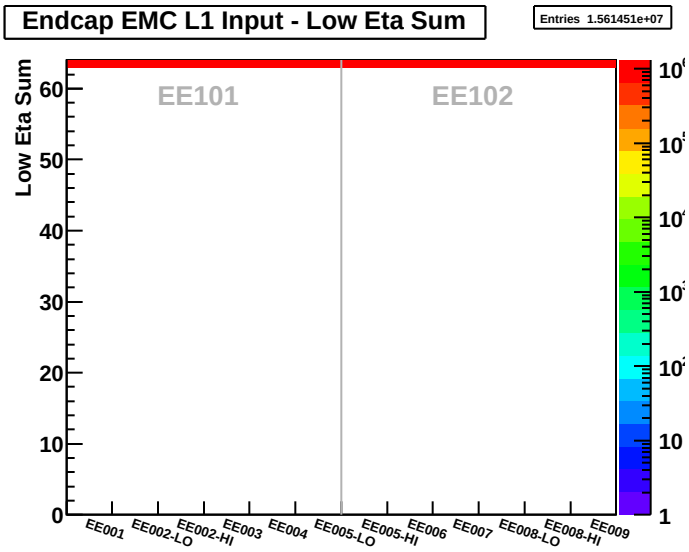

Barrel EMC L1 Input - Low Eta Sum
Entries 15
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

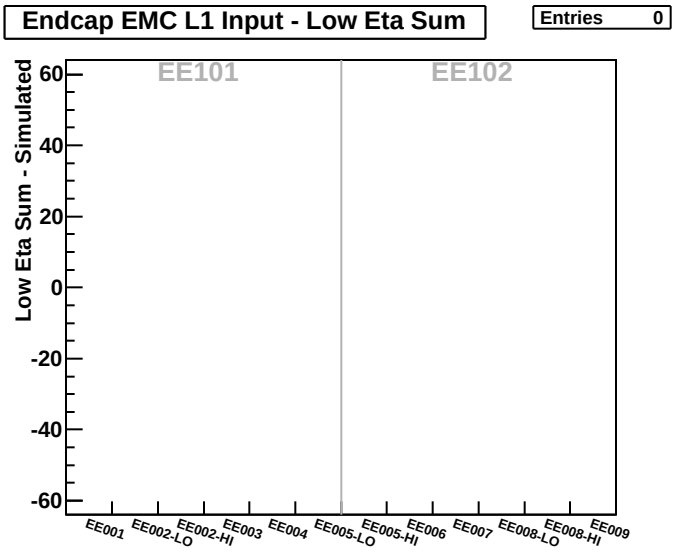

Barrel EMC L1 Input - High Eta Sum
Entries 4.684352e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

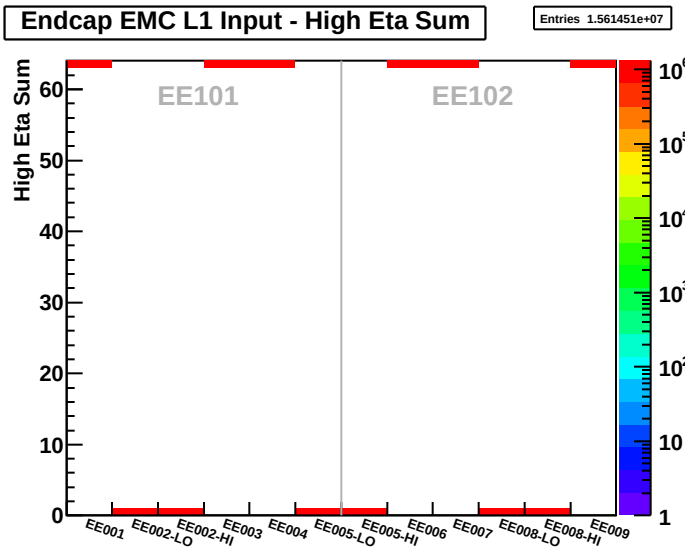

Barrel EMC L1 Input - High Eta Sum
Entries 17
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

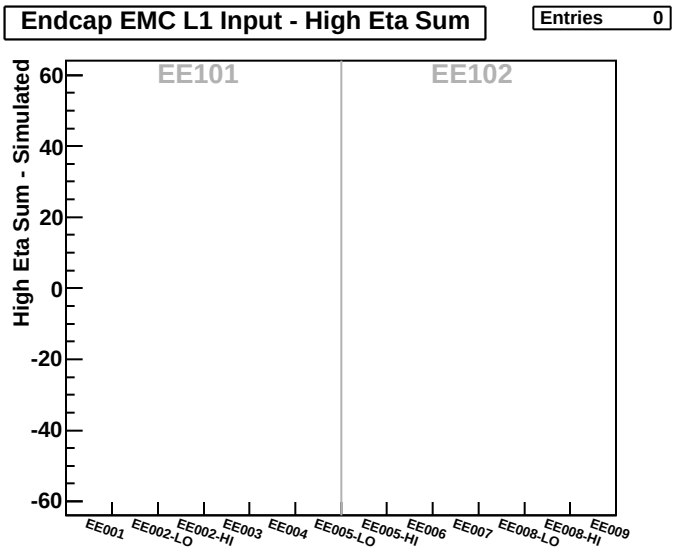

Barrel EMC L1 Input - High Tower Bits
Entries 4.684352e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

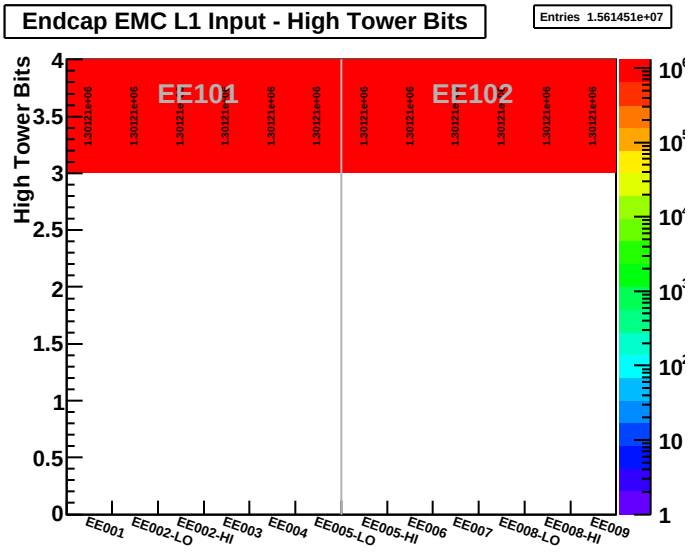

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

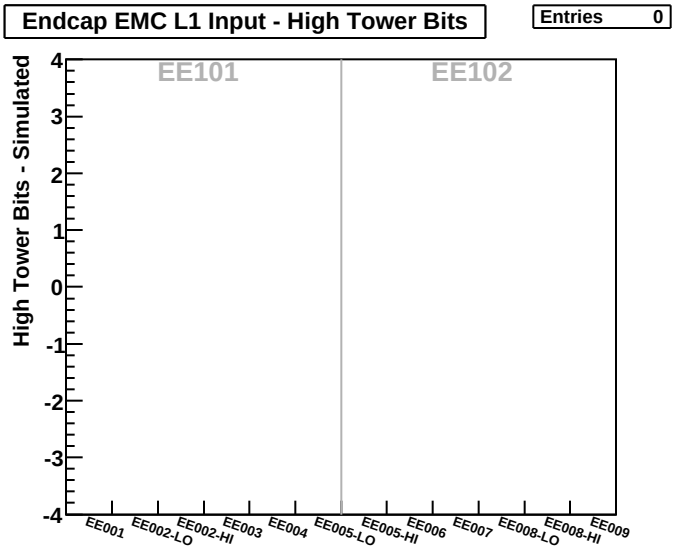

Endcap EMC L1 Input - Low Eta Sum
Entries 1.561451e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

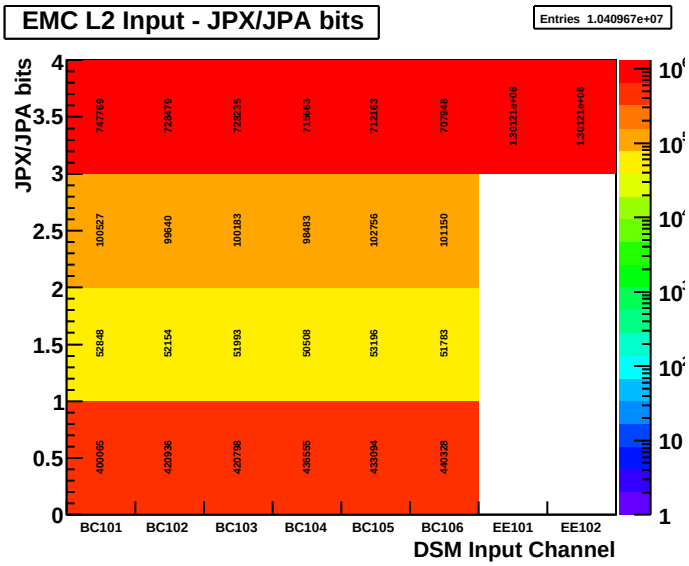

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

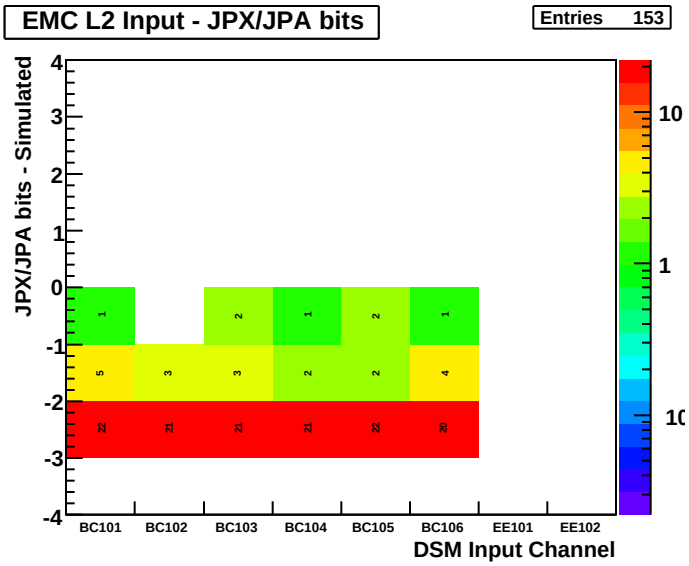

Endcap EMC L1 Input - High Eta Sum
Entries 1.561451e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

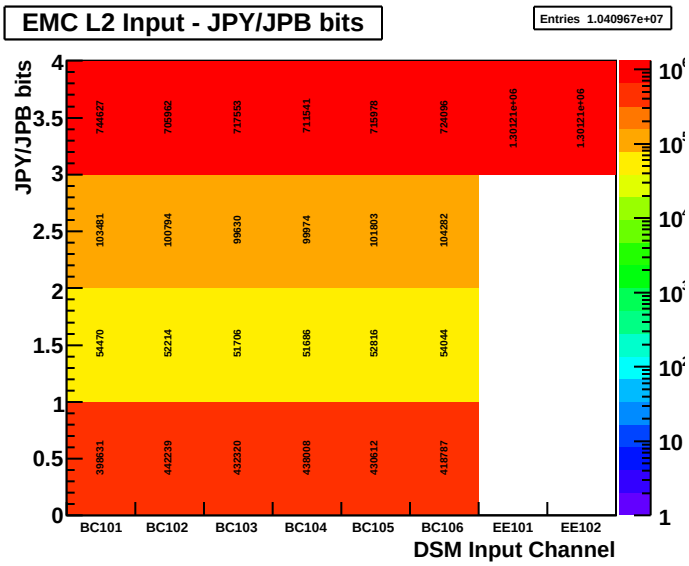

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

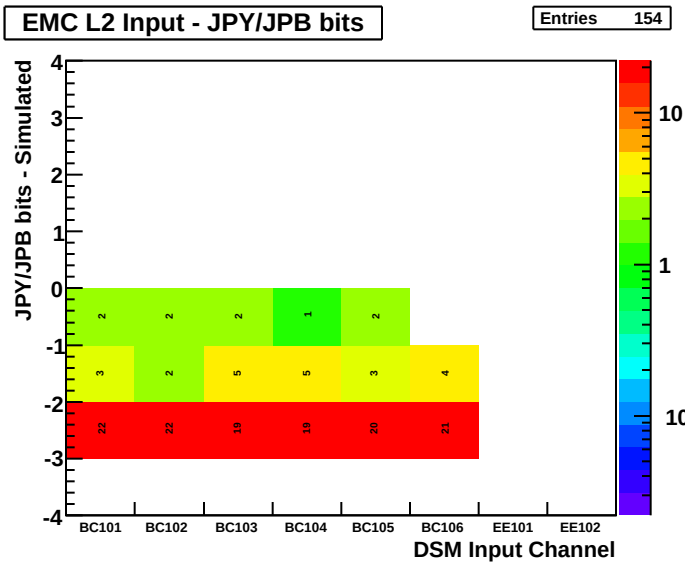

Endcap EMC L1 Input - High Tower Bits
Entries 1.561451e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

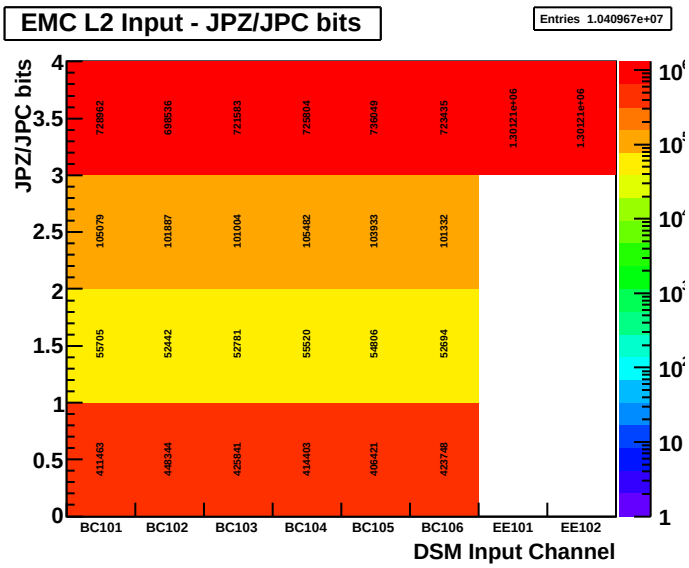

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

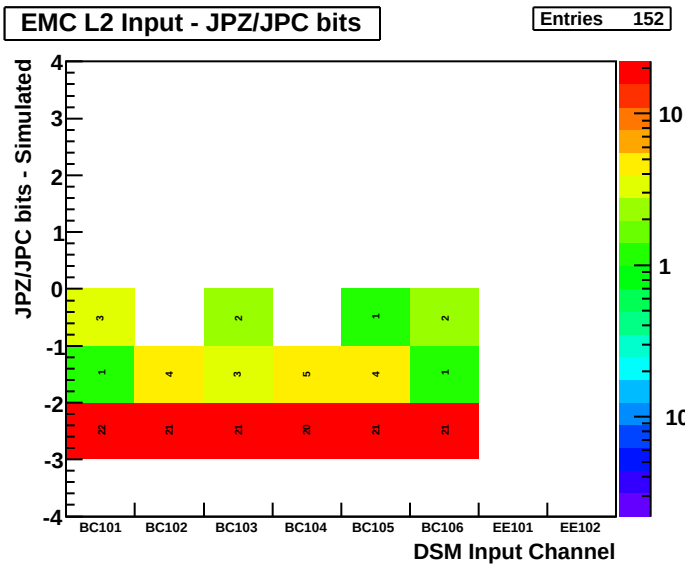

EMC L2 Input - JPX/JPA bits
Entries 1.040967e+07
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
747769 728479 728235 715663 712163 707948 1.30121e+06 1.30121e+06
100527 99640 100183 98483 102756 101150
52848 52154 51993 50508 53196 51783
400065 420936 420798 436555 433094 440328

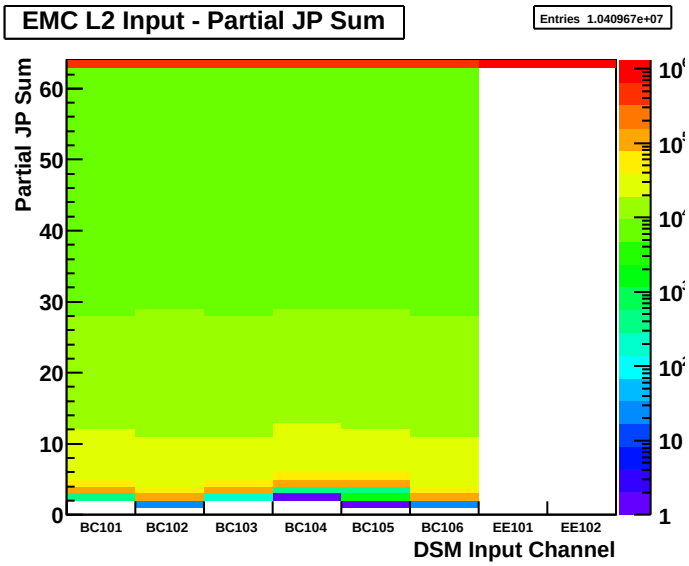

EMC L2 Input - JPX/JPA bits
Entries 153
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

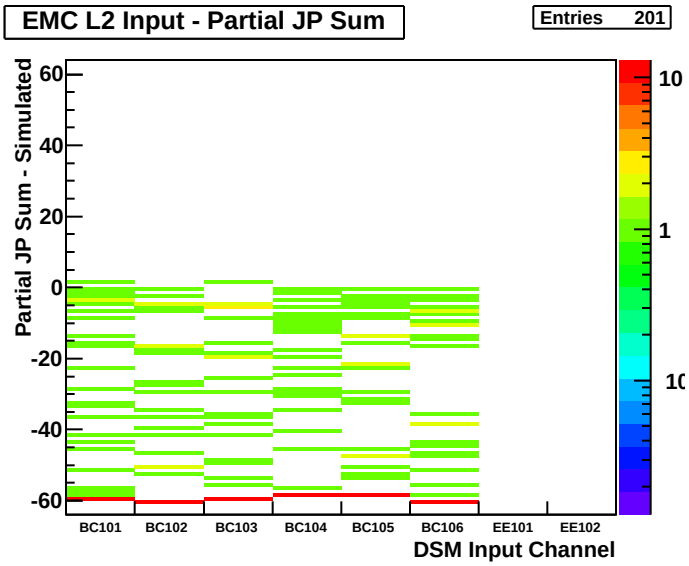

EMC L2 Input - JPY/JPB bits
Entries 1.040967e+07
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
744627 705982 717553 711541 715978 724096 1.30121e+06 1.30121e+06
103481 100794 99630 99974 101803 104282
54470 52214 51706 51686 52816 54044
398631 442239 432320 438008 430612 418787

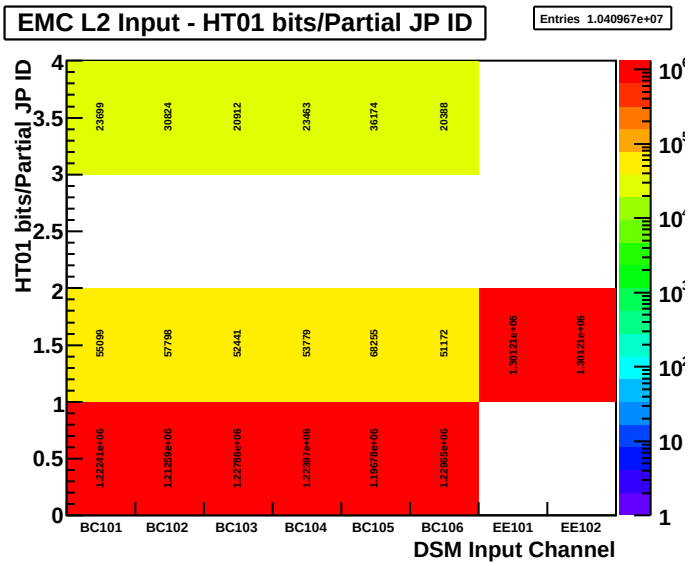

EMC L2 Input - JPY/JPB bits
Entries 154
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

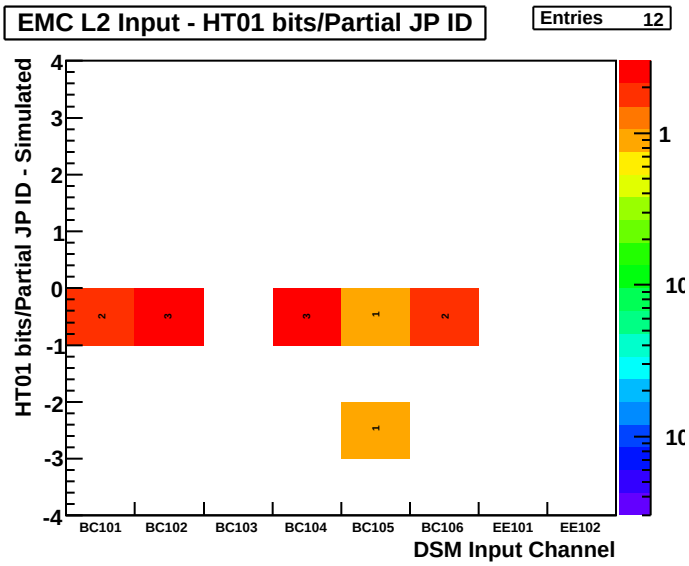

EMC L2 Input - JPZ/JPC bits
Entries 1.040967e+07
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
728962 698536 721583 723804 730049 723435 1.30121e+06 1.30121e+06
105079 101887 101004 105482 103933 101332
55705 52442 52781 55520 54806 52694
411463 448344 425941 414403 406421 423748

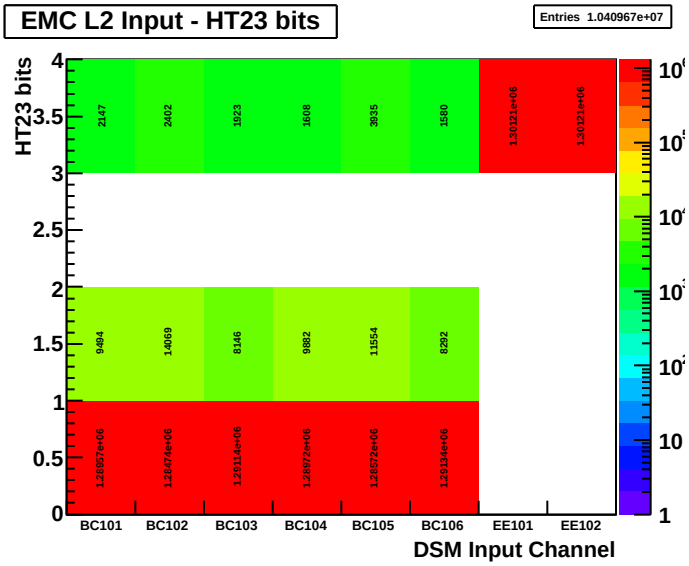

EMC L2 Input - JPZ/JPC bits
Entries 152
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

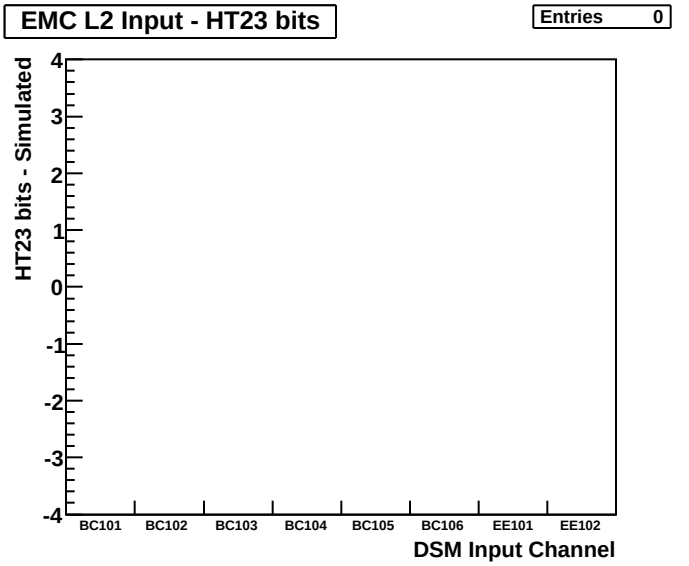

EMC L2 Input - Partial JP Sum
Entries 1.040967e+07
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

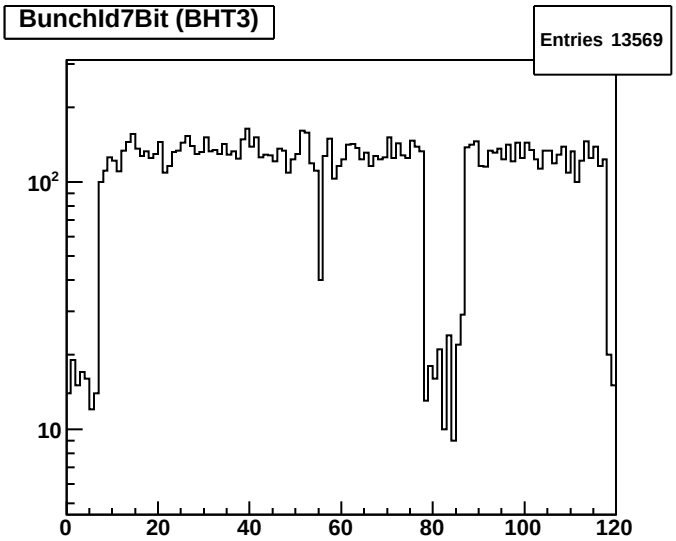

EMC L2 Input - Partial JP Sum
Entries 201
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

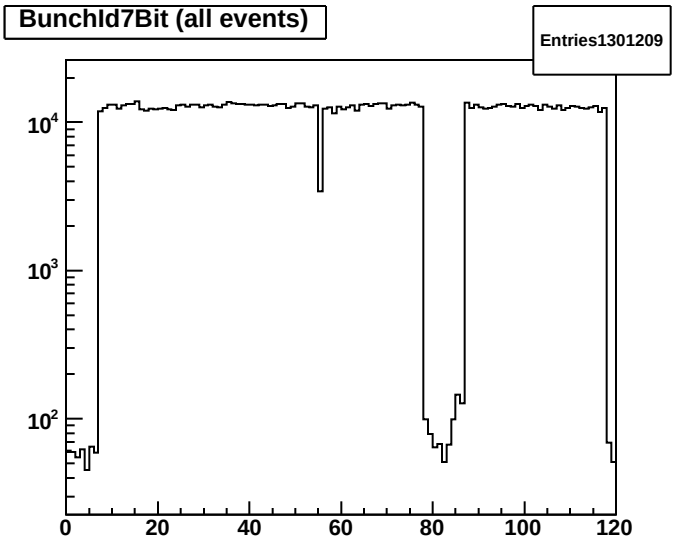

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1.040967e+07
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

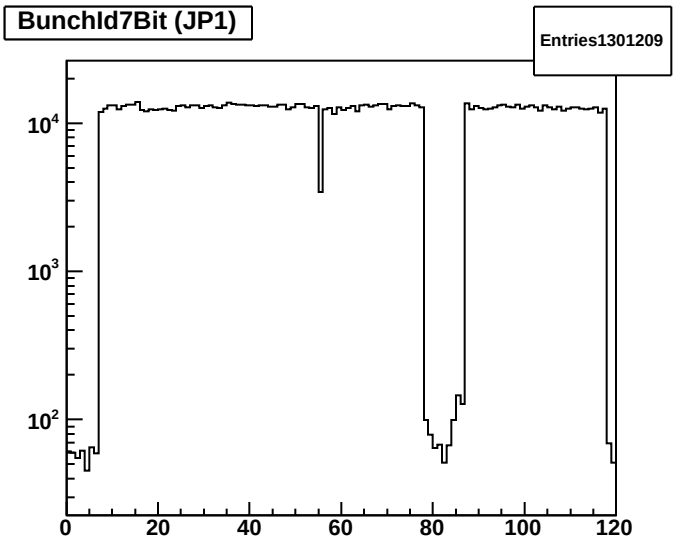

EMC L2 Input - HT01 bits/Partial JP ID
Entries 12
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

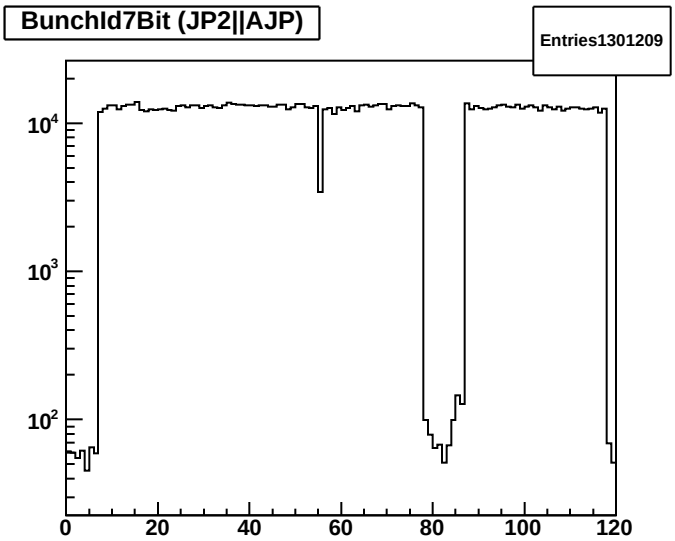

EMC L2 Input - HT23 bits
Entries 1.040967e+07
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

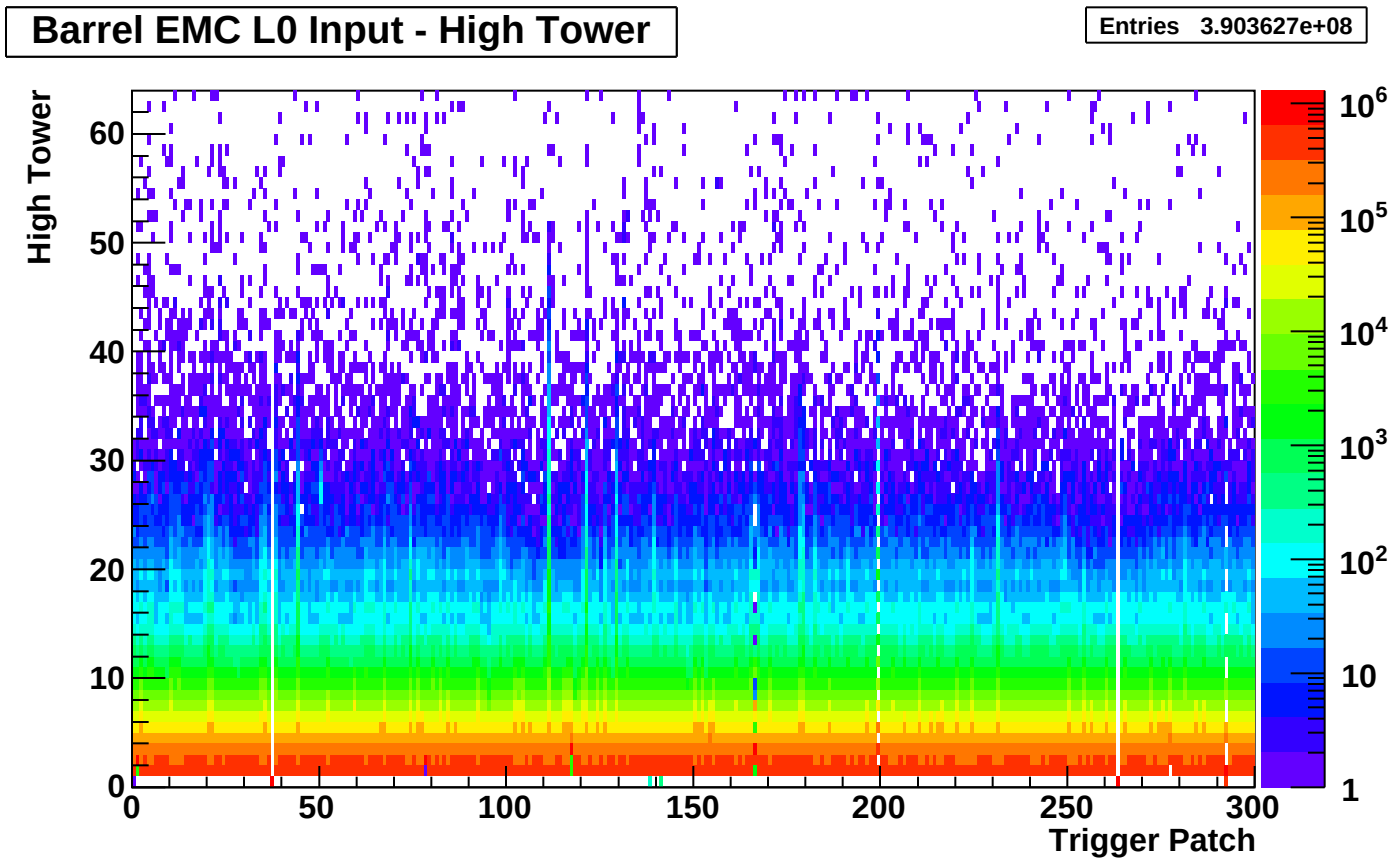

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

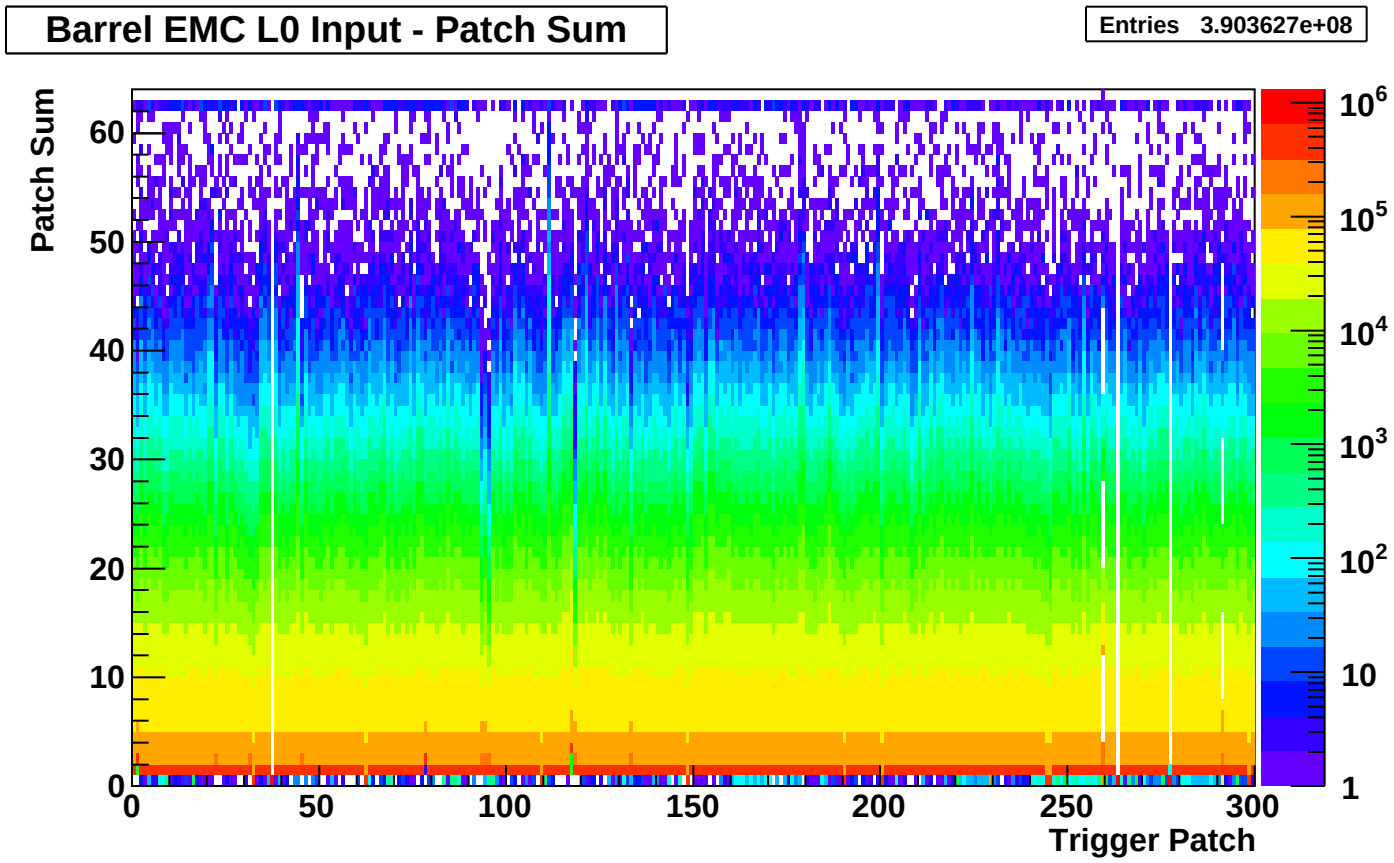

BunchId7Bit (BHT3)
Entries 13569
10^2
10
0
20
40
60
80
100
120

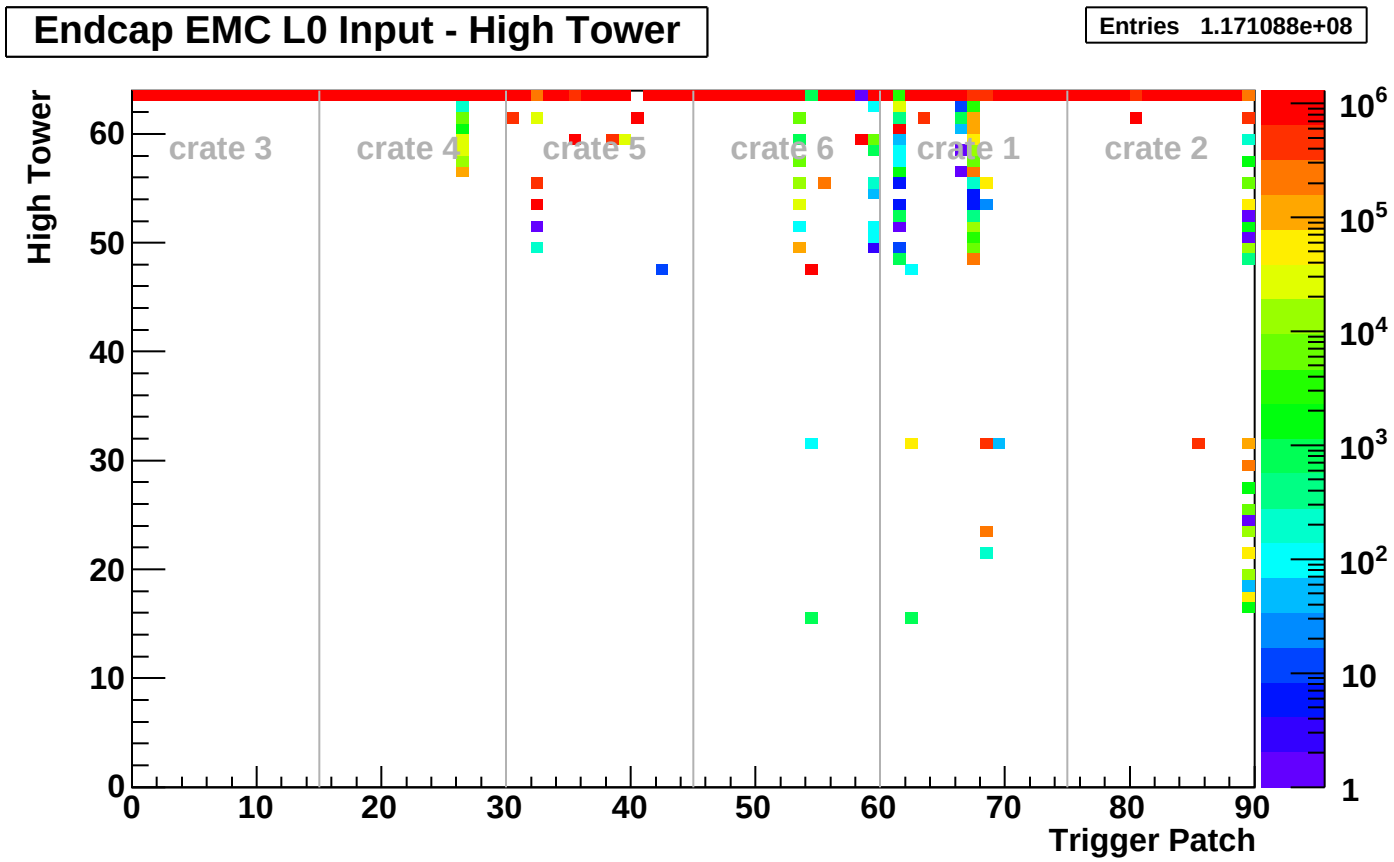

BunchId7Bit (all events)
Entries1301209
10^4
10^3
10^2
0
20
40
60
80
100
120

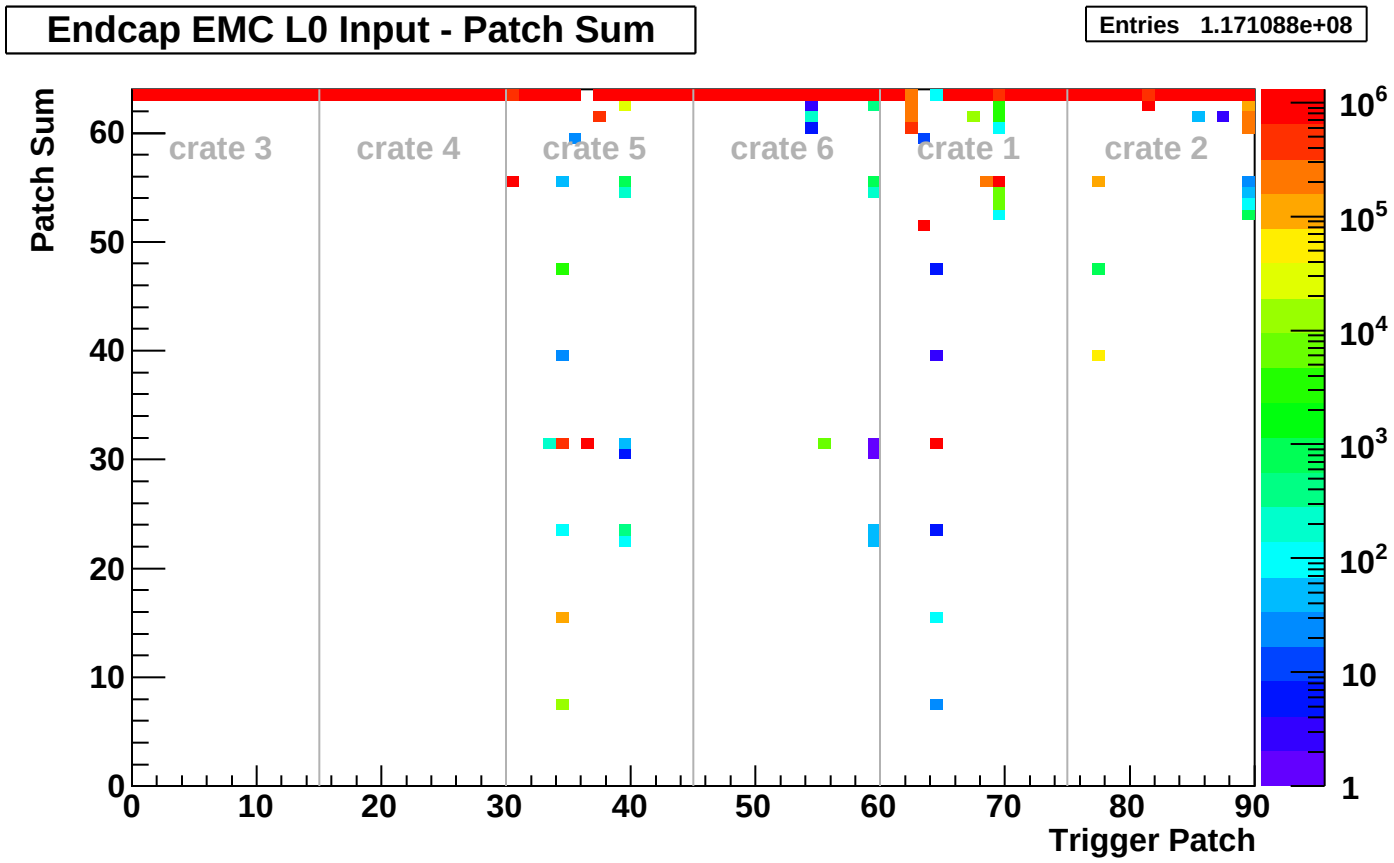

BunchId7Bit (JP1)
Entries1301209
10^4
10^3
10^2
0
20
40
60
80
100
120

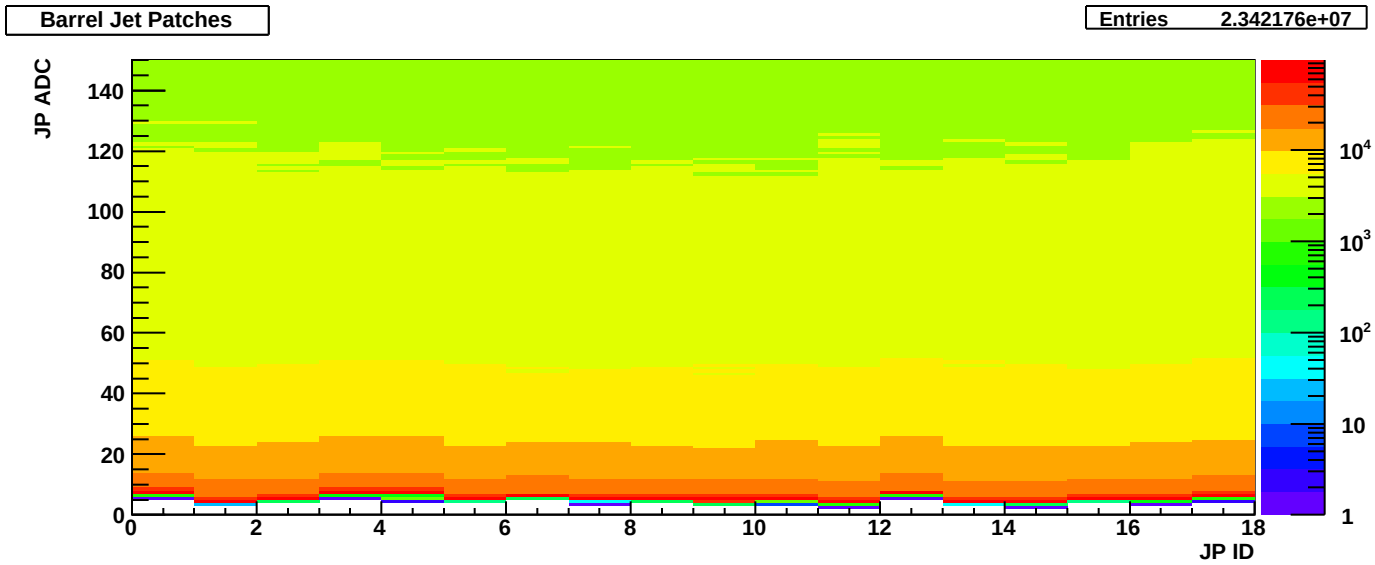

BunchId7Bit (JP2||AJP)
Entries1301209
10^4
10^3
10^2
0
20
40
60
80
100
120

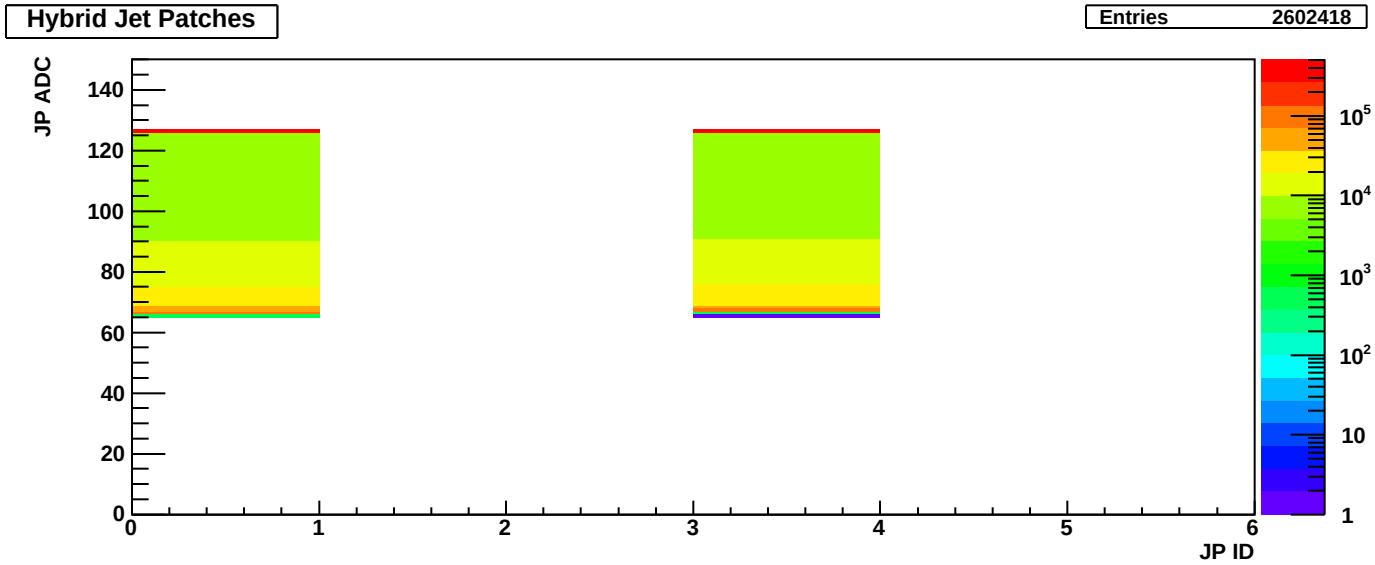

Barrel EMC L0 Input - High Tower
Entries 3.903627e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

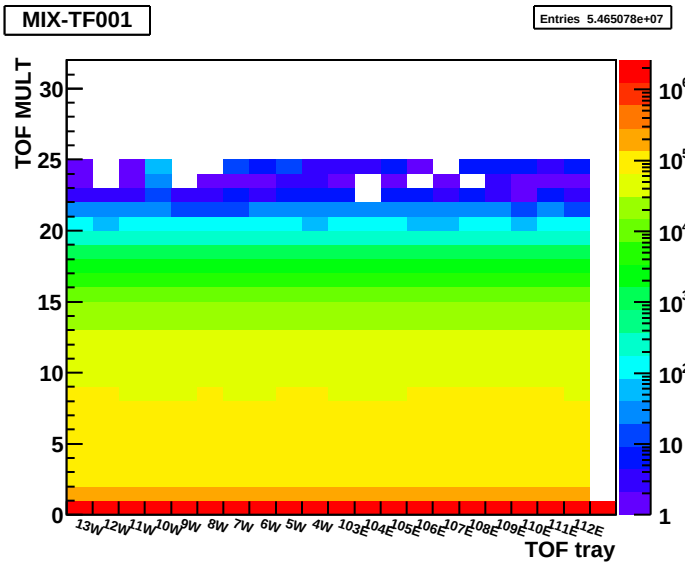

Barrel EMC L0 Input - Patch Sum
Entries 3.903627e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

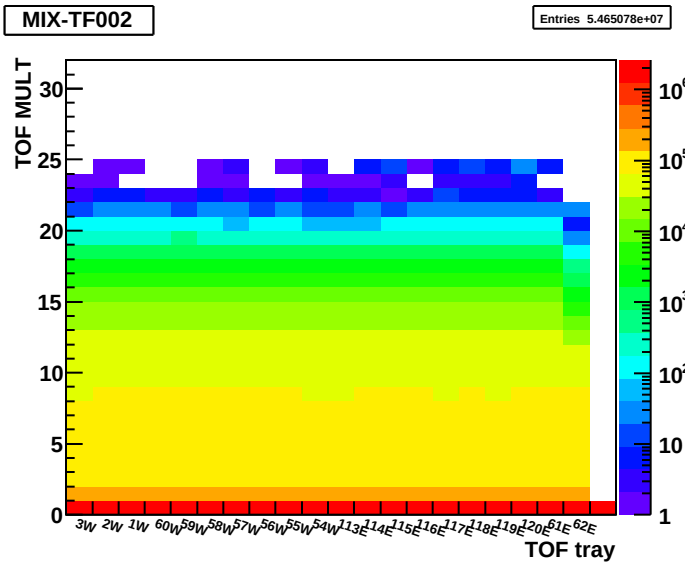

Endcap EMC L0 Input - High Tower
Entries 1.171088e+08
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

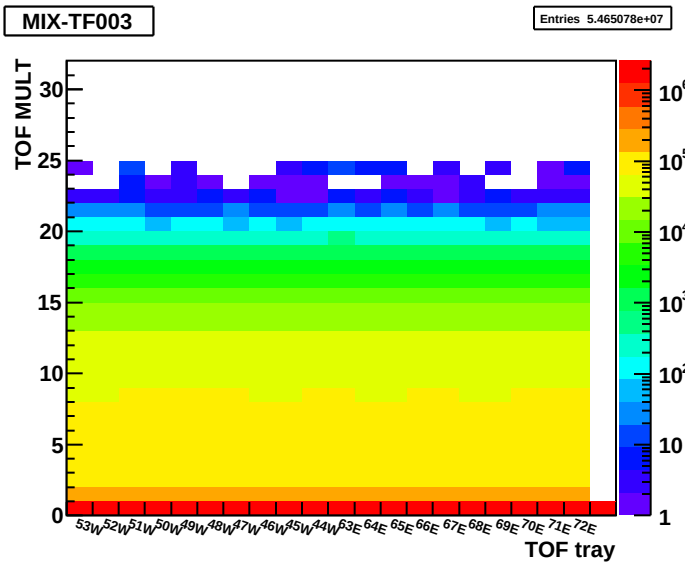

Endcap EMC L0 Input - Patch Sum
Entries 1.171088e+08
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

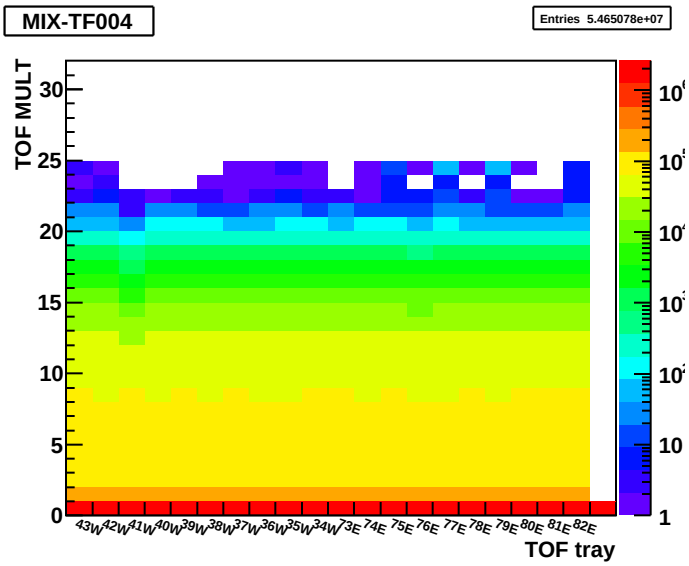

Barrel Jet Patches
Entries 2.342176e+07
JP ADC
JP ID

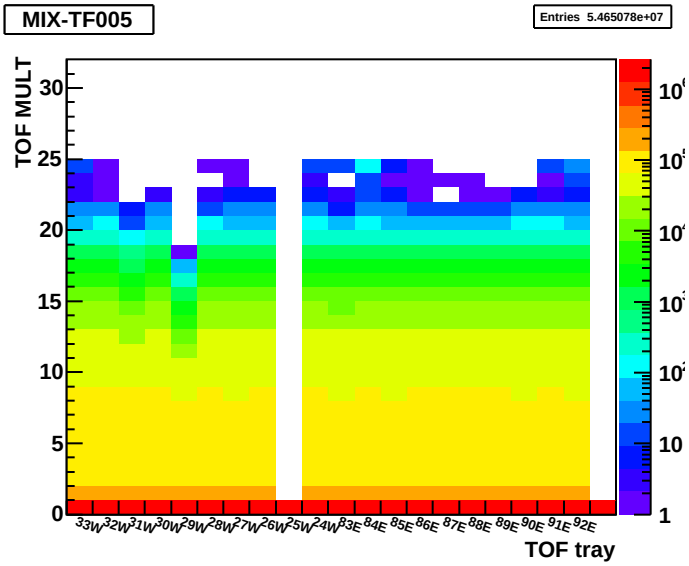

Hybrid Jet Patches
Entries 2602418
JP ADC
JP ID

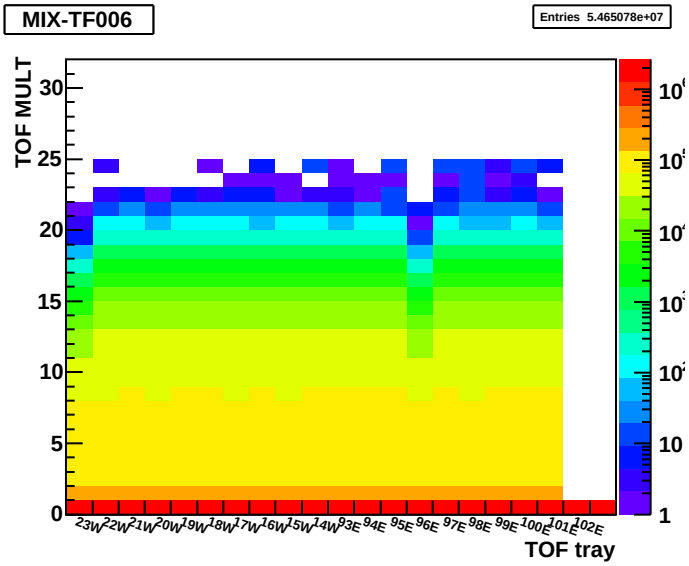

MIX-TF001
Entries 5.465078e+07
TOF MULT
TOF tray

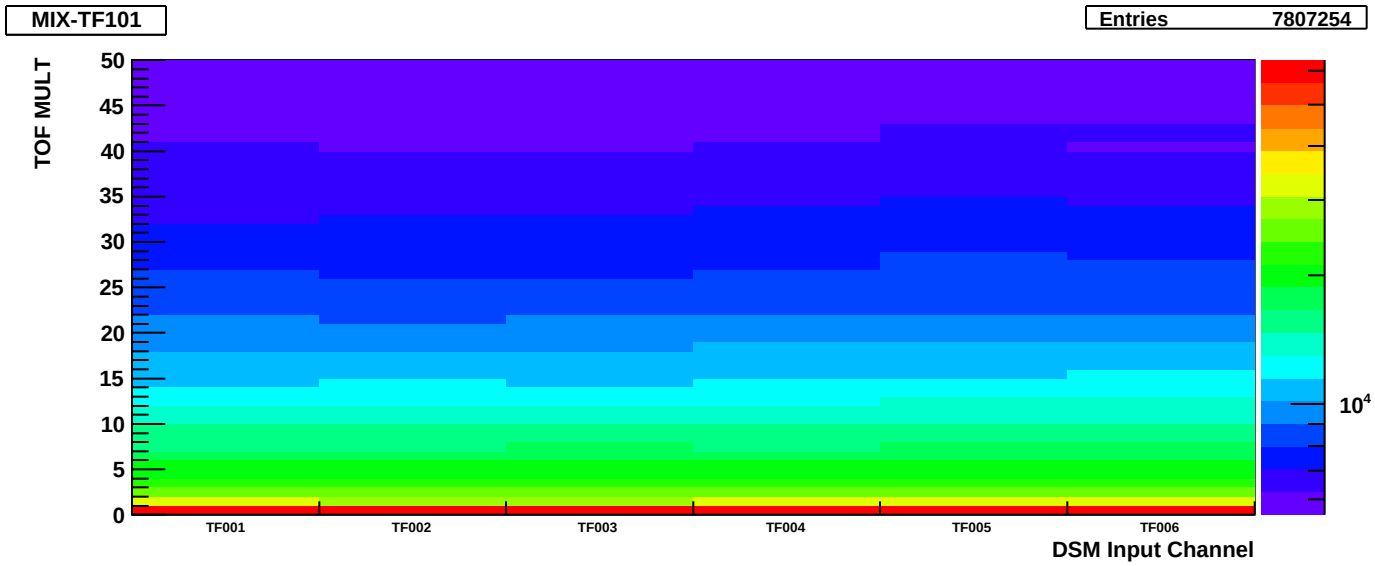

MIX-TF002
Entries 5.465078e+07
TOF MULT
TOF tray

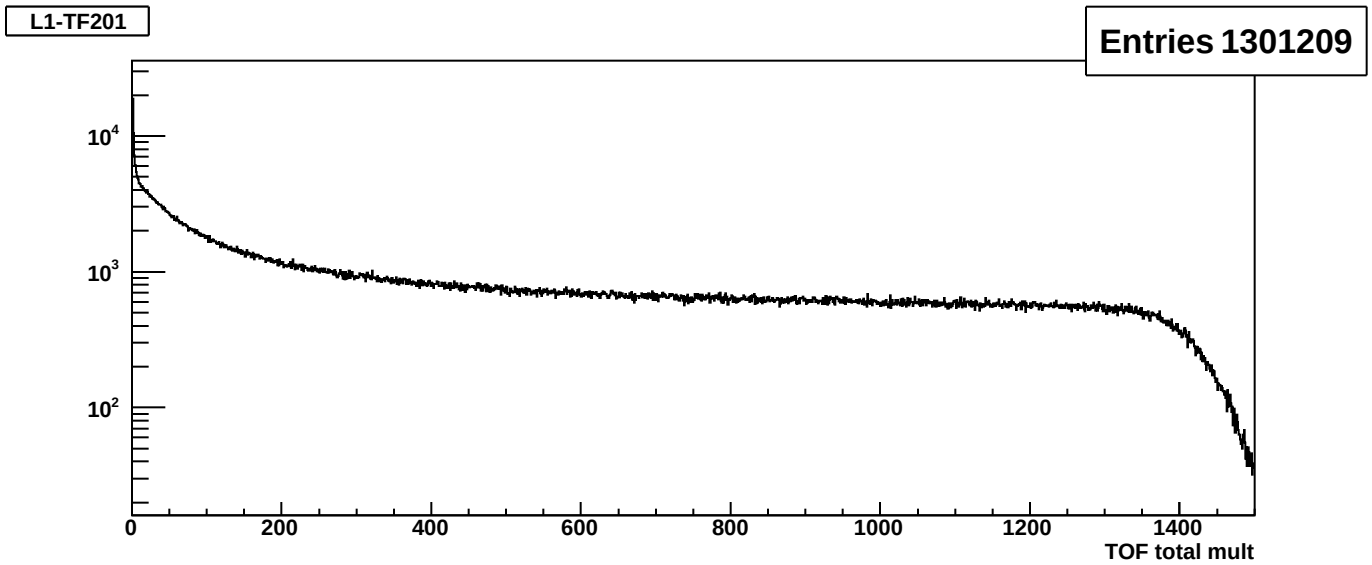

MIX-TF003
Entries 5.465078e+07
TOF MULT
TOF tray

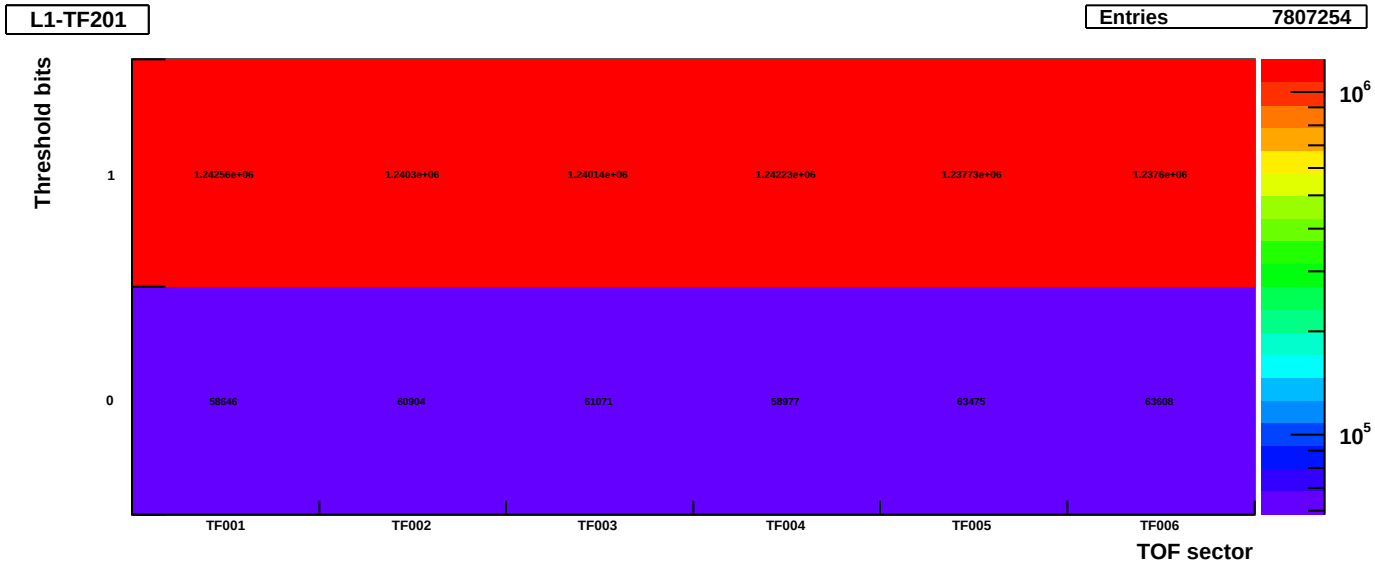

MIX-TF004
Entries 5.465078e+07
TOF MULT
TOF tray

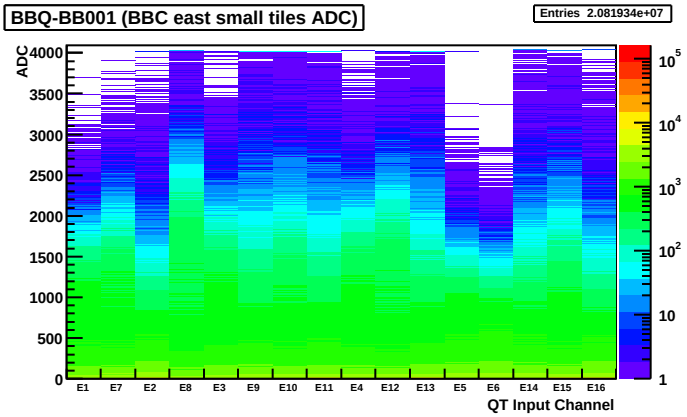

MIX-TF005
Entries 5.465078e+07
TOF MULT
TOF tray

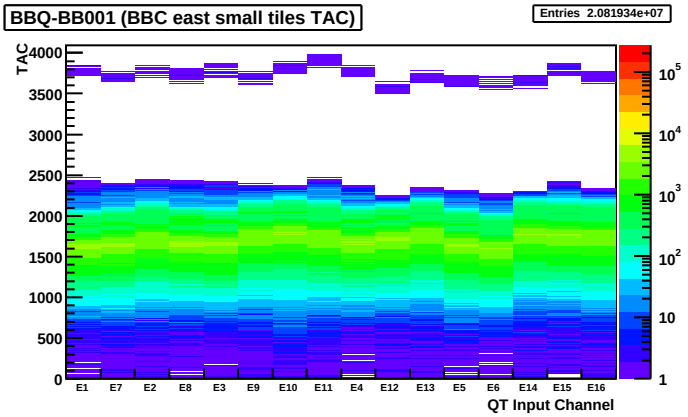

MIX-TF006
Entries 5.465078e+07
TOF MULT
TOF tray

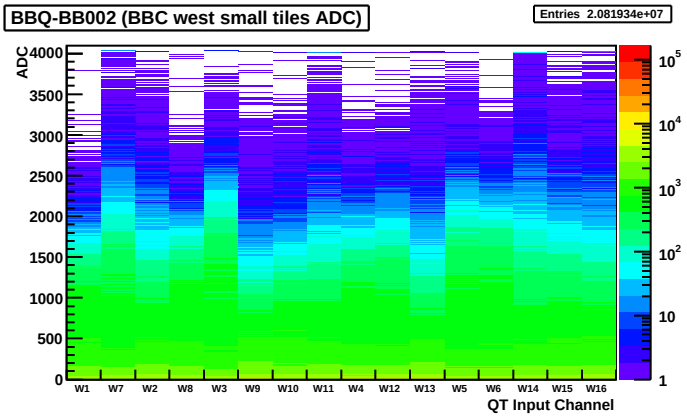

MIX-TF101
Entries 7807254
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

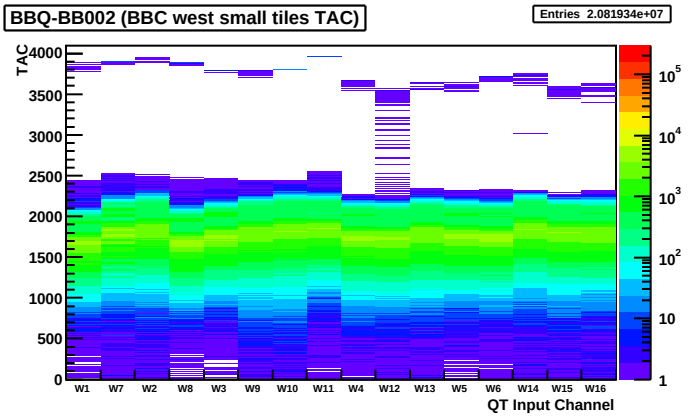

L1-TF201
Entries 1301209
10^4
10^3
10^2
0
200
400
600
800
1000
1200
1400
TOF total mult

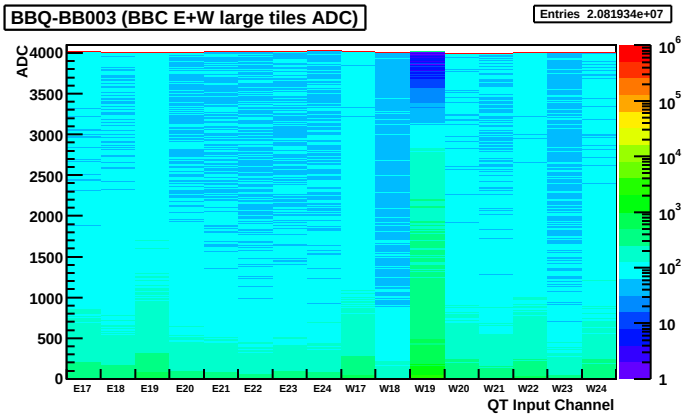

L1-TF201
Entries 7807254
Threshold bits
1
0
1.24256e+06
1.2403e+06
1.24014e+06
1.24223e+06
1.23773e+06
1.2376e+06
58646
60904
61071
58977
63475
63608
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^6
10^5

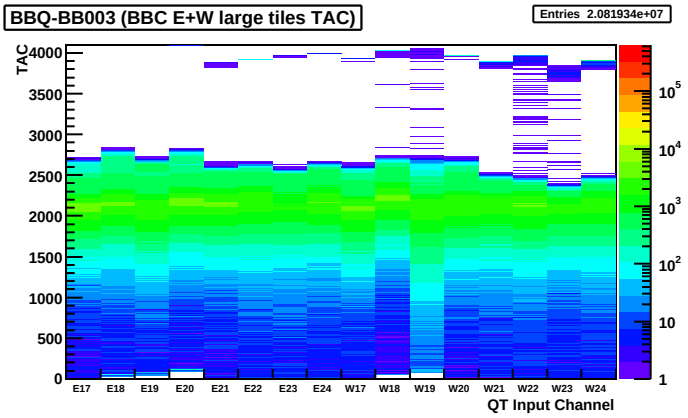

BBQ-BB001 (BBC east small tiles ADC)
Entries 2.081934e+07
ADC
QT Input Channel

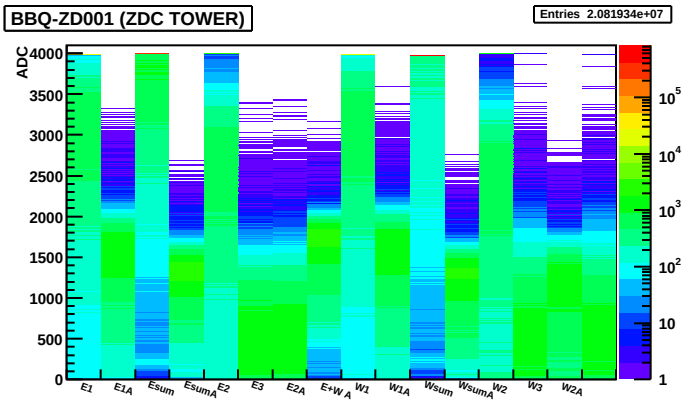

BBQ-BB001 (BBC east small tiles TAC)
Entries 2.081934e+07
TAC
QT Input Channel

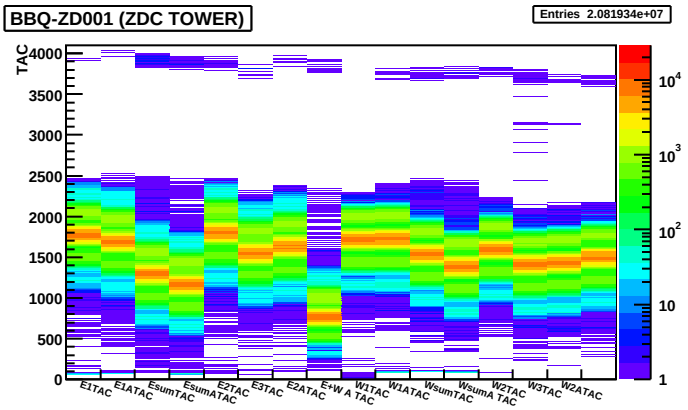

BBQ-BB002 (BBC west small tiles ADC)
Entries 2.081934e+07
ADC
QT Input Channel

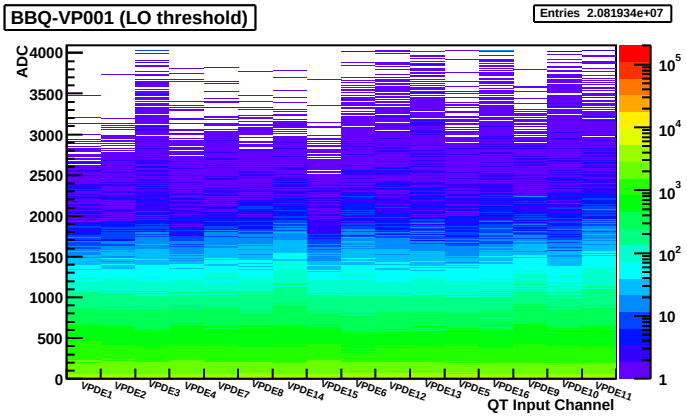

BBQ-BB002 (BBC west small tiles TAC)
Entries 2.081934e+07
TAC
QT Input Channel

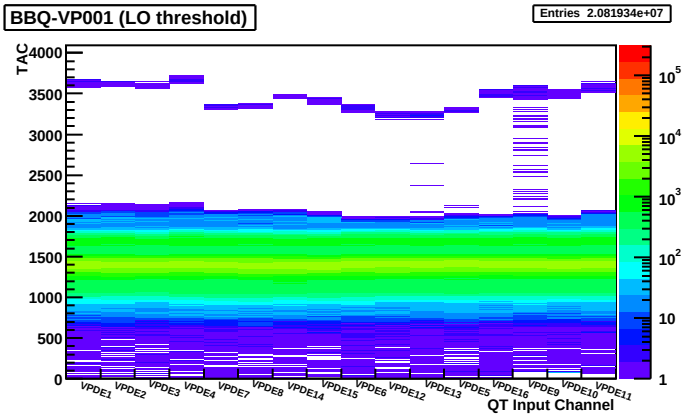

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2.081934e+07
ADC
QT Input Channel

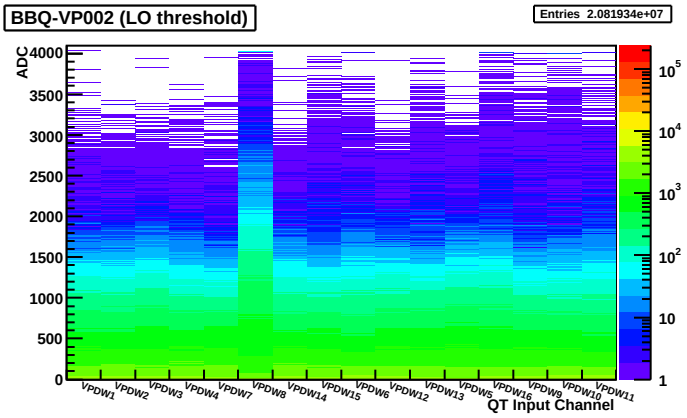

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2.081934e+07
TAC
QT Input Channel

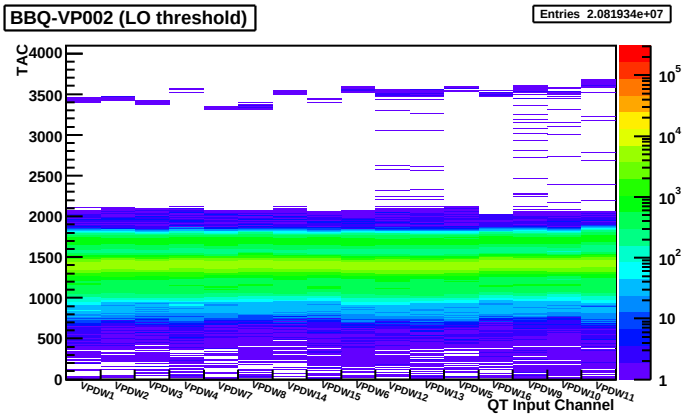

BBQ-ZD001 (ZDC TOWER)
Entries 2.081934e+07
ADC

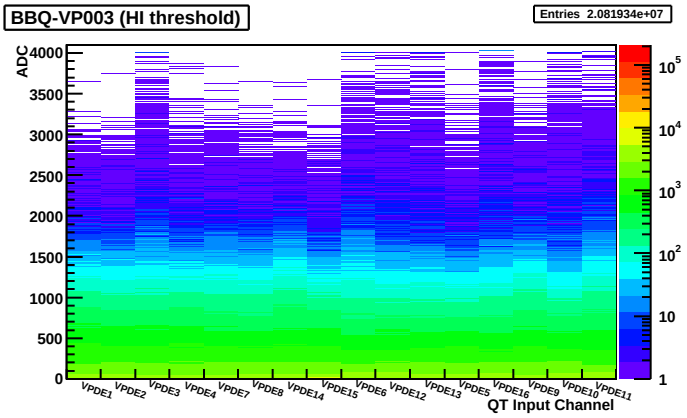

BBQ-ZD001 (ZDC TOWER)
Entries 2.081934e+07
TAC

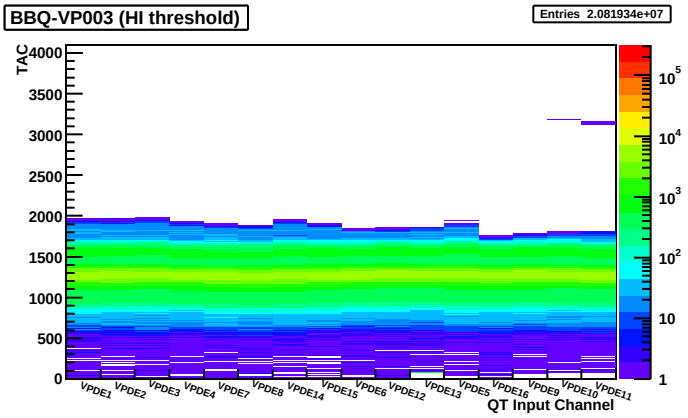

BBQ-VP001 (LO threshold)
Entries 2.081934e+07
ADC
QT Input Channel

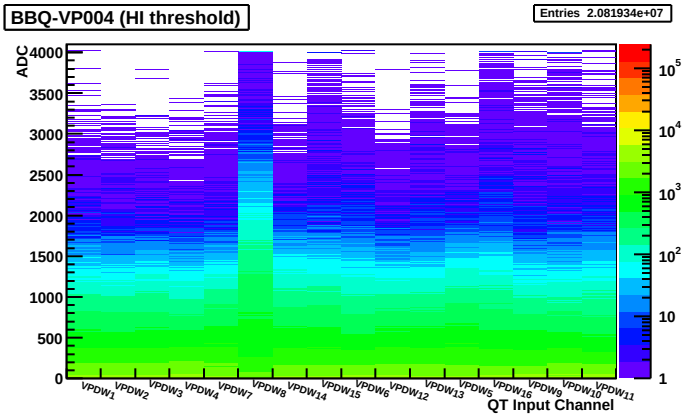

BBQ-VP001 (LO threshold)
Entries 2.081934e+07
TAC
QT Input Channel

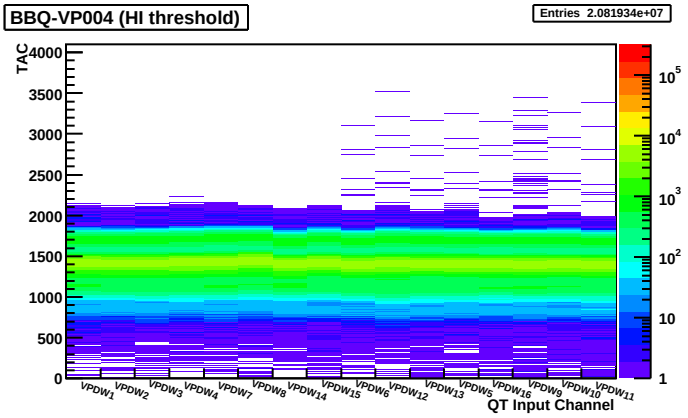

BBQ-VP002 (LO threshold)
Entries 2.081934e+07
ADC
QT Input Channel

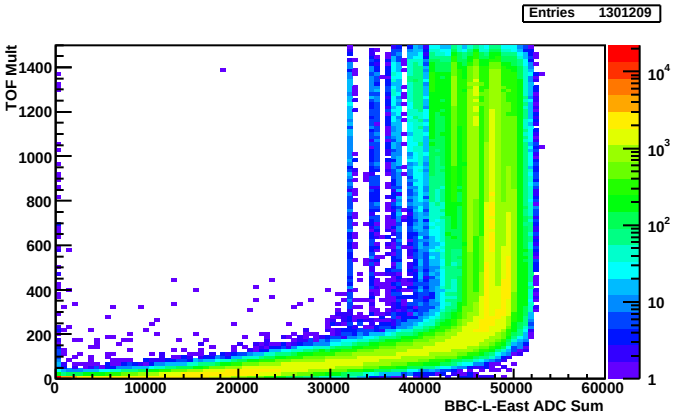

BBQ-VP002 (LO threshold)
Entries 2.081934e+07
TAC
QT Input Channel

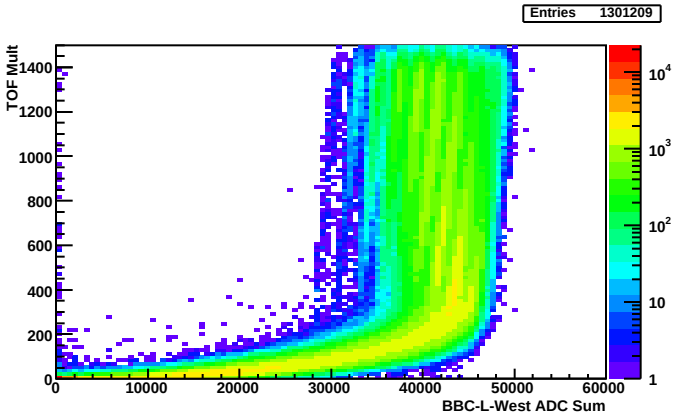

BBQ-VP003 (HI threshold)
Entries 2.081934e+07
ADC
QT Input Channel

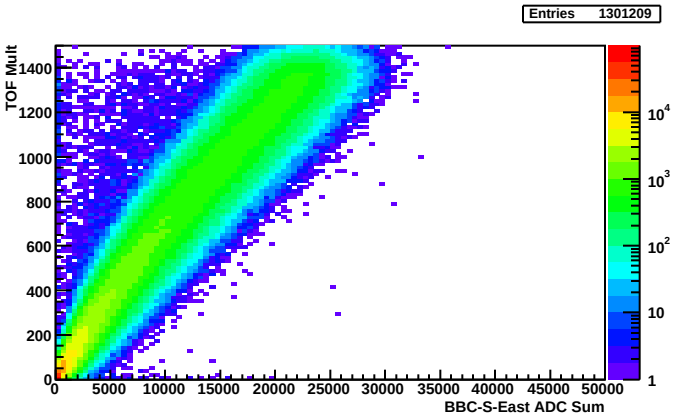

BBQ-VP003 (HI threshold)
Entries 2.081934e+07
TAC
QT Input Channel

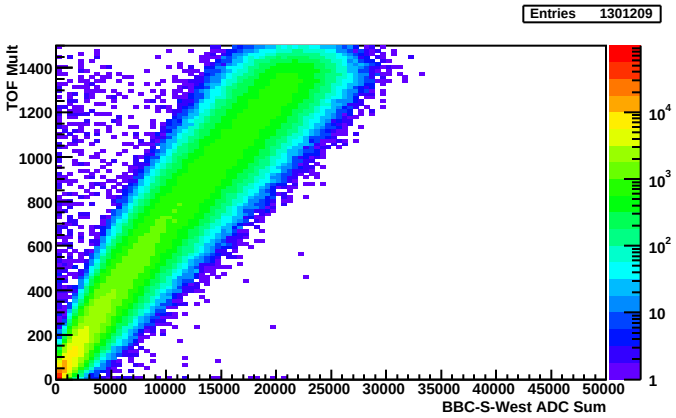

BBQ-VP004 (HI threshold)
Entries 2.081934e+07
ADC
QT Input Channel

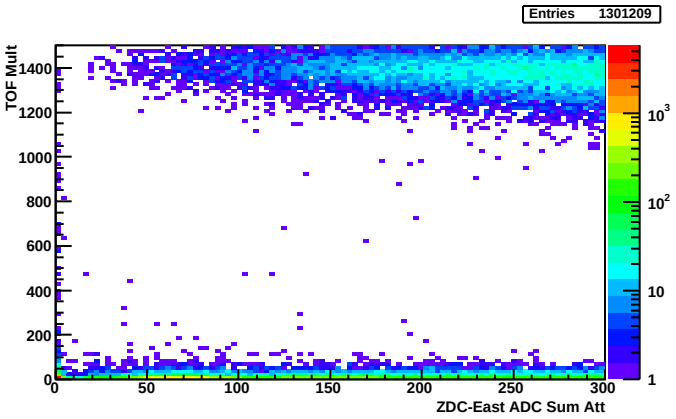

BBQ-VP004 (HI threshold)
Entries 2.081934e+07
TAC
QT Input Channel

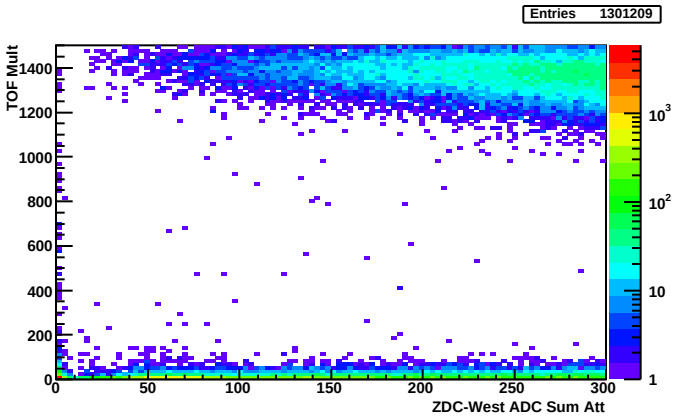

Entries 1301209
TOF Mult
BBC-L-East ADC Sum

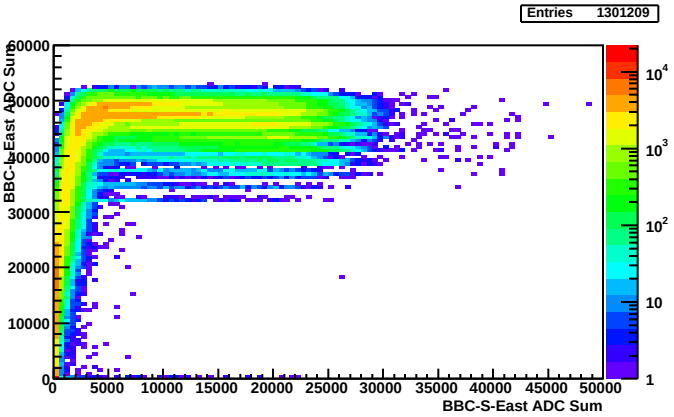

Entries 1301209
TOF Mult
BBC-L-West ADC Sum

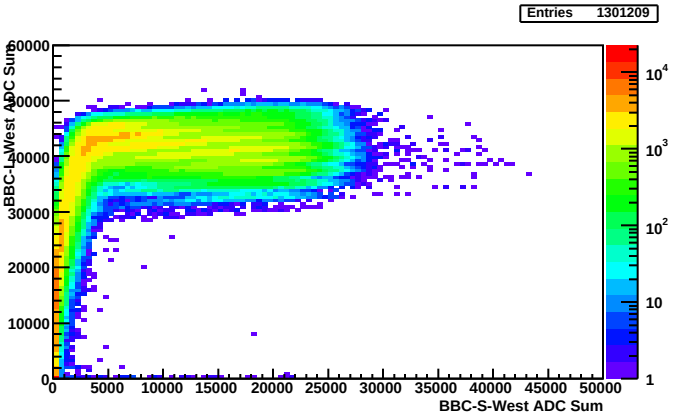

Entries 1301209
TOF Mult
BBC-S-East ADC Sum

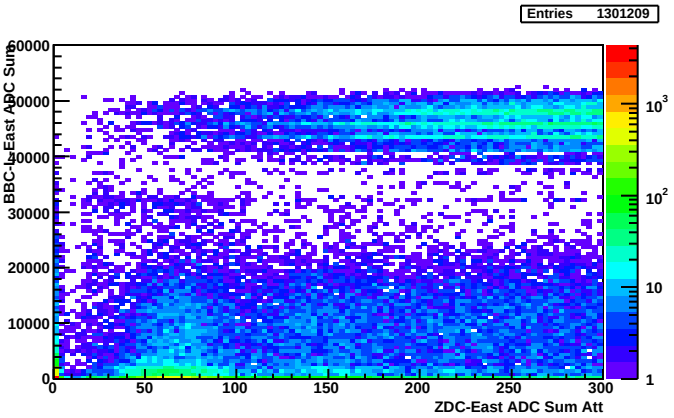

Entries 1301209
TOF Mult
BBC-S-West ADC Sum

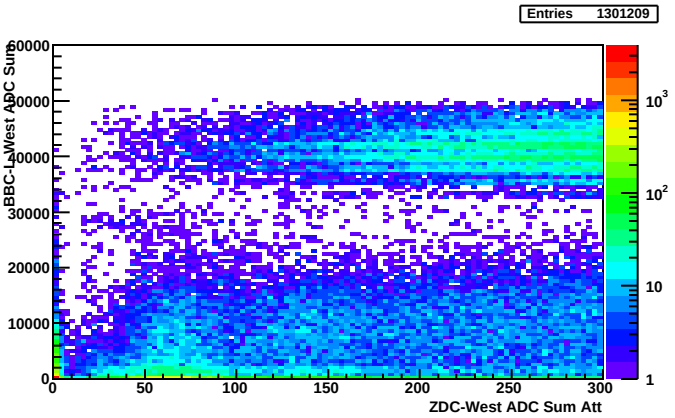

Entries 1301209
TOF Mult
ZDC-East ADC Sum Att

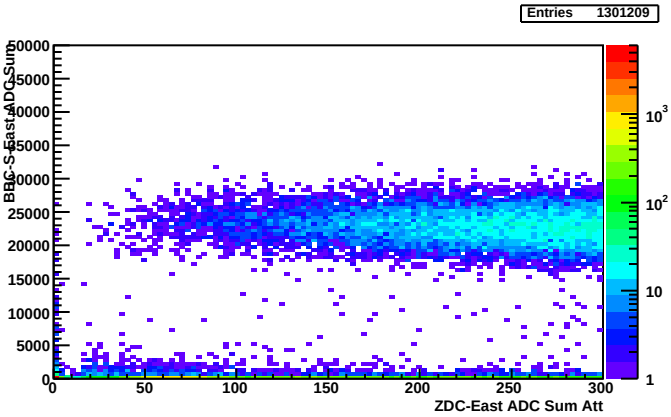

Entries 1301209
TOF Mult
ZDC-West ADC Sum Att

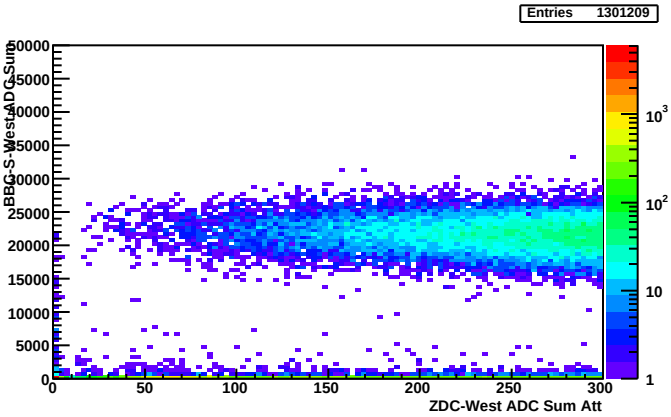

Entries 1301209
BBC-L-East ADC Sum
BBC-S-East ADC Sum

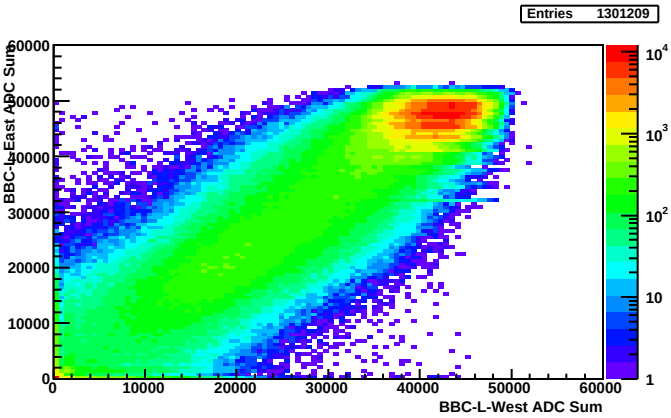

Entries 1301209
BBC-L-West ADC Sum
BBC-S-West ADC Sum

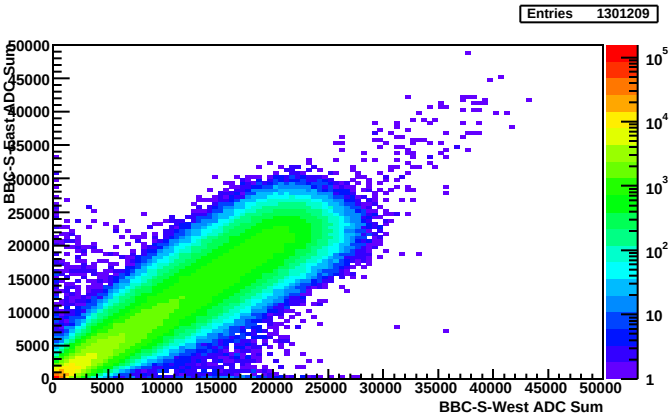

Entries 1301209
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

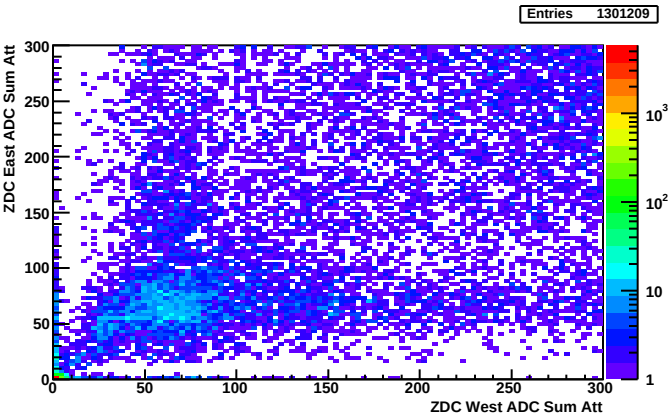

Entries 1301209
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

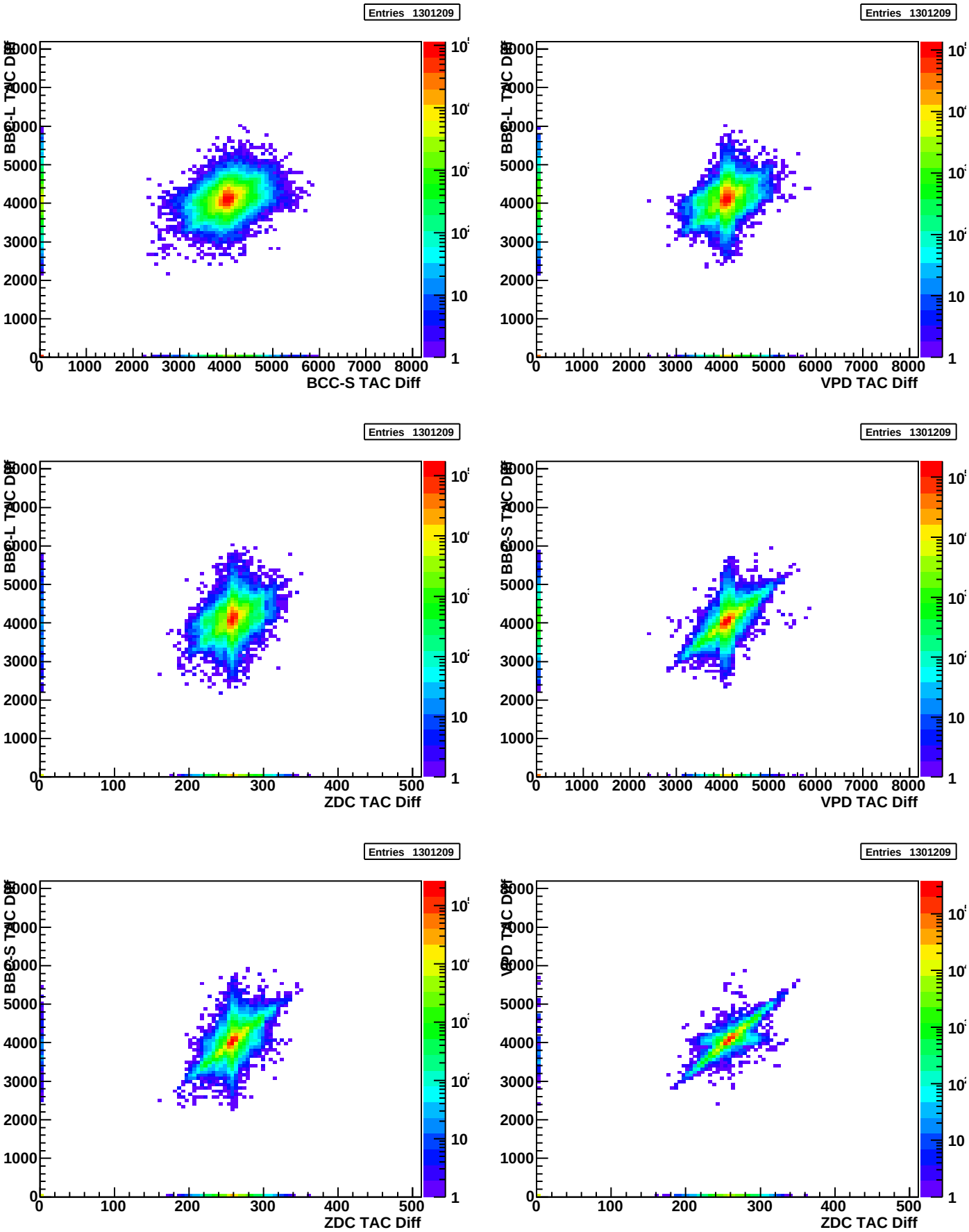

Entries 1301209
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

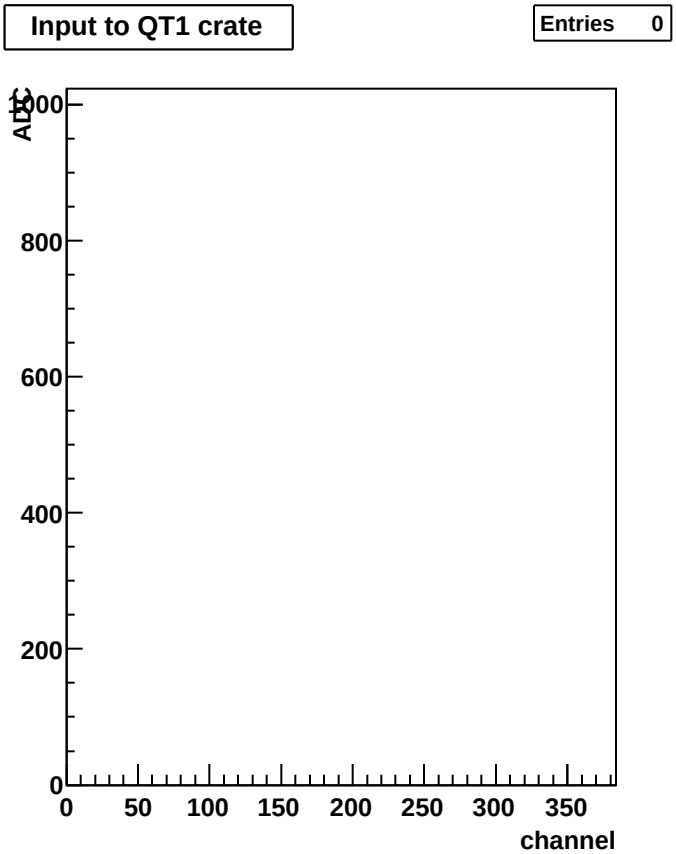

Entries 1301209
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

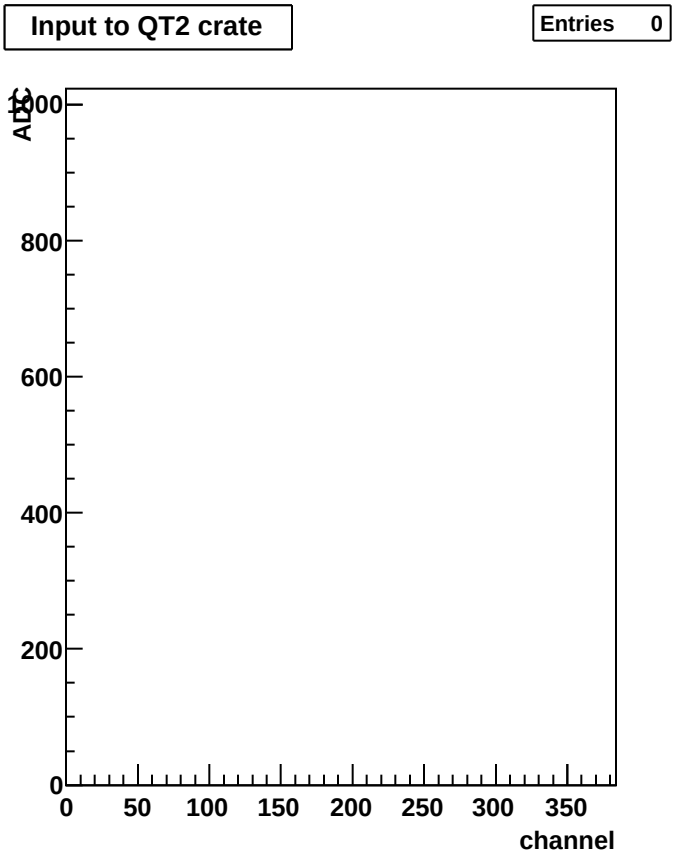

Entries 1301209
BBC-L-East ADC Sum
BBC-L-West ADC Sum

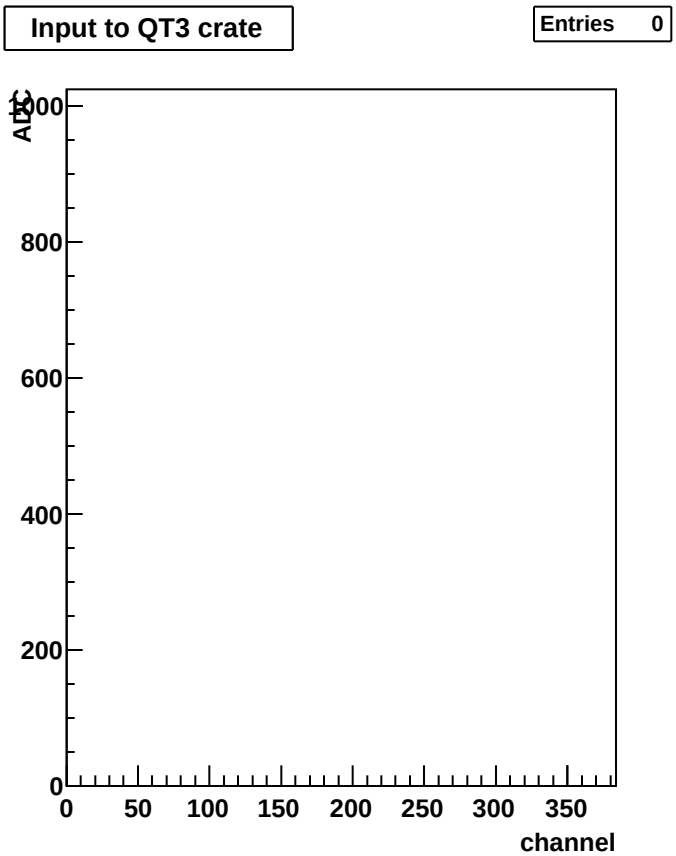

Entries 1301209
BBC-S-East ADC Sum
BBC-S-West ADC Sum

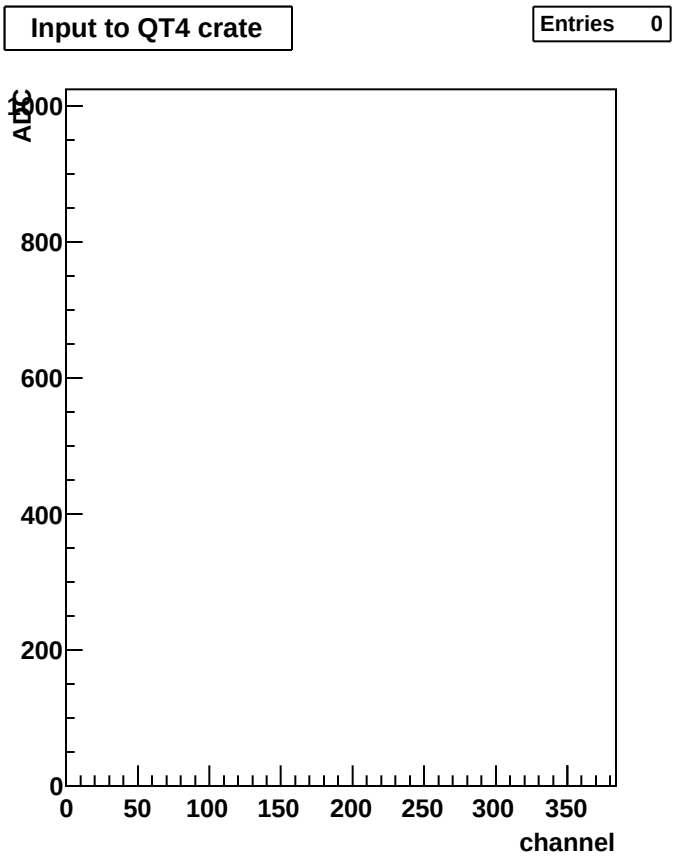

Entries 1301209
ZDC East ADC Sum Att
ZDC West ADC Sum Att

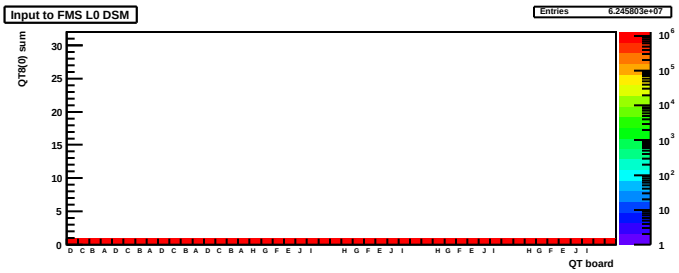

Entries 1301209
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1301209
BBC-L TAC Diff
VPD TAC Diff
Entries 1301209
BBC-L TAC Diff
ZDC TAC Diff
Entries 1301209
BBC-S TAC Diff
VPD TAC Diff
Entries 1301209
BBC-S TAC Diff
ZDC TAC Diff
Entries 1301209
VPD TAC Diff
ZDC TAC Diff

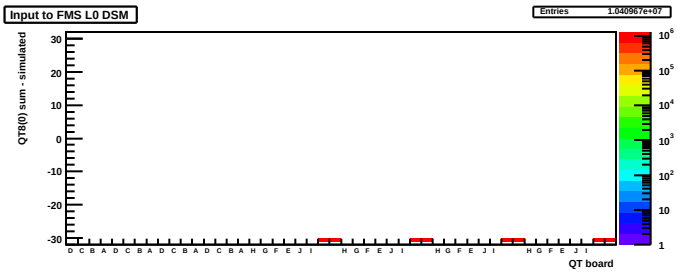

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

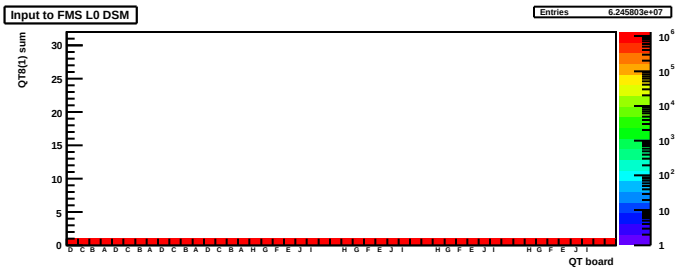

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

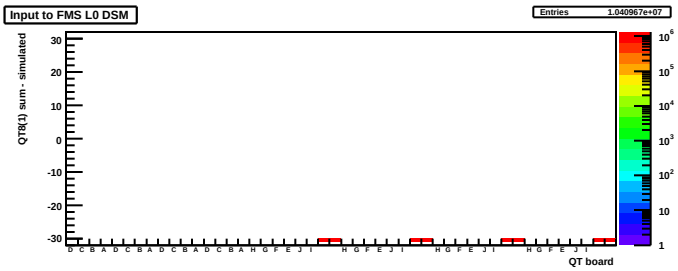

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

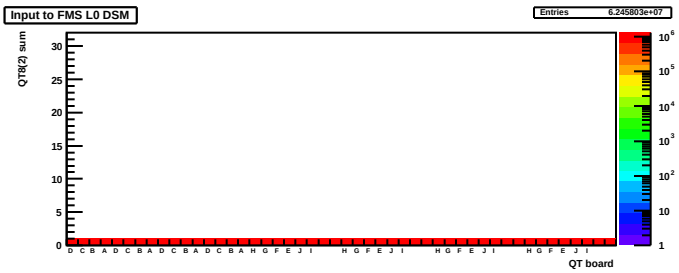

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

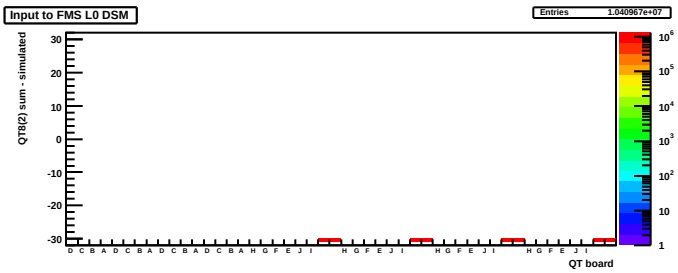

Input to FMS L0 DSM
Entries 6.245803e+07
QT8(0) sum
QT board

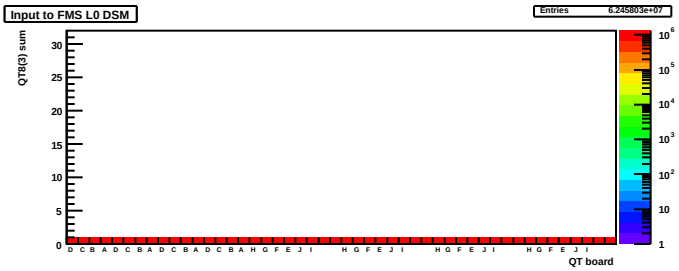

Input to FMS L0 DSM
Entries 1.040967e+07
QT8(0) sum - simulated
QT board

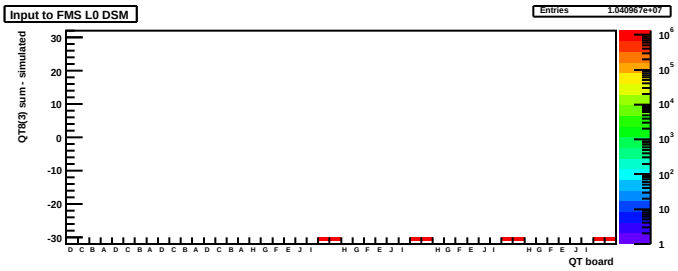

Input to FMS L0 DSM
Entries 6.245803e+07
QT8(1) sum
QT board

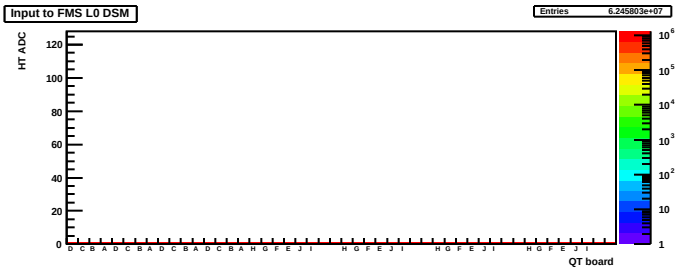

Input to FMS L0 DSM
Entries 1.040967e+07
QT8(1) sum - simulated
QT board

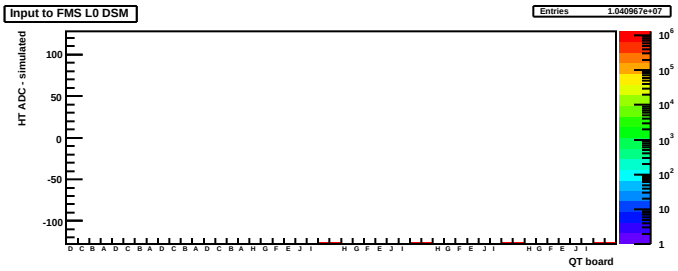

Input to FMS L0 DSM
Entries 6.245803e+07
QT8(2) sum
QT board

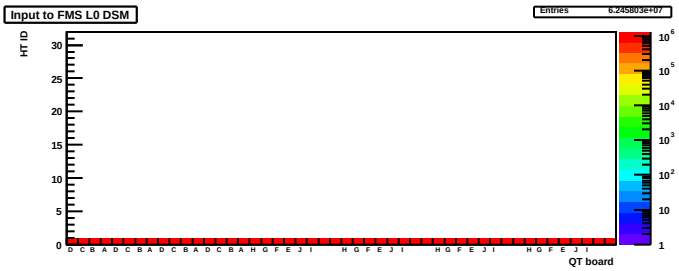

Input to FMS L0 DSM
Entries 1.040967e+07
QT8(2) sum - simulated
QT board

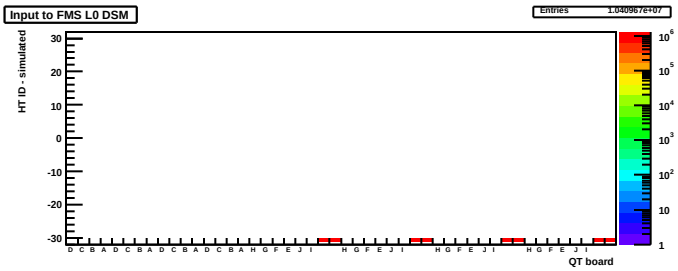

Input to FMS L0 DSM
Entries 6.245803e+07
QT8(3) sum
QT board

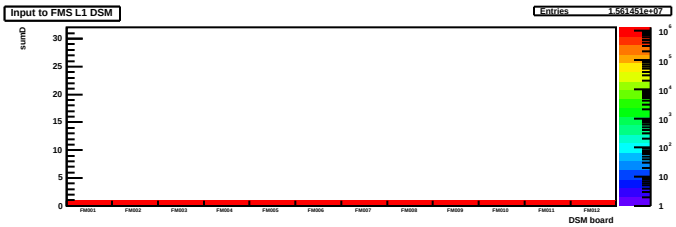

Input to FMS L0 DSM
Entries 1.040967e+07
QT8(3) sum - simulated
QT board

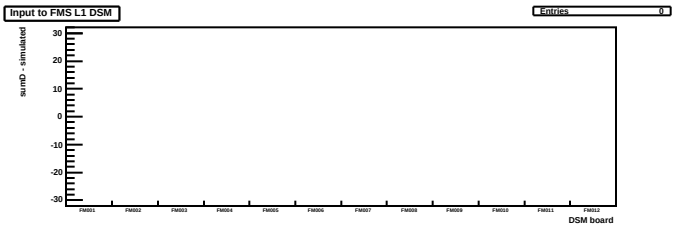

Input to FMS L0 DSM
Entries 6.245803e+07
HT ADC
QT board

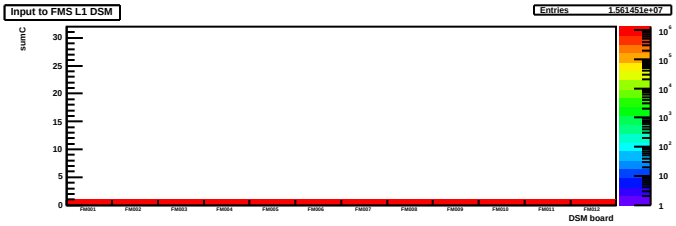

Input to FMS L0 DSM
Entries 1.040967e+07
HT ADC - simulated
QT board

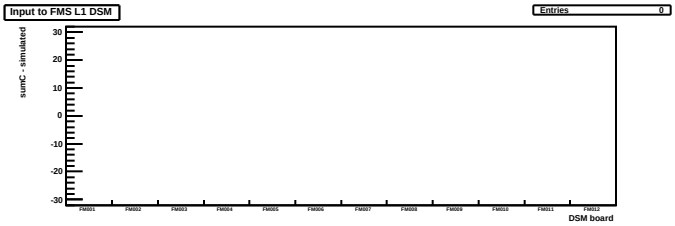

Input to FMS L0 DSM
Entries 6.245803e+07
HT ID
QT board

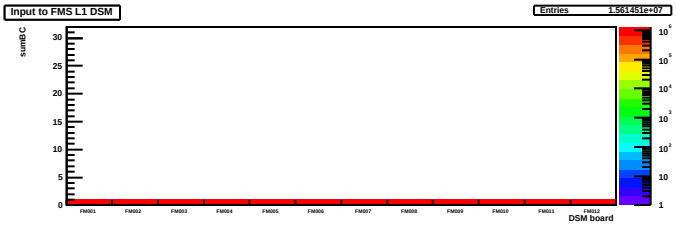

Input to FMS L0 DSM
Entries 1.040967e+07
HT ID - simulated
QT board

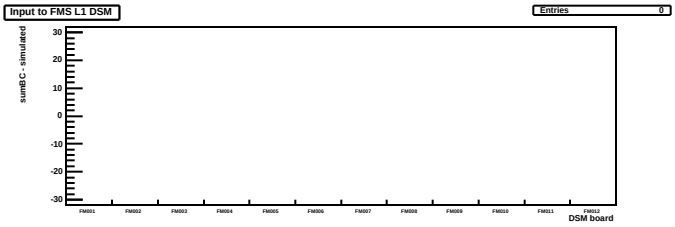

Input to FMS L1 DSM
Entries 1.561451e+07
sumD
DSM board

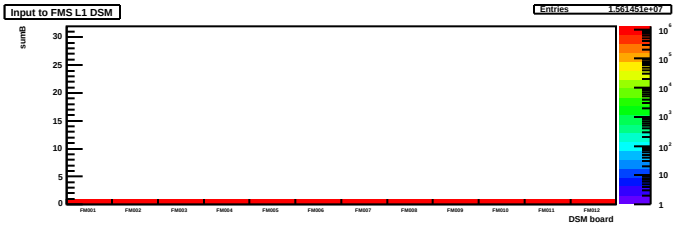

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

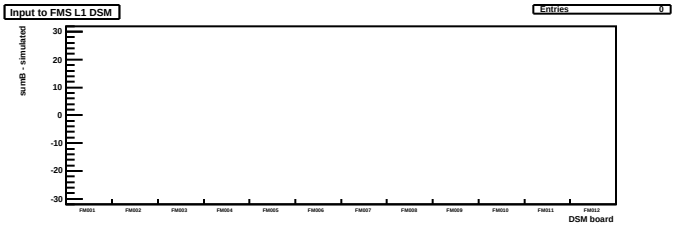

Input to FMS L1 DSM
Entries 1.561451e+07
sumC
DSM board

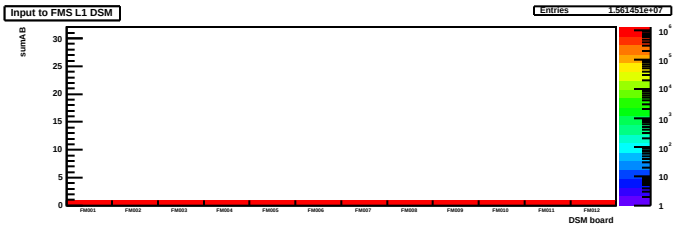

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

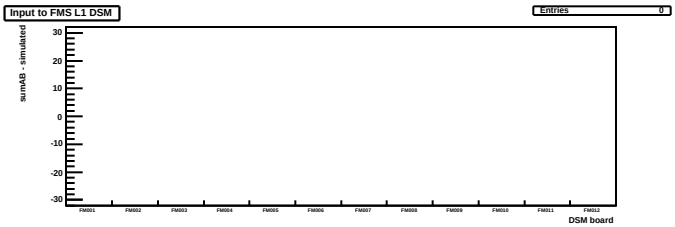

Input to FMS L1 DSM
Entries 1.561451e+07
sumBC
DSM board

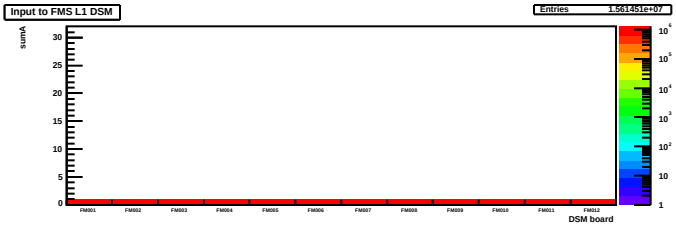

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

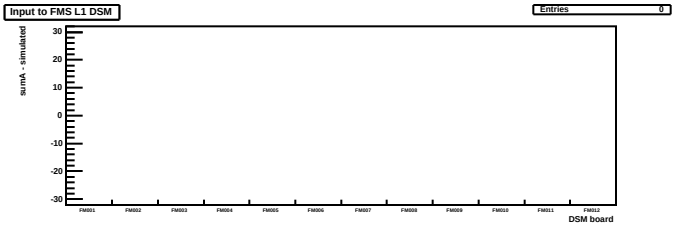

Input to FMS L1 DSM
Entries 1.561451e+07
sumB
DSM board

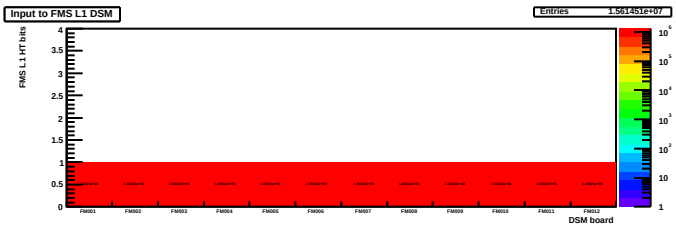

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

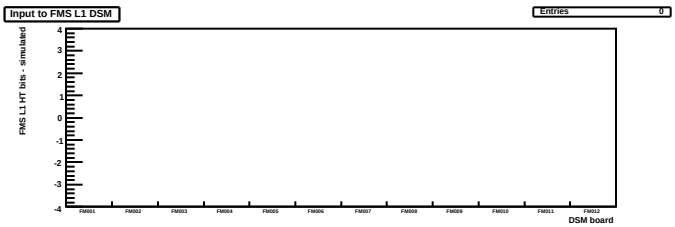

Input to FMS L1 DSM
Entries 1.561451e+07
sumAB
DSM board

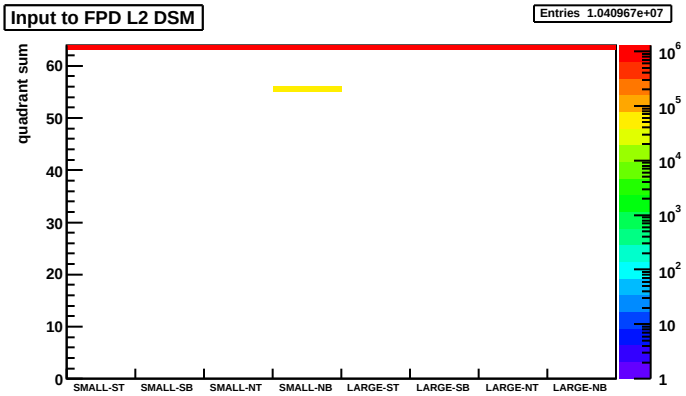

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

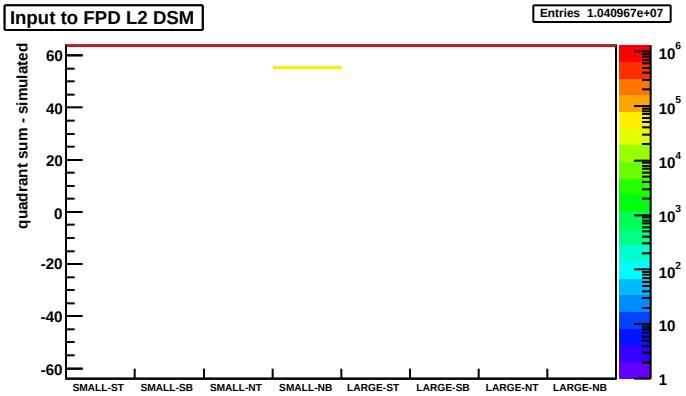

Input to FMS L1 DSM
Entries 1.561451e+07
sumA
DSM board

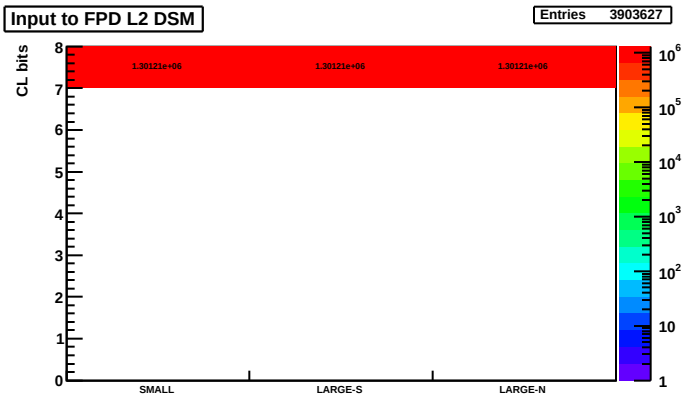

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

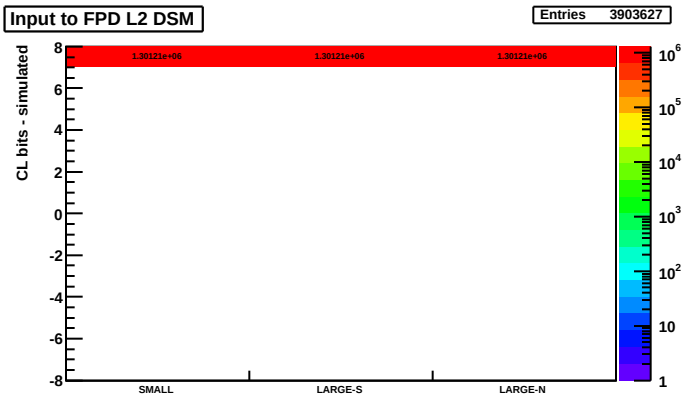

Input to FMS L1 DSM
Entries 1.561451e+07
FMS L1 HT bits
DSM board

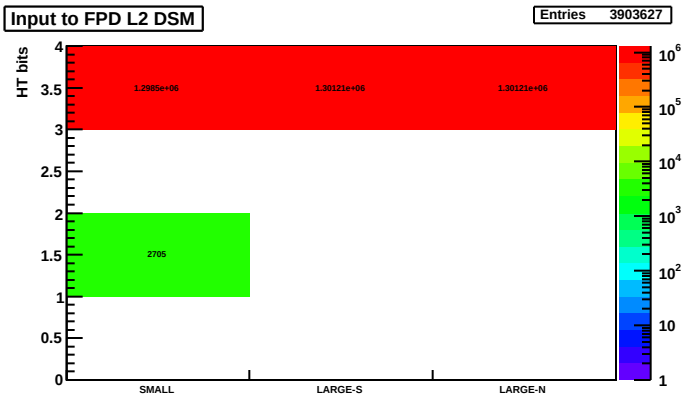

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

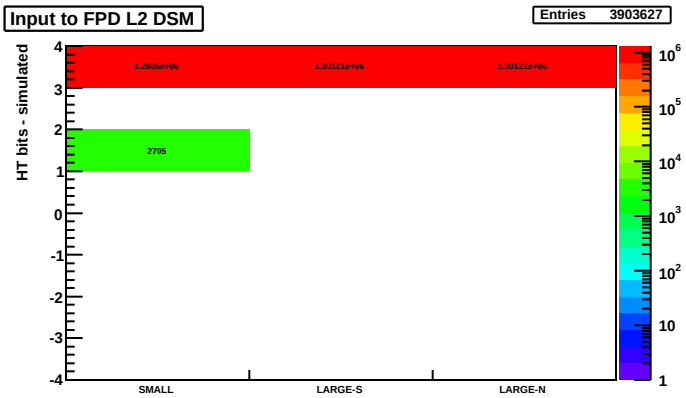

Input to FPD L2 DSM
Entries 1.040967e+07
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

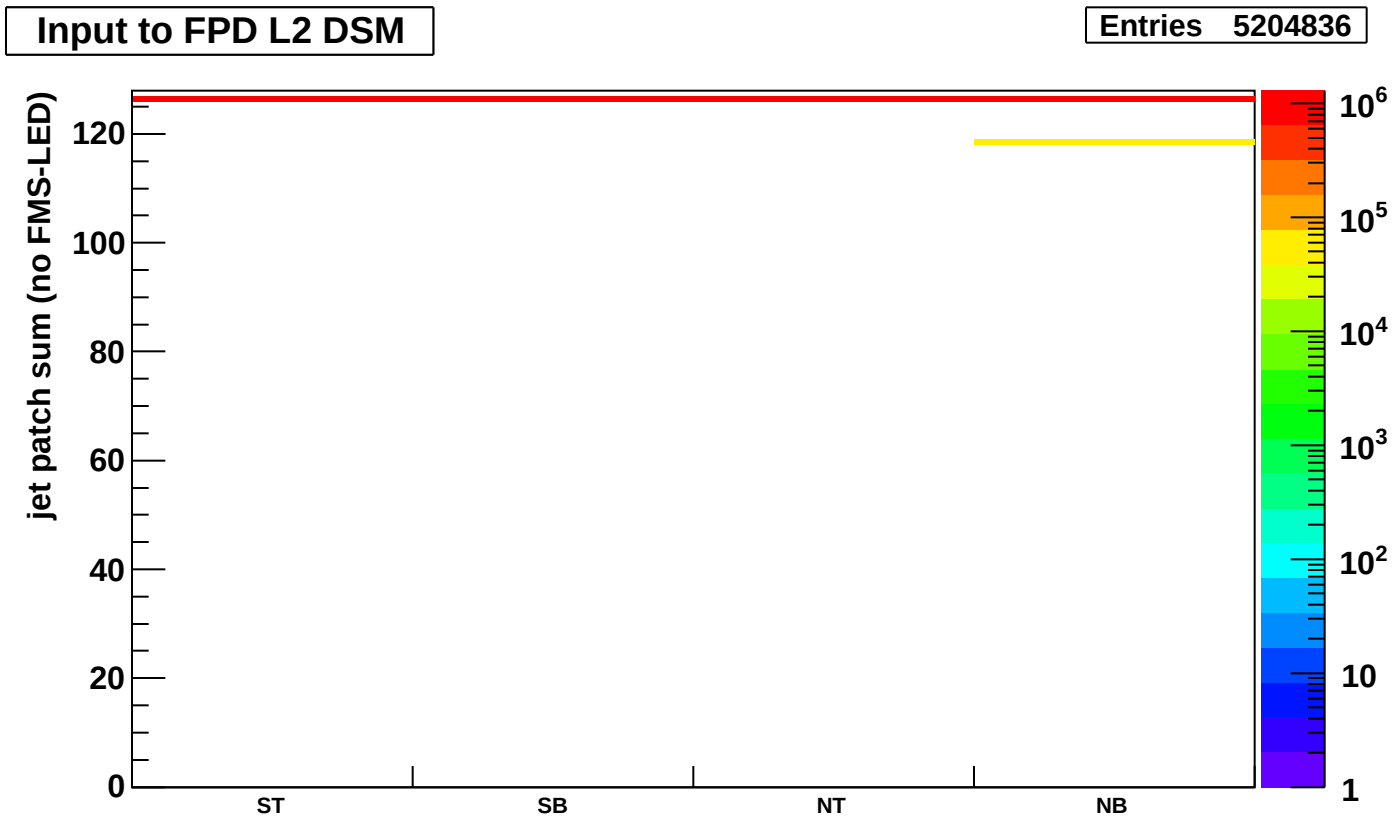

Input to FPD L2 DSM
Entries 1.040967e+07
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

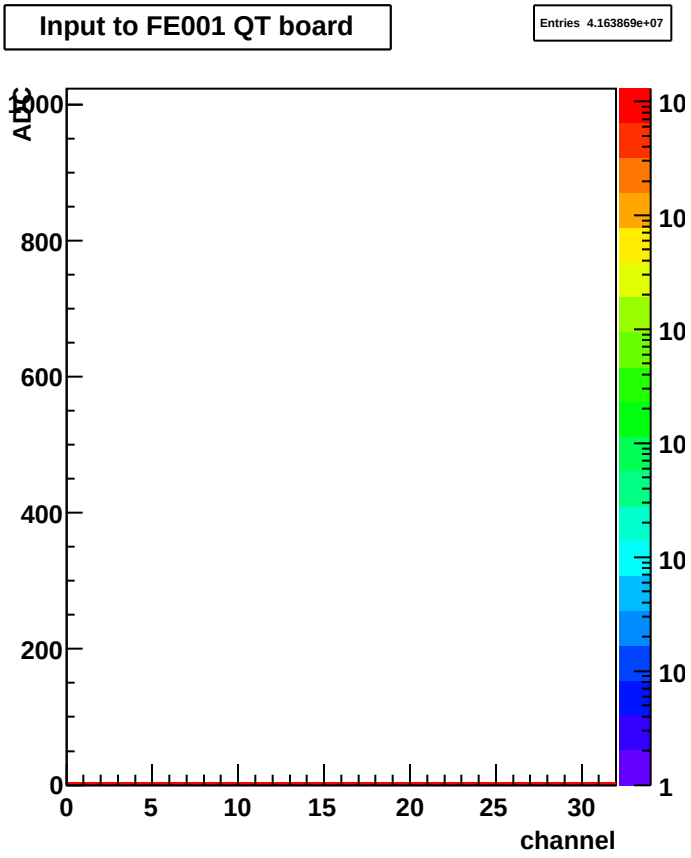

Input to FPD L2 DSM
Entries 3903627
CL bits
1.30121e+06
1.30121e+06
1.30121e+06
SMALL
LARGE-S
LARGE-N

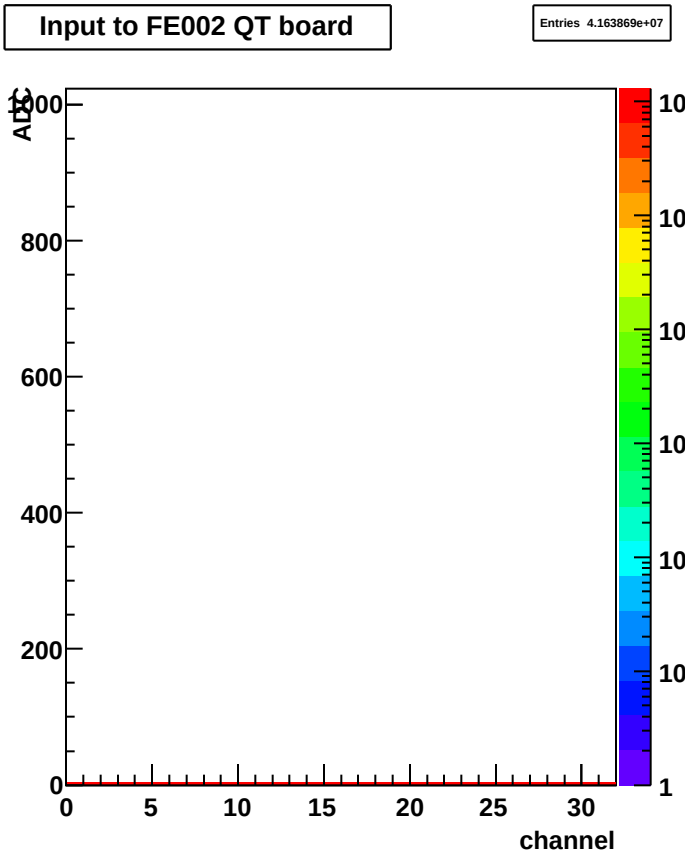

Input to FPD L2 DSM
Entries 3903627
CL bits - simulated
1.30121e+06
1.30121e+06
1.30121e+06
SMALL
LARGE-S
LARGE-N

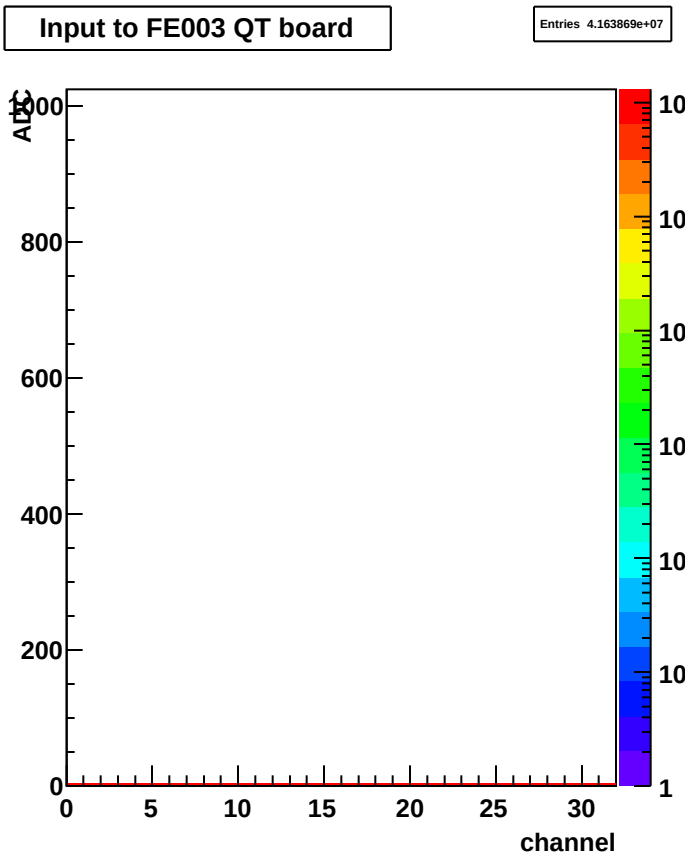

Input to FPD L2 DSM
Entries 3903627
HT bits
1.2985e+06
1.30121e+06
1.30121e+06
2705
SMALL
LARGE-S
LARGE-N

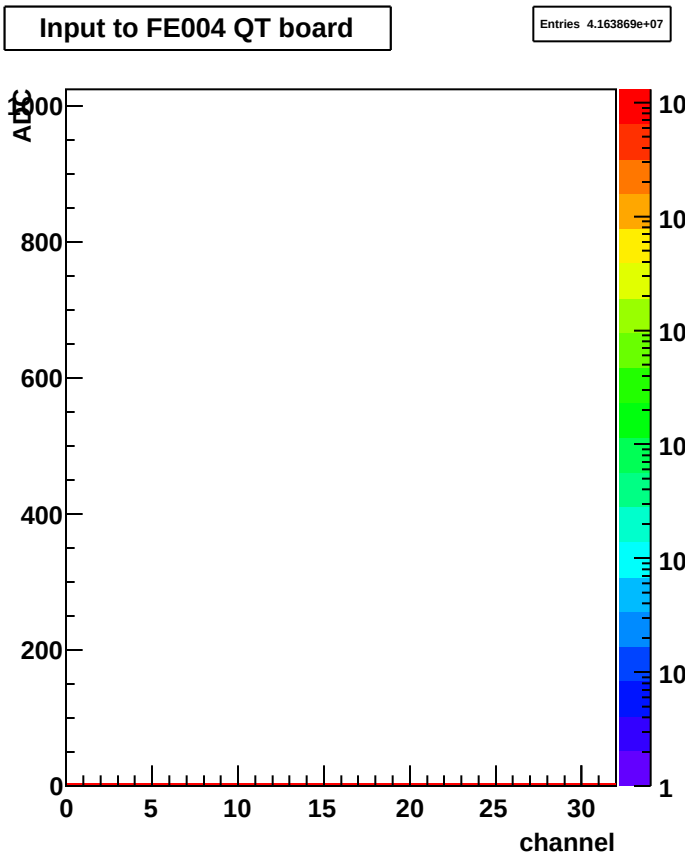

Input to FPD L2 DSM
Entries 3903627
HT bits - simulated
1.2985e+06
1.30121e+06
1.30121e+06
2705
SMALL
LARGE-S
LARGE-N

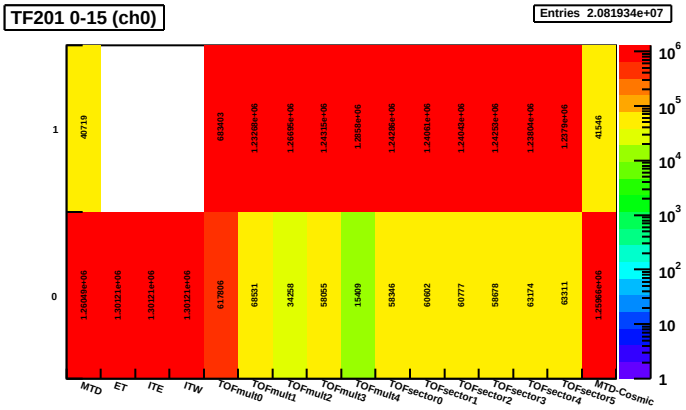

Input to FPD L2 DSM
Entries 5204836
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5
10^6

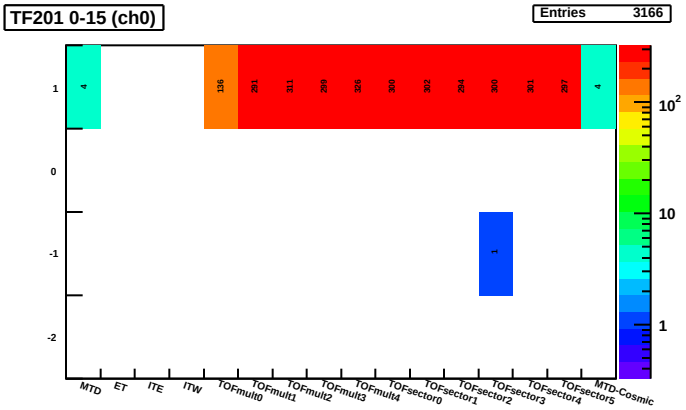

Input to FE001 QT board
Entries 4.163869e+07
ADC
channel

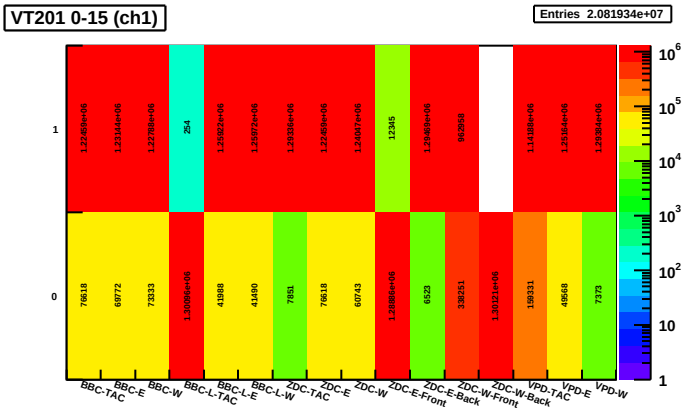

Input to FE002 QT board
Entries 4.163869e+07
ADC
channel

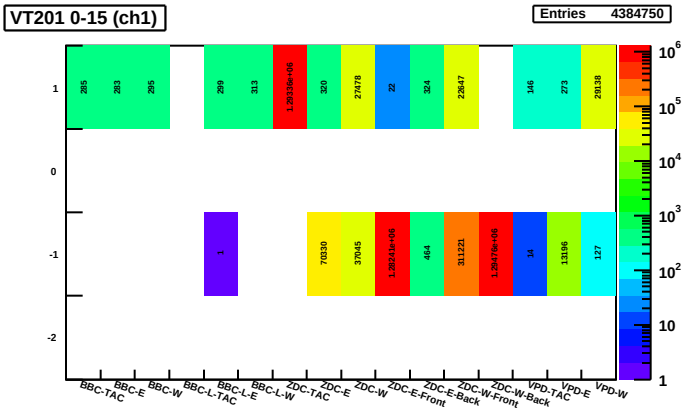

Input to FE003 QT board
Entries 4.163869e+07
ADC
channel

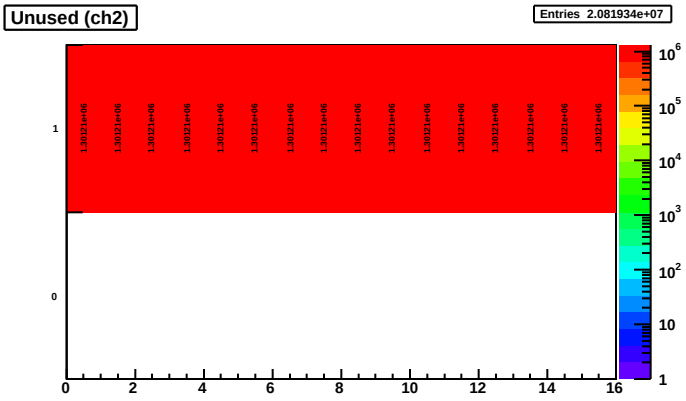

Input to FE004 QT board
Entries 4.163869e+07
ADC
channel

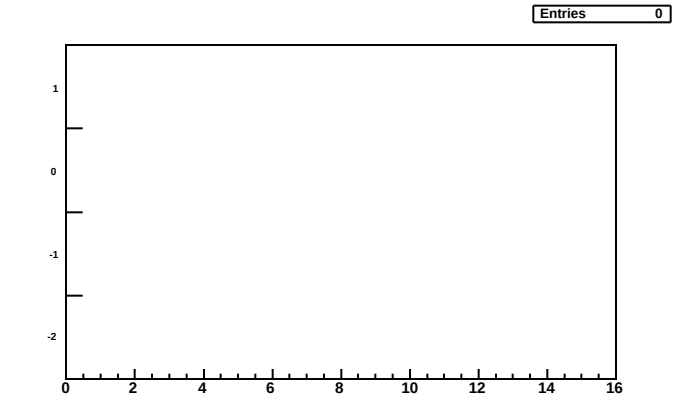

TF201 0-15 (ch0)
Entries 2.081934e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

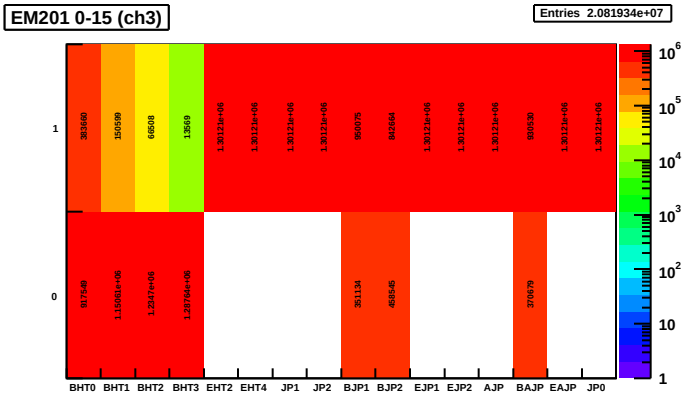

TF201 0-15 (ch0)
Entries 3166
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

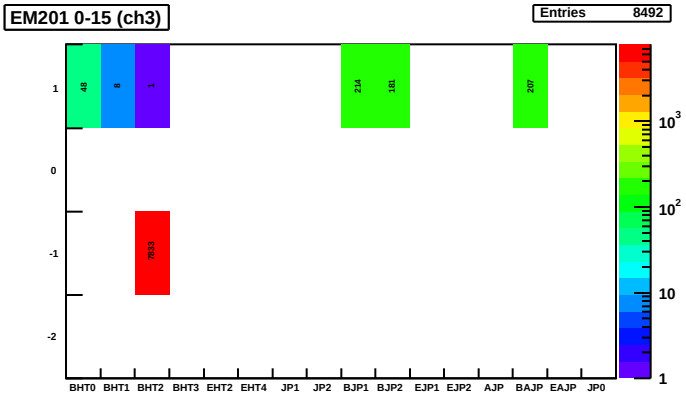

VT201 0-15 (ch1)
Entries 2.081934e+07
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 4384750
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

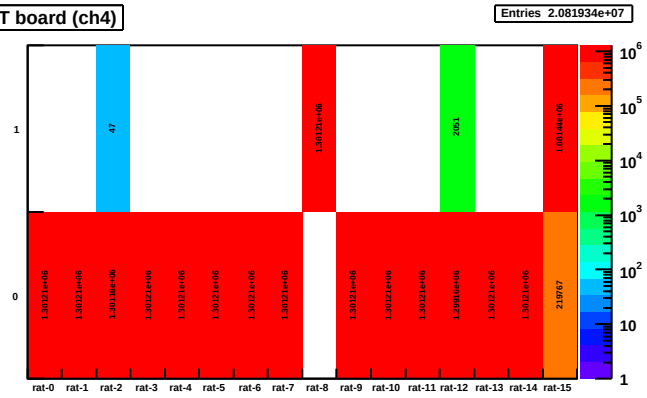

Unused (ch2)
Entries 2.081934e+07

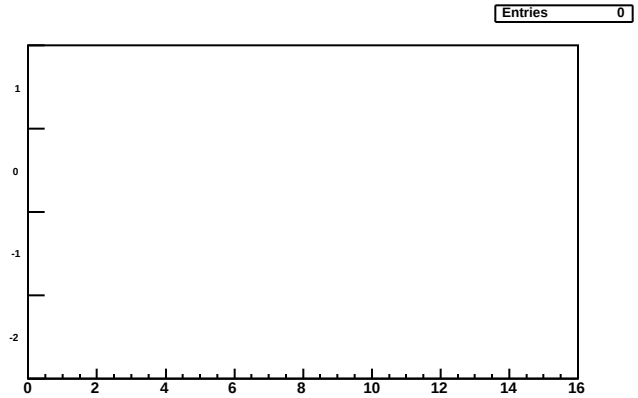

Entries 0


EM201 0-15 (ch3)
Entries 2.081934e+07
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

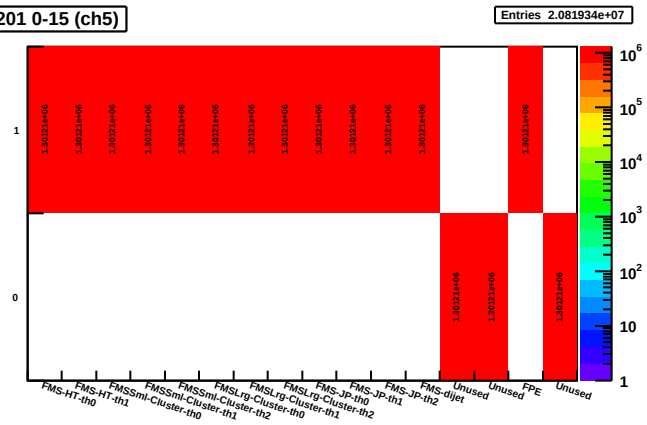

EM201 0-15 (ch3)
Entries 8492
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

RAT board (ch4)

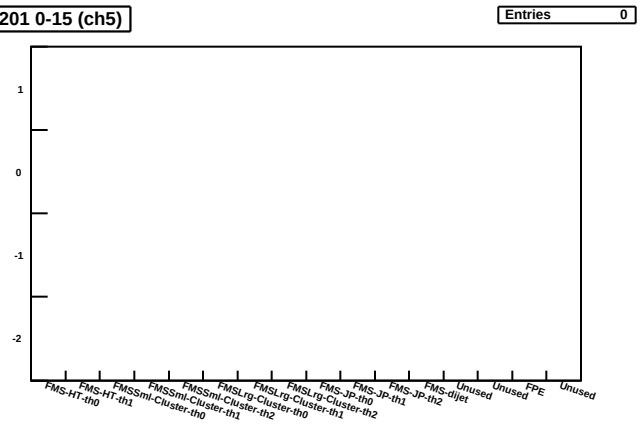




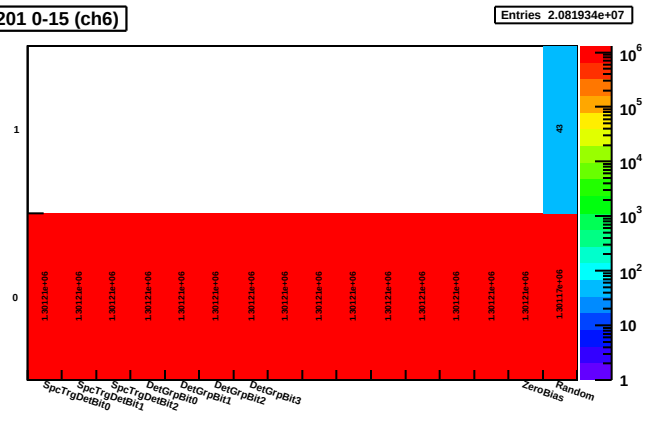
FP201 0-15 (ch5)

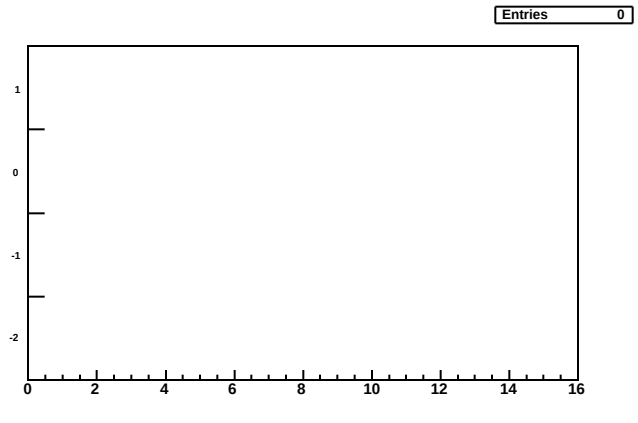
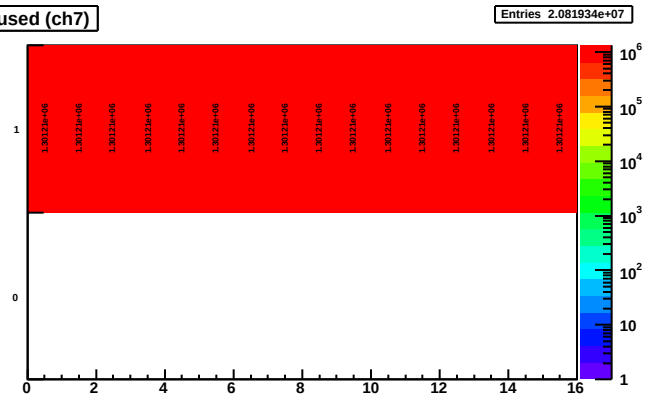
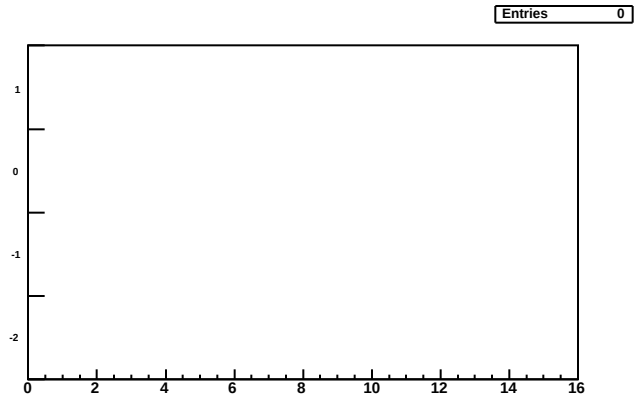


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)