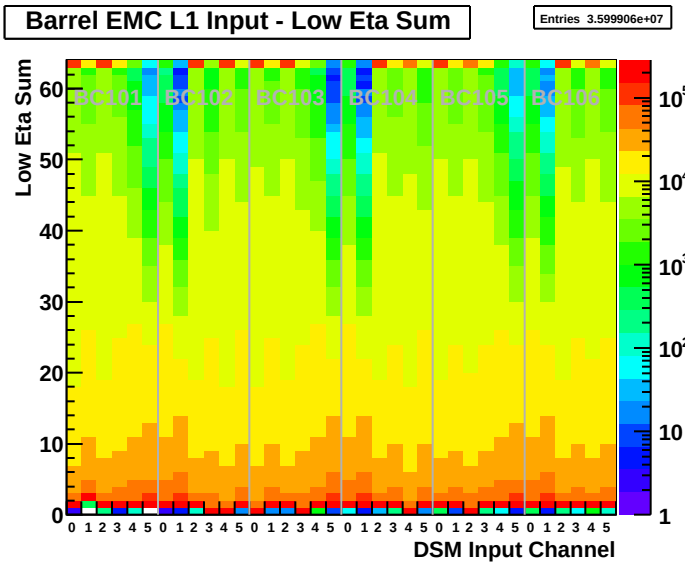
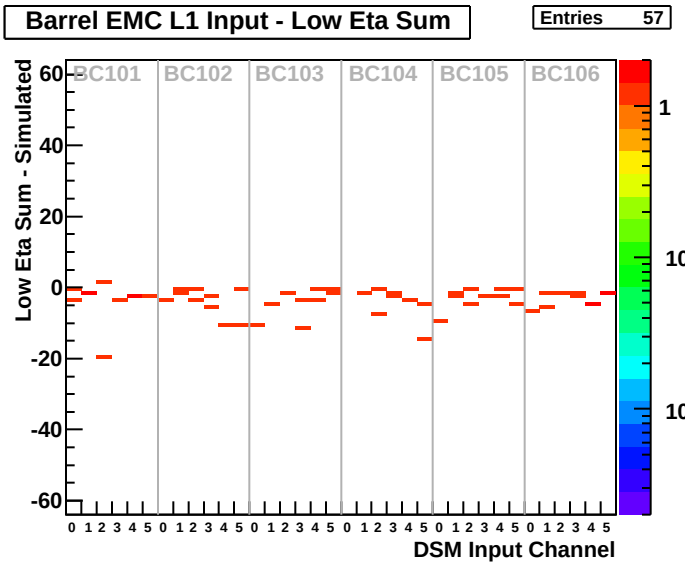
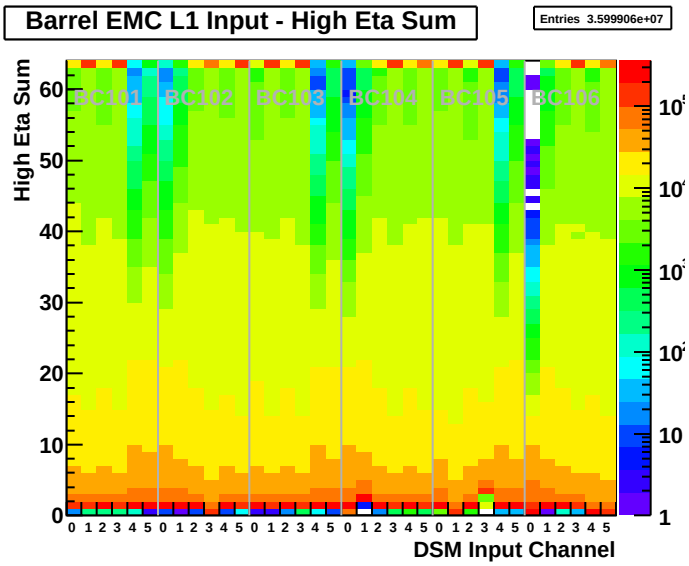
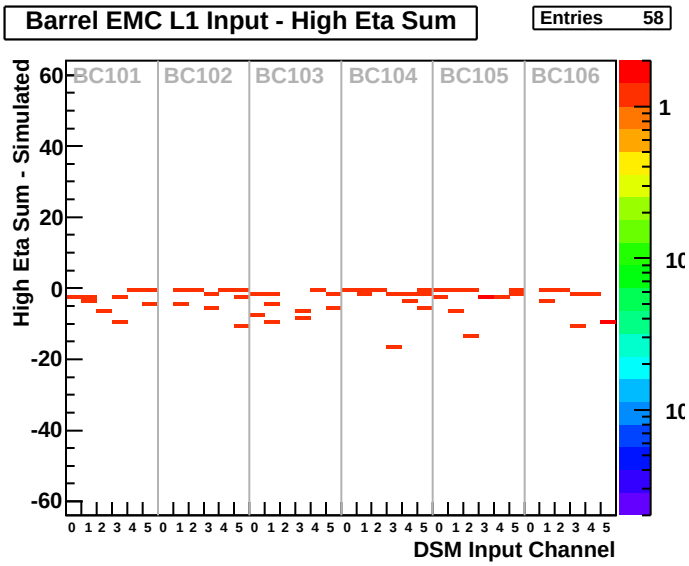
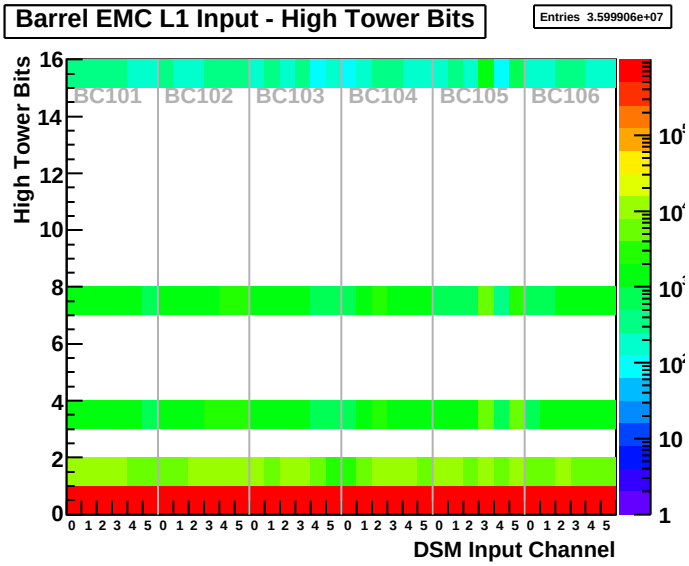
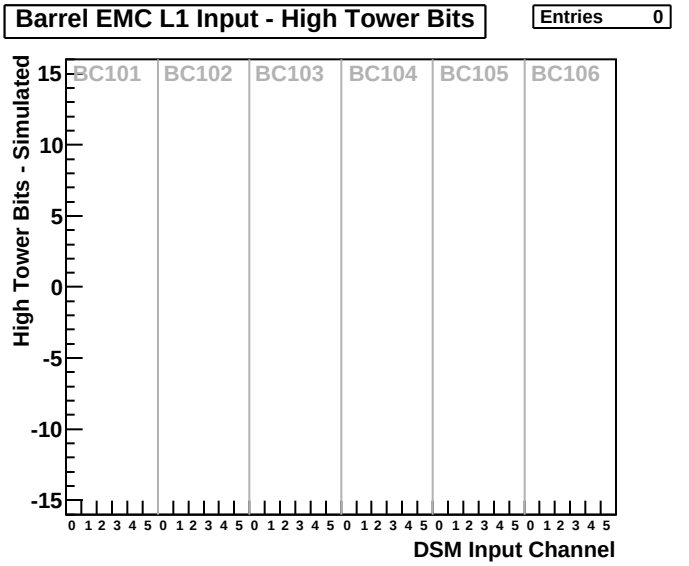

Barrel EMC L1 Input - Low Eta Sum
Entries 3.599906e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

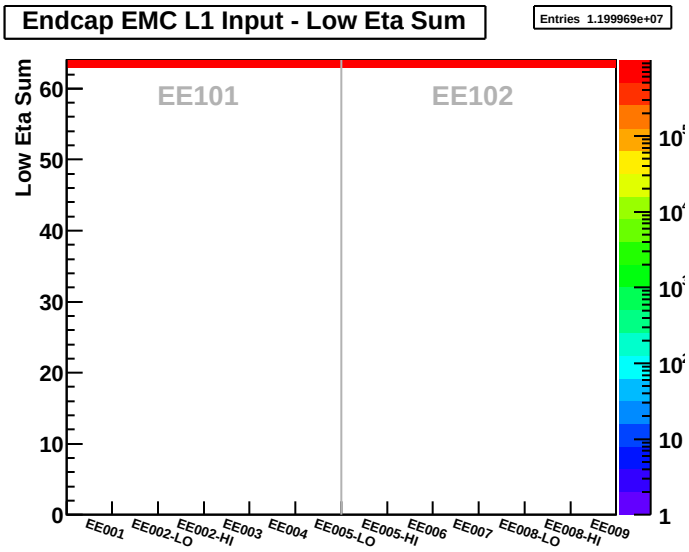

Barrel EMC L1 Input - Low Eta Sum
Entries 57
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

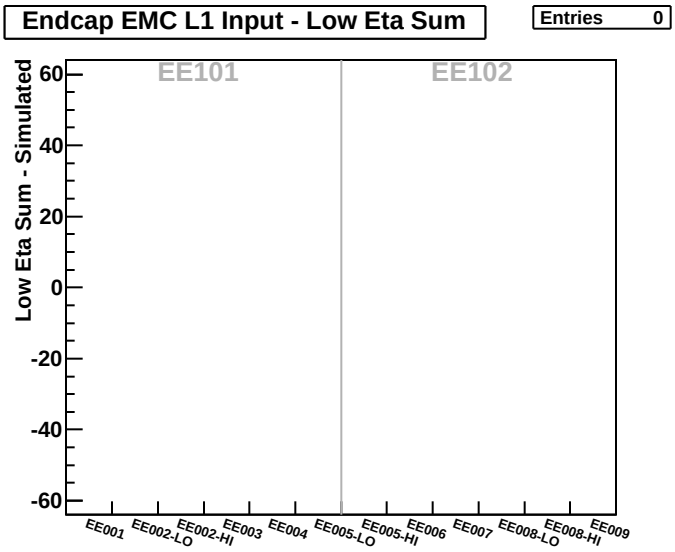

Barrel EMC L1 Input - High Eta Sum
Entries 3.599906e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

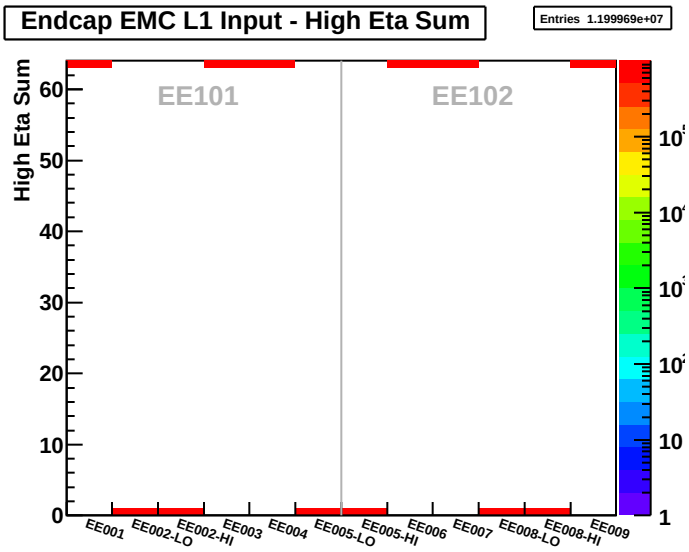

Barrel EMC L1 Input - High Eta Sum
Entries 58
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

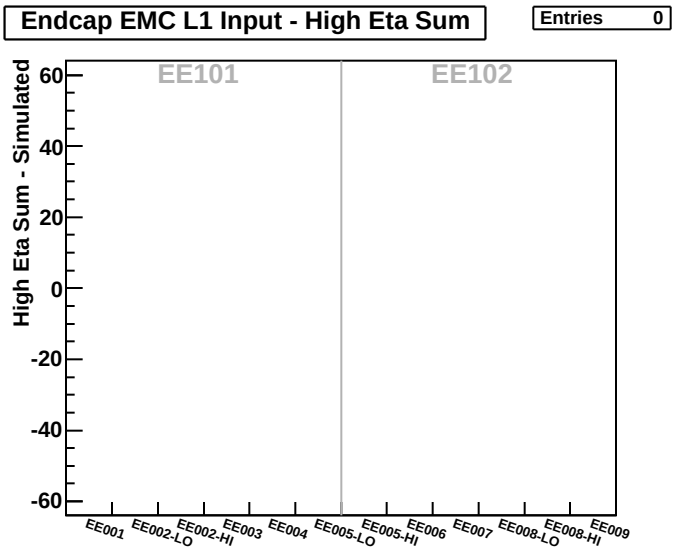

Barrel EMC L1 Input - High Tower Bits
Entries 3.599906e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

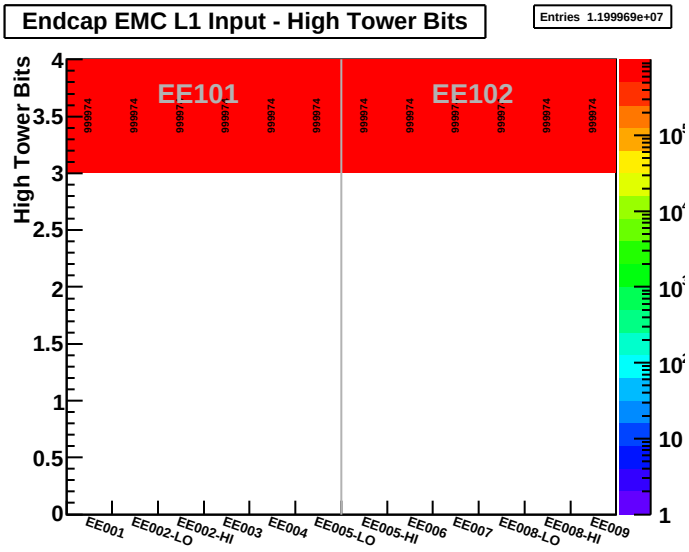

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

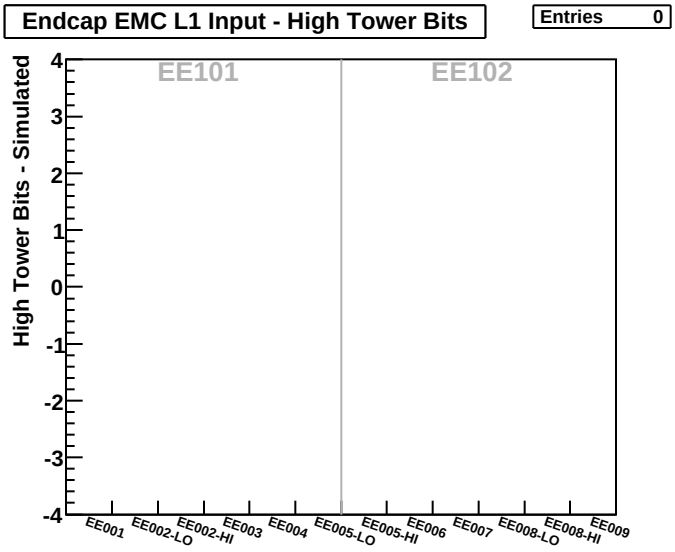

Endcap EMC L1 Input - Low Eta Sum
Entries 1.199969e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

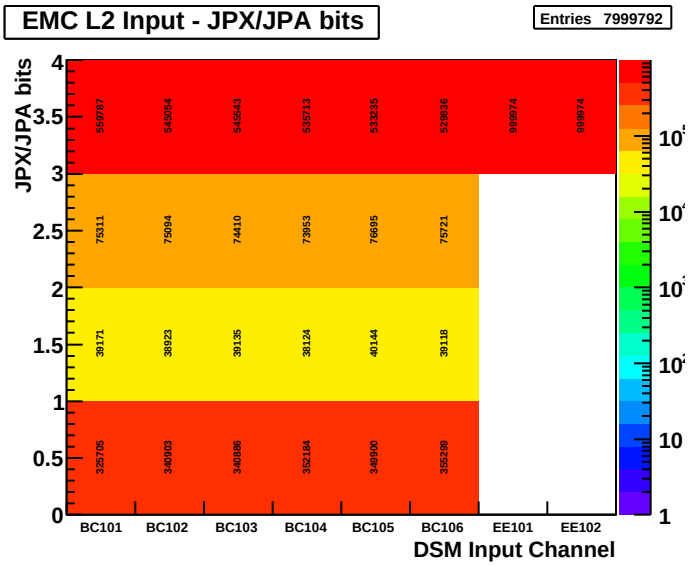

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

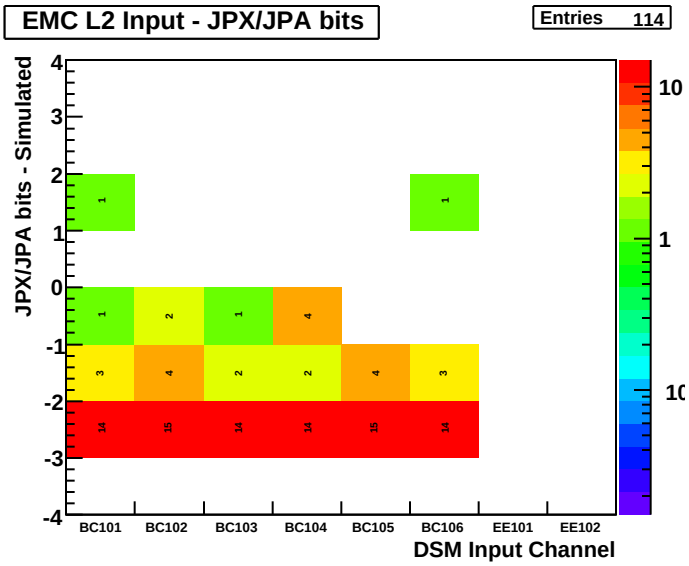

Endcap EMC L1 Input - High Eta Sum
Entries 1.199969e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

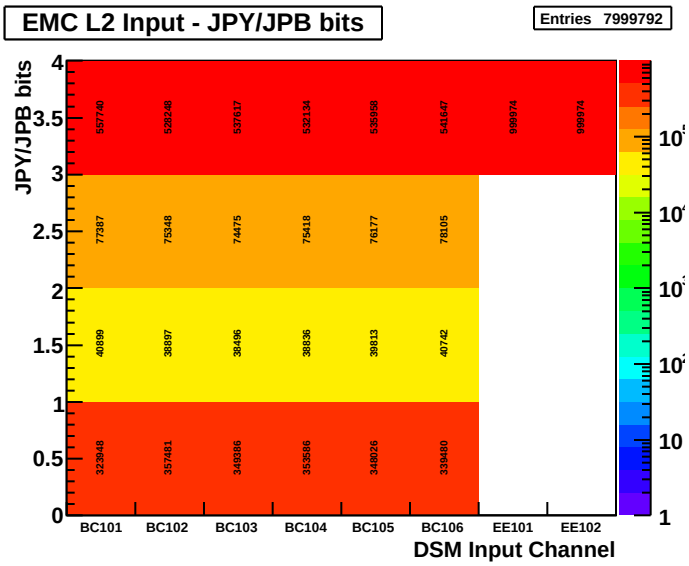

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

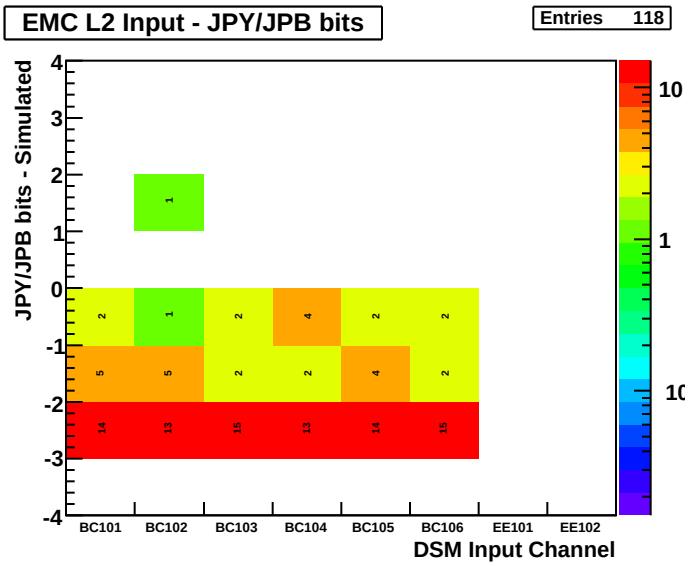

Endcap EMC L1 Input - High Tower Bits
Entries 1.199969e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

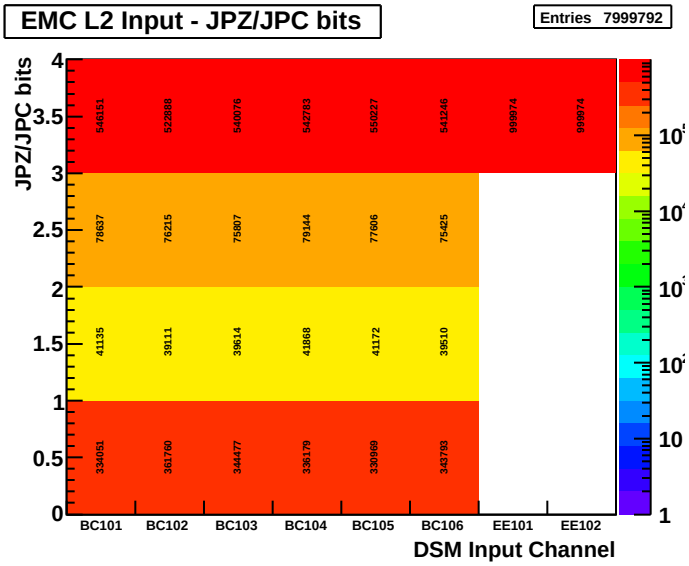

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

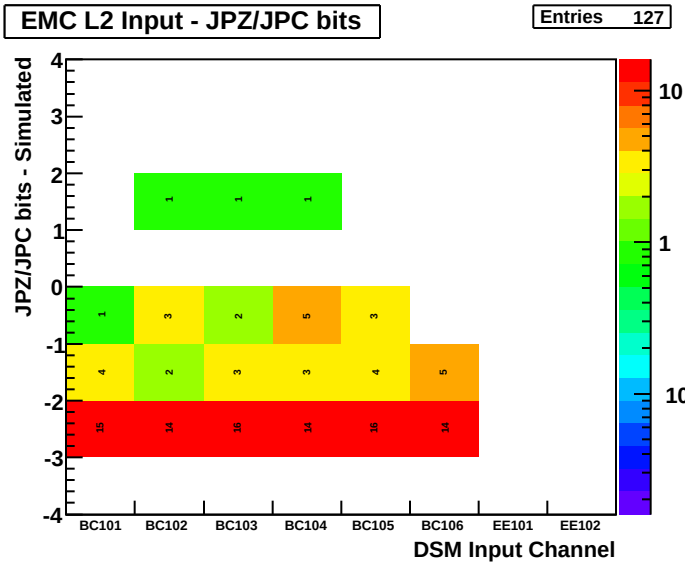

EMC L2 Input - JPX/JPA bits
Entries 7999792
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
559787 546054 546543 535713 533235 520836 999974 999974
75311 75094 74410 73953 76695 75721
39171 38923 39135 38124 40144 39118
325705 340903 340886 352184 349900 355299

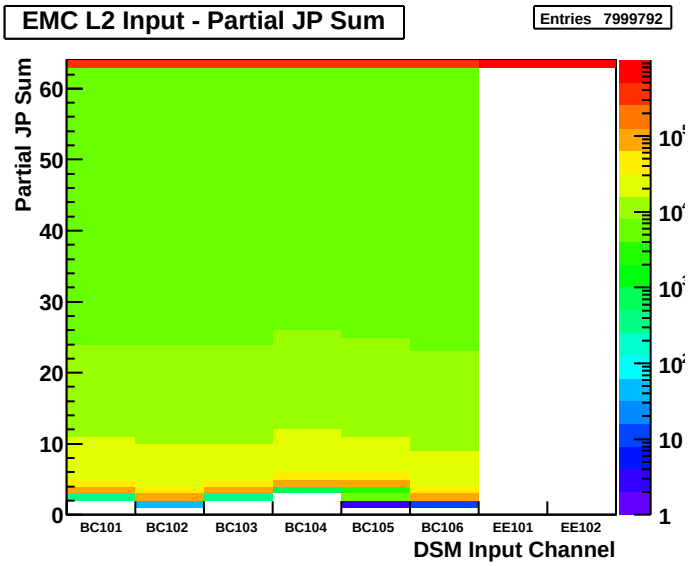

EMC L2 Input - JPX/JPA bits
Entries 114
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

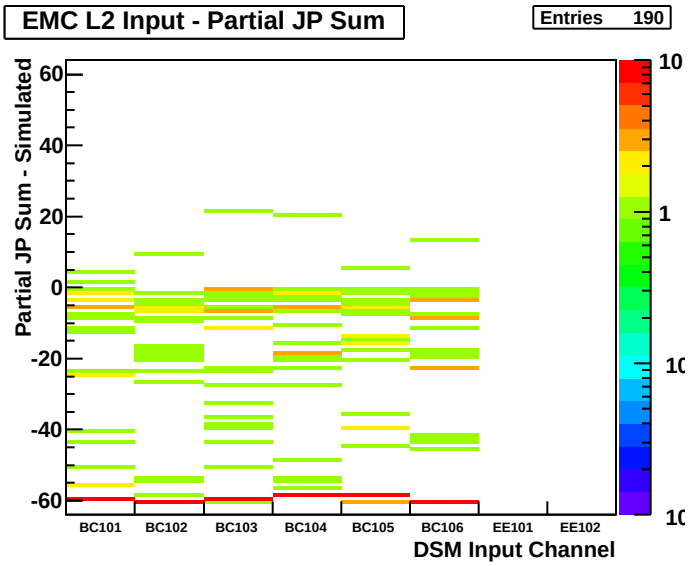

EMC L2 Input - JPY/JPB bits
Entries 7999792
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
557740 528248 537617 532134 535958 541647 999974 999974
77387 75348 74475 75418 76177 78105
40899 38897 38496 38836 39813 40742
323948 357481 349386 353586 348026 339480

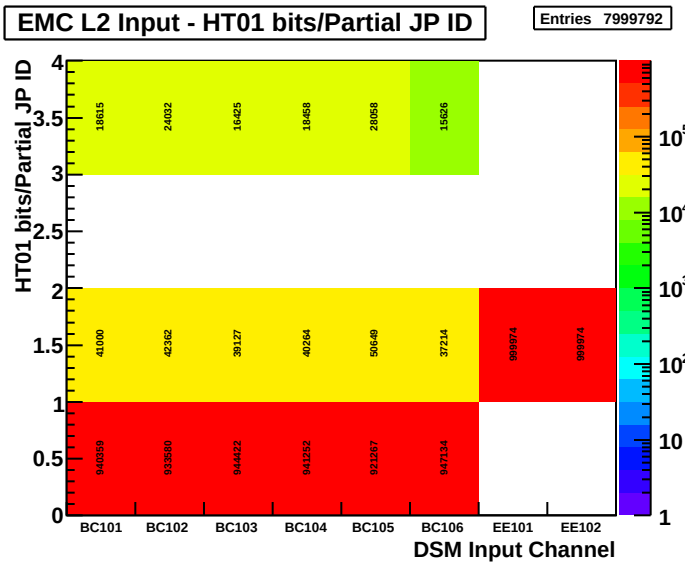

EMC L2 Input - JPY/JPB bits
Entries 118
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

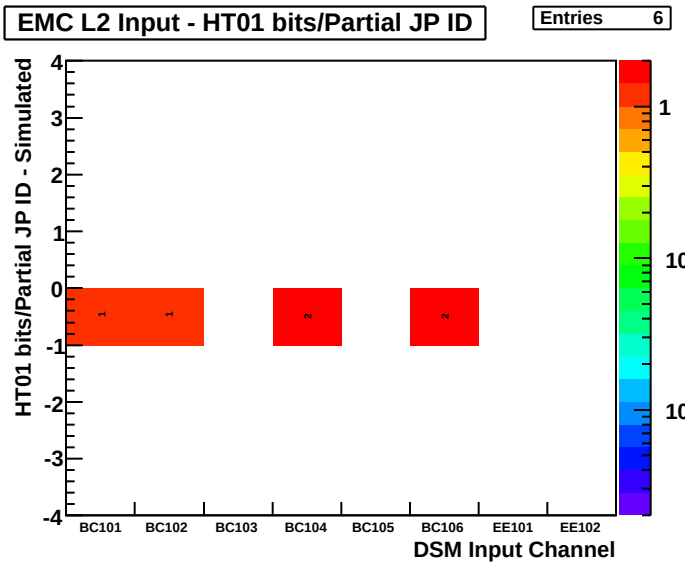

EMC L2 Input - JPZ/JPC bits
Entries 7999792
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
546151 522868 540076 542783 550227 541246 999974 999974
78637 76215 75807 79144 77606 75425
41135 39111 39614 41868 41172 39510
334051 361760 344477 336179 330969 343793

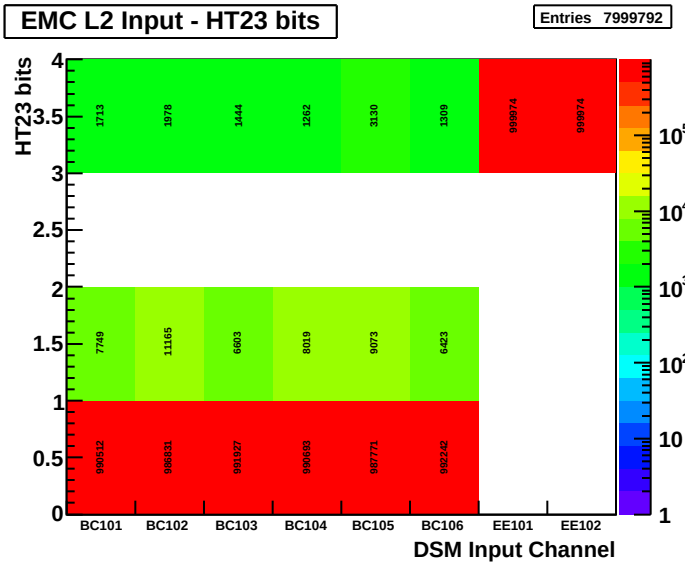

EMC L2 Input - JPZ/JPC bits
Entries 127
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

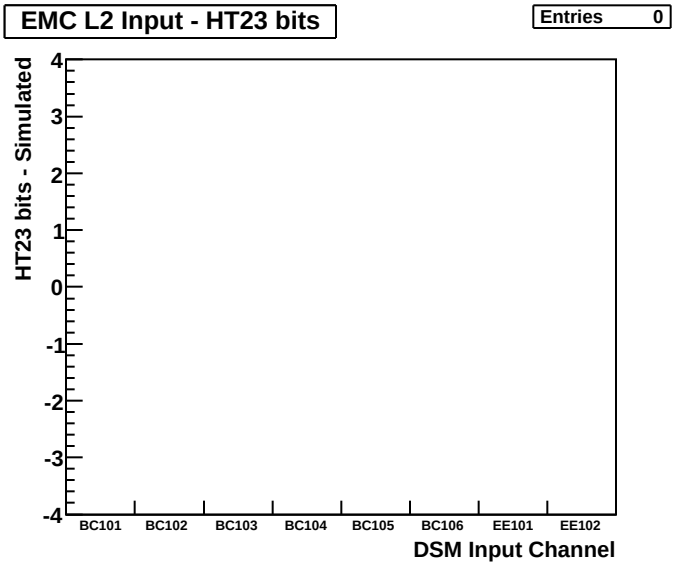

EMC L2 Input - Partial JP Sum
Entries 7999792
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

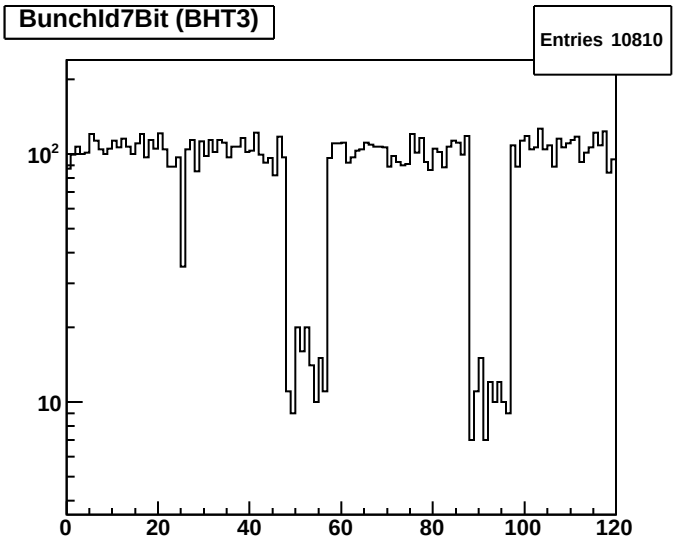

EMC L2 Input - Partial JP Sum
Entries 190
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

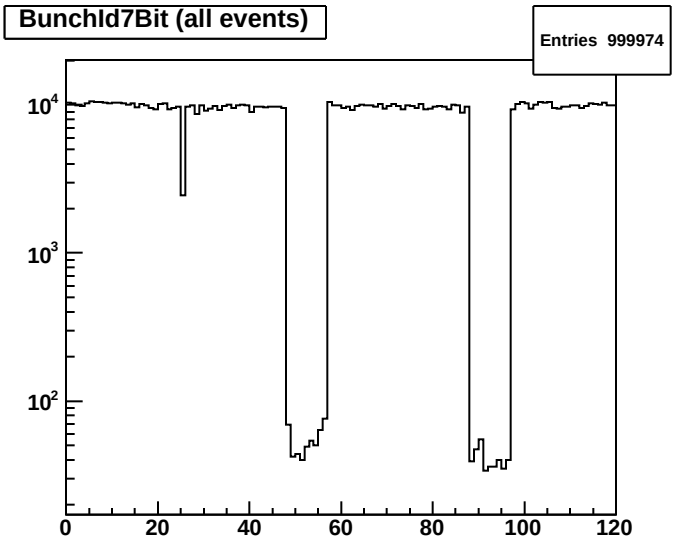

EMC L2 Input - HT01 bits/Partial JP ID
Entries 7999792
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

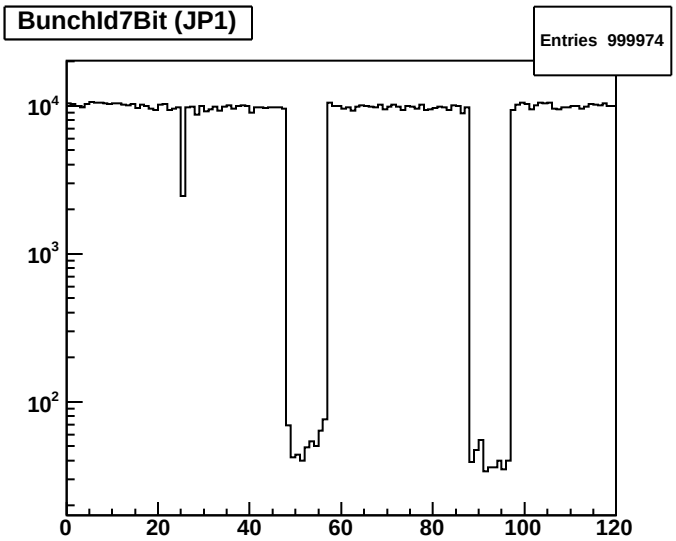

EMC L2 Input - HT01 bits/Partial JP ID
Entries 6
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

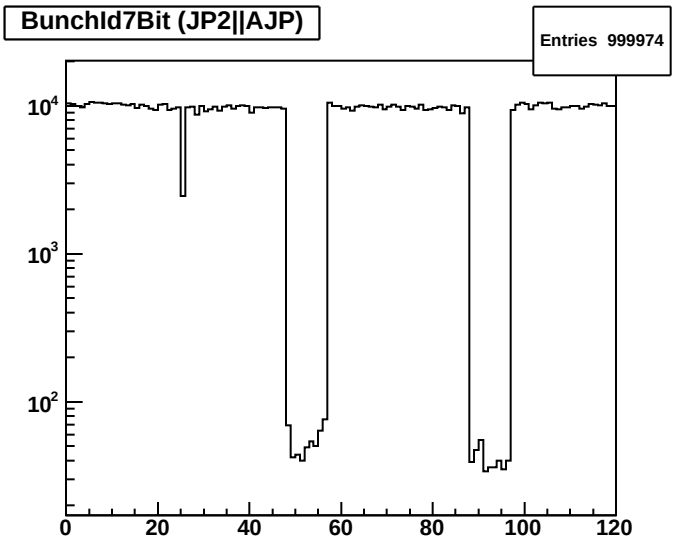

EMC L2 Input - HT23 bits
Entries 7999792
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

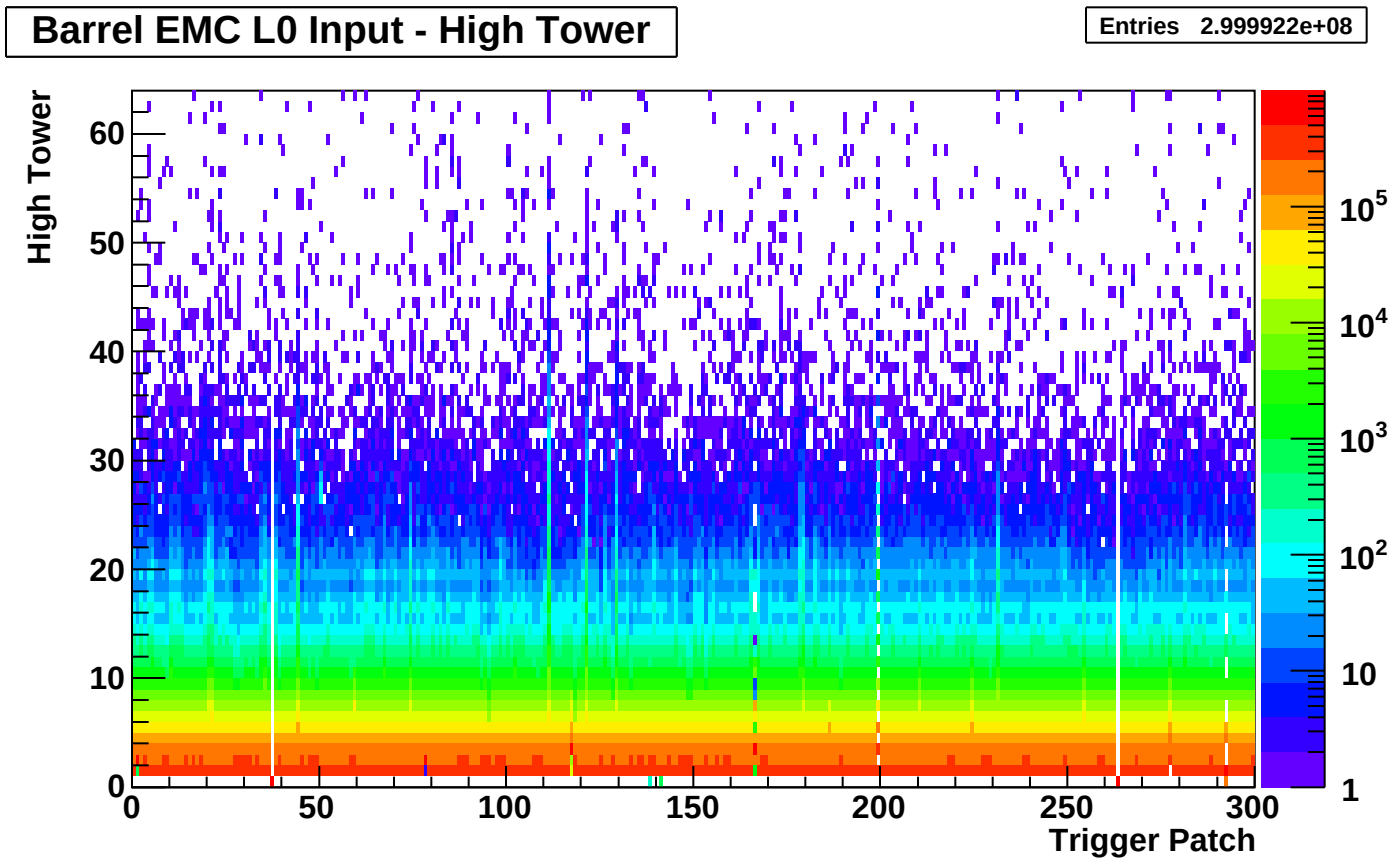

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

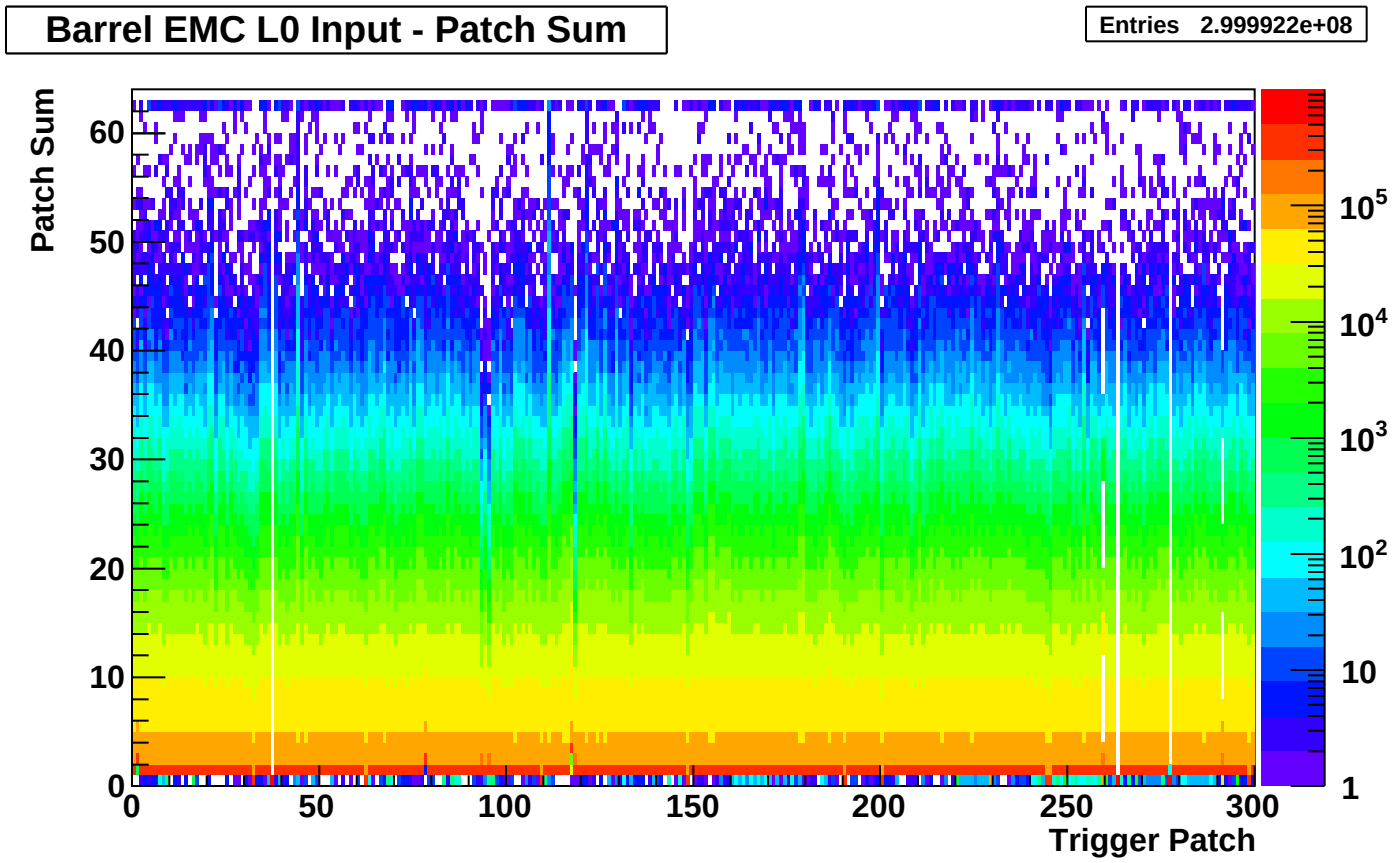

BunchId7Bit (BHT3)
Entries 10810
10^2
10
0
20
40
60
80
100
120

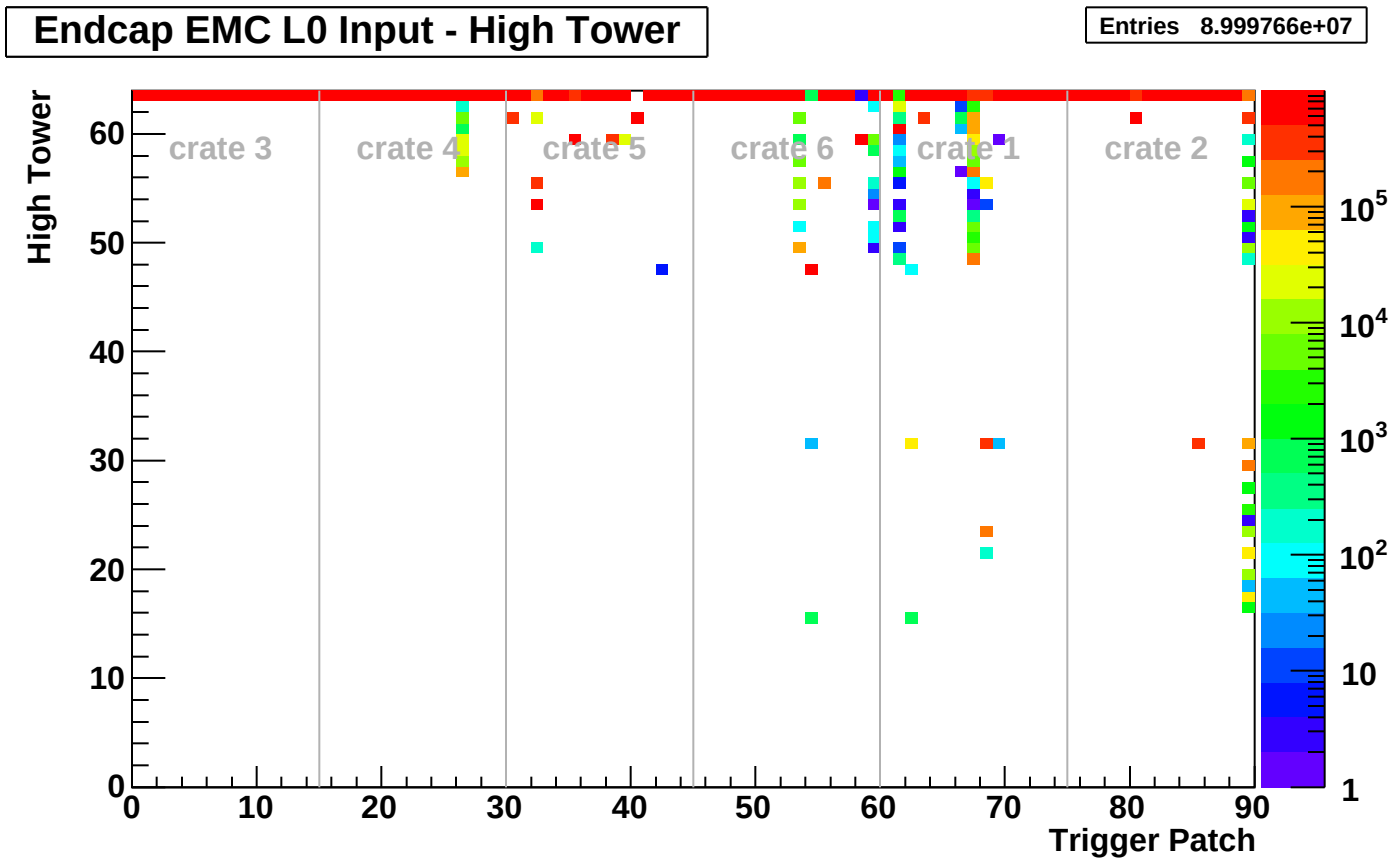

BunchId7Bit (all events)
Entries 999974
10^4
10^3
10^2
0
20
40
60
80
100
120

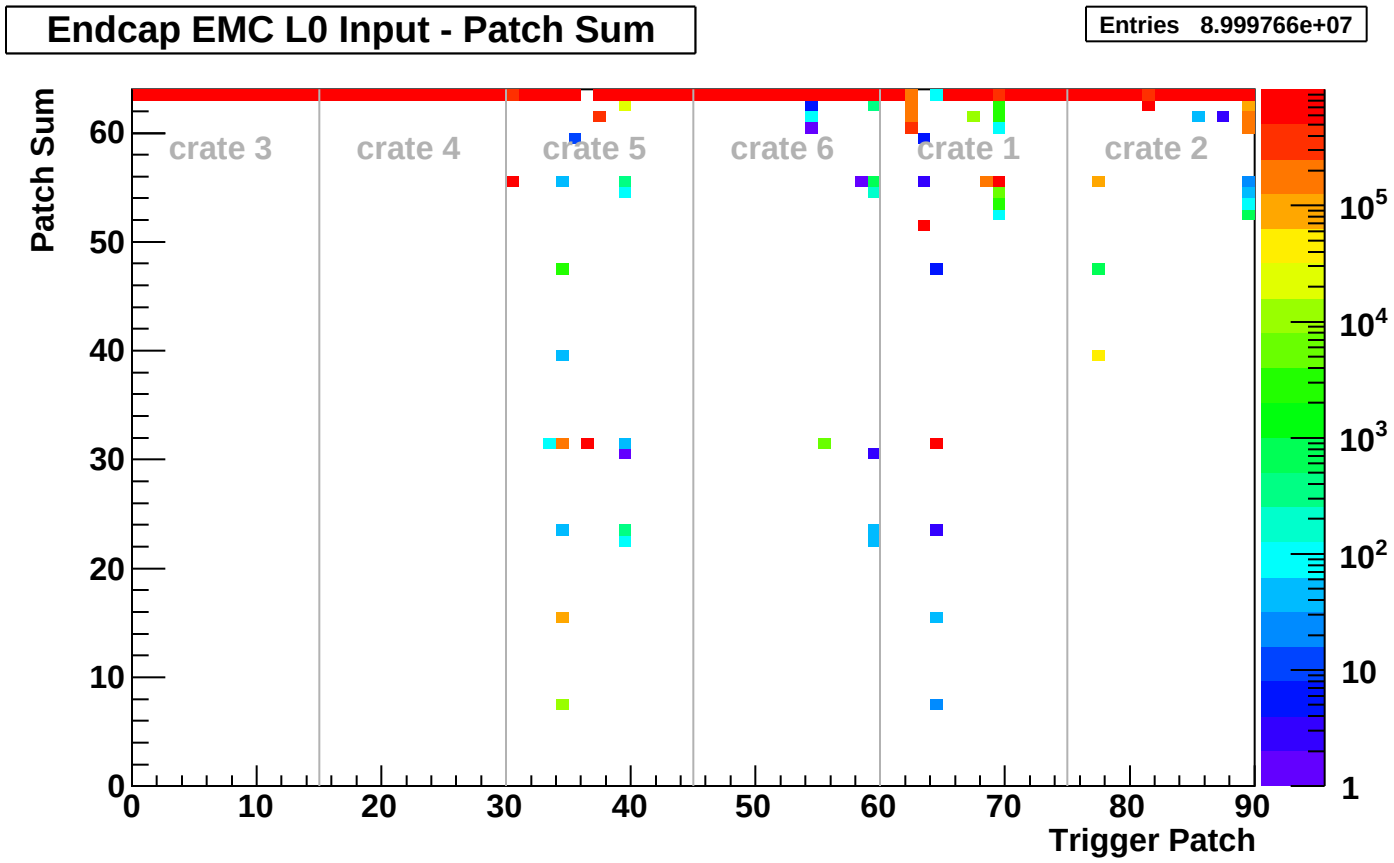

BunchId7Bit (JP1)
Entries 999974
10^4
10^3
10^2
0
20
40
60
80
100
120

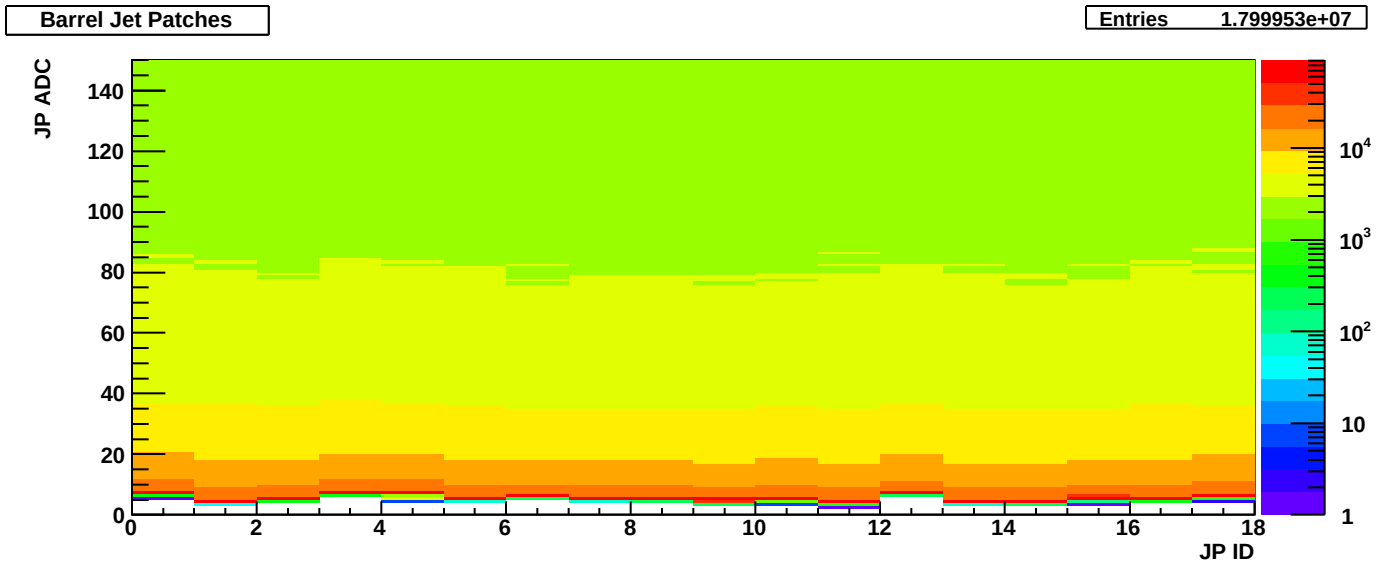

BunchId7Bit (JP2||AJP)
Entries 999974
10^4
10^3
10^2
0
20
40
60
80
100
120

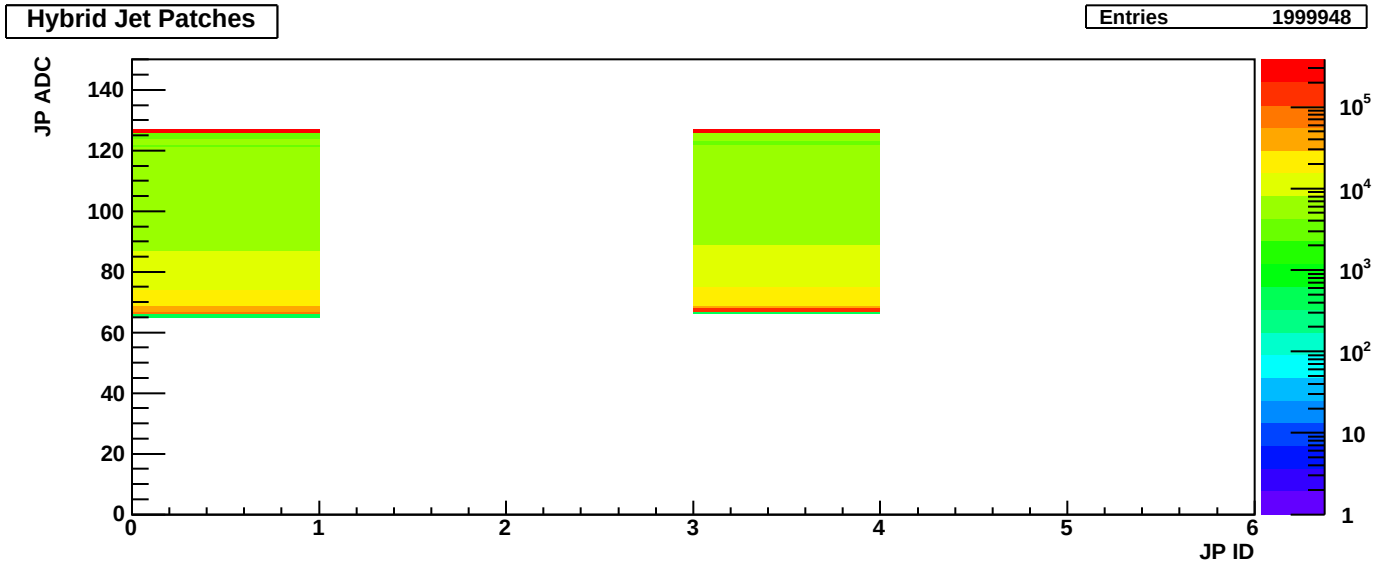

Barrel EMC L0 Input - High Tower
Entries 2.999922e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

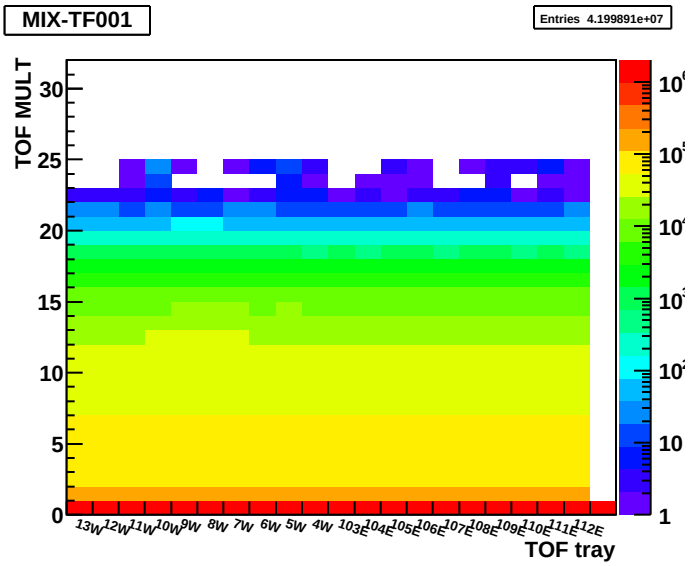

Barrel EMC L0 Input - Patch Sum
Entries 2.999922e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

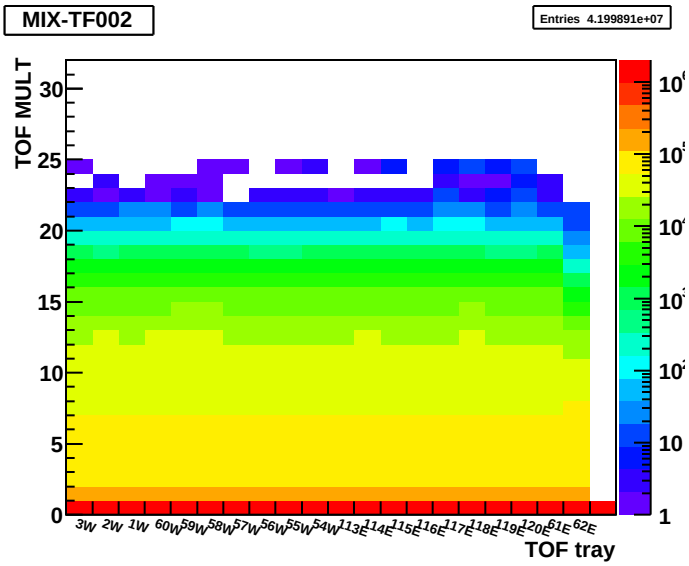

Endcap EMC L0 Input - High Tower
Entries 8.999766e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

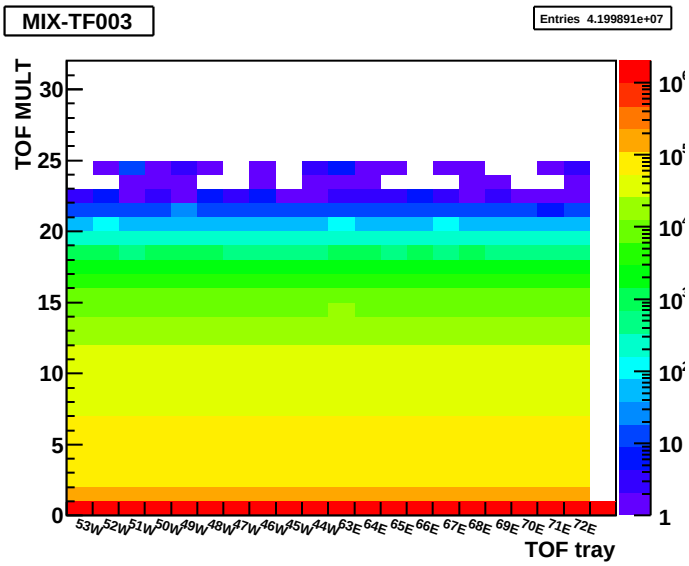

Endcap EMC L0 Input - Patch Sum
Entries 8.999766e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

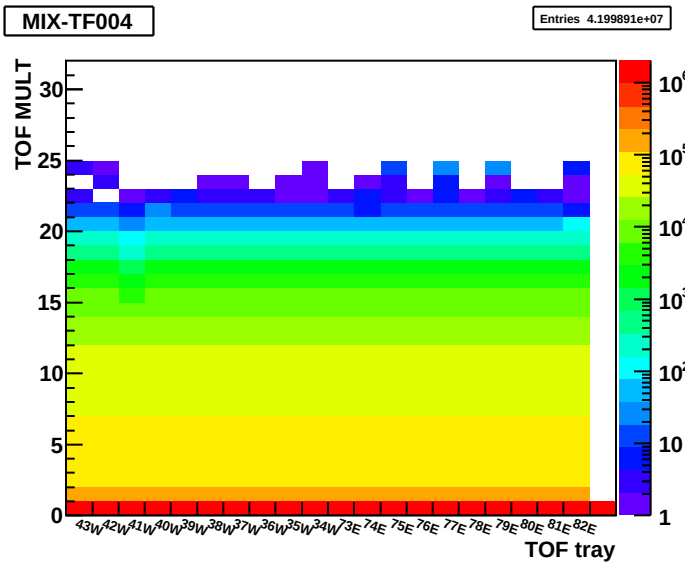

Barrel Jet Patches
Entries 1.799953e+07
JP ADC
JP ID

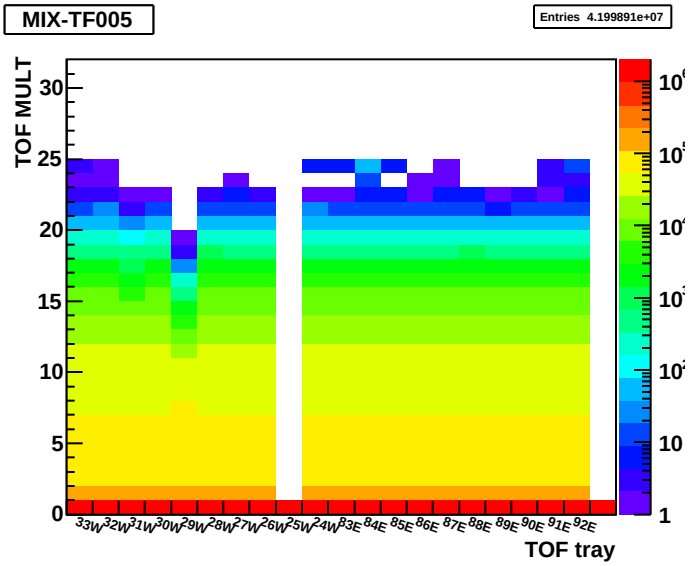

Hybrid Jet Patches
Entries 1999948
JP ADC
JP ID

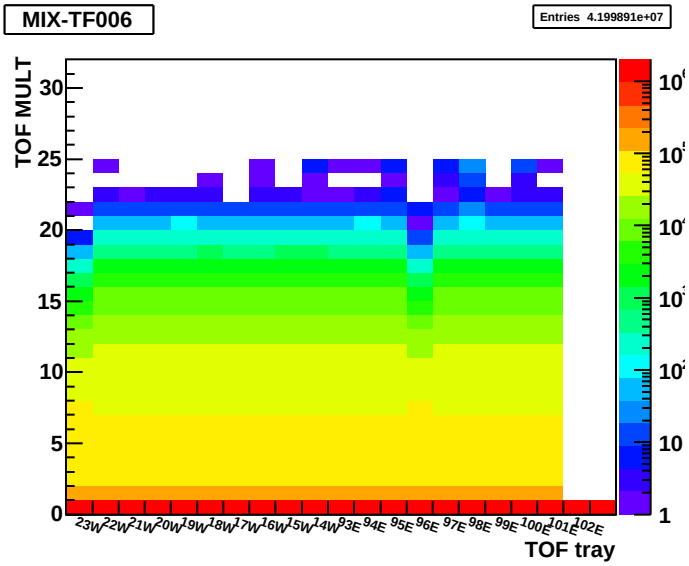

MIX-TF001
Entries 4.199891e+07
TOF MULT
TOF tray

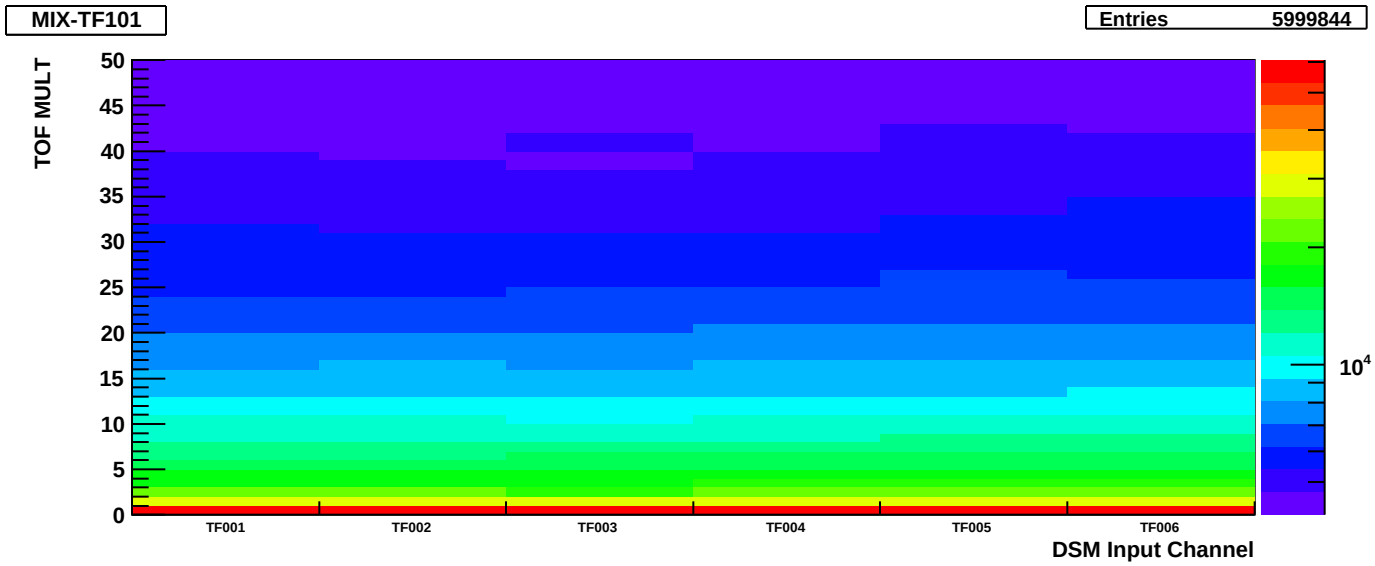

MIX-TF002
Entries 4.199891e+07
TOF MULT
TOF tray

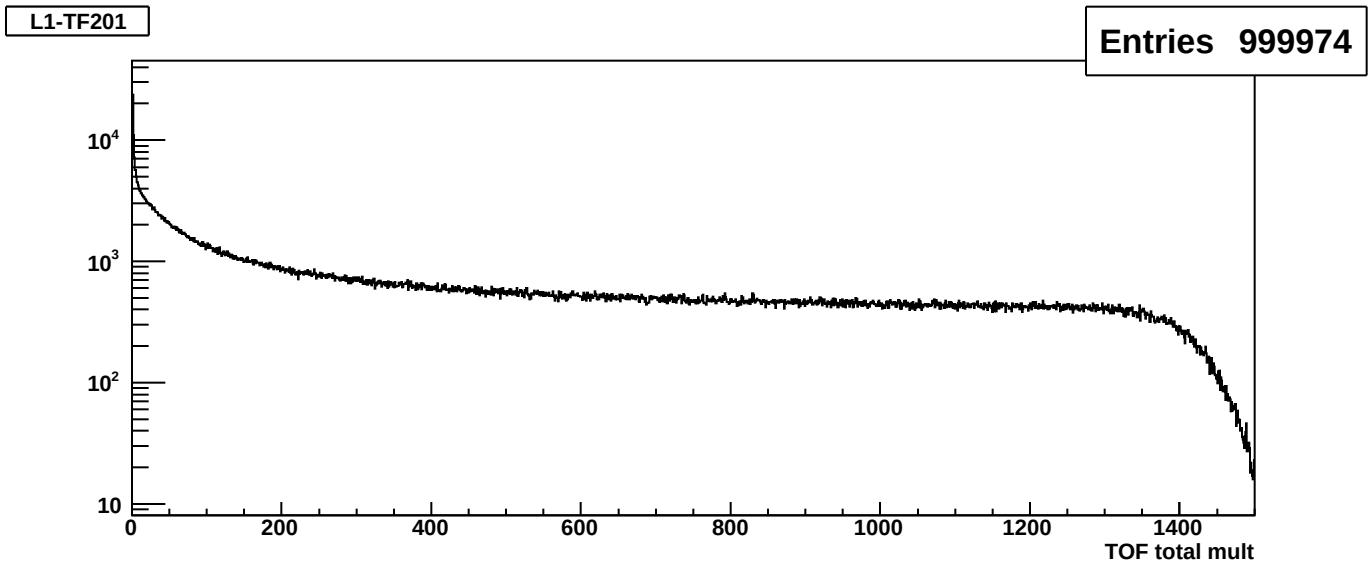

MIX-TF003
Entries 4.199891e+07
TOF MULT
TOF tray

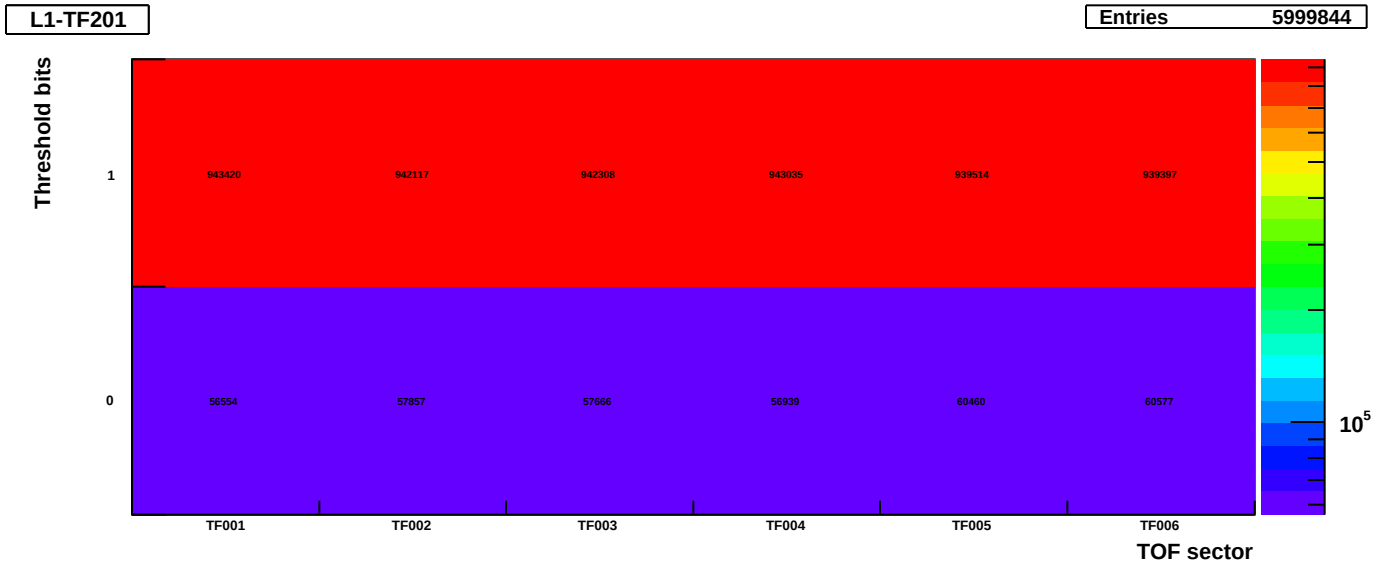

MIX-TF004
Entries 4.199891e+07
TOF MULT
TOF tray

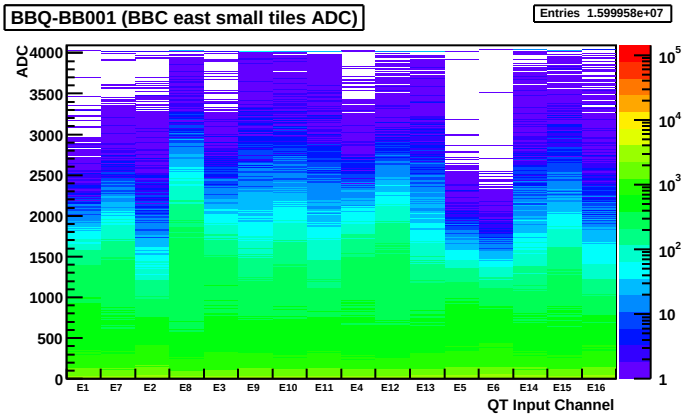

MIX-TF005
Entries 4.199891e+07
TOF MULT
TOF tray

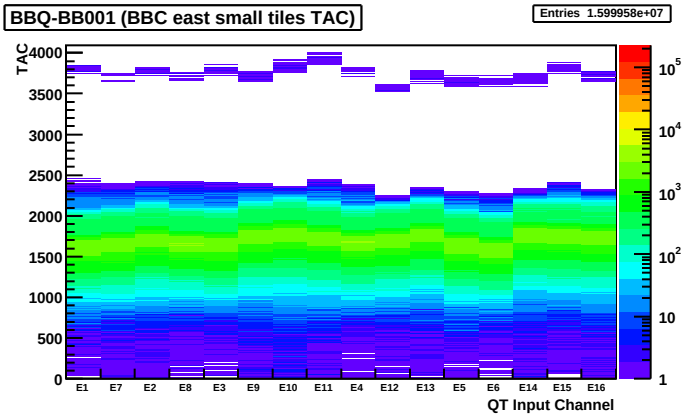

MIX-TF006
Entries 4.199891e+07
TOF MULT
TOF tray

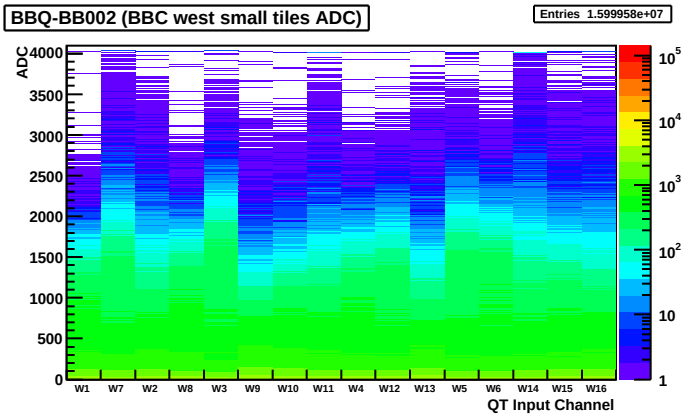

MIX-TF101
Entries 5999844
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

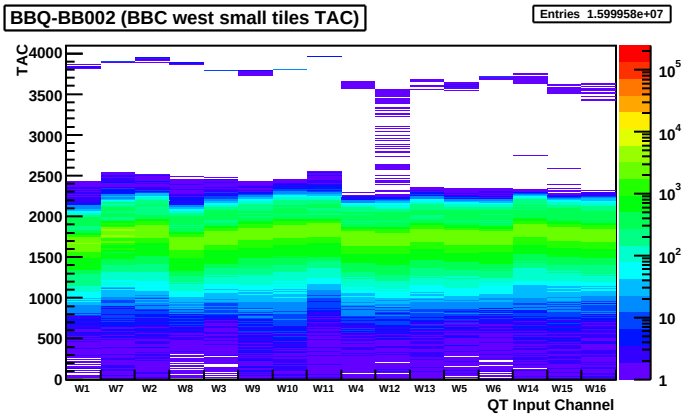

L1-TF201
Entries 999974
10^4
10^3
10^2
10
0
200
400
600
800
1000
1200
1400
TOF total mult

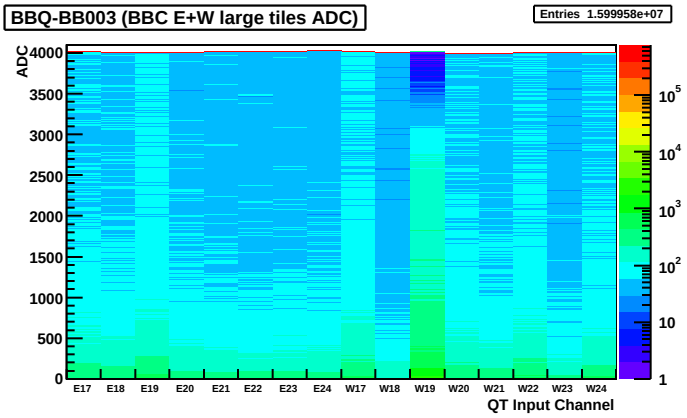

L1-TF201
Entries 5999844
Threshold bits
1
0
943420
942117
942308
943035
939514
939397
56554
57857
57666
56939
60460
60577
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

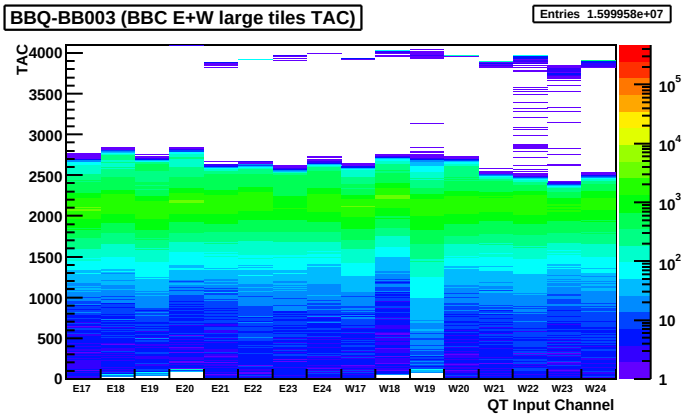

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.599958e+07
ADC
QT Input Channel

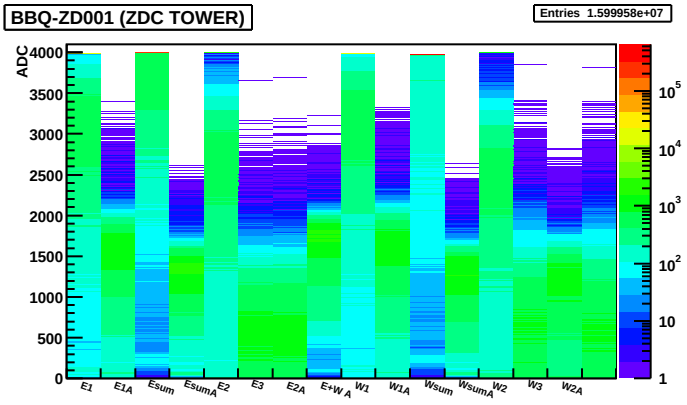

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.599958e+07
TAC
QT Input Channel

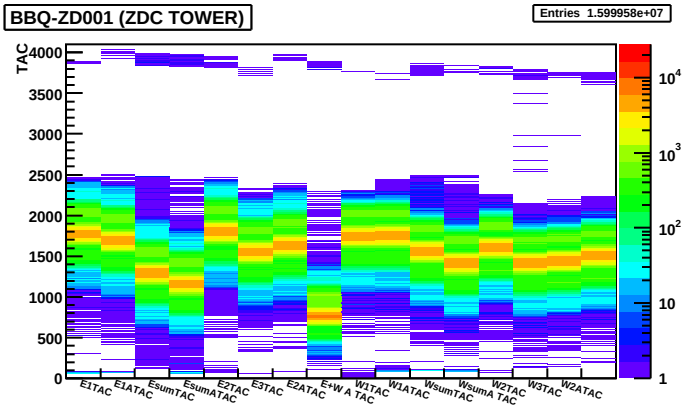

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.599958e+07
ADC
QT Input Channel

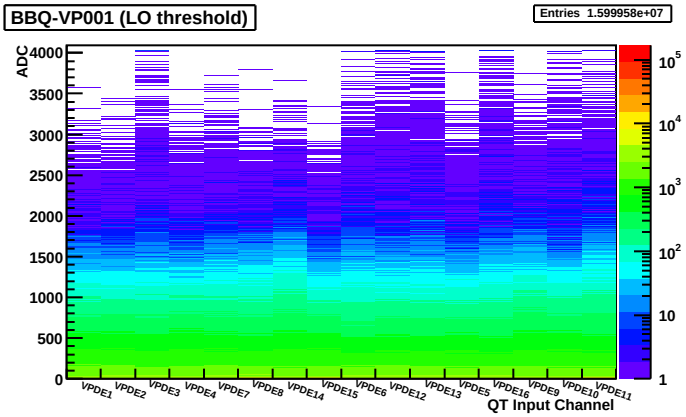

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.599958e+07
TAC
QT Input Channel

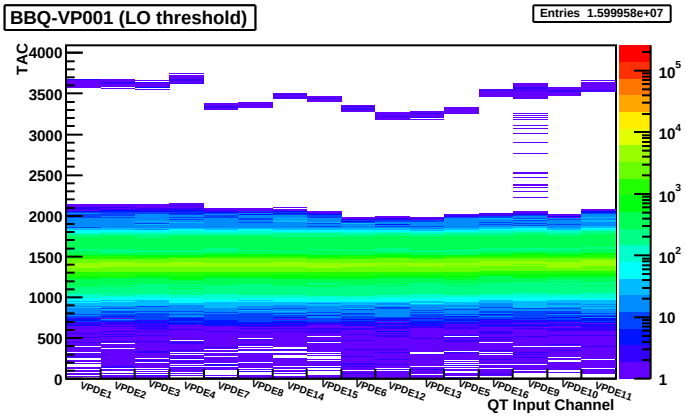

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.599958e+07
ADC
QT Input Channel

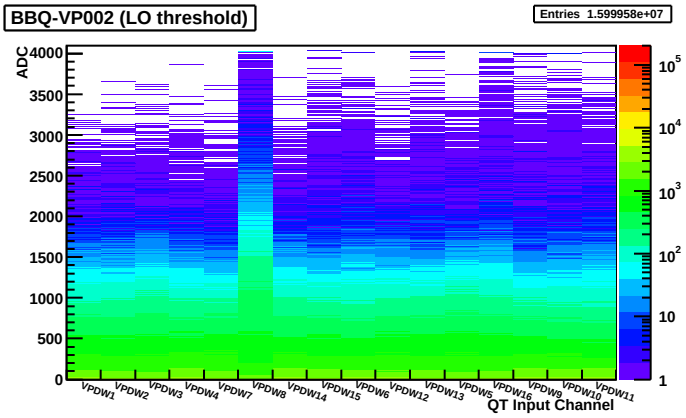

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.599958e+07
TAC
QT Input Channel

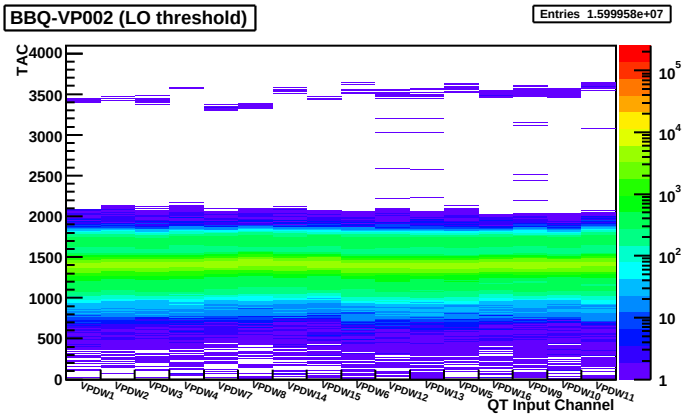

BBQ-ZD001 (ZDC TOWER)
Entries 1.599958e+07
ADC

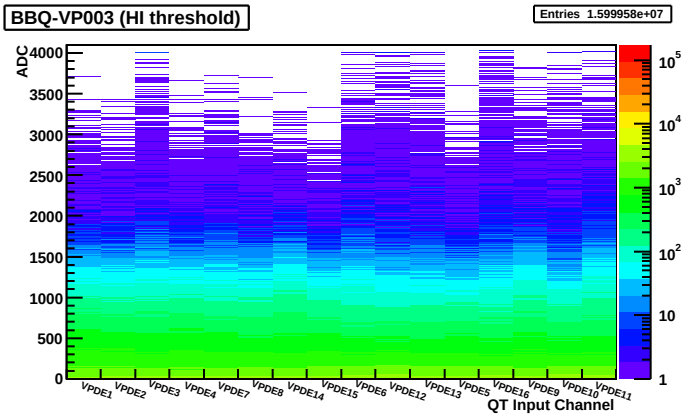

BBQ-ZD001 (ZDC TOWER)
Entries 1.599958e+07
TAC

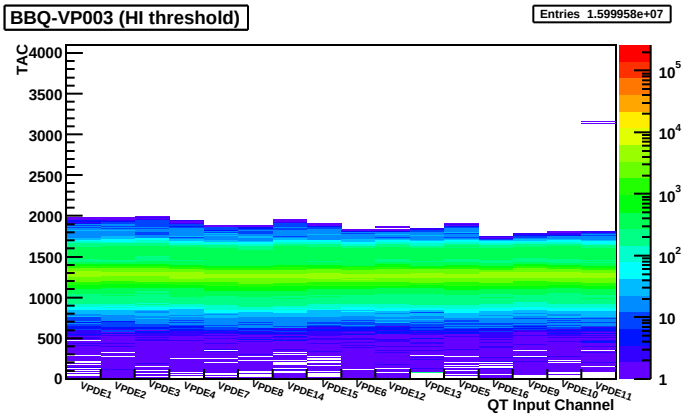

BBQ-VP001 (LO threshold)
Entries 1.599958e+07
ADC
QT Input Channel

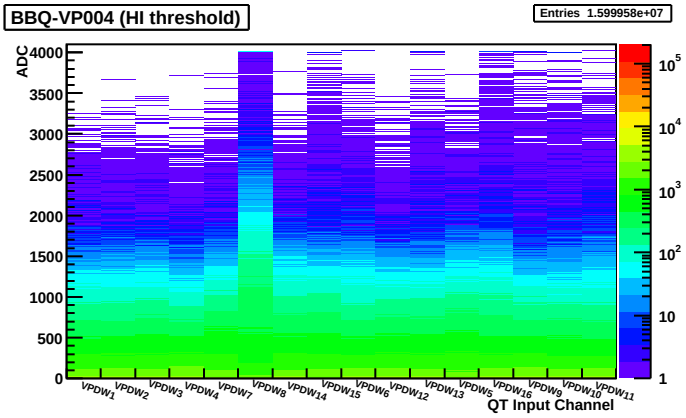

BBQ-VP001 (LO threshold)
Entries 1.599958e+07
TAC
QT Input Channel

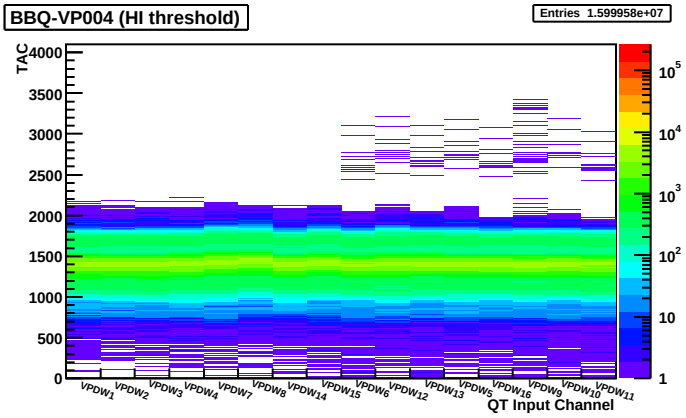

BBQ-VP002 (LO threshold)
Entries 1.599958e+07
ADC
QT Input Channel

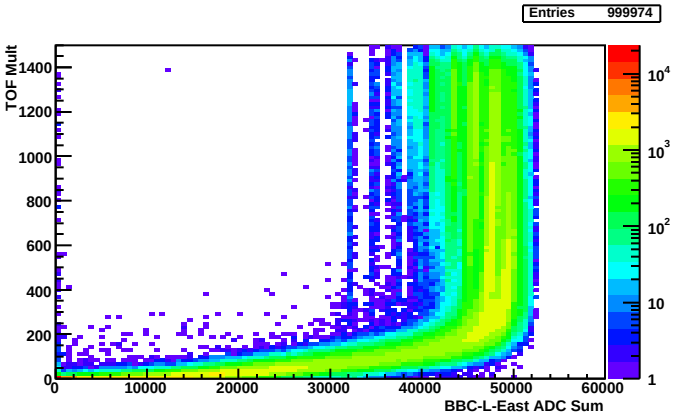

BBQ-VP002 (LO threshold)
Entries 1.599958e+07
TAC
QT Input Channel

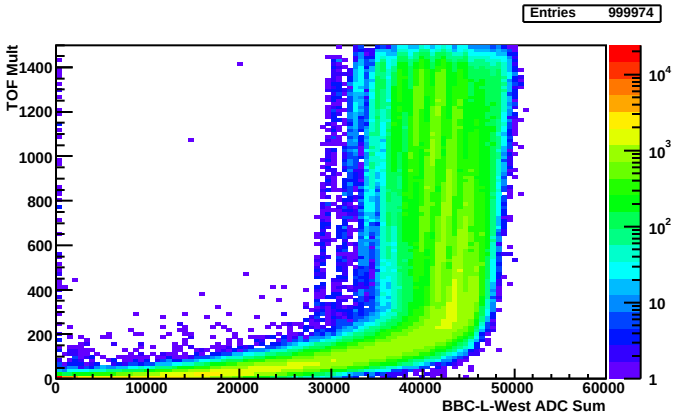

BBQ-VP003 (HI threshold)
Entries 1.599958e+07
ADC
QT Input Channel

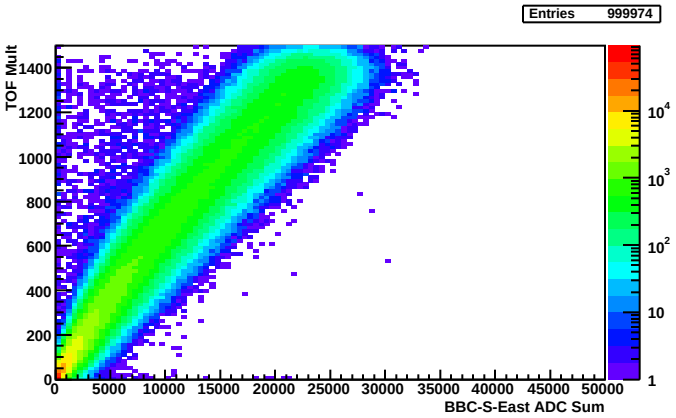

BBQ-VP003 (HI threshold)
Entries 1.599958e+07
TAC
QT Input Channel

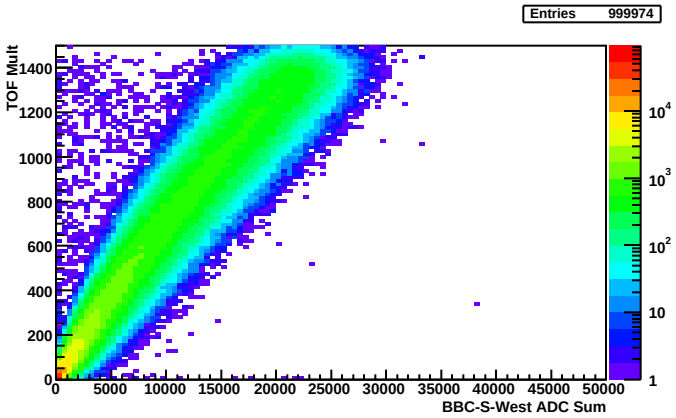

BBQ-VP004 (HI threshold)
Entries 1.599958e+07
ADC
QT Input Channel

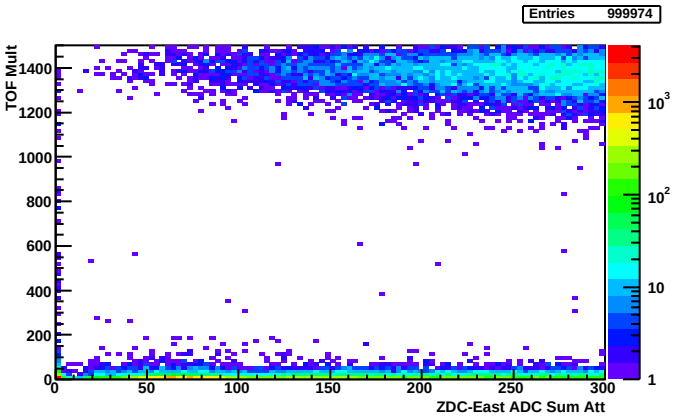

BBQ-VP004 (HI threshold)
Entries 1.599958e+07
TAC
QT Input Channel

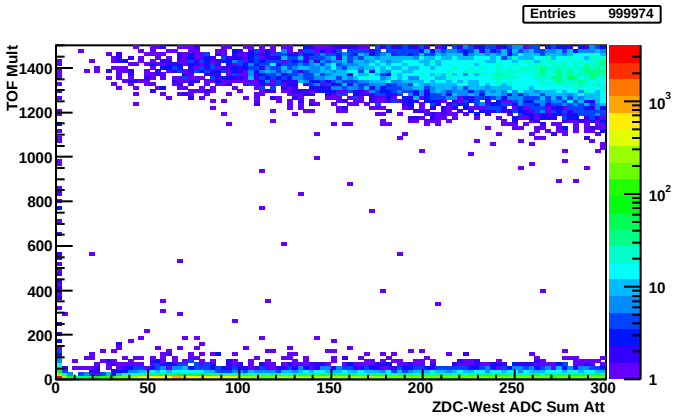

Entries 999974
TOF Mult
BBC-L-East ADC Sum

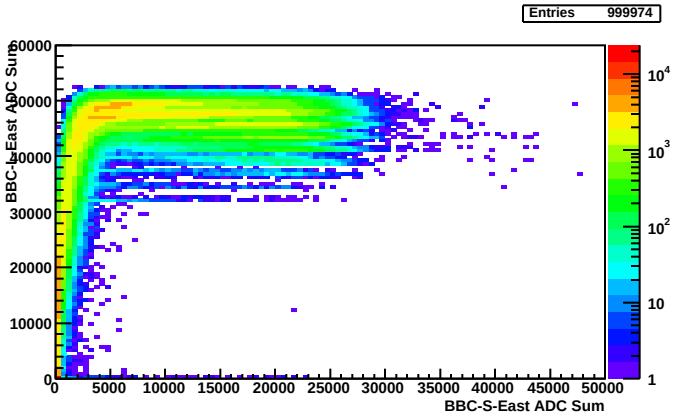

Entries 999974
TOF Mult
BBC-L-West ADC Sum

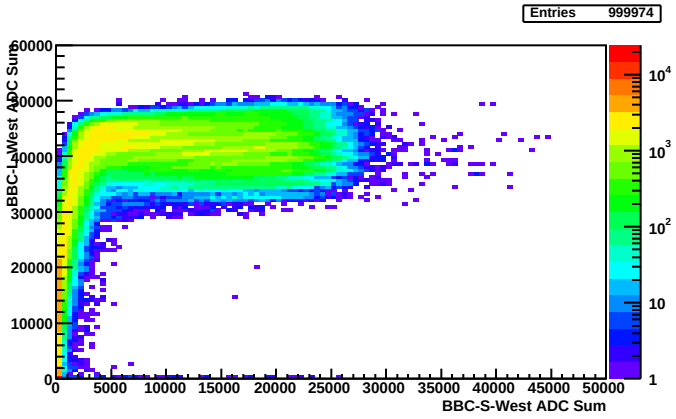

Entries 999974
TOF Mult
BBC-S-East ADC Sum

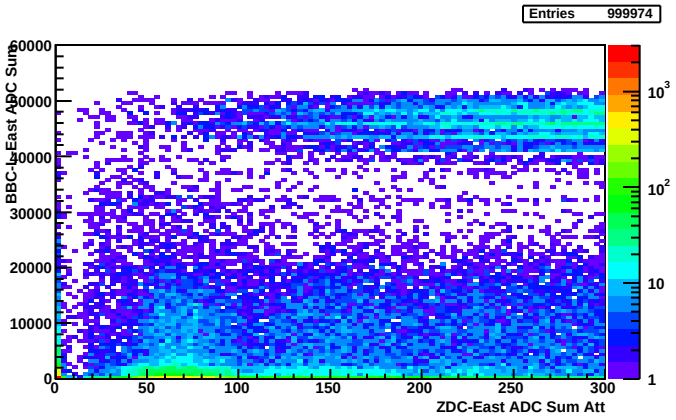

Entries 999974
TOF Mult
BBC-S-West ADC Sum

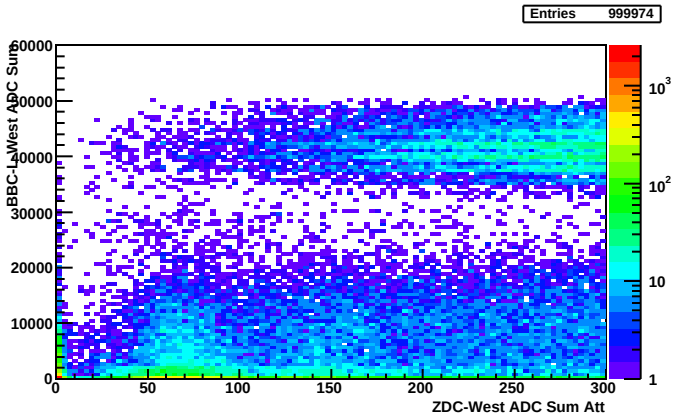

Entries 999974
TOF Mult
ZDC-East ADC Sum Att


Entries 999974
TOF Mult
ZDC-West ADC Sum Att


Entries 999974
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 999974
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 999974
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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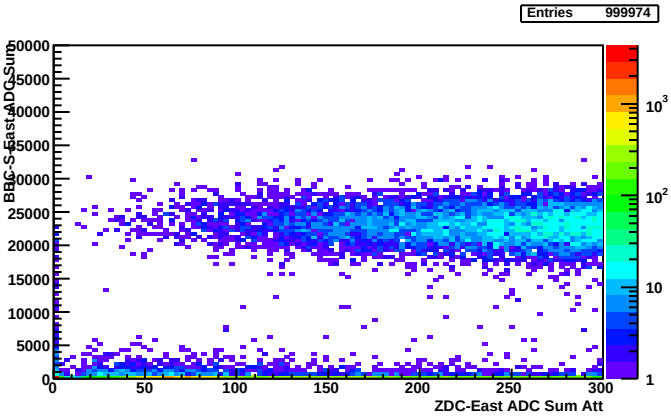
BBC-L-West ADC Sum
ZDC-West ADC Sum Att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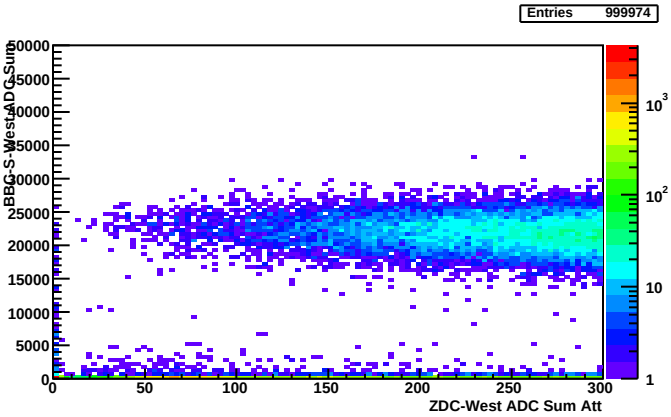
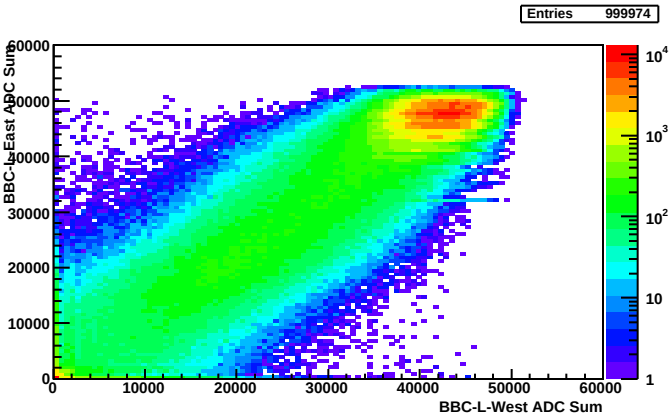
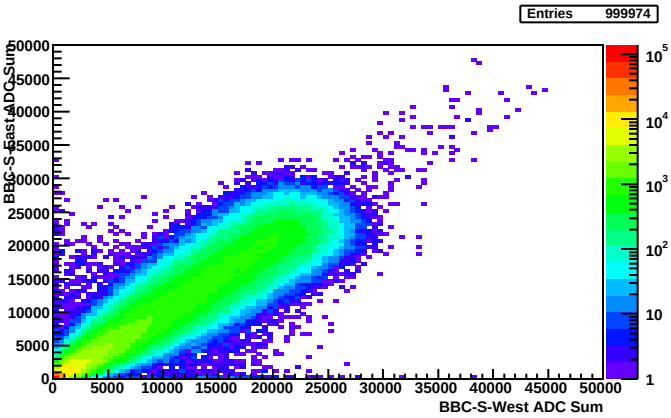
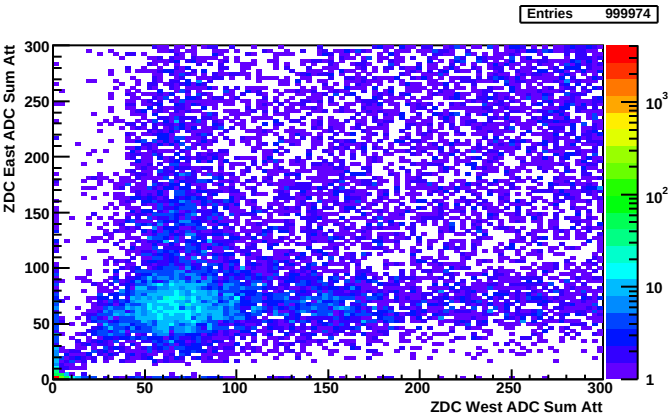
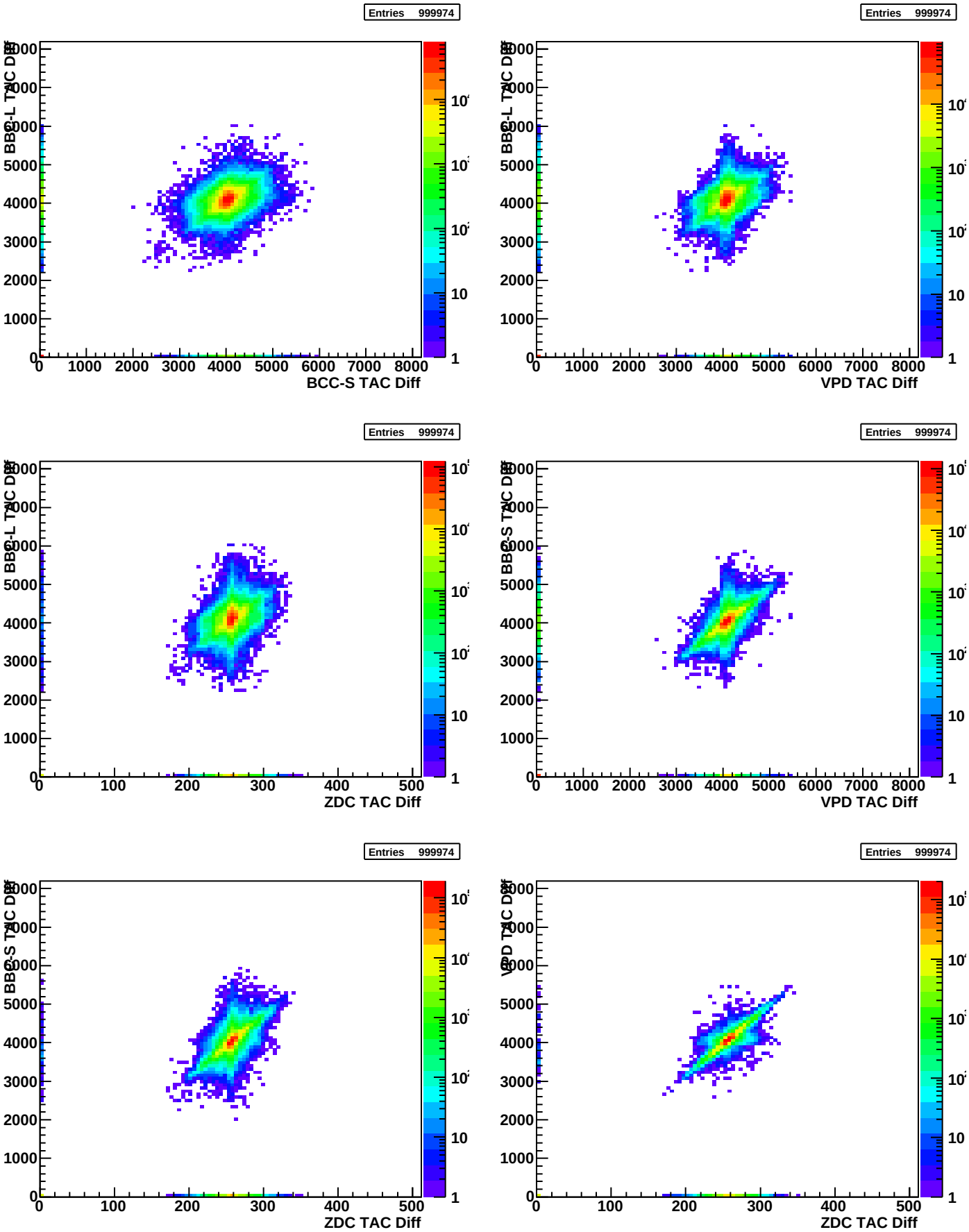
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

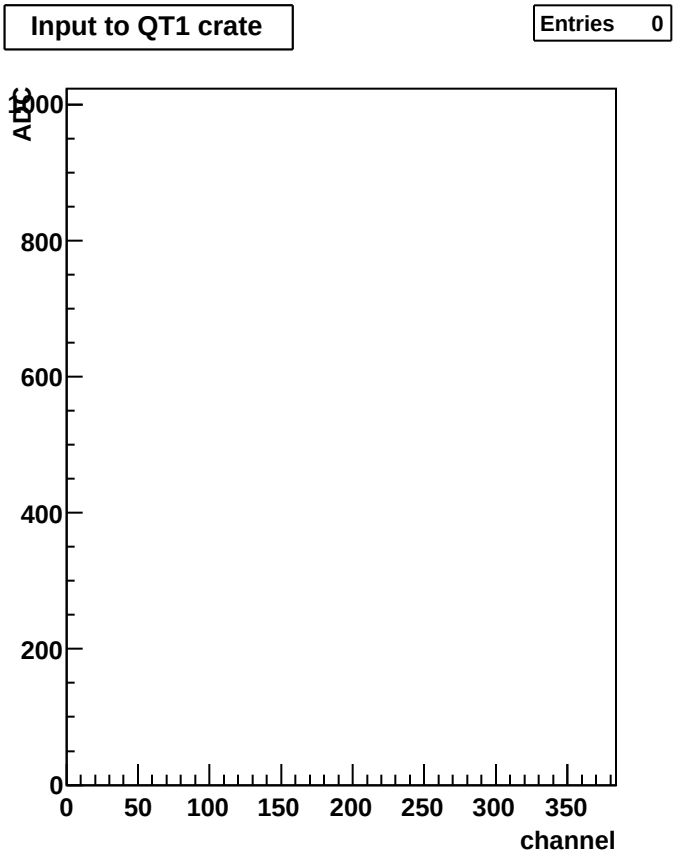

Entries 999974
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

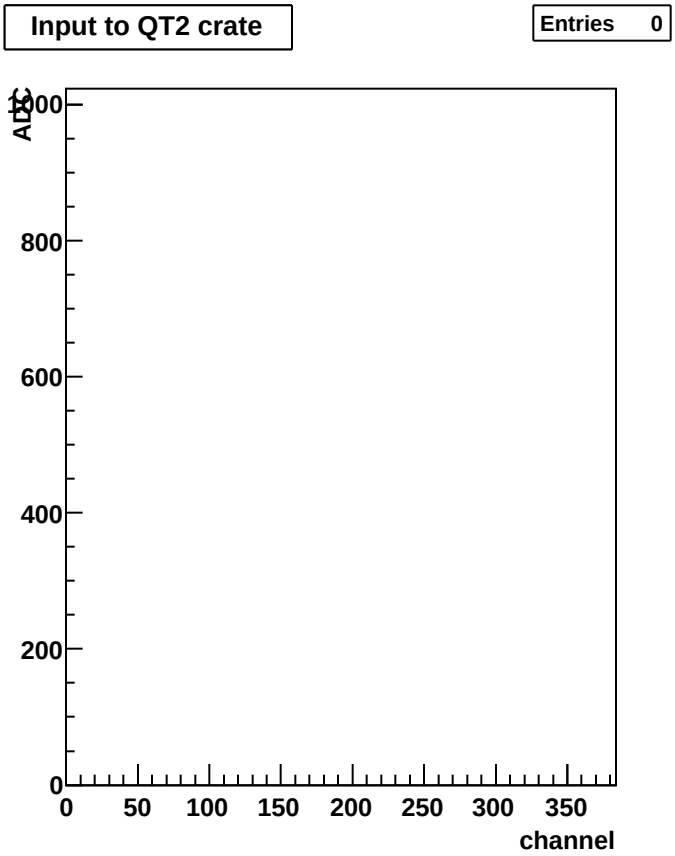

Entries 999974
BBC-L-East ADC Sum
BBC-L-West ADC Sum

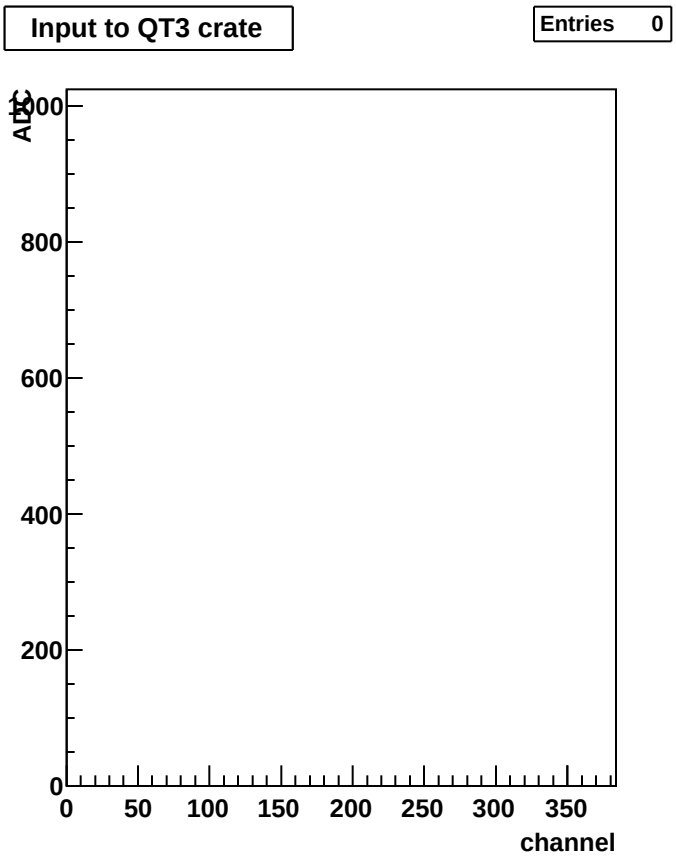

Entries 999974
BBC-S-East ADC Sum
BBC-S-West ADC Sum

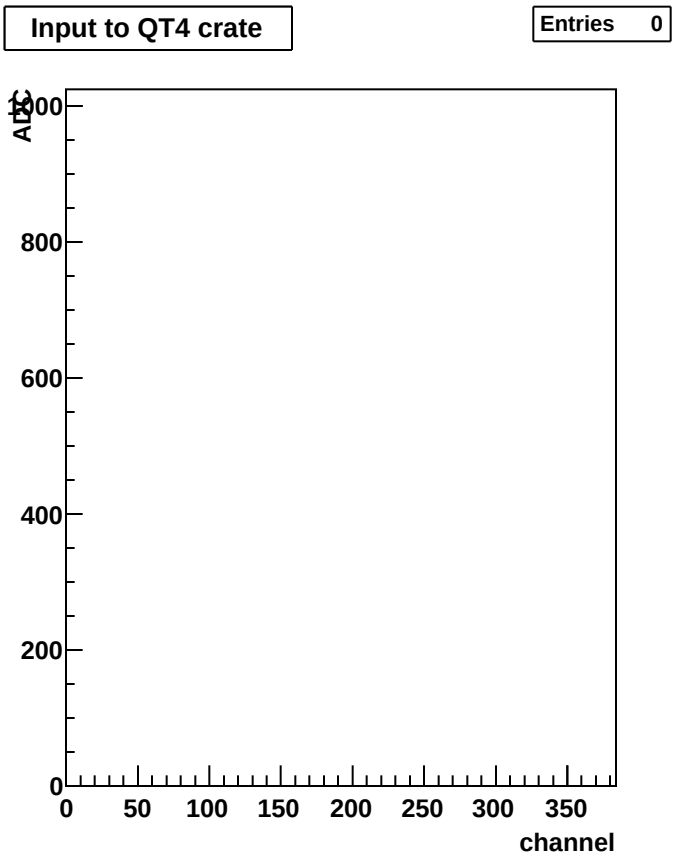

Entries 999974
ZDC East ADC Sum Att
ZDC West ADC Sum Att

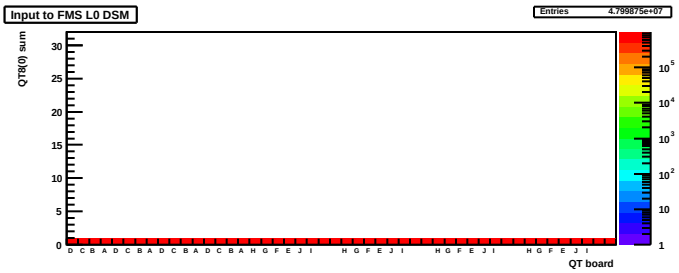

Entries 999974
BBC-L TAC Diff
BCC-S TAC Diff
Entries 999974
BBC-L TAC Diff
VPD TAC Diff
Entries 999974
BBC-L TAC Diff
ZDC TAC Diff
Entries 999974
BBC-S TAC Diff
VPD TAC Diff
Entries 999974
BBC-S TAC Diff
ZDC TAC Diff
Entries 999974
VPD TAC Diff
ZDC TAC Diff

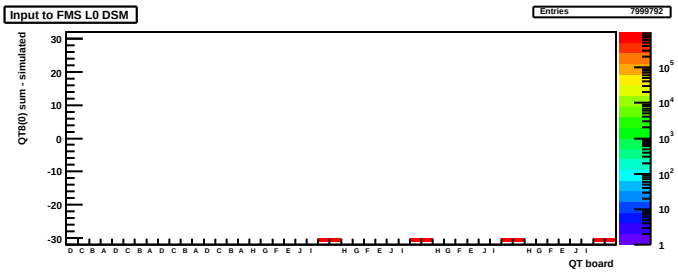

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

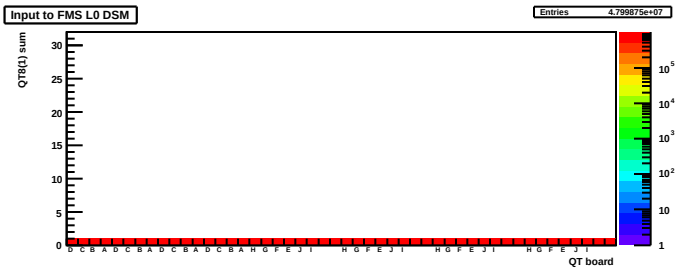

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

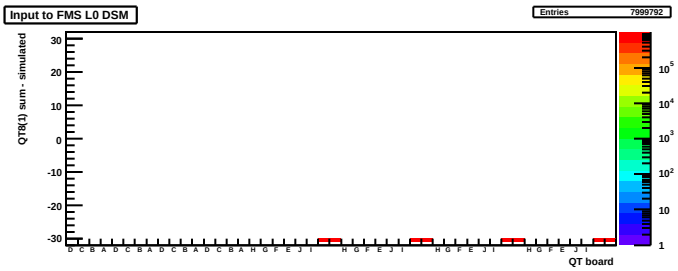

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

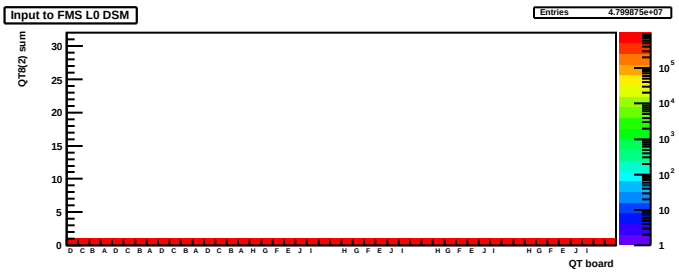

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

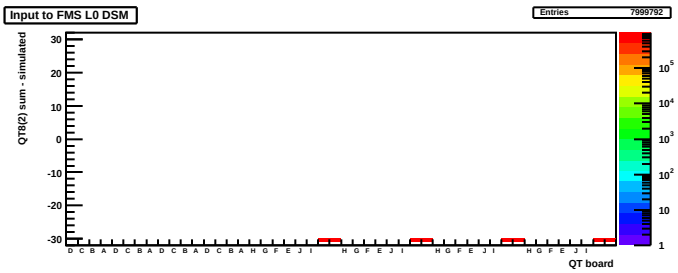

Input to FMS L0 DSM
Entries 4.799875e+07
QT8(0) sum
QT board

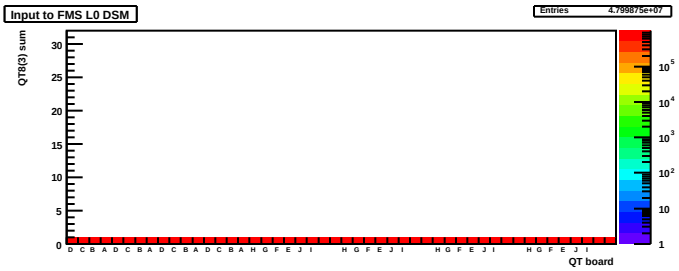

Input to FMS L0 DSM
Entries 7999792
QT8(0) sum - simulated
QT board

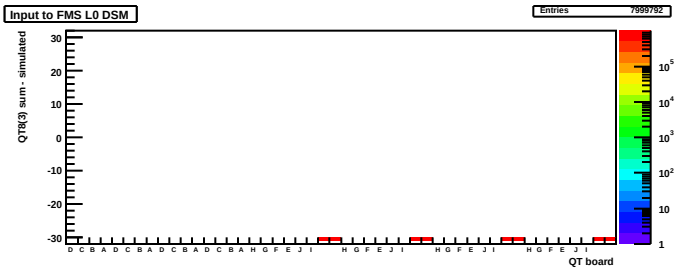

Input to FMS L0 DSM
Entries 4.799875e+07
QT8(1) sum
QT board

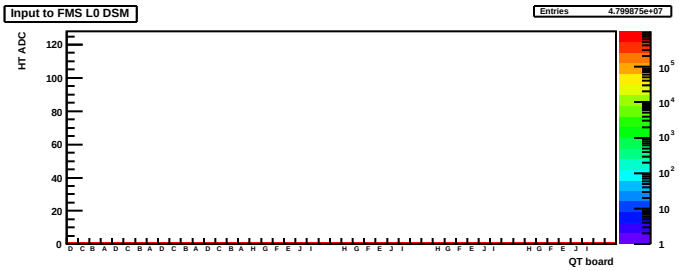

Input to FMS L0 DSM
Entries 7999792
QT8(1) sum - simulated
QT board

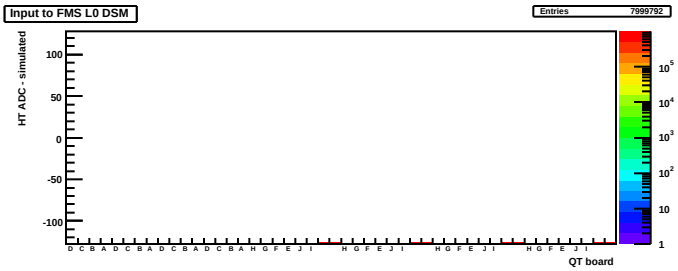

Input to FMS L0 DSM
Entries 4.799875e+07
QT8(2) sum
QT board

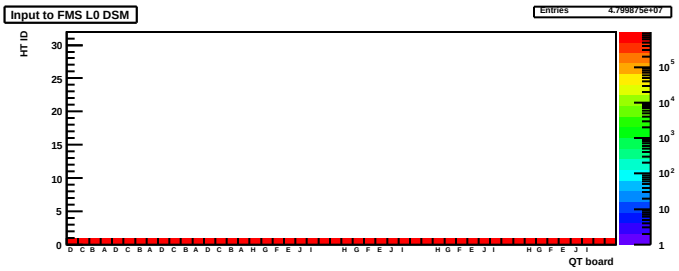

Input to FMS L0 DSM
Entries 7999792
QT8(2) sum - simulated
QT board

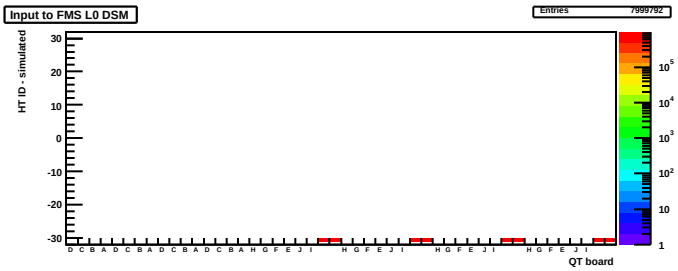

Input to FMS L0 DSM
Entries 4.799875e+07
QT8(3) sum
QT board

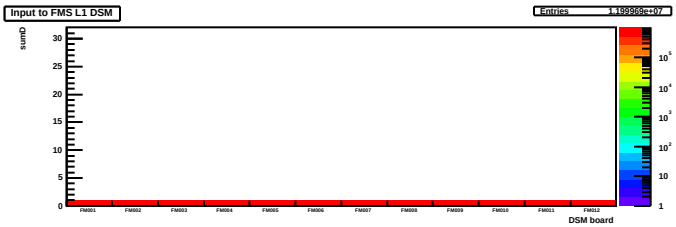

Input to FMS L0 DSM
Entries 7999792
QT8(3) sum - simulated
QT board

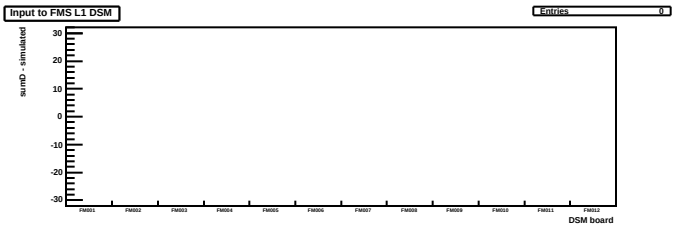

Input to FMS L0 DSM
Entries 4.799875e+07
HT ADC
QT board

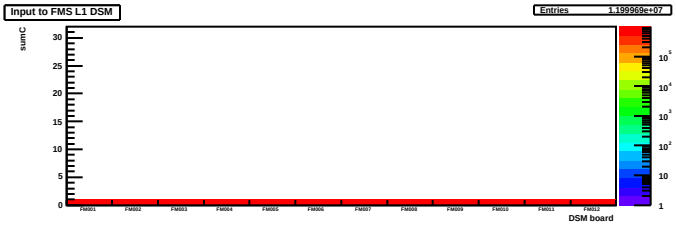

Input to FMS L0 DSM
Entries 7999792
HT ADC - simulated
QT board

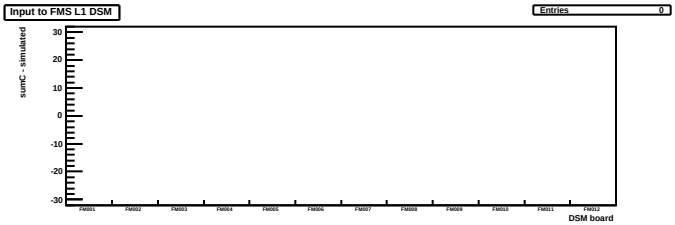

Input to FMS L0 DSM
Entries 4.799875e+07
HT ID
QT board

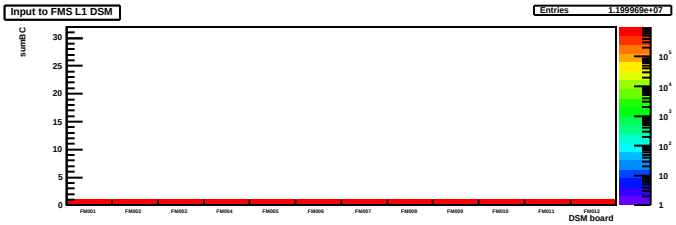

Input to FMS L0 DSM
Entries 7999792
HT ID - simulated
QT board

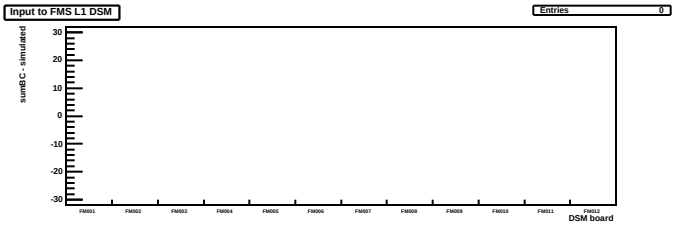

Input to FMS L1 DSM
Entries 1.199969e+07
sumD
DSM board

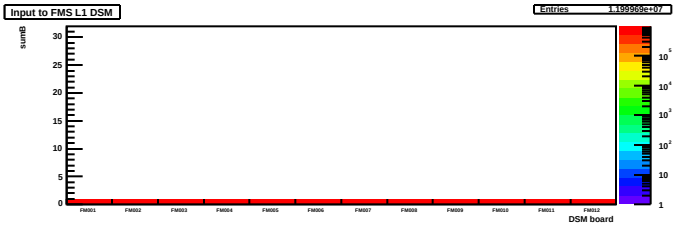

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

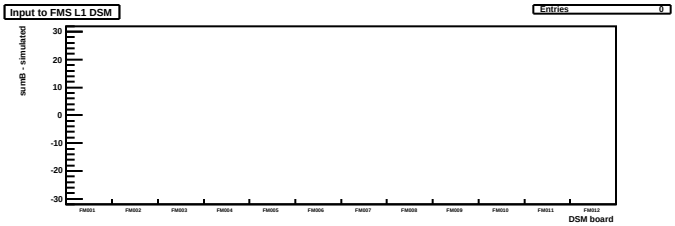

Input to FMS L1 DSM
Entries 1.199969e+07
sumC
DSM board

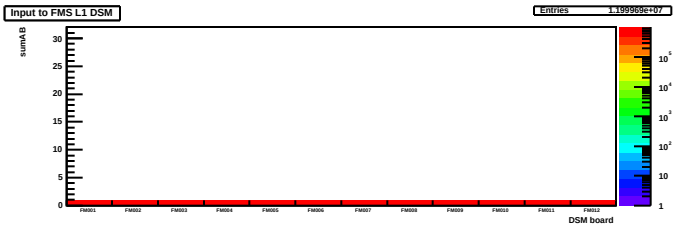

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

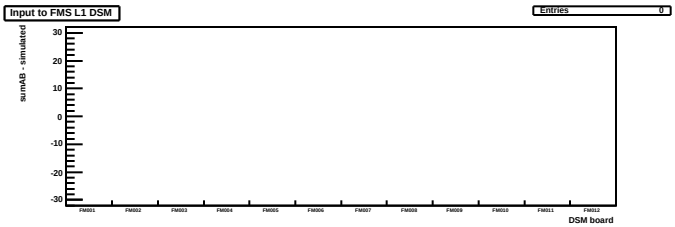

Input to FMS L1 DSM
Entries 1.199969e+07
sumBC
DSM board

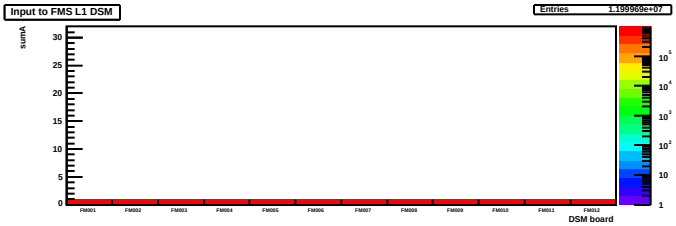

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

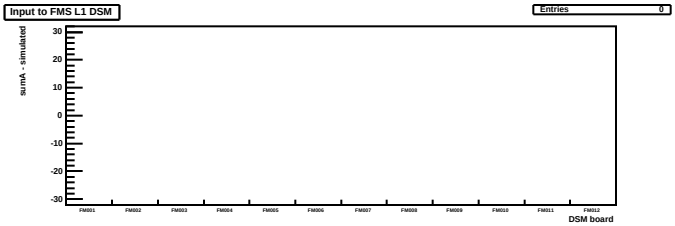

Input to FMS L1 DSM
Entries 1.199969e+07
sumB
DSM board

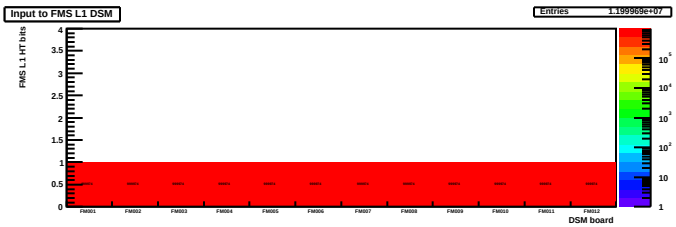

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

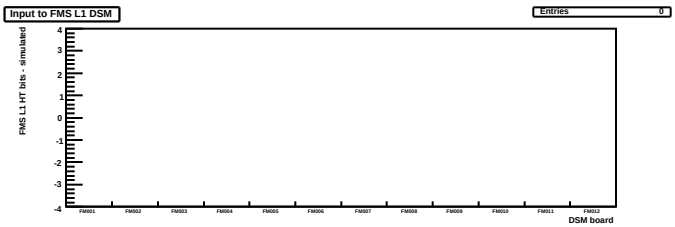

Input to FMS L1 DSM
Entries 1.199969e+07
sumAB
DSM board

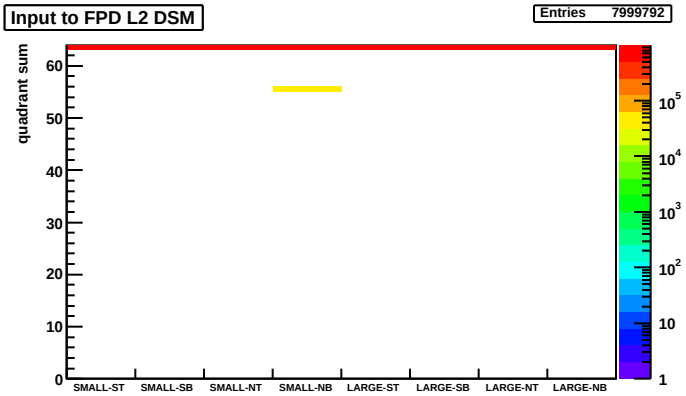

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

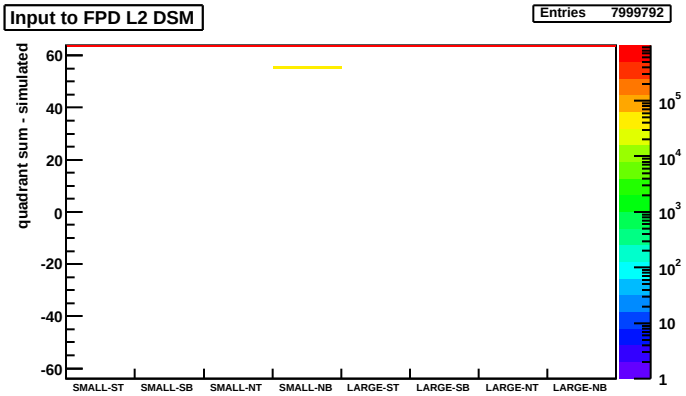

Input to FMS L1 DSM
Entries 1.199969e+07
sumA
DSM board

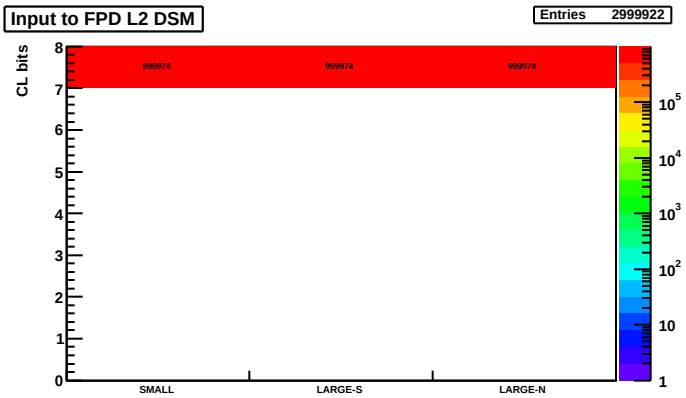

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

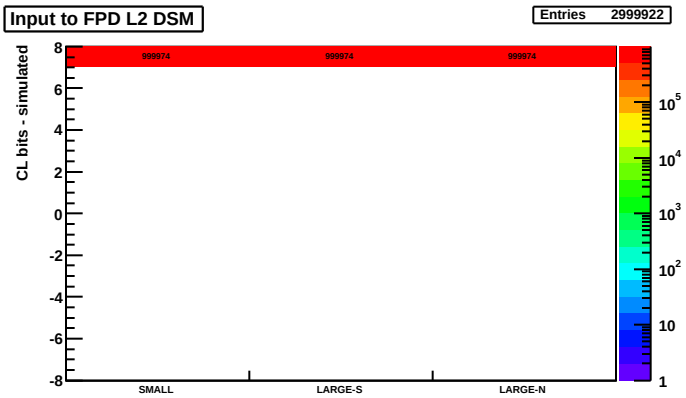

Input to FMS L1 DSM
Entries 1.199969e+07
FMS L1 HT bits
DSM board

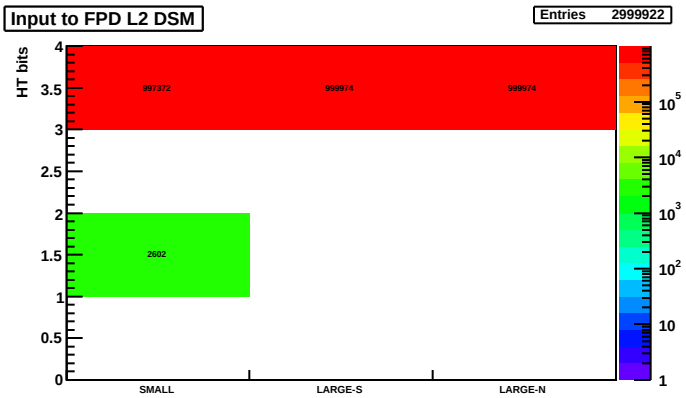

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

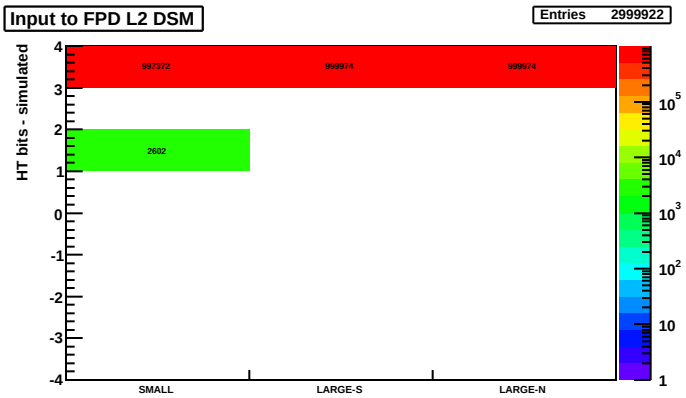

Input to FPD L2 DSM
Entries 7999792
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

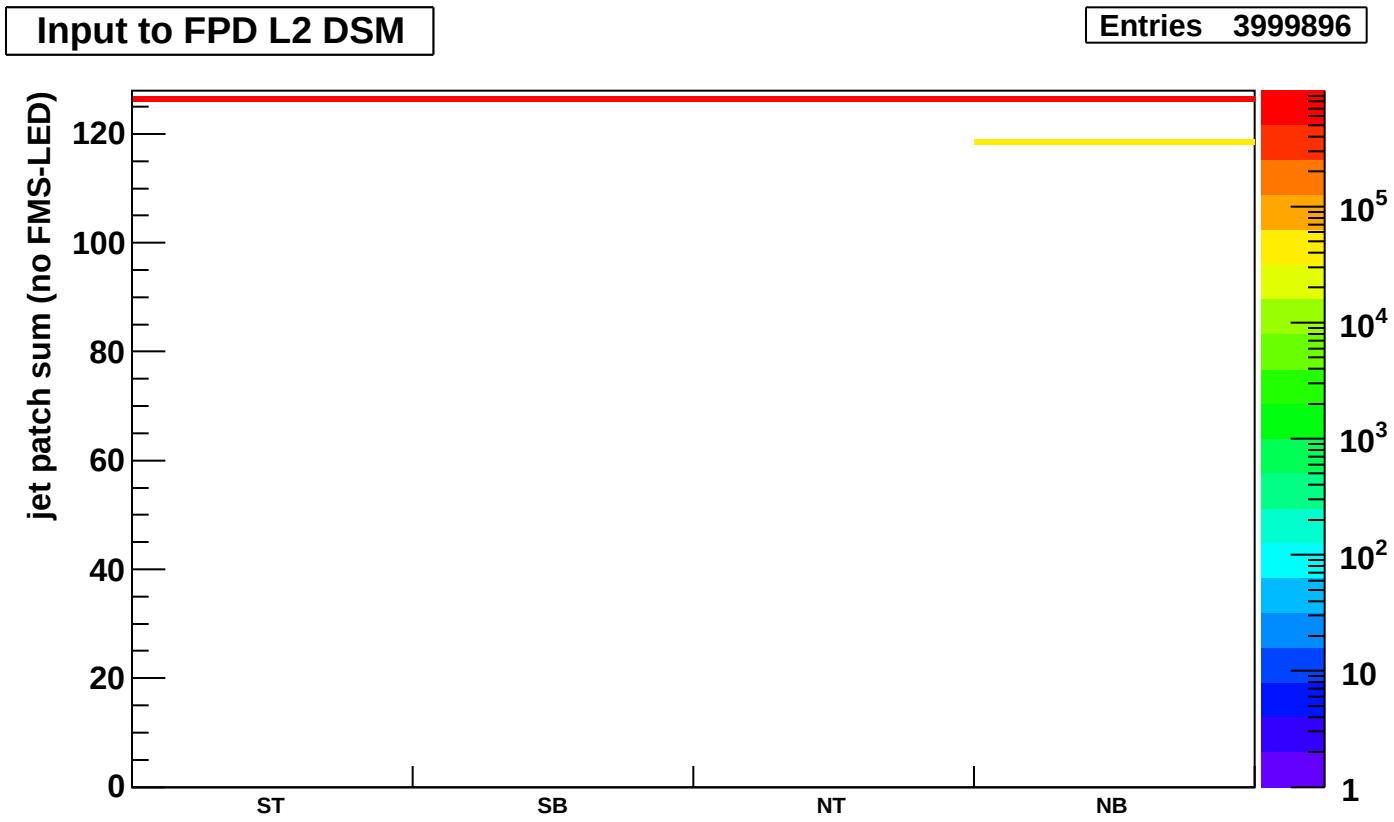

Input to FPD L2 DSM
Entries 7999792
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

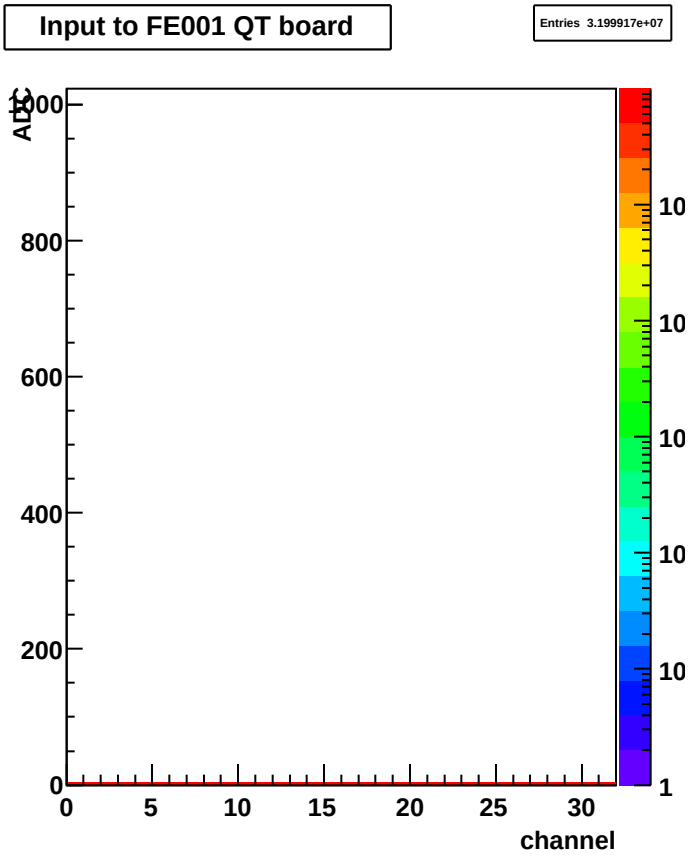

Input to FPD L2 DSM
Entries 2999922
CL bits
999974
999974
999974
SMALL
LARGE-S
LARGE-N

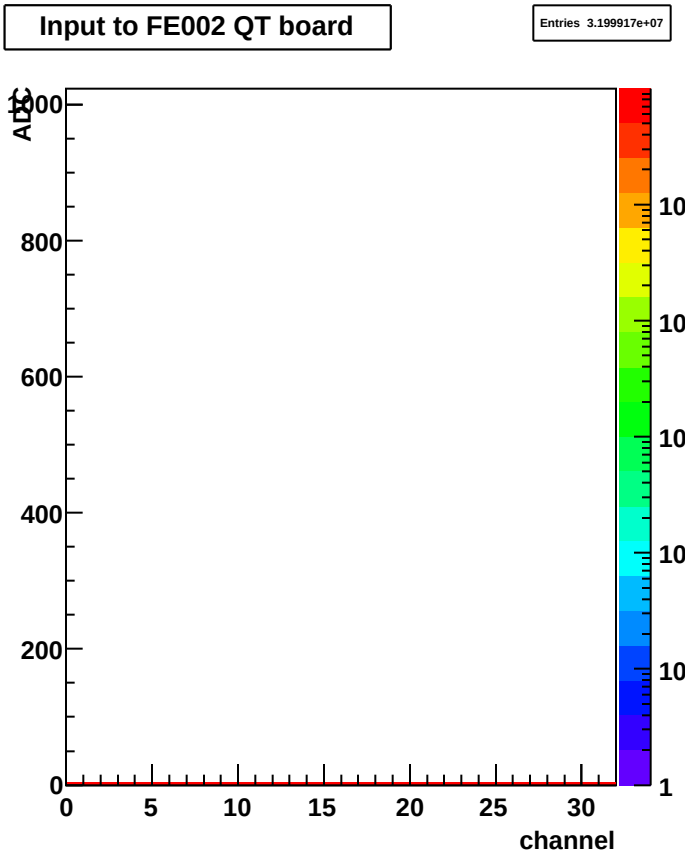

Input to FPD L2 DSM
Entries 2999922
CL bits - simulated
999974
999974
999974
SMALL
LARGE-S
LARGE-N

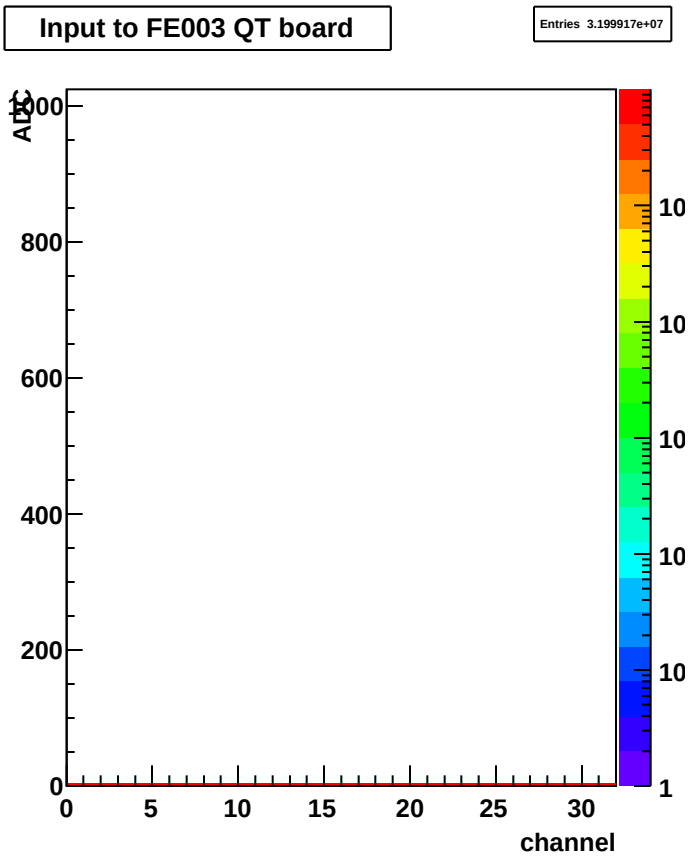

Input to FPD L2 DSM
Entries 2999922
HT bits
997372
999974
999974
2602
SMALL
LARGE-S
LARGE-N

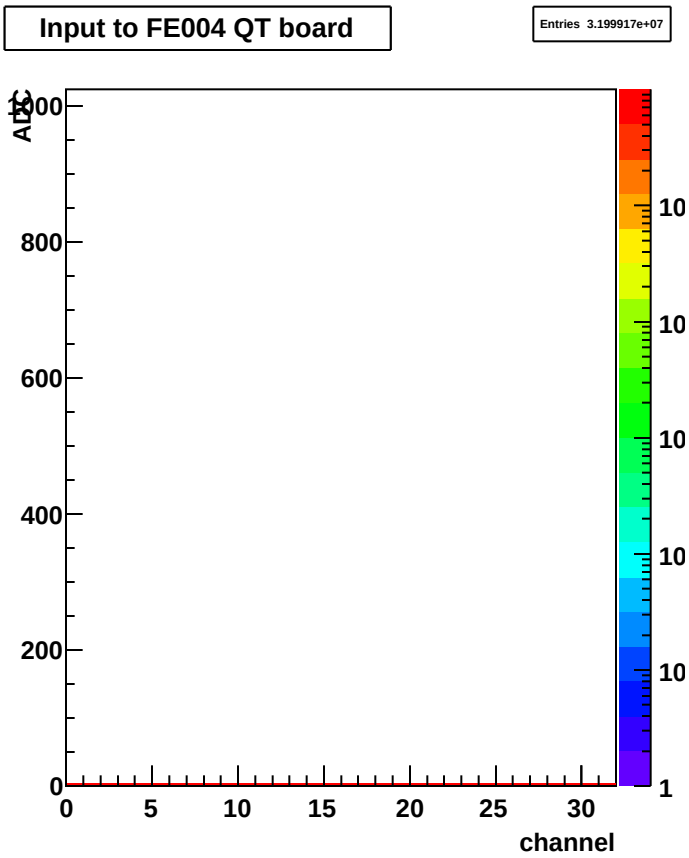

Input to FPD L2 DSM
Entries 2999922
HT bits - simulated
997372
999974
999974
2602
SMALL
LARGE-S
LARGE-N


Input to FPD L2 DSM
Entries 3999896
jet patch sum (no FMS-LED)
120
100
80
60
40
20
0
ST
SB
NT
NB
10^5
10^4
10^3
10^2
10
1

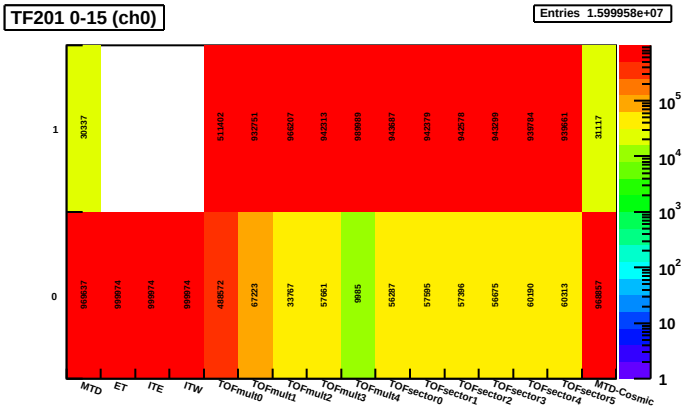

Input to FE001 QT board
Entries 3.199917e+07
ADC
channel


Input to FE002 QT board
Entries 3.199917e+07
ADC
channel

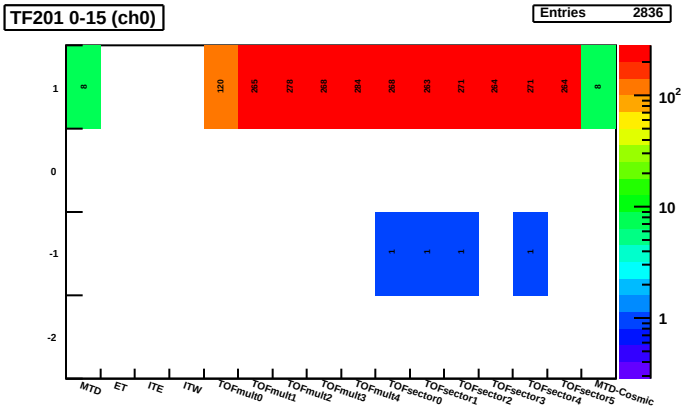

Input to FE003 QT board
Entries 3.199917e+07
ADC
channel


Input to FE004 QT board
Entries 3.199917e+07
ADC
channel

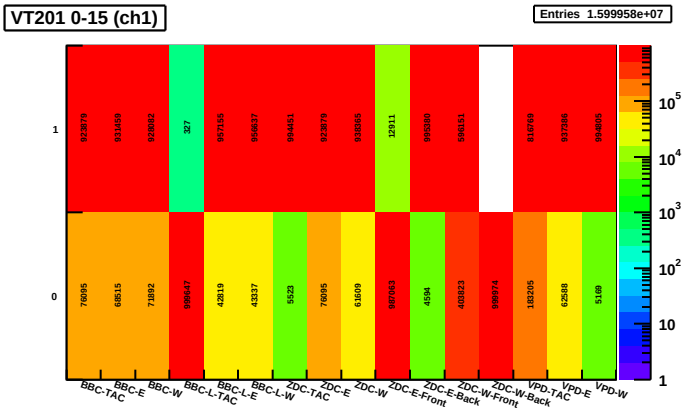
TF201 0-15 (ch0)



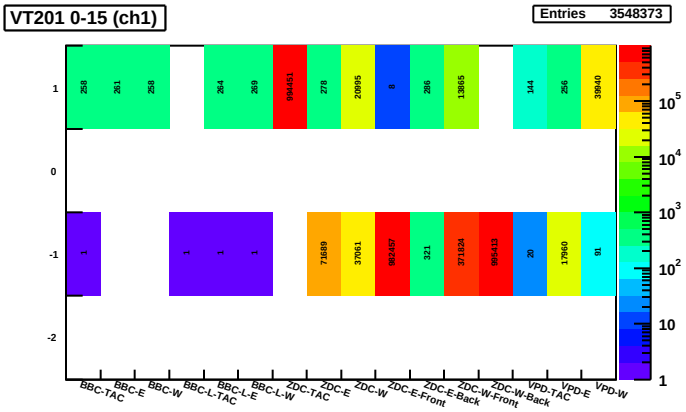
TF201 0-15 (ch0)



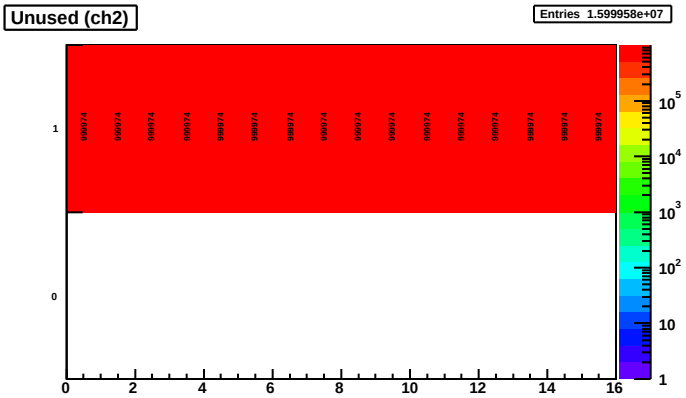
VT201 0-15 (ch1)

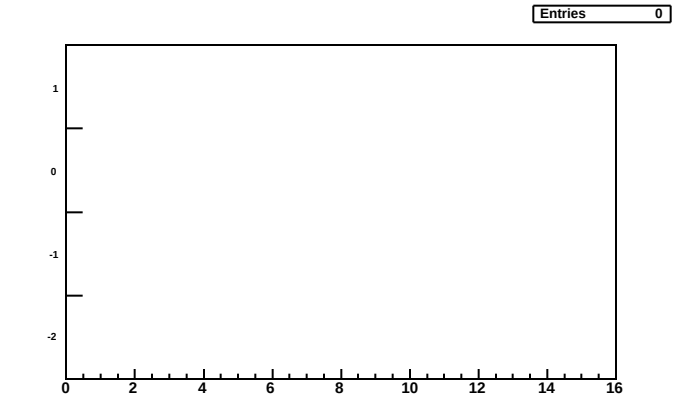


VT201 0-15 (ch1)

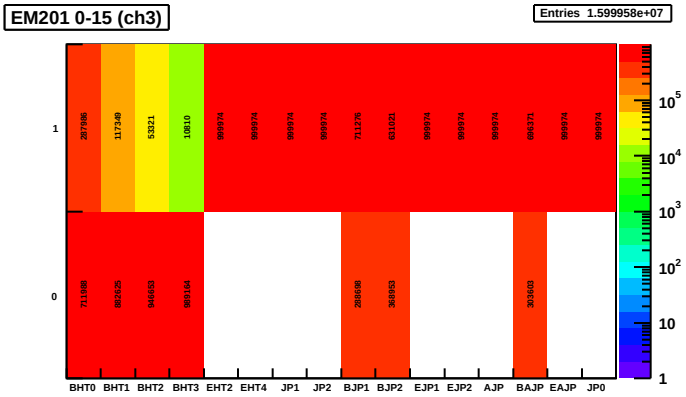


Unused (ch2)

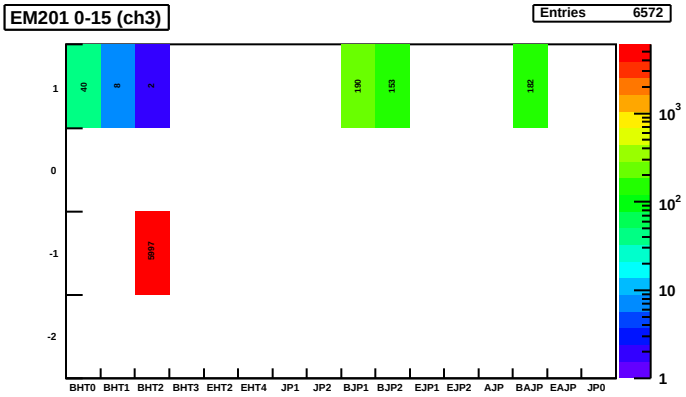




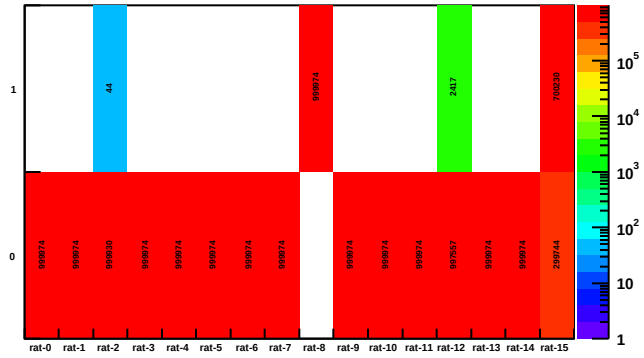
EM201 0-15 (ch3)

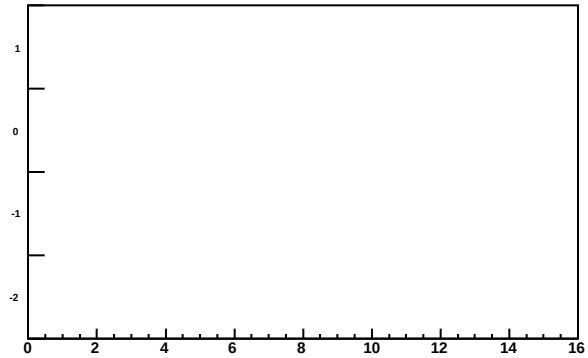


EM201 0-15 (ch3)

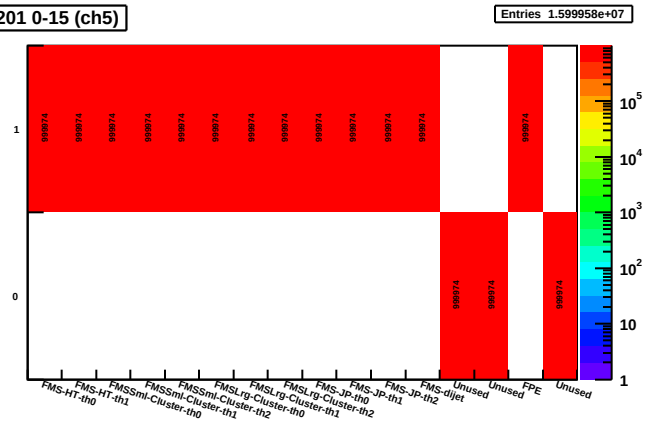


RAT board (ch4)

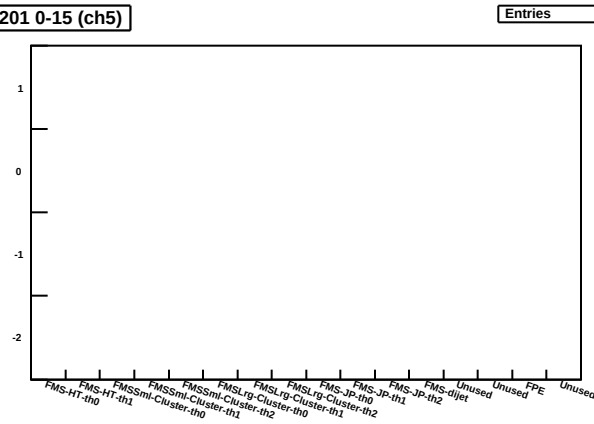




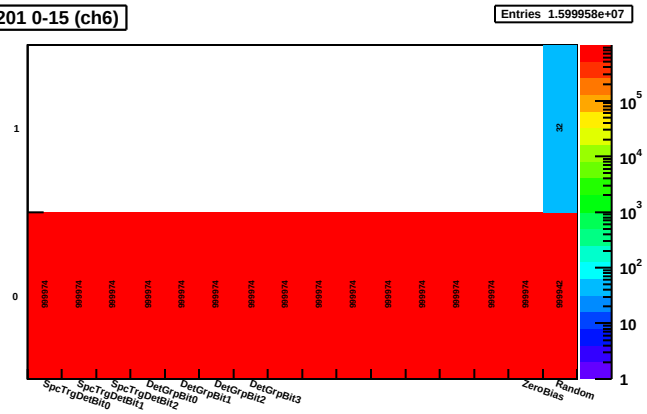
FP201 0-15 (ch5)

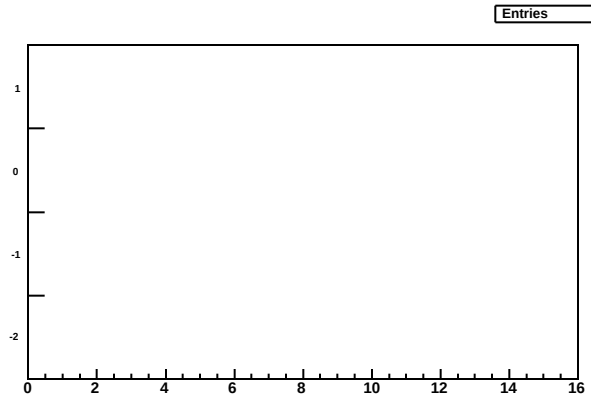
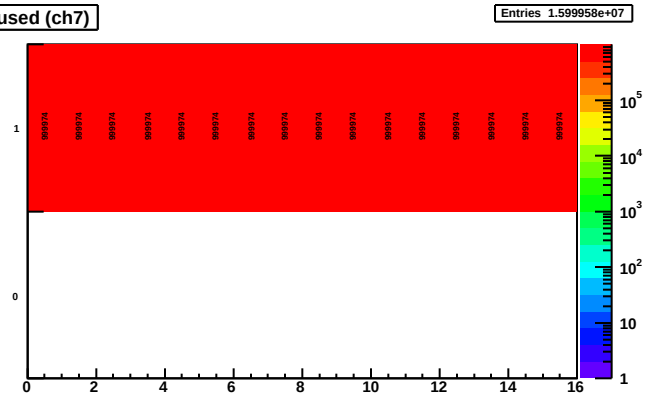
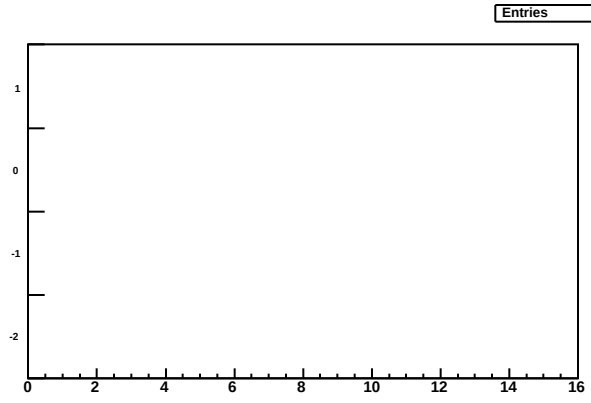


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)