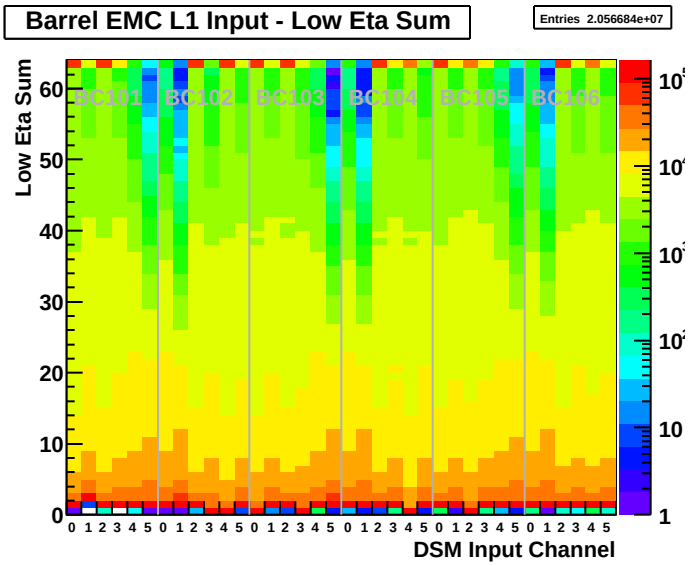
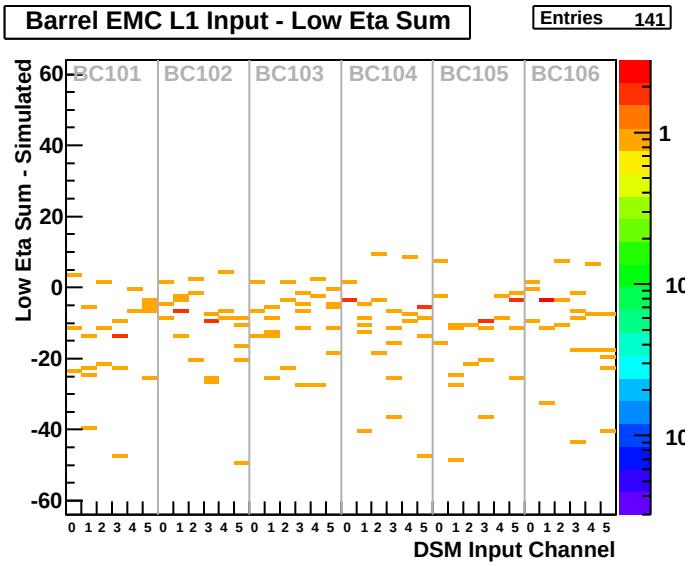
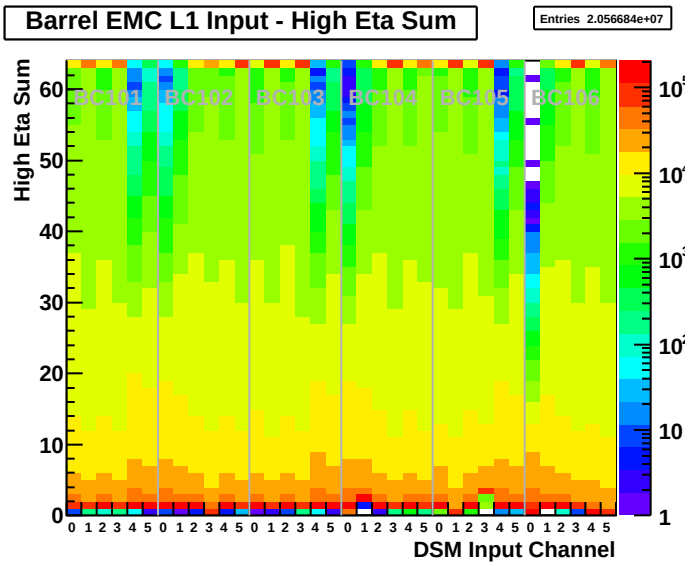
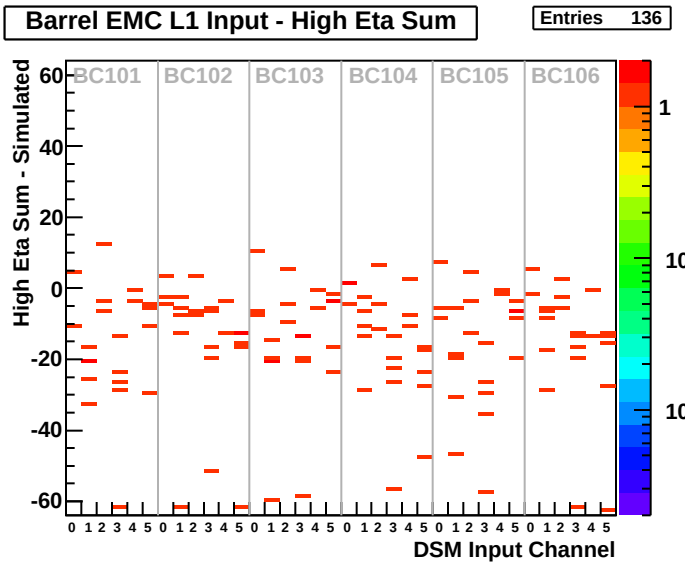
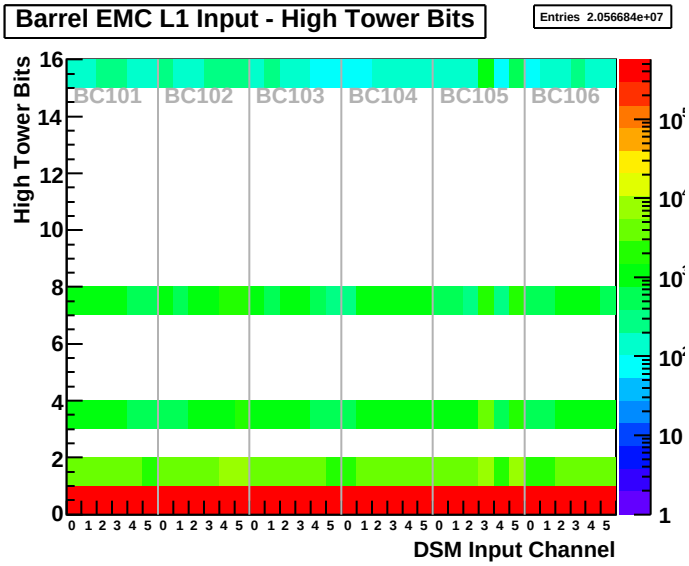
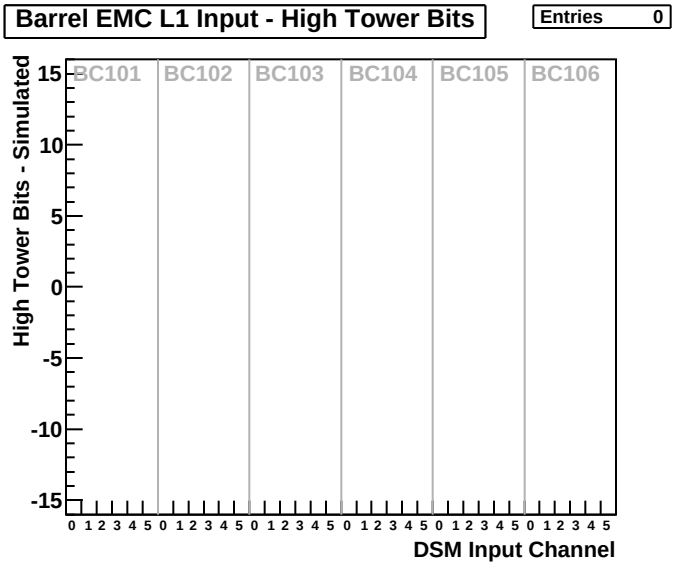

Barrel EMC L1 Input - Low Eta Sum
Entries 2.056684e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

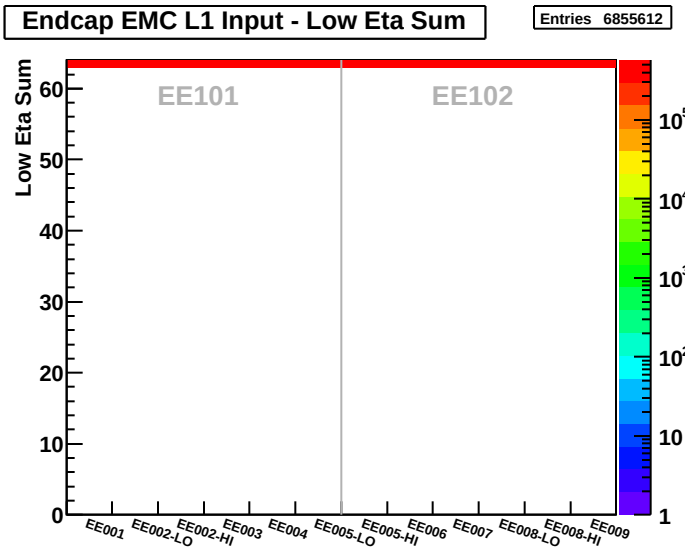

Barrel EMC L1 Input - Low Eta Sum
Entries 141
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

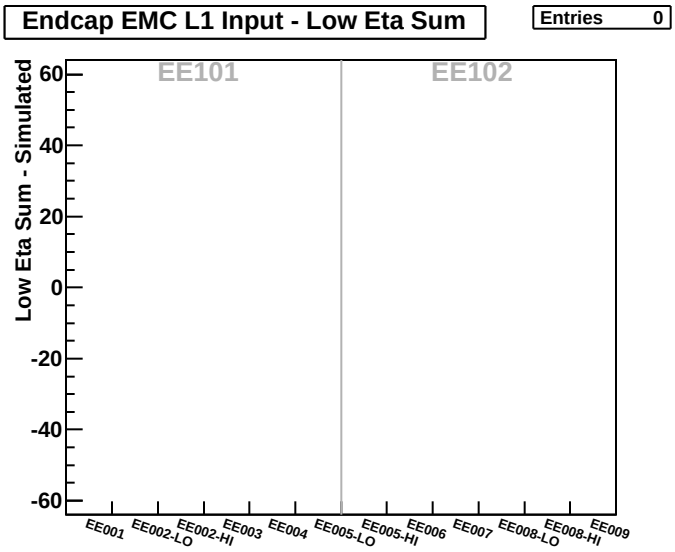

Barrel EMC L1 Input - High Eta Sum
Entries 2.056684e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

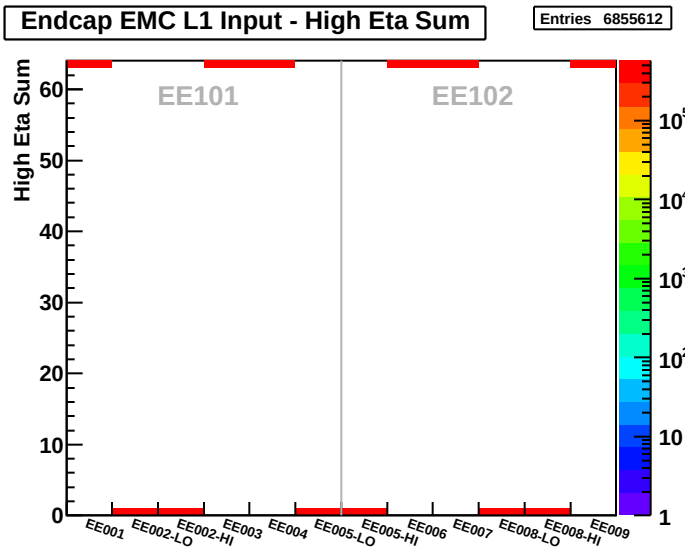

Barrel EMC L1 Input - High Eta Sum
Entries 136
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

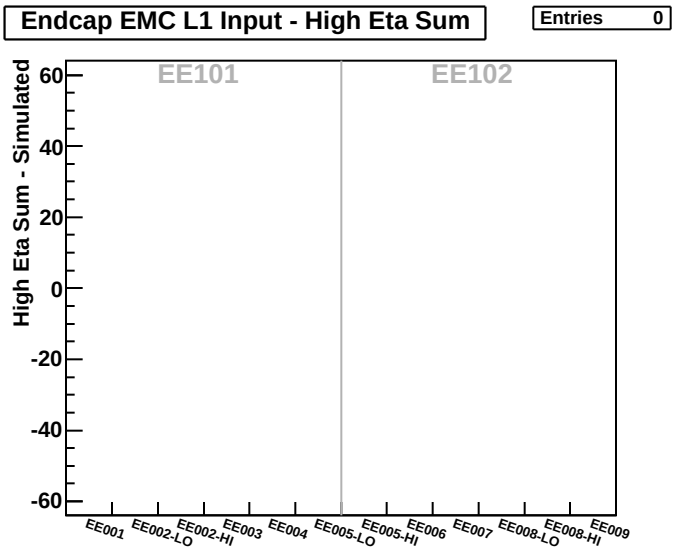

Barrel EMC L1 Input - High Tower Bits
Entries 2.056684e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

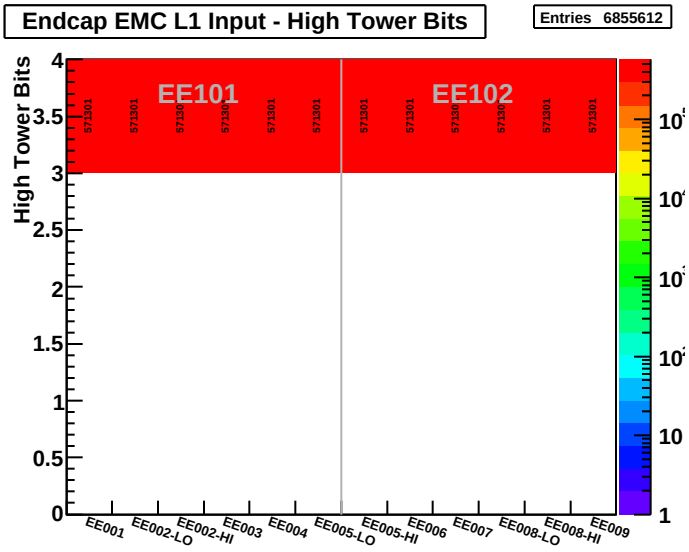

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

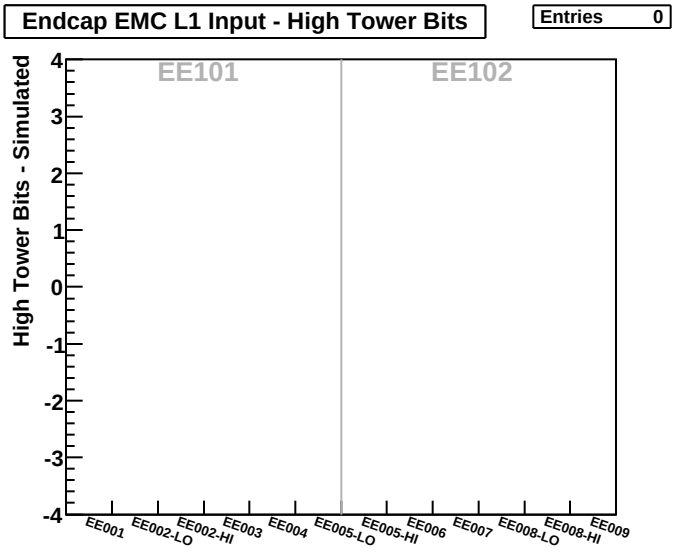

Endcap EMC L1 Input - Low Eta Sum
Entries 6855612
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

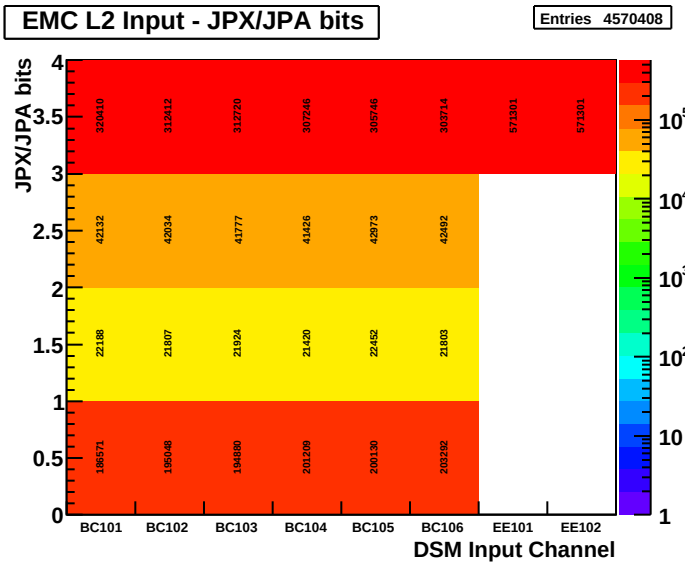

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

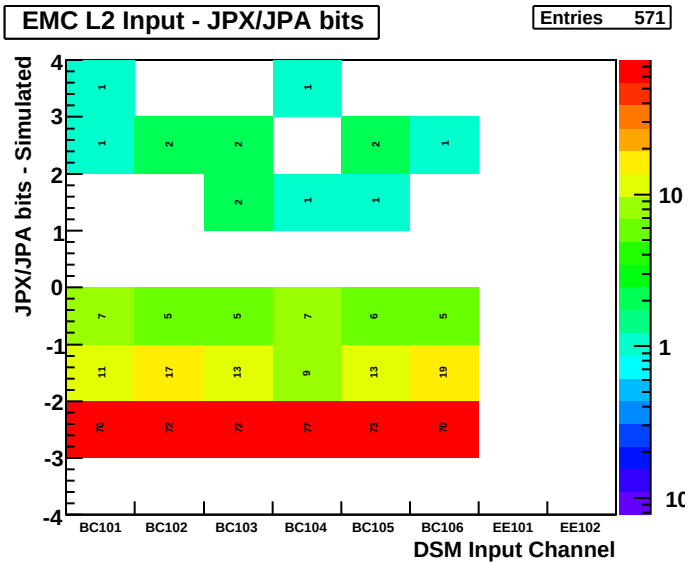

Endcap EMC L1 Input - High Eta Sum
Entries 6855612
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

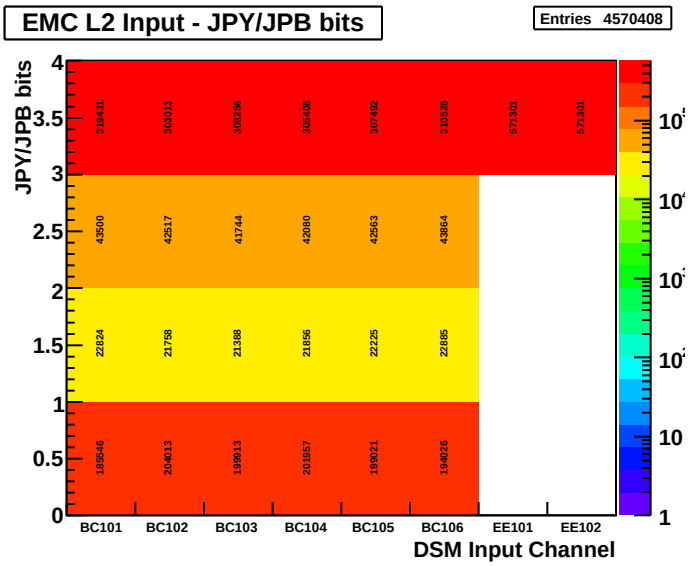

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

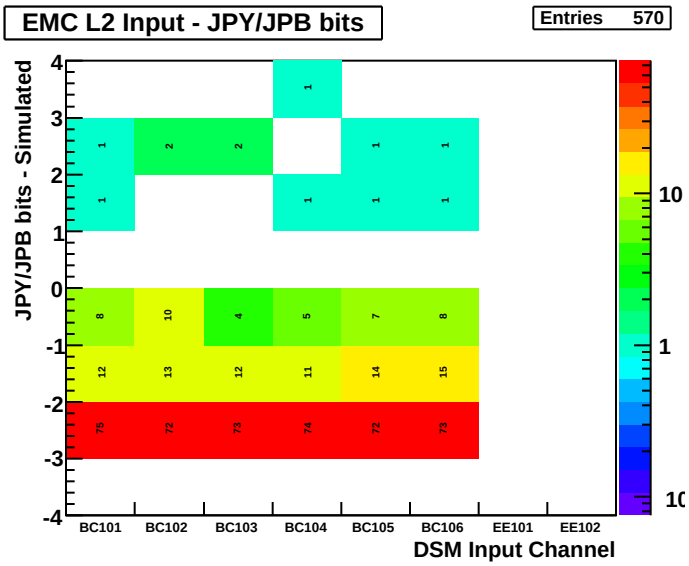

Endcap EMC L1 Input - High Tower Bits
Entries 6855612
High Tower Bits
EE101
EE102
571301
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

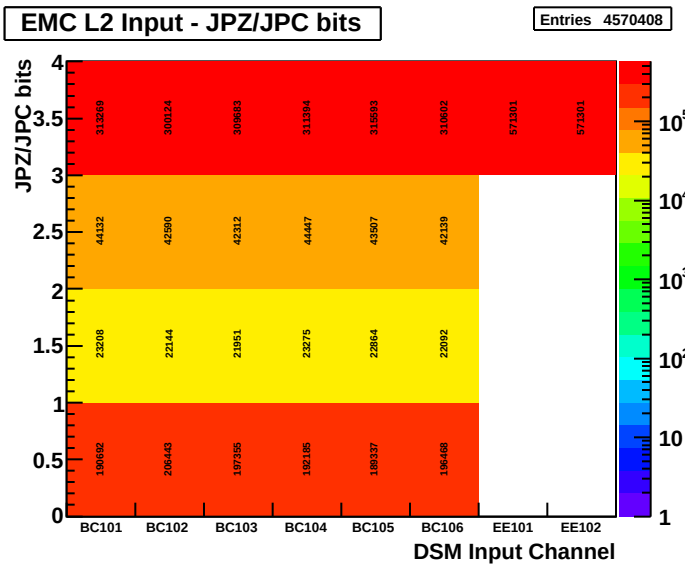

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

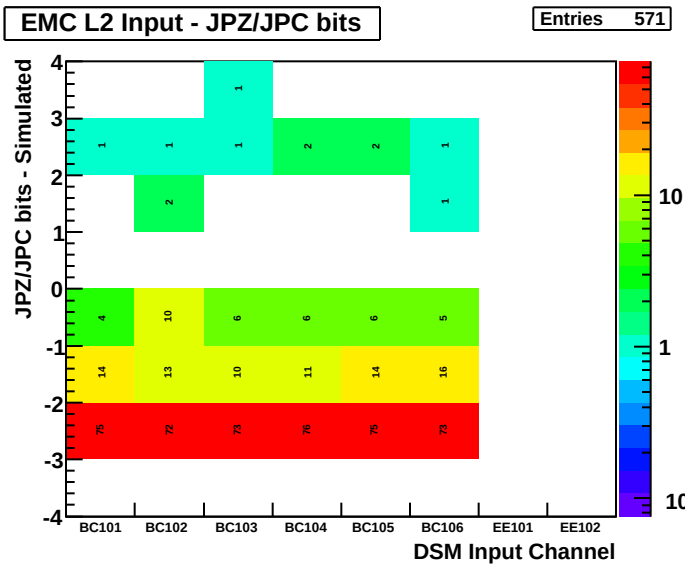

EMC L2 Input - JPX/JPA bits
Entries 4570408
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
320410 312412 312720 307246 305746 303714 571301 571301
42132 42034 41777 41426 42973 42492
22188 21807 21924 21420 22452 21803
186571 195048 194880 201209 200130 203292

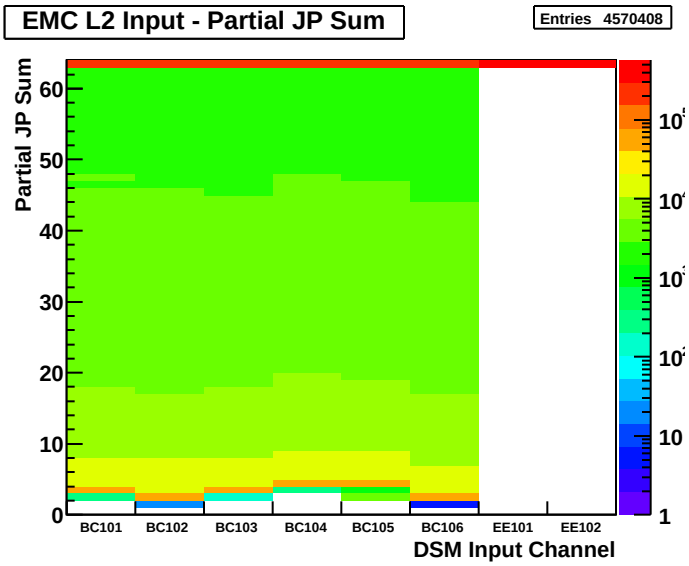

EMC L2 Input - JPX/JPA bits
Entries 571
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1 1 1
1 2 2 2 1
2 1 1
7 5 5 7 6 5
11 17 13 9 13 19
76 72 72 77 73 70

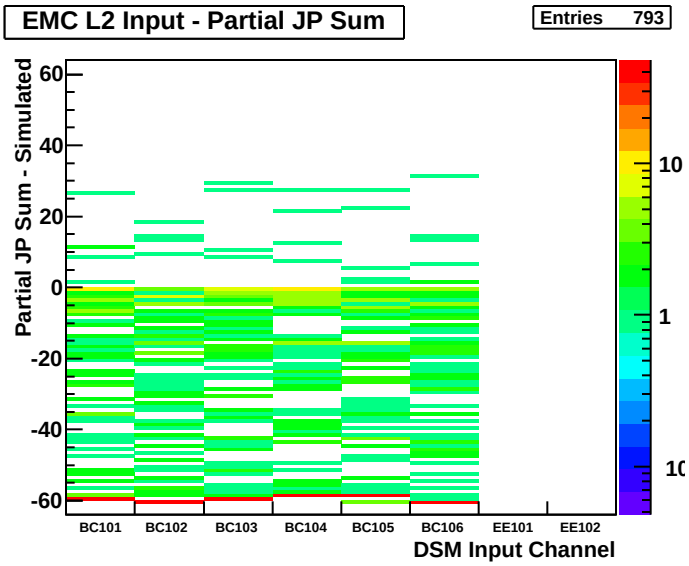

EMC L2 Input - JPY/JPB bits
Entries 4570408
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
319431 303013 308256 305408 307492 310526 571301 571301
43500 42517 43744 42080 42563 43864
22824 21758 21388 21856 22225 22885
185546 204013 199913 201957 199021 194026

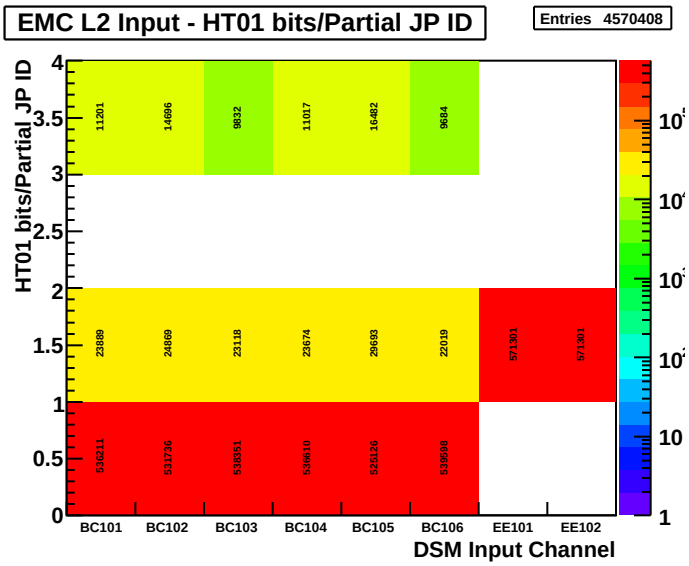

EMC L2 Input - JPY/JPB bits
Entries 570
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1
1 2 2 1 1
1 1 1 1
8 10 4 5 7 8
12 13 12 11 14 15
75 72 73 74 72 73

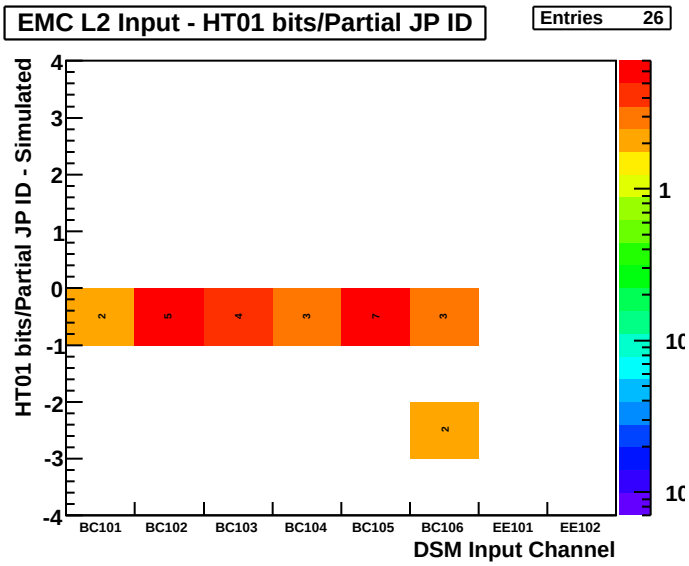

EMC L2 Input - JPZ/JPC bits
Entries 4570408
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
313269 300124 309683 311394 315593 310602 571301 571301
44132 42590 42312 44447 43507 42139
23208 22144 21951 23275 22864 22092
190692 206443 197355 192185 189337 196468

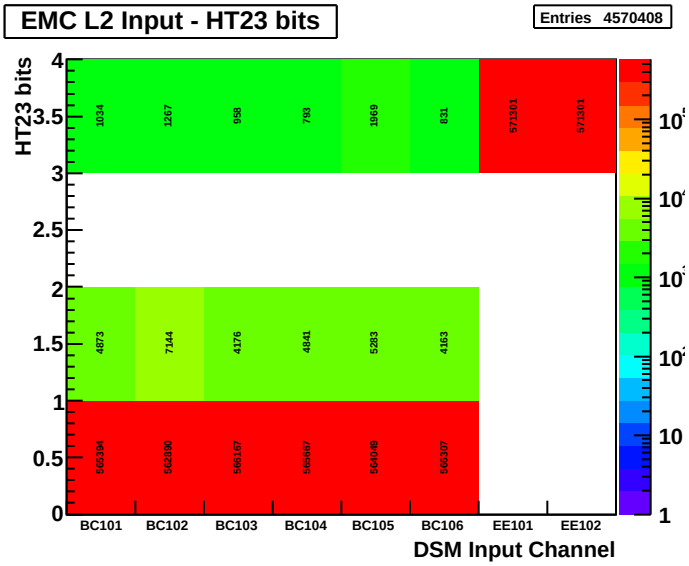

EMC L2 Input - JPZ/JPC bits
Entries 571
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1
1 1 1 2 2 1
2 1
4 10 6 6 6 5
14 13 10 11 14 16
75 72 73 76 75 73

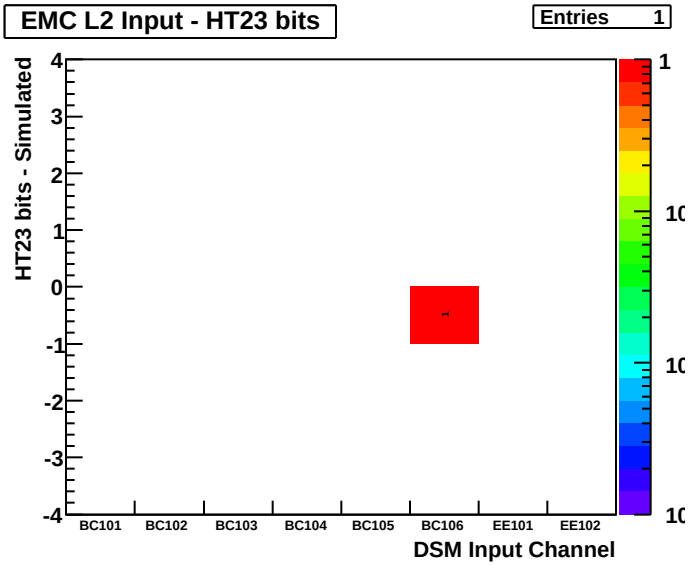

EMC L2 Input - Partial JP Sum
Entries 4570408
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

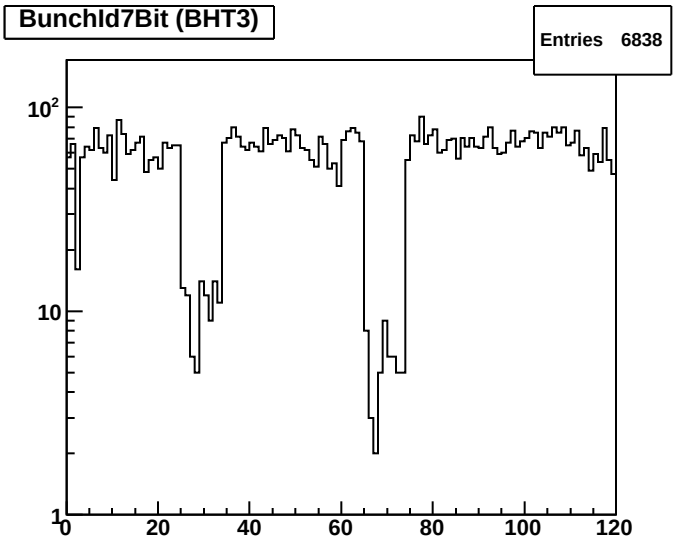

EMC L2 Input - Partial JP Sum
Entries 793
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

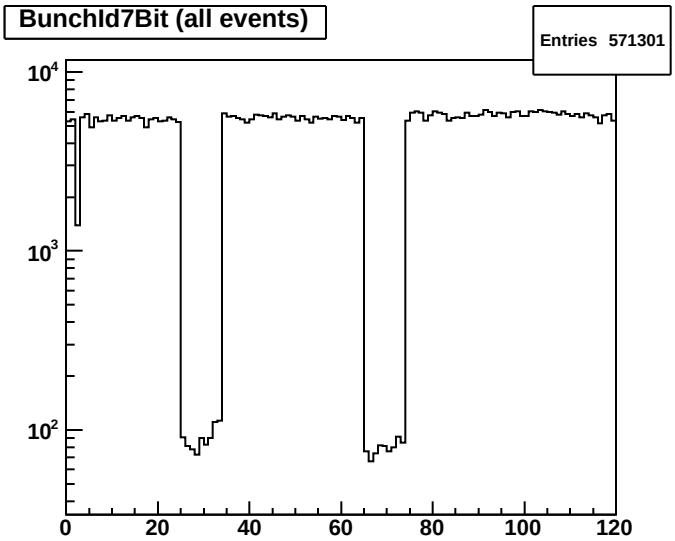

EMC L2 Input - HT01 bits/Partial JP ID
Entries 4570408
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

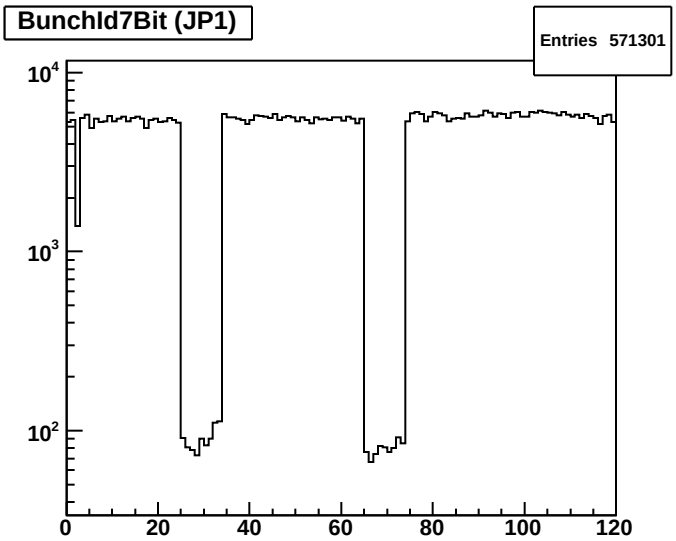

EMC L2 Input - HT01 bits/Partial JP ID
Entries 26
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

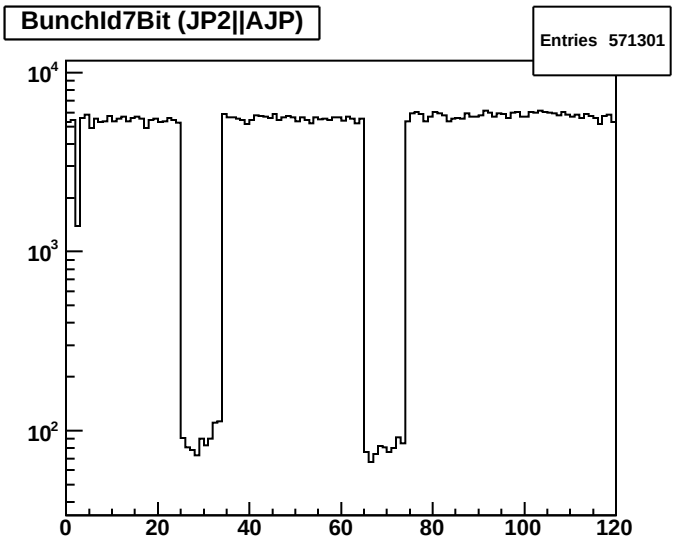

EMC L2 Input - HT23 bits
Entries 4570408
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

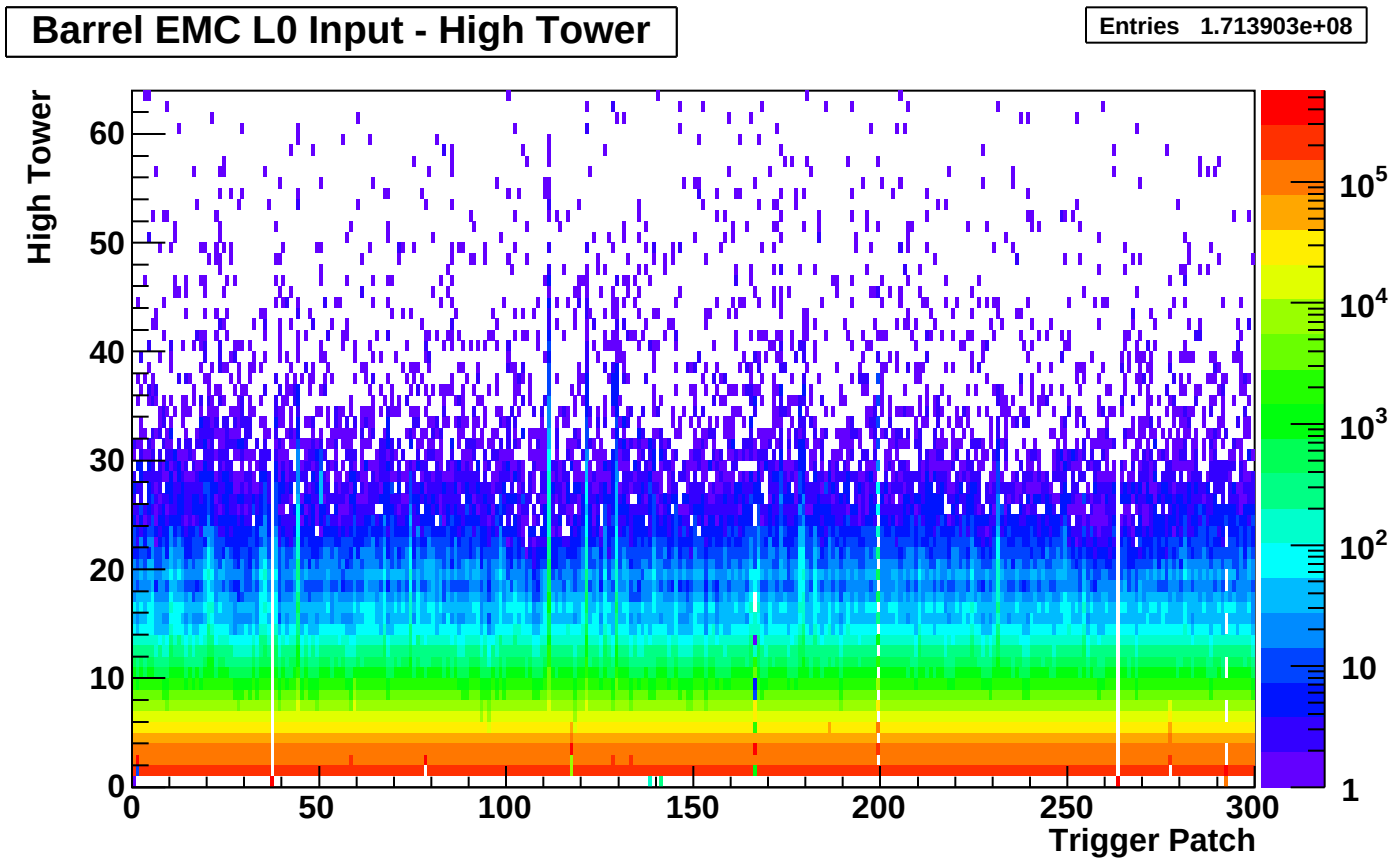

EMC L2 Input - HT23 bits
Entries 1
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

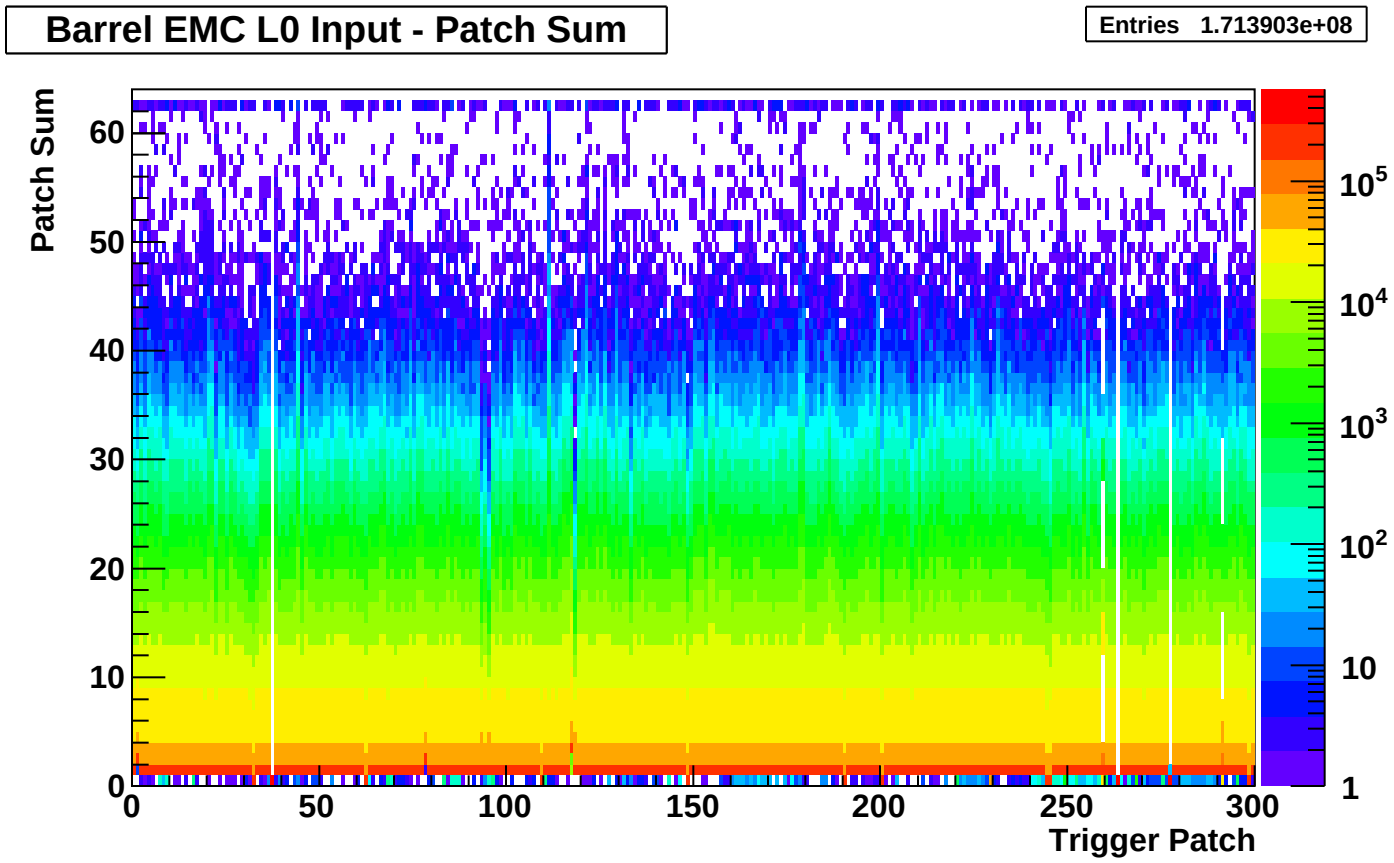

BunchId7Bit (BHT3)
Entries 6838
10^2
10
1
0
20
40
60
80
100
120

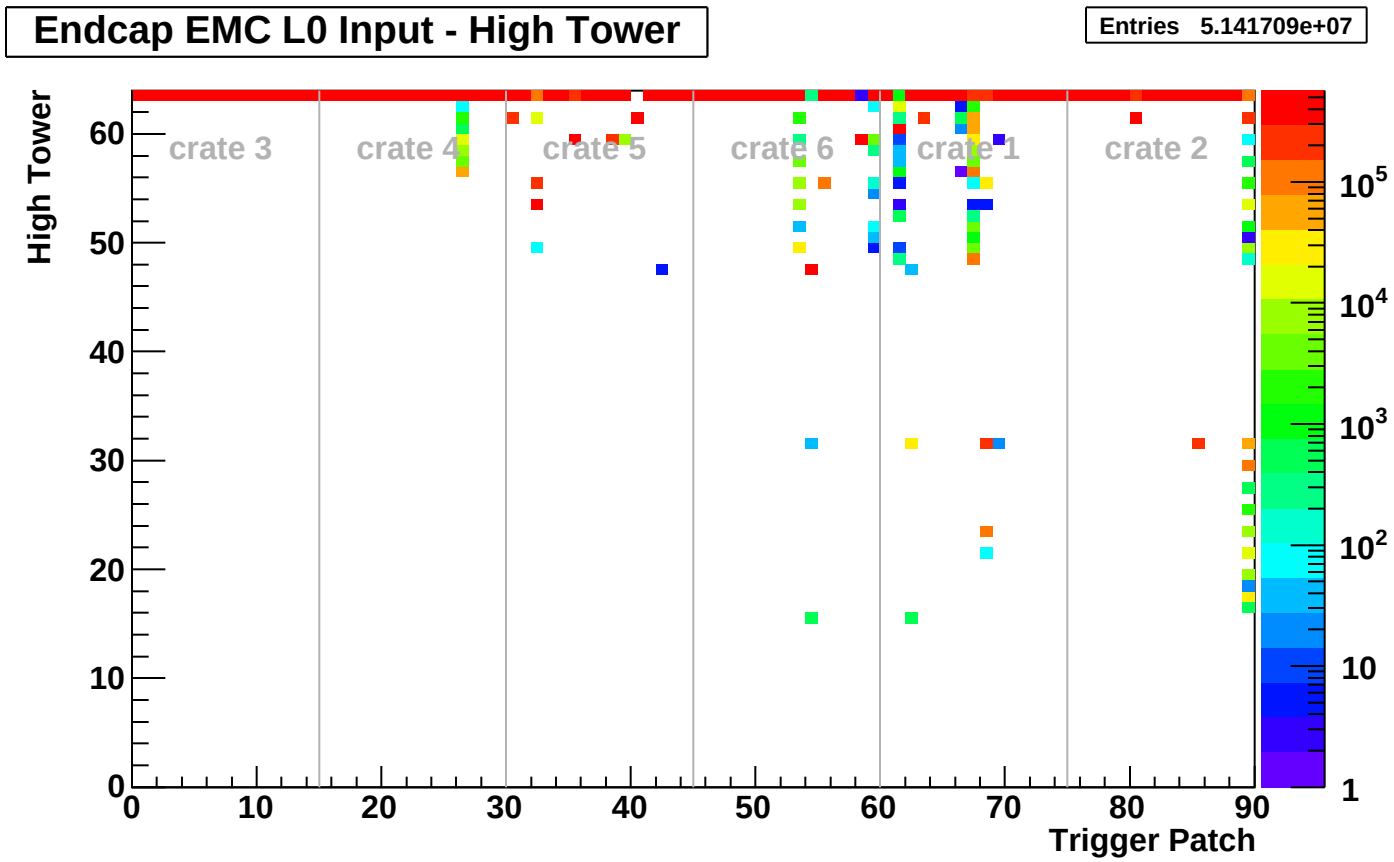

BunchId7Bit (all events)
Entries 571301
10^4
10^3
10^2
0
20
40
60
80
100
120

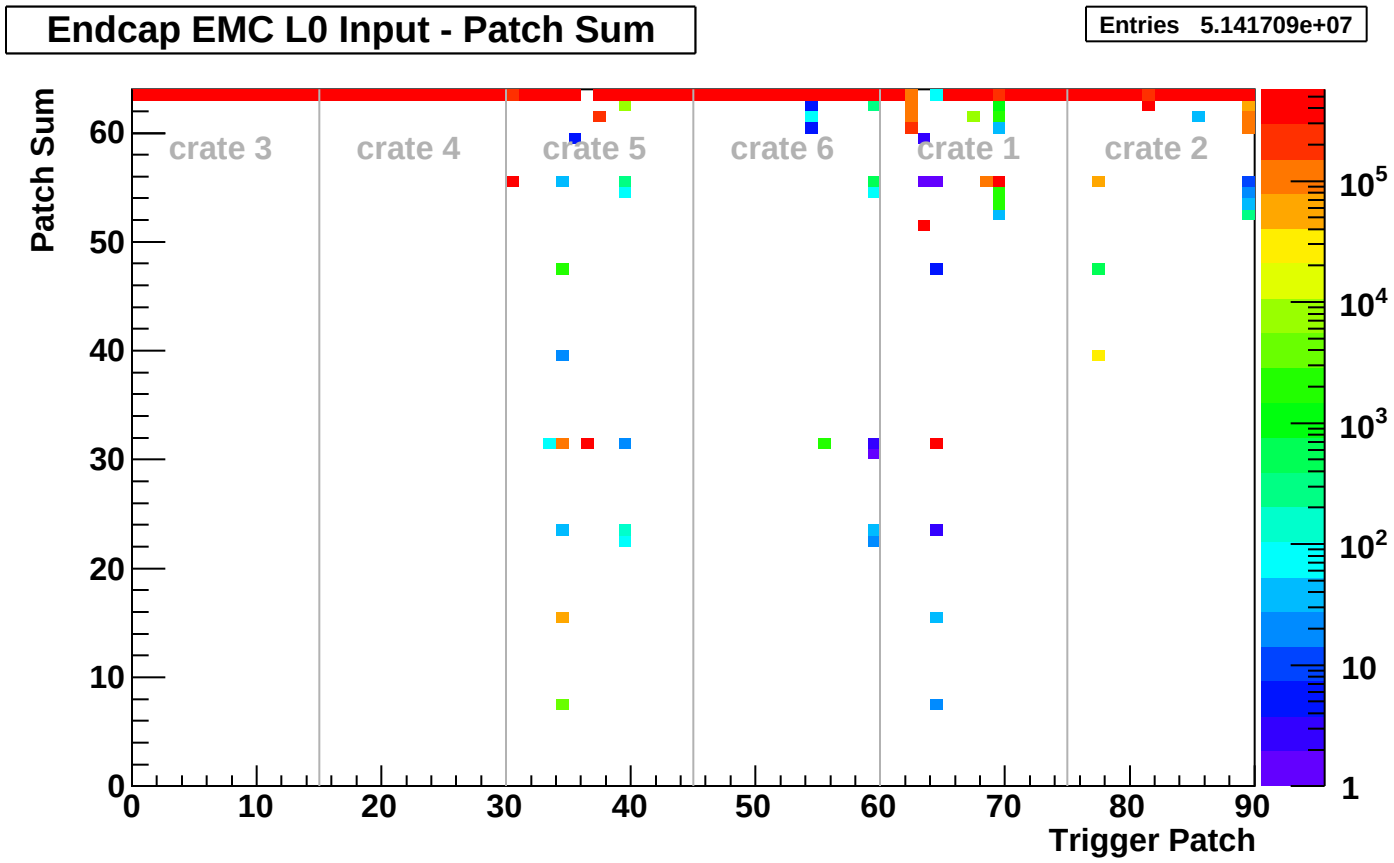

BunchId7Bit (JP1)
Entries 571301
10^4
10^3
10^2
0
20
40
60
80
100
120

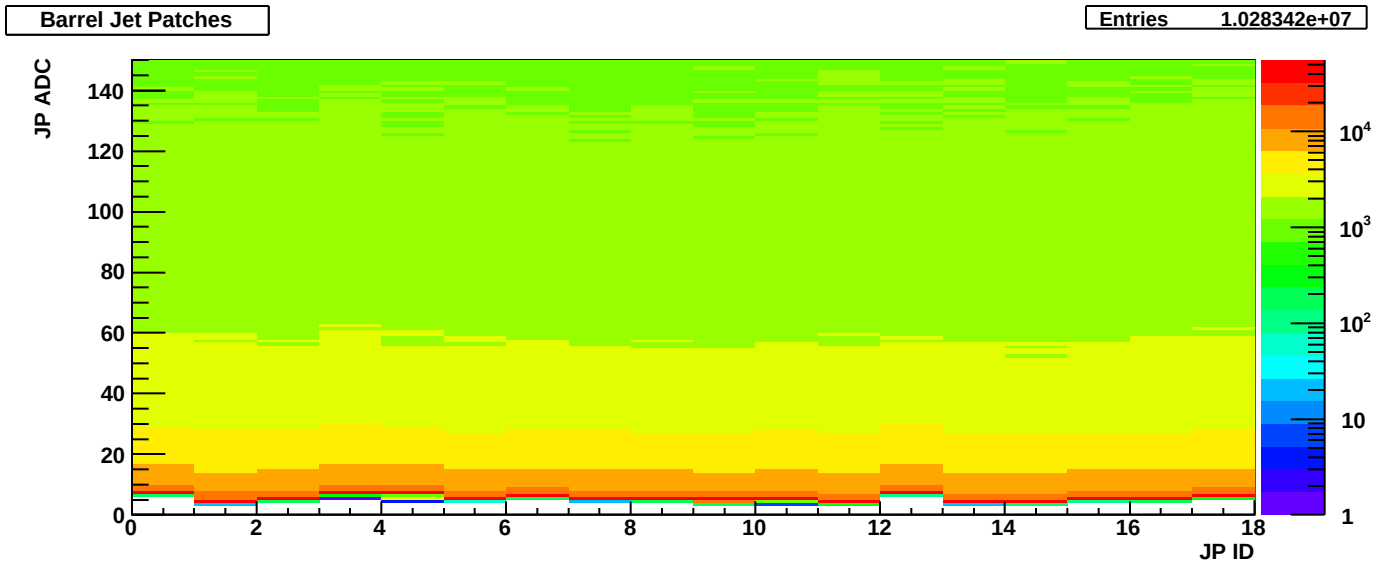

BunchId7Bit (JP2||AJP)
Entries 571301
10^4
10^3
10^2
0
20
40
60
80
100
120

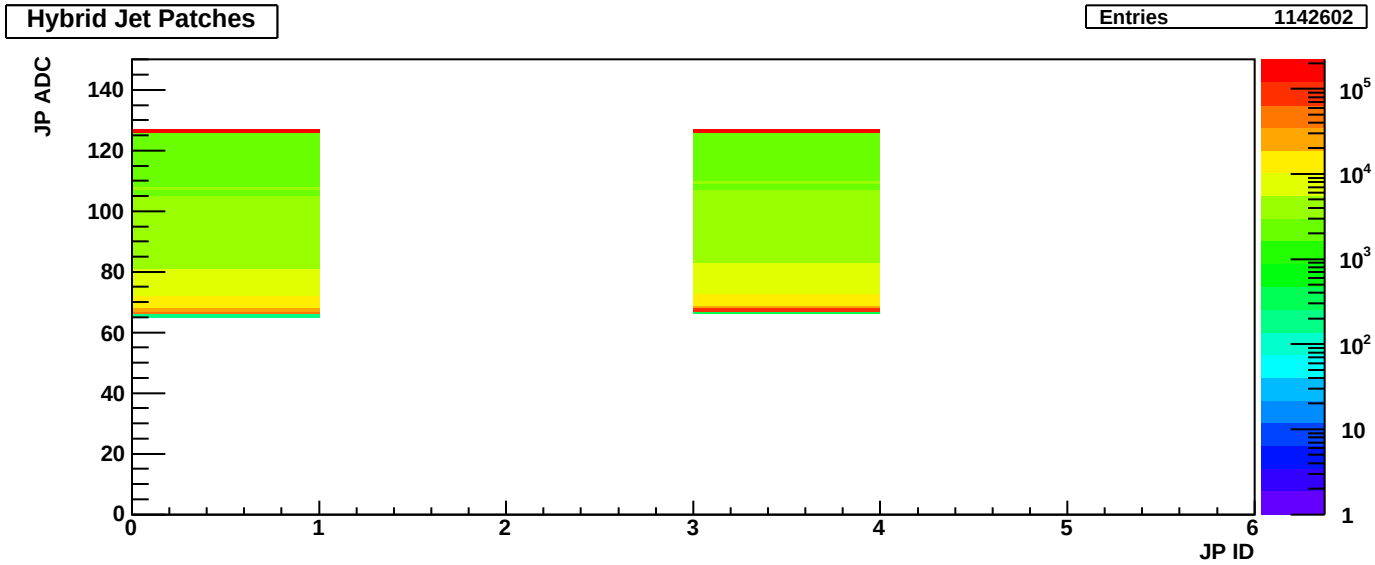

Barrel EMC L0 Input - High Tower
Entries 1.713903e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

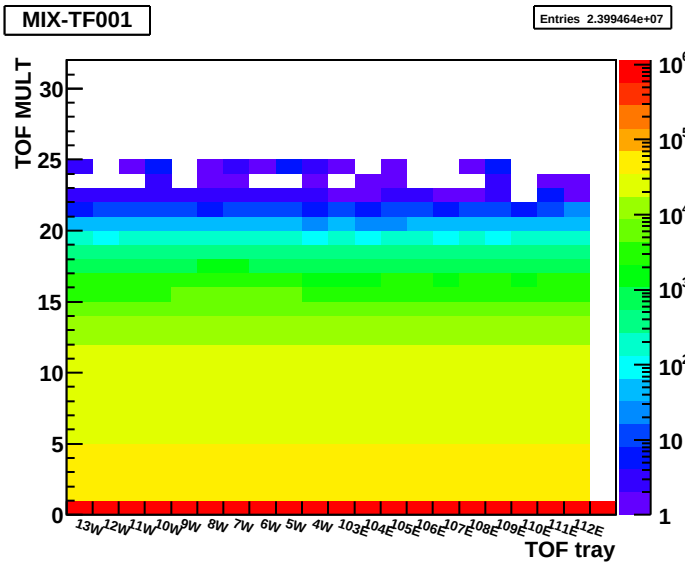

Barrel EMC L0 Input - Patch Sum
Entries 1.713903e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

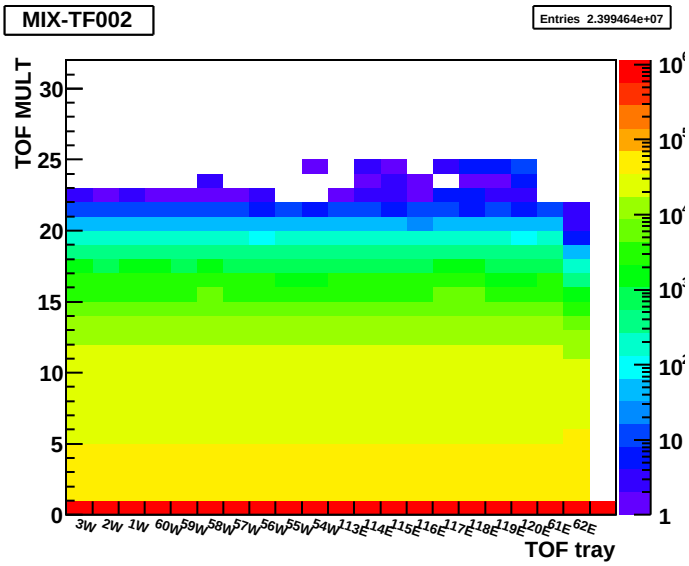

Endcap EMC L0 Input - High Tower
Entries 5.141709e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

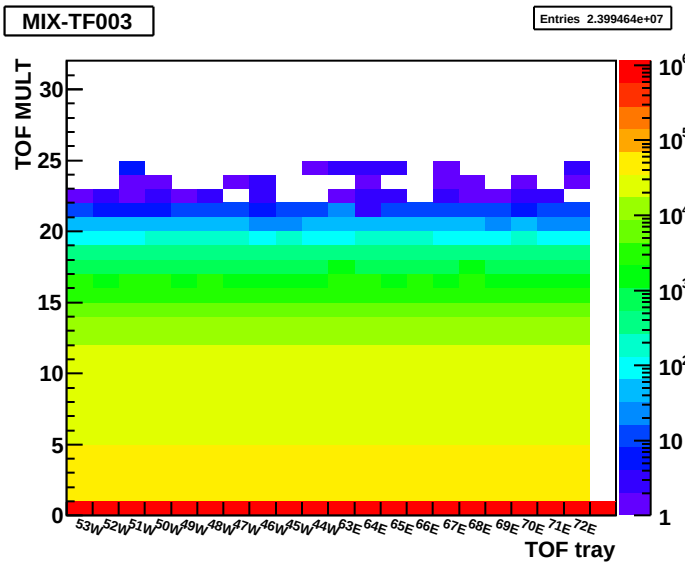

Endcap EMC L0 Input - Patch Sum
Entries 5.141709e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

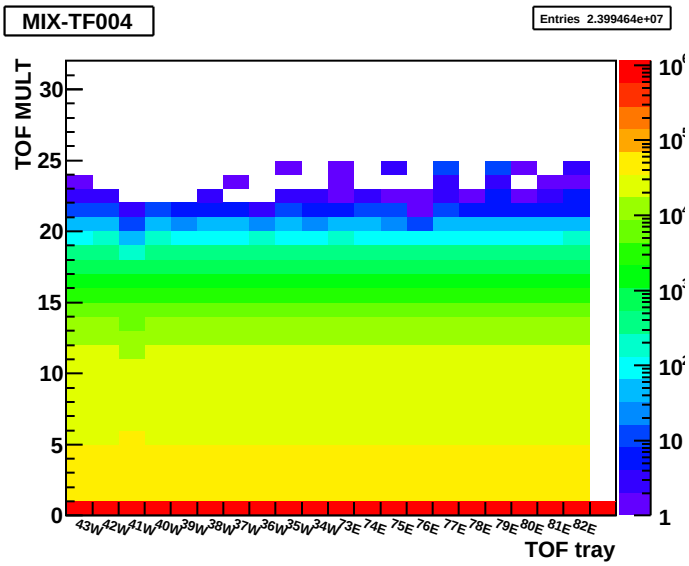

Barrel Jet Patches
Entries 1.028342e+07
JP ADC
JP ID

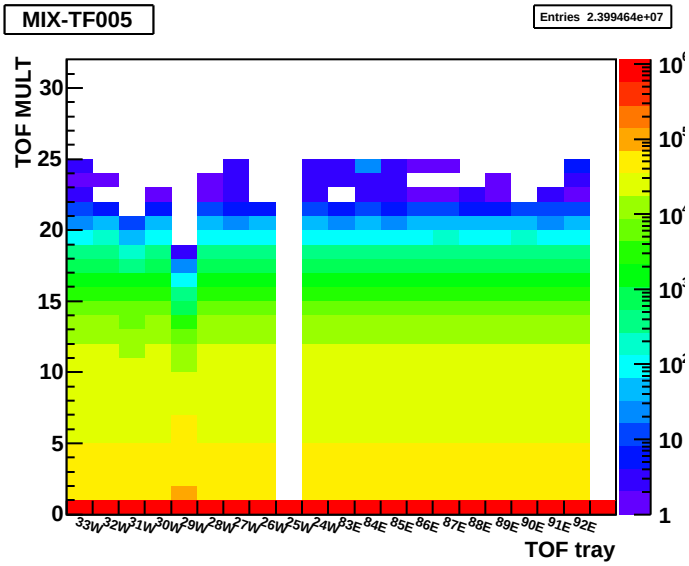

Hybrid Jet Patches
Entries 1142602
JP ADC
JP ID

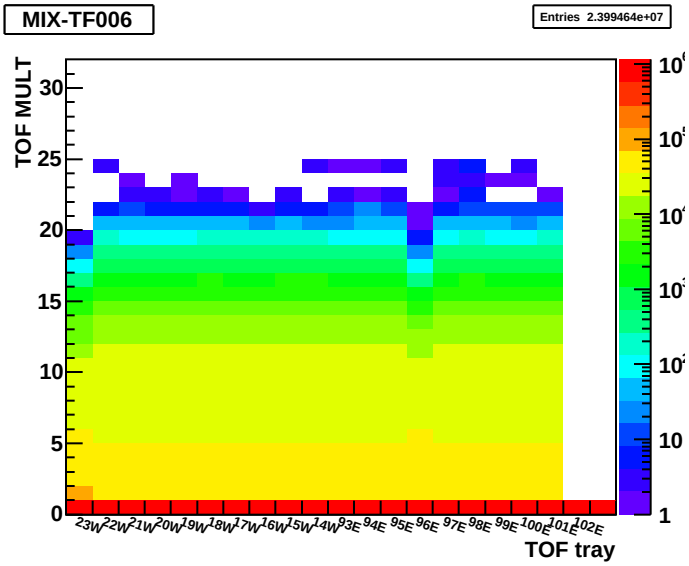

MIX-TF001
Entries 2.399464e+07
TOF MULT
TOF tray

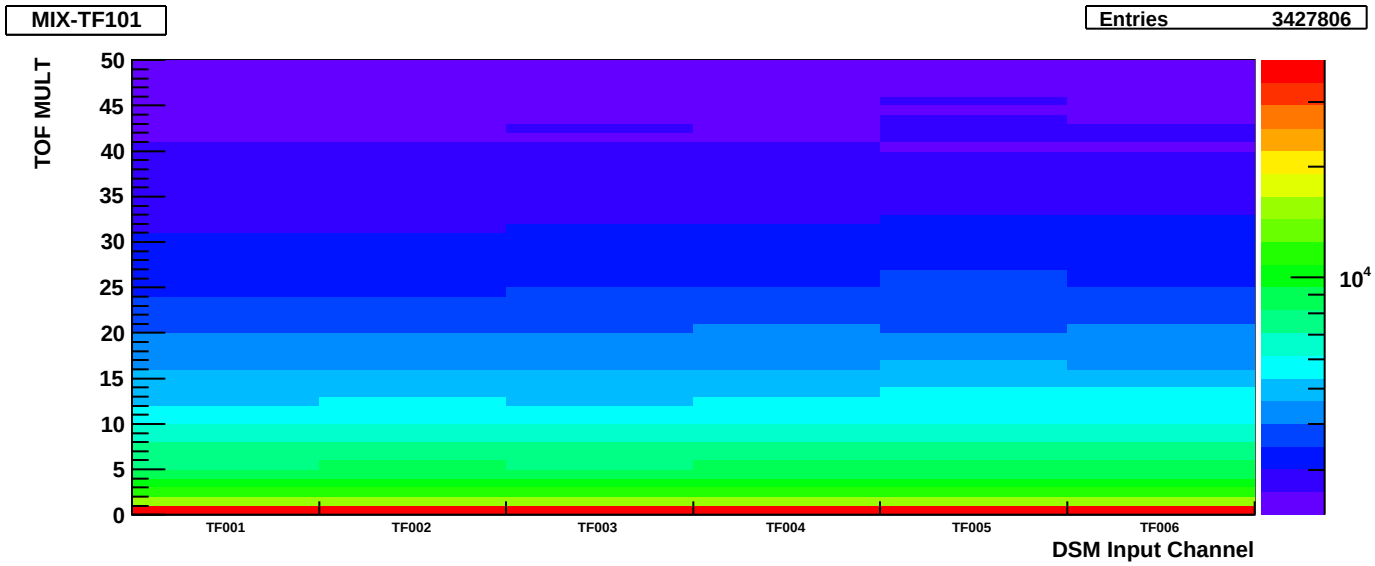

MIX-TF002
Entries 2.399464e+07
TOF MULT
TOF tray

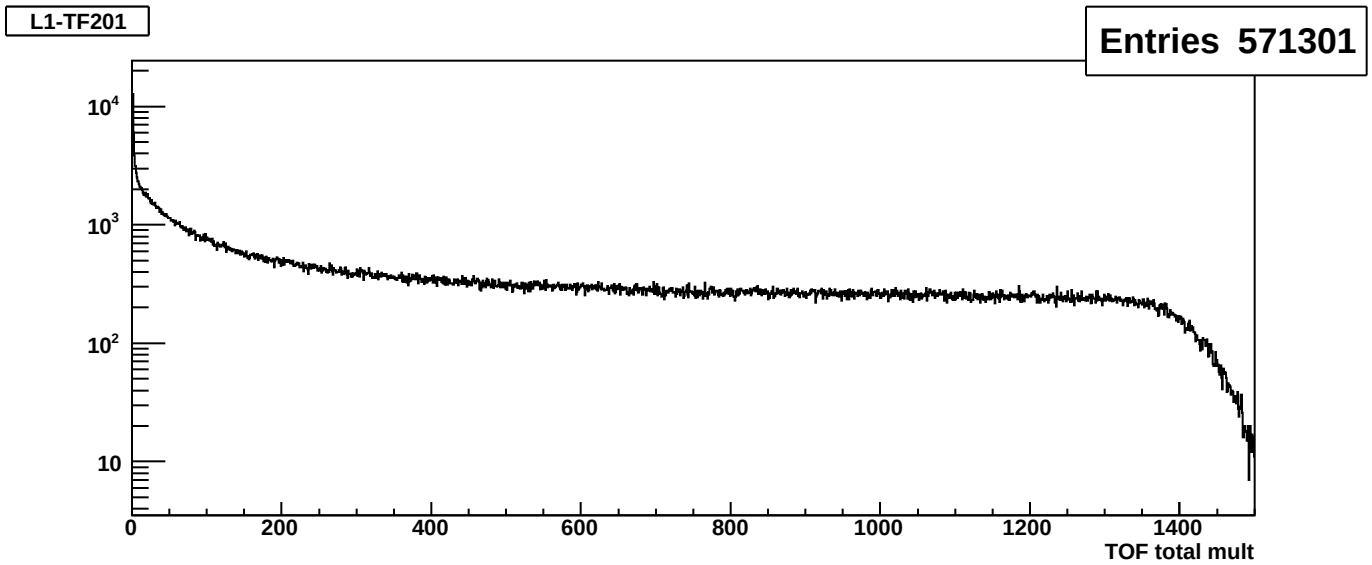

MIX-TF003
Entries 2.399464e+07
TOF MULT
TOF tray

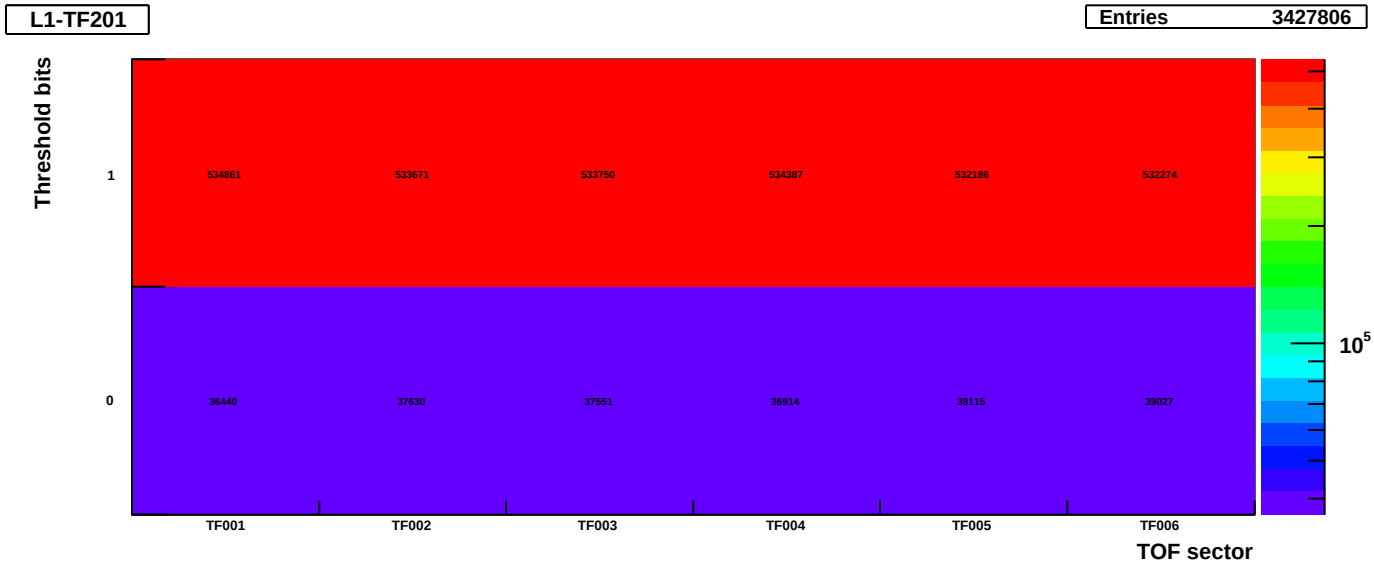

MIX-TF004
Entries 2.399464e+07
TOF MULT
TOF tray

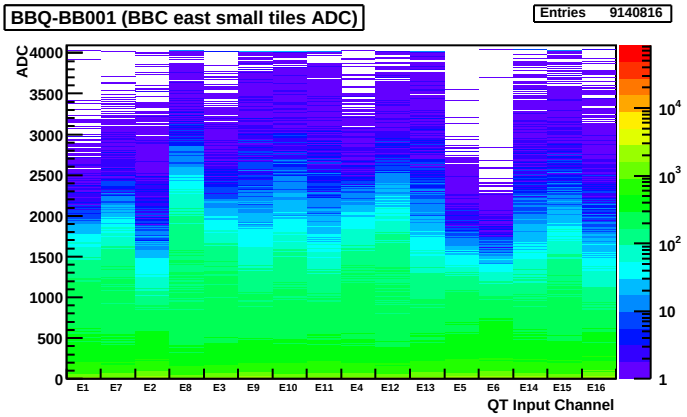

MIX-TF005
Entries 2.399464e+07
TOF MULT
TOF tray

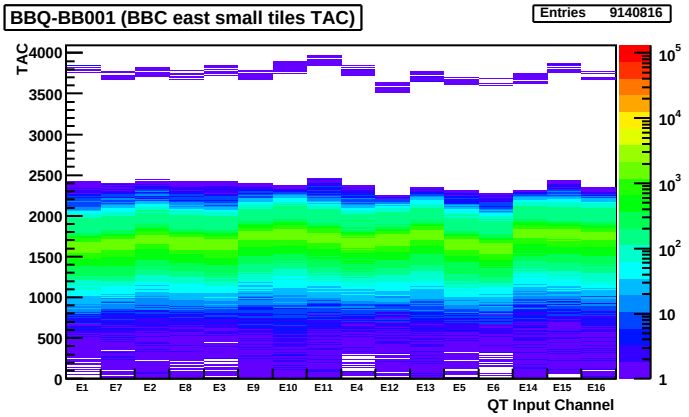

MIX-TF006
Entries 2.399464e+07
TOF MULT
TOF tray

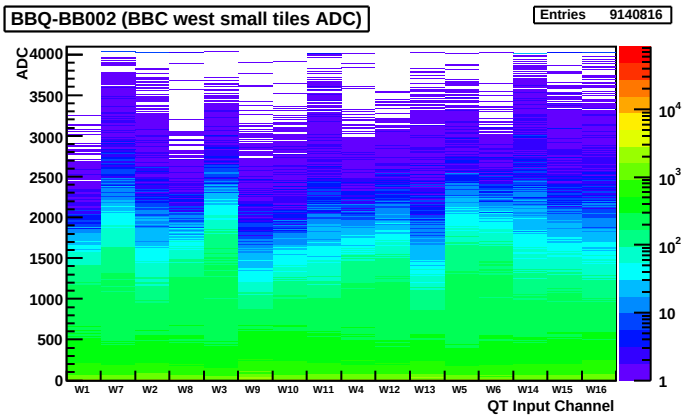

MIX-TF101
Entries 3427806
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

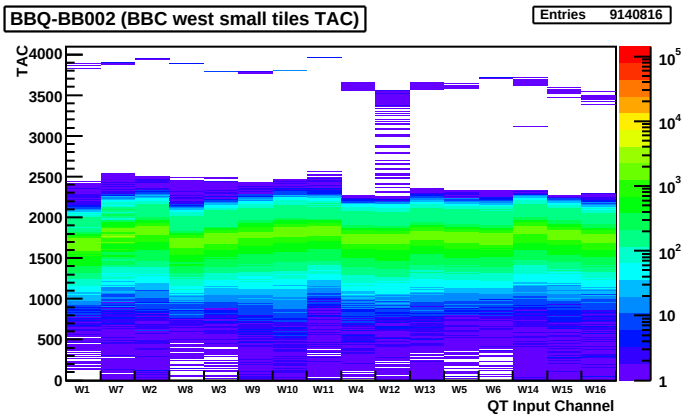

L1-TF201
Entries 571301
10^4
10^3
10^2
10
0
200
400
600
800
1000
1200
1400
TOF total mult

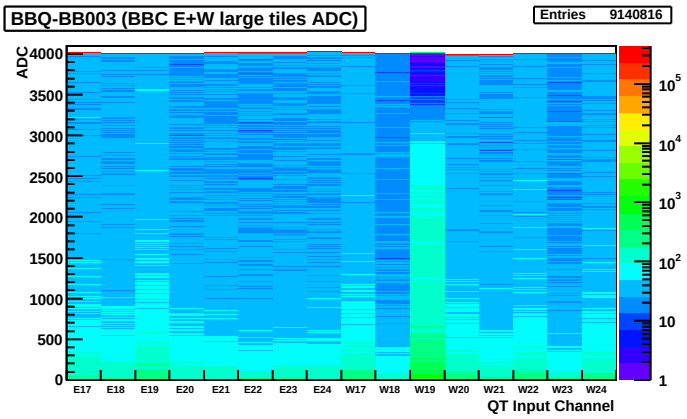

L1-TF201
Entries 3427806
Threshold bits
1
0
534861
533671
533750
534387
532186
532274
36440
37630
37551
36914
39115
39027
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

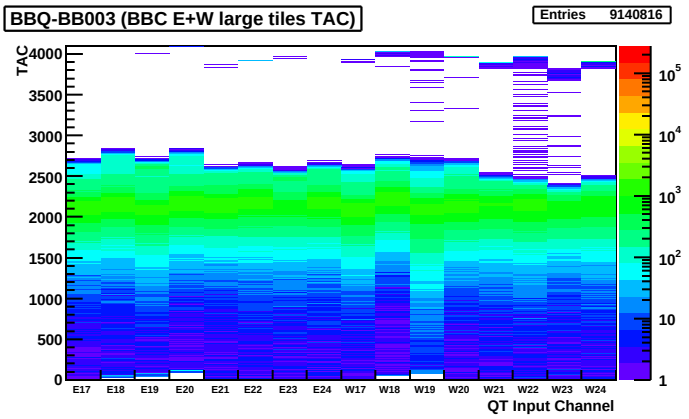

BBQ-BB001 (BBC east small tiles ADC)
Entries 9140816
ADC
QT Input Channel

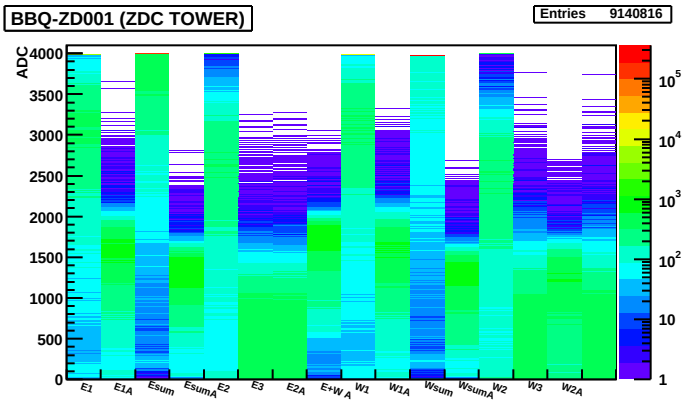

BBQ-BB001 (BBC east small tiles TAC)
Entries 9140816
TAC
QT Input Channel

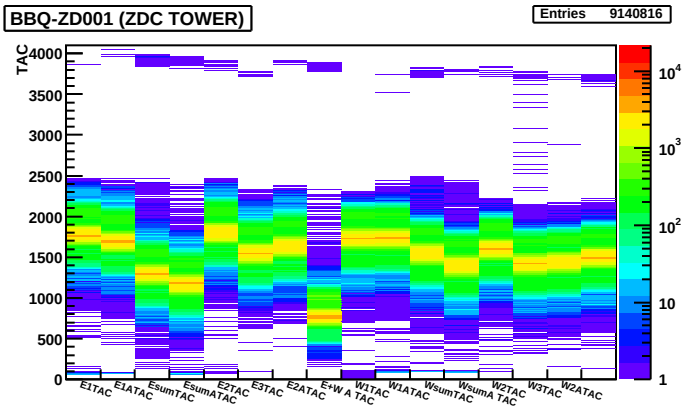

BBQ-BB002 (BBC west small tiles ADC)
Entries 9140816
ADC
QT Input Channel

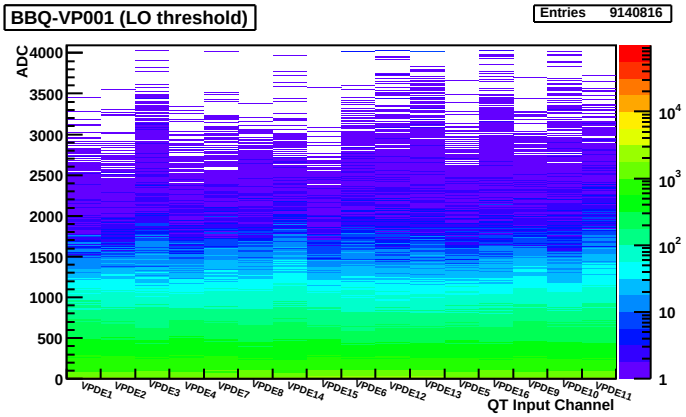

BBQ-BB002 (BBC west small tiles TAC)
Entries 9140816
TAC
QT Input Channel

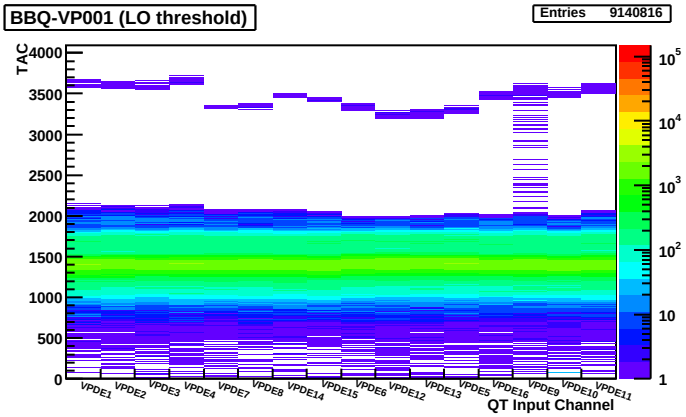

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 9140816
ADC
QT Input Channel

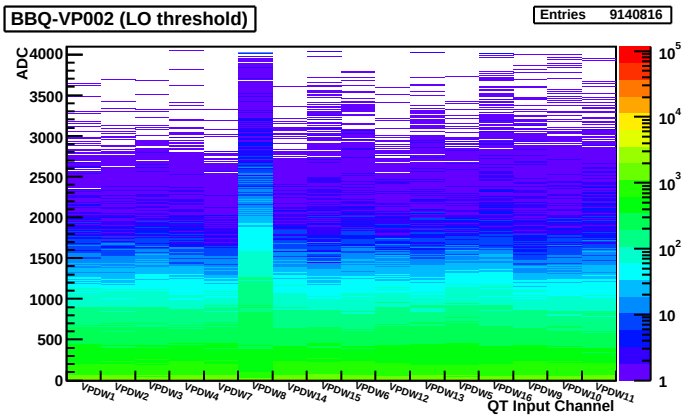

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 9140816
TAC
QT Input Channel

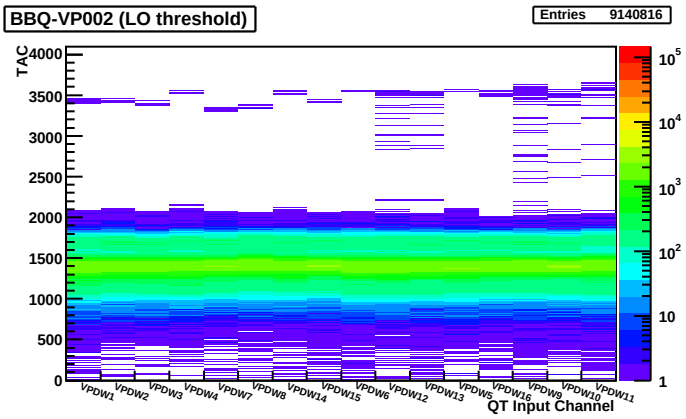

BBQ-ZD001 (ZDC TOWER)
Entries 9140816
ADC

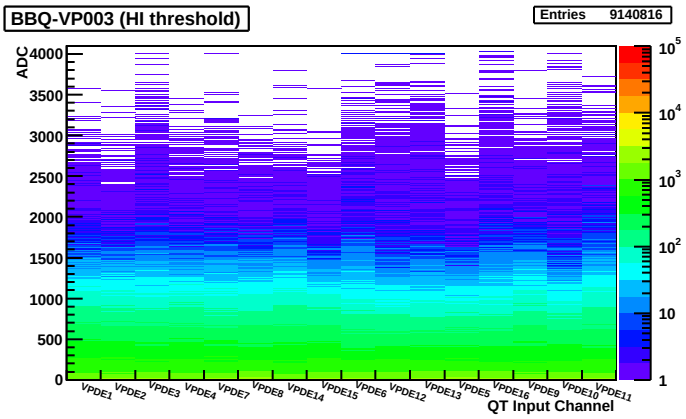

BBQ-ZD001 (ZDC TOWER)
Entries 9140816
TAC

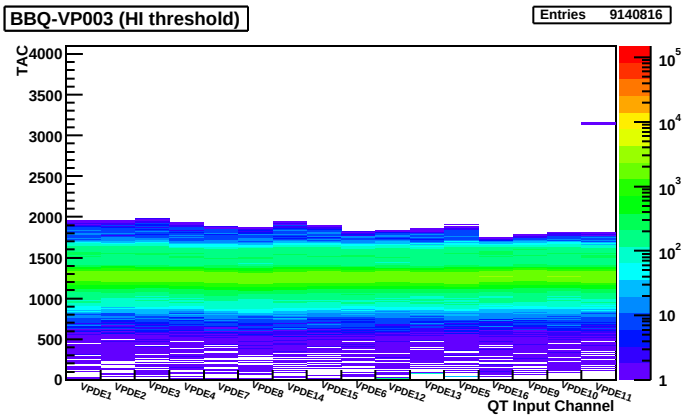

BBQ-VP001 (LO threshold)
Entries 9140816
ADC
QT Input Channel

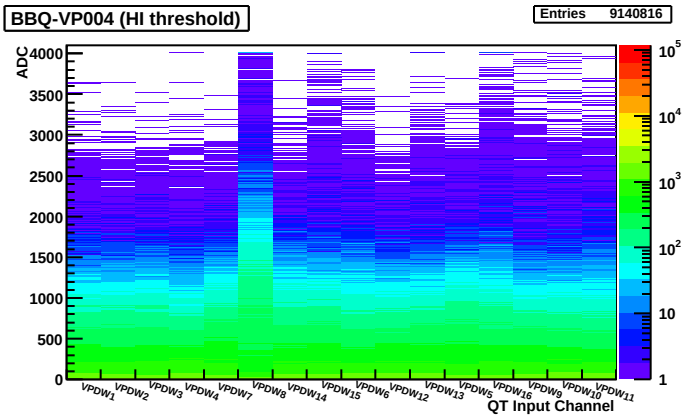

BBQ-VP001 (LO threshold)
Entries 9140816
TAC
QT Input Channel

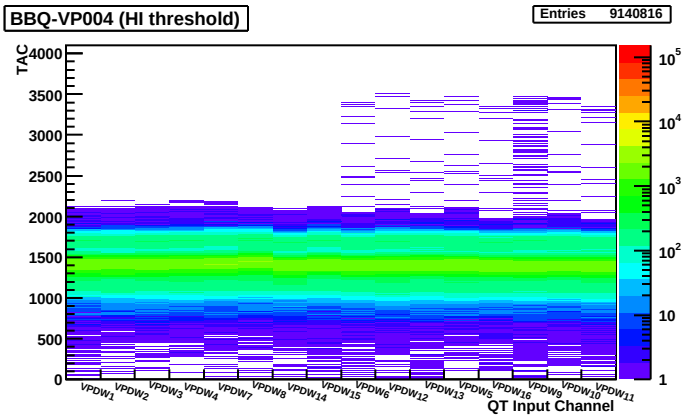

BBQ-VP002 (LO threshold)
Entries 9140816
ADC
QT Input Channel

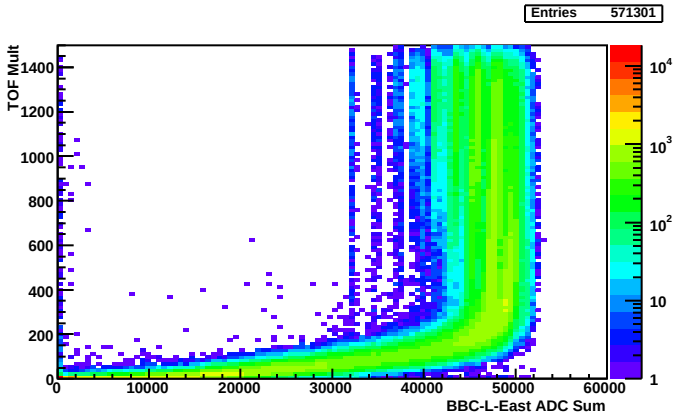

BBQ-VP002 (LO threshold)
Entries 9140816
TAC
QT Input Channel

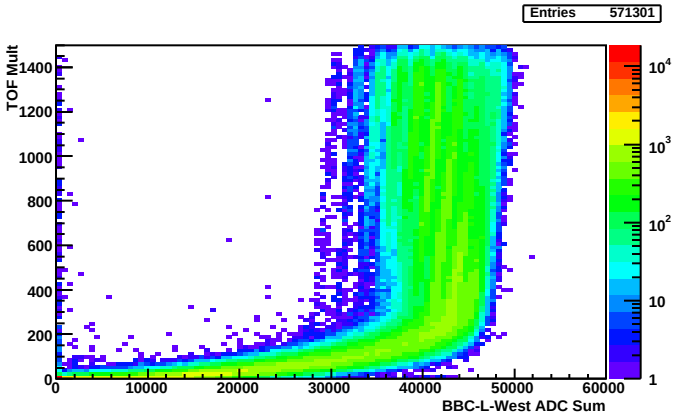

BBQ-VP003 (HI threshold)
Entries 9140816
ADC
QT Input Channel

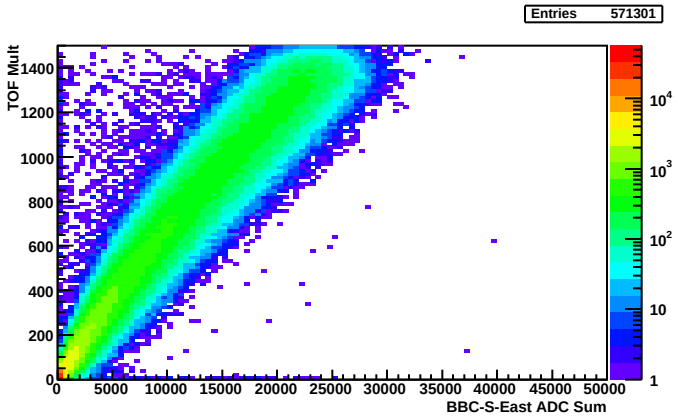

BBQ-VP003 (HI threshold)
Entries 9140816
TAC
QT Input Channel

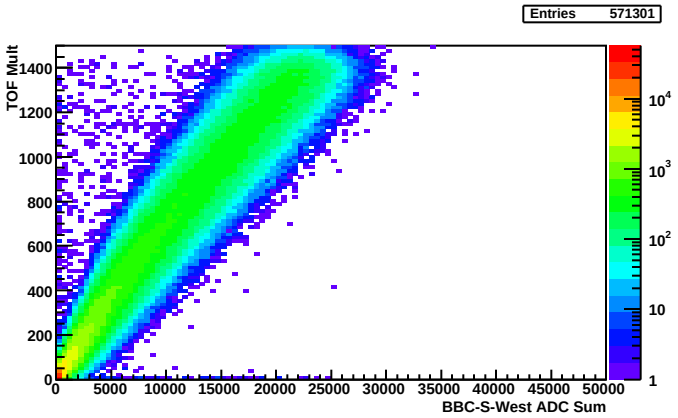

BBQ-VP004 (HI threshold)
Entries 9140816
ADC
QT Input Channel

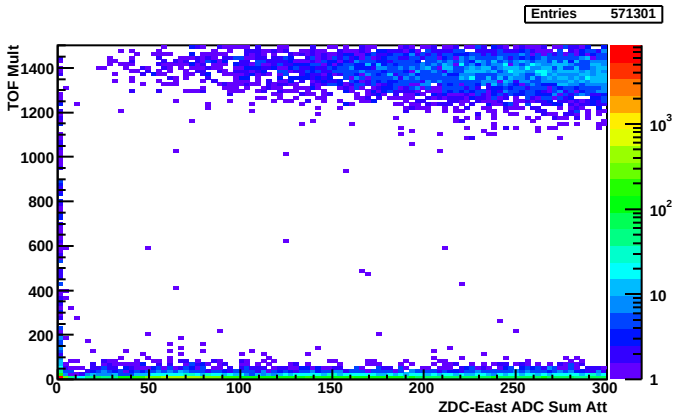

BBQ-VP004 (HI threshold)
Entries 9140816
TAC
QT Input Channel

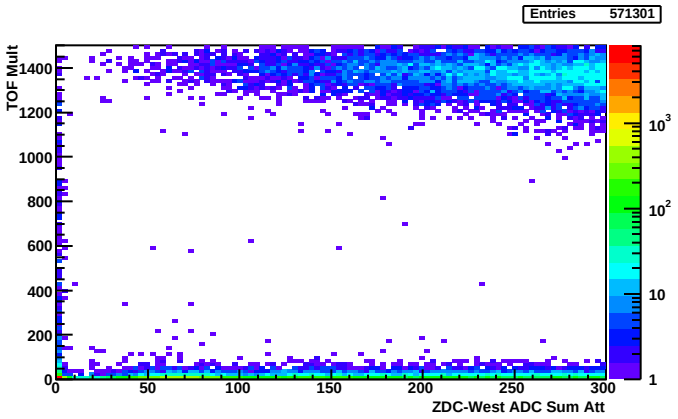

Entries 571301
TOF Mult
BBC-L-East ADC Sum

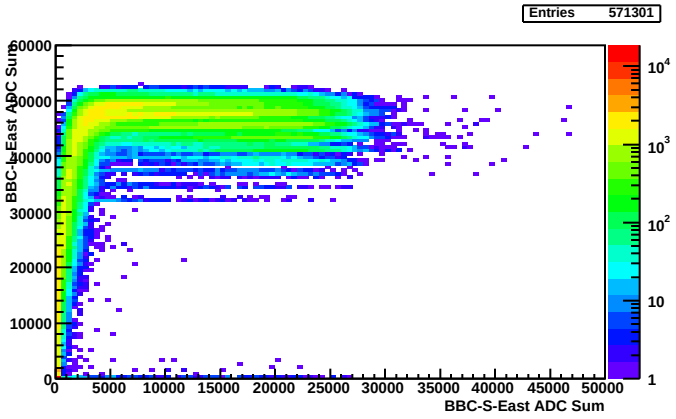

Entries 571301
TOF Mult
BBC-L-West ADC Sum

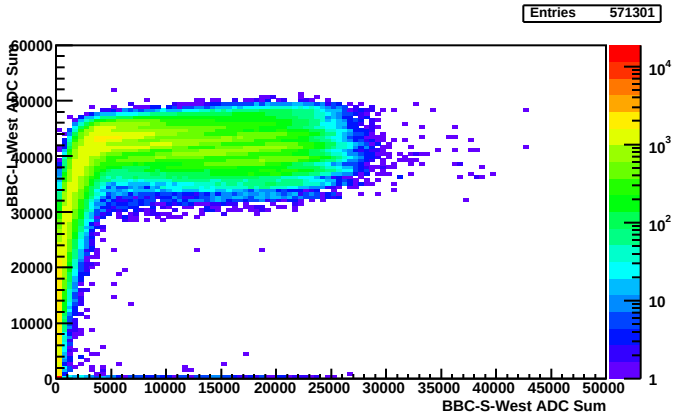

Entries 571301
TOF Mult
BBC-S-East ADC Sum

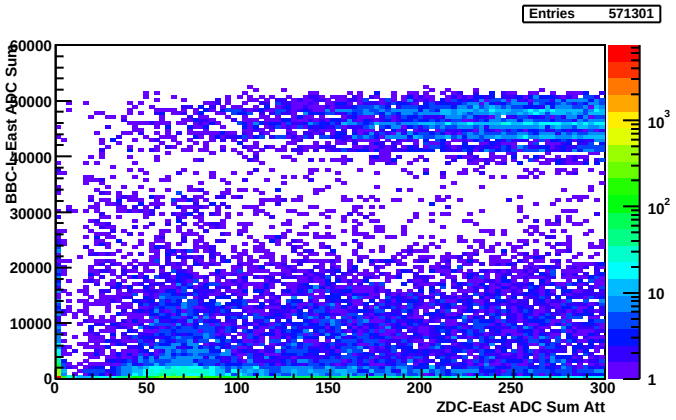

Entries 571301
TOF Mult
BBC-S-West ADC Sum

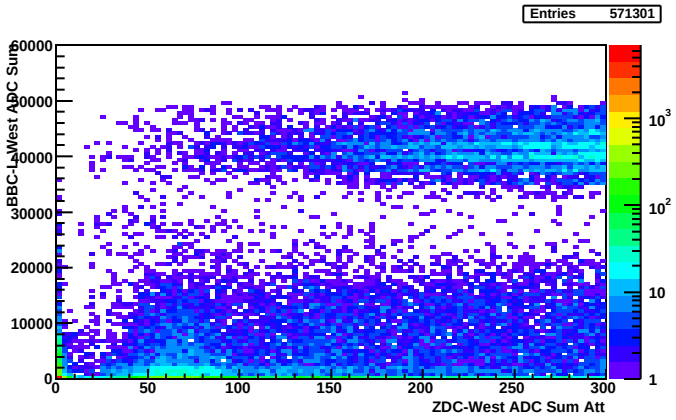

Entries 571301
TOF Mult
ZDC-East ADC Sum Att

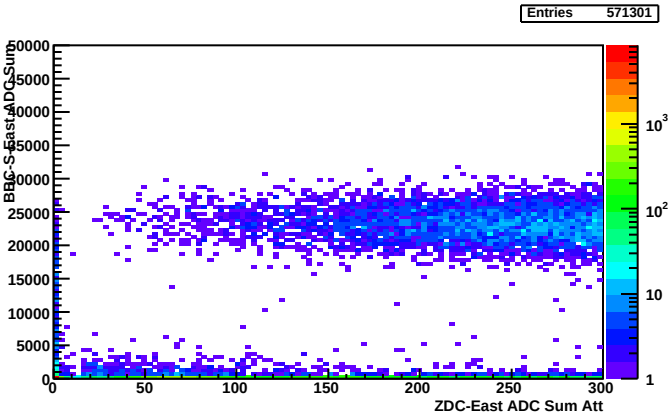

Entries 571301
TOF Mult
ZDC-West ADC Sum Att

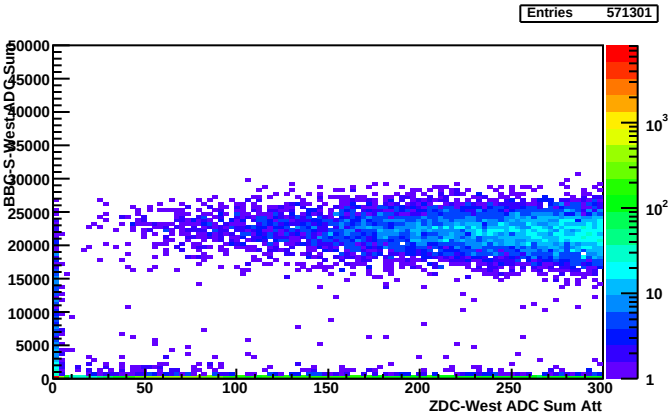

Entries 571301
BBC-L-East ADC Sum
BBC-S-East ADC Sum

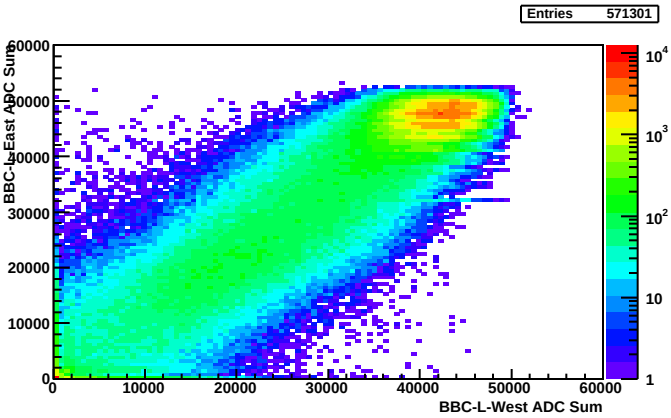

Entries 571301
BBC-L-West ADC Sum
BBC-S-West ADC Sum

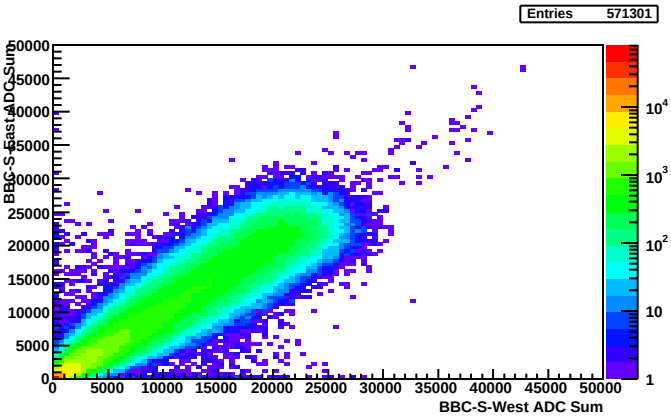

Entries 571301
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

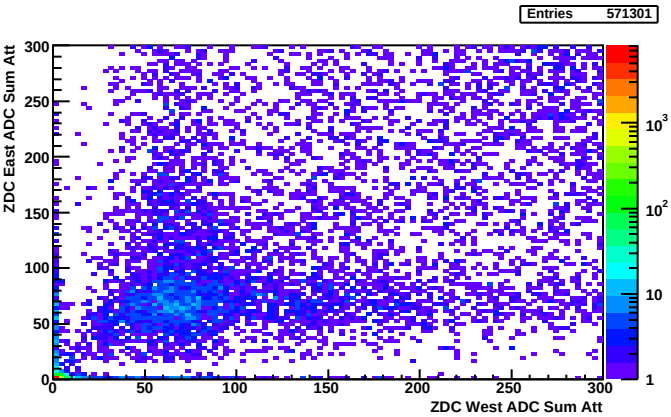

Entries 571301
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

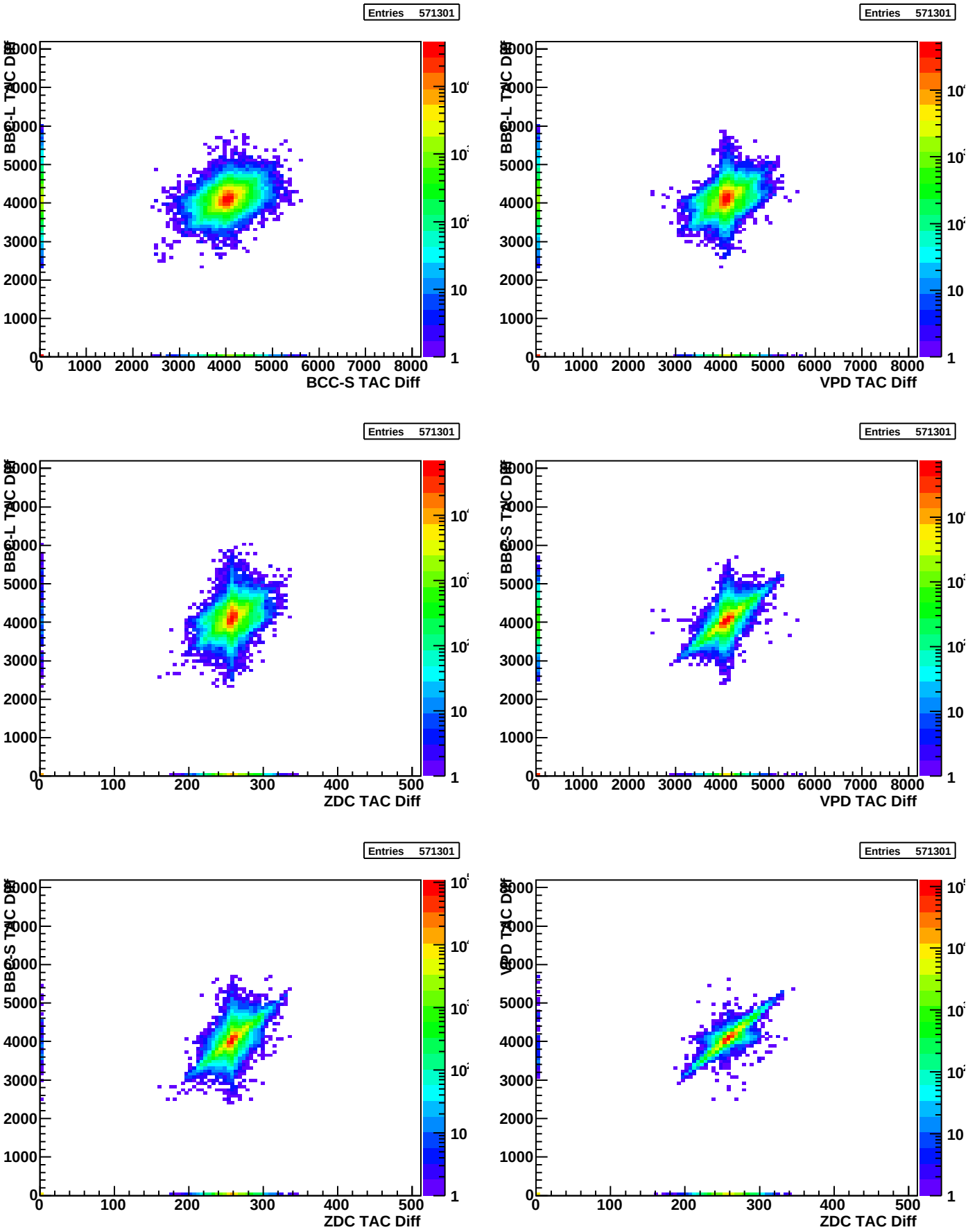

Entries 571301
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

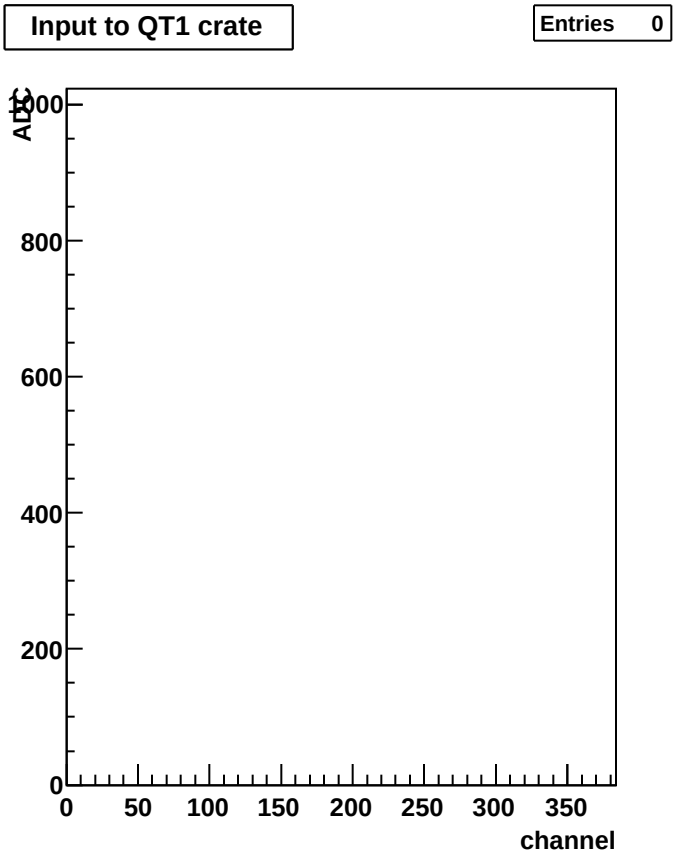

Entries 571301
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

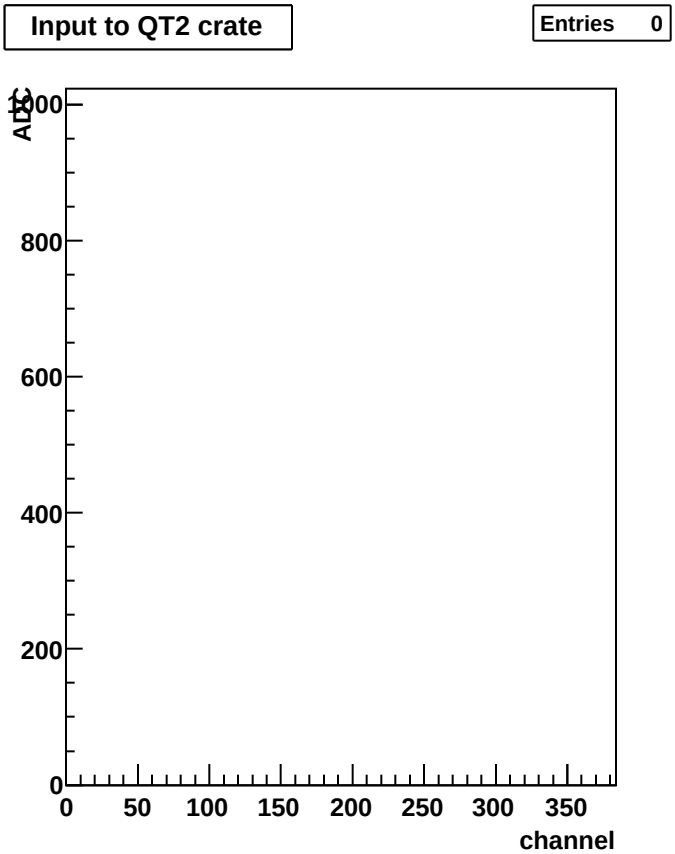

Entries 571301
BBC-L-East ADC Sum
BBC-L-West ADC Sum

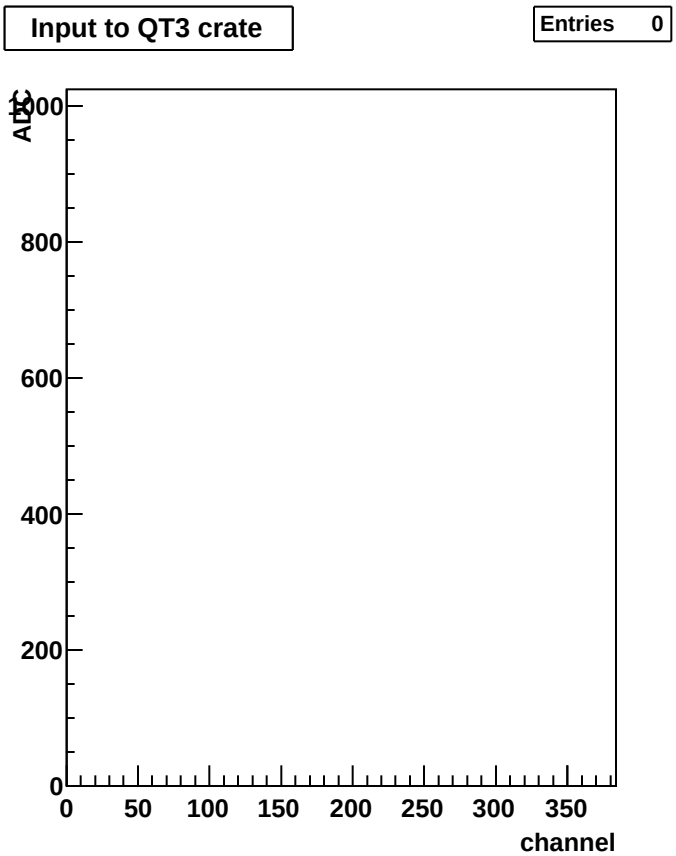

Entries 571301
BBC-S-East ADC Sum
BBC-S-West ADC Sum

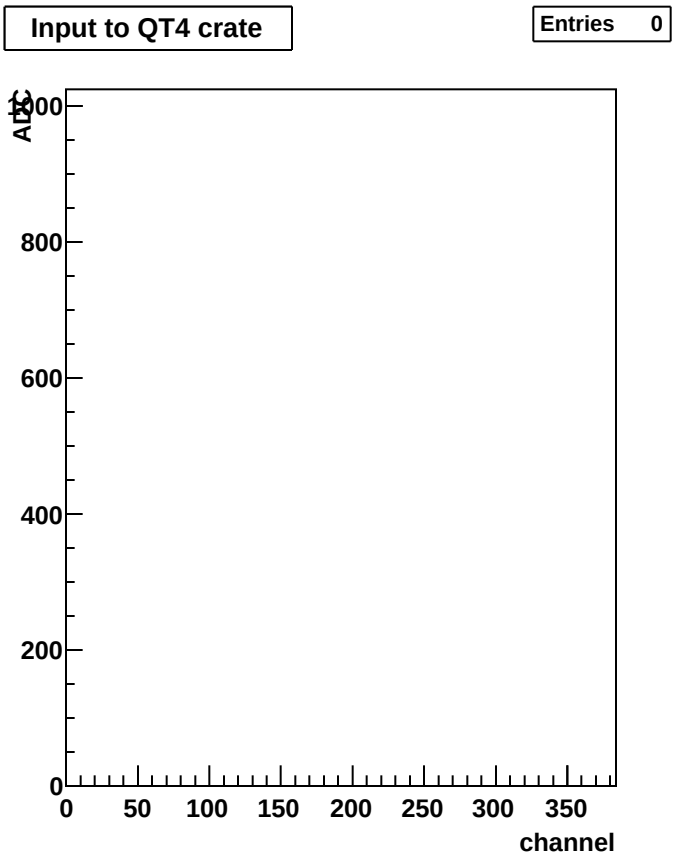

Entries 571301
ZDC East ADC Sum Att
ZDC West ADC Sum Att

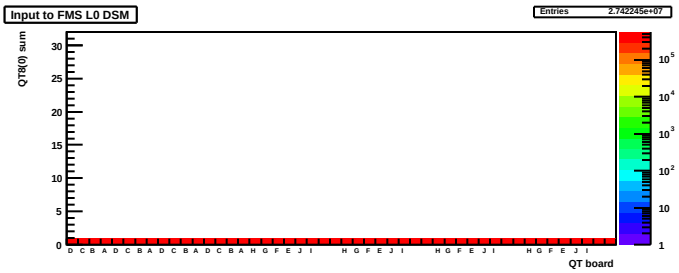

Entries 571301
BBC-L TAC Diff
BCC-S TAC Diff
Entries 571301
BBC-L TAC Diff
VPD TAC Diff
Entries 571301
BBC-L TAC Diff
ZDC TAC Diff
Entries 571301
BBC-S TAC Diff
VPD TAC Diff
Entries 571301
BBC-S TAC Diff
ZDC TAC Diff
Entries 571301
VPD TAC Diff
ZDC TAC Diff

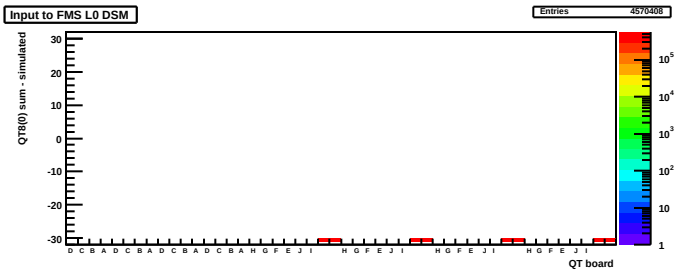

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

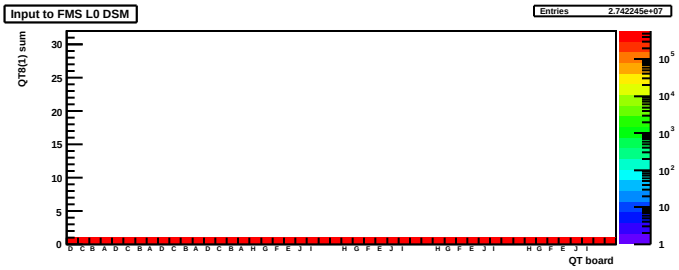

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

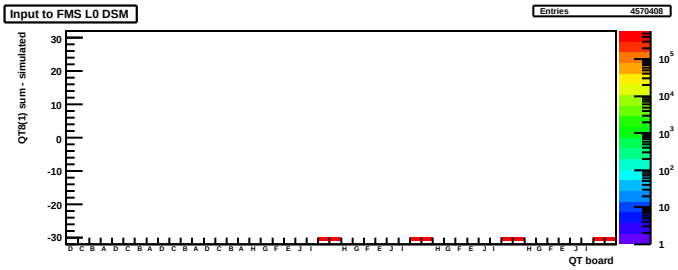

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

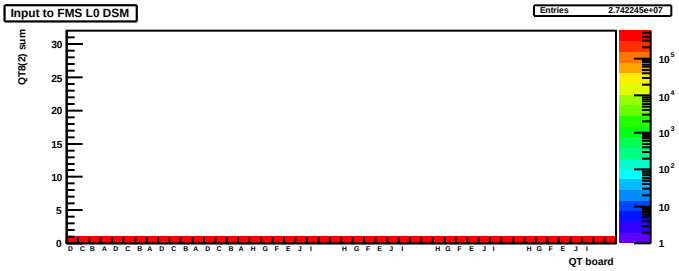

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

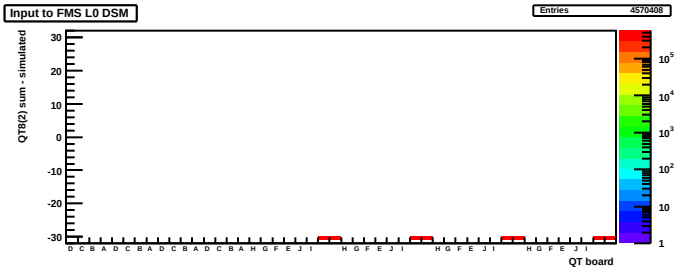

Input to FMS L0 DSM
Entries 2.742245e+07
QT8(0) sum
QT board

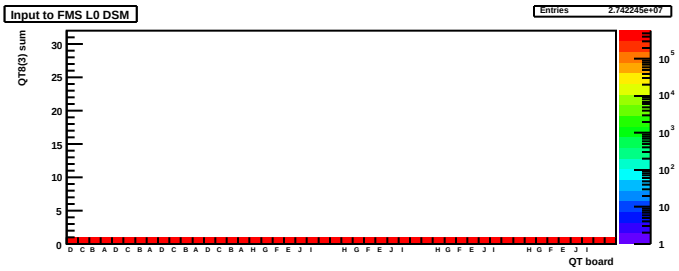

Input to FMS L0 DSM
Entries 4570408
QT8(0) sum - simulated
QT board

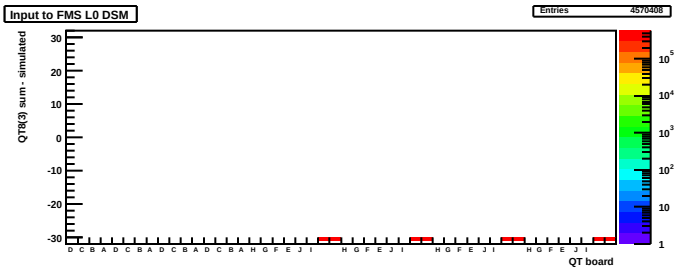

Input to FMS L0 DSM
Entries 2.742245e+07
QT8(1) sum
QT board

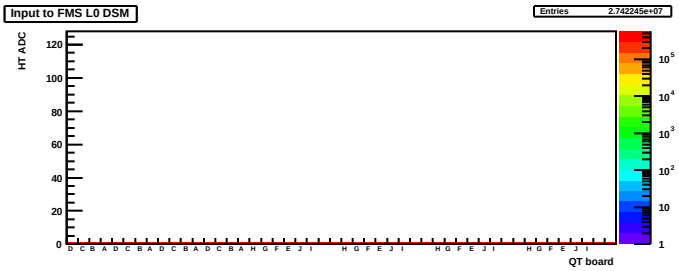

Input to FMS L0 DSM
Entries 4570408
QT8(1) sum - simulated
QT board

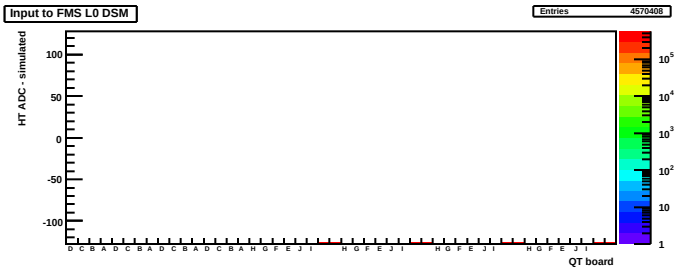

Input to FMS L0 DSM
Entries 2.742245e+07
QT8(2) sum
QT board

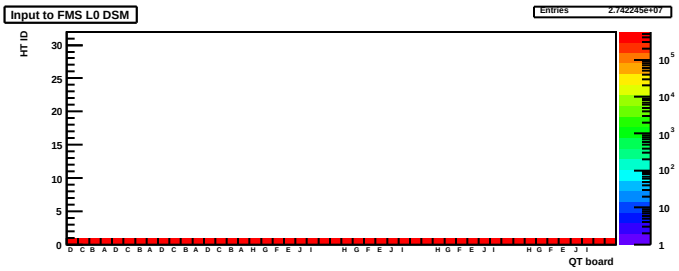

Input to FMS L0 DSM
Entries 4570408
QT8(2) sum - simulated
QT board

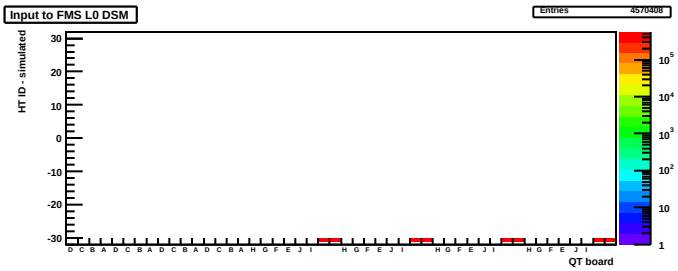

Input to FMS L0 DSM
Entries 2.742245e+07
QT8(3) sum
QT board

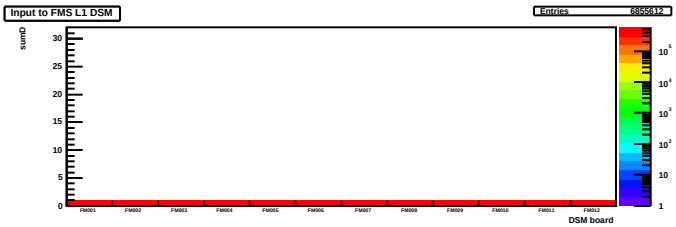

Input to FMS L0 DSM
Entries 4570408
QT8(3) sum - simulated
QT board

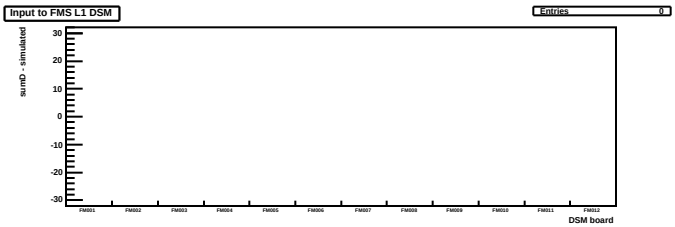

Input to FMS L0 DSM
Entries 2.742245e+07
HT ADC
QT board

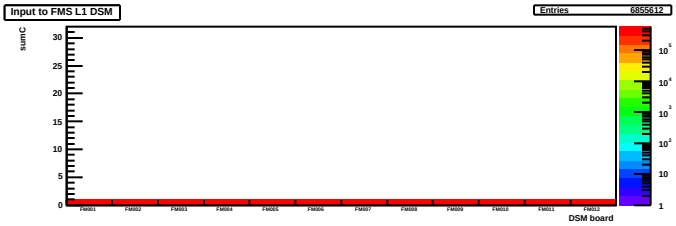

Input to FMS L0 DSM
Entries 4570408
HT ADC - simulated
QT board

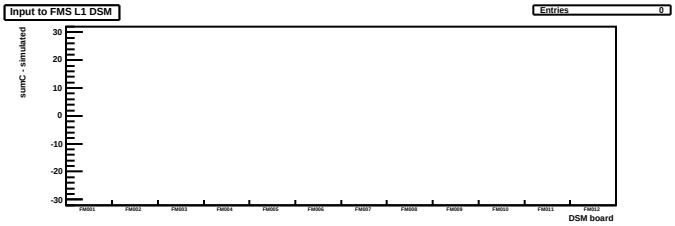

Input to FMS L0 DSM
Entries 2.742245e+07
HT ID
QT board

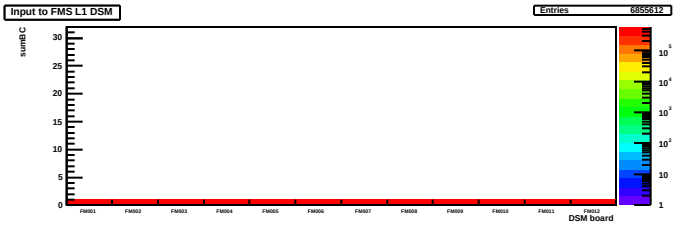

Input to FMS L0 DSM
Entries 4570408
HT ID - simulated
QT board

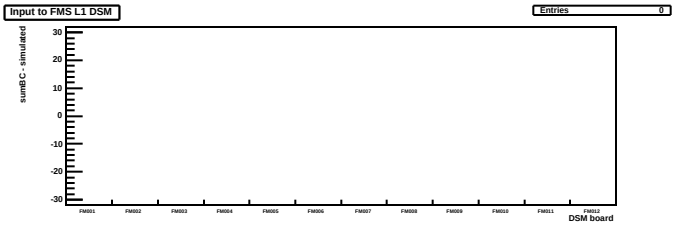

Input to FMS L1 DSM
Entries 6855612
sumD
DSM board

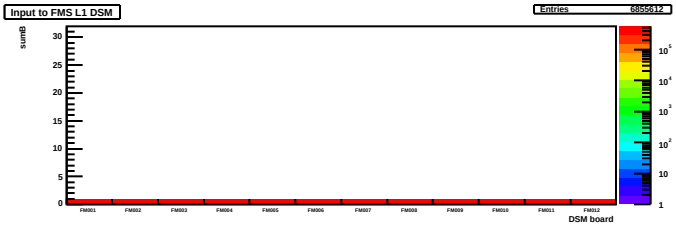

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

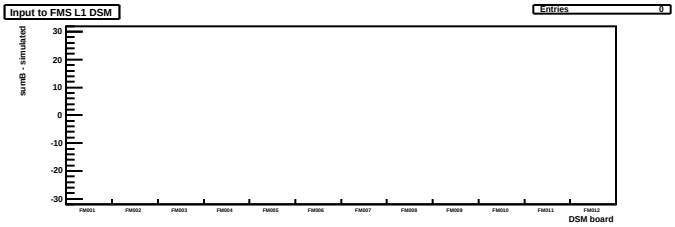

Input to FMS L1 DSM
Entries 6855612
sumC
DSM board

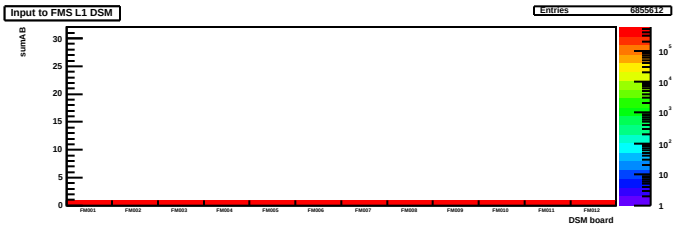

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

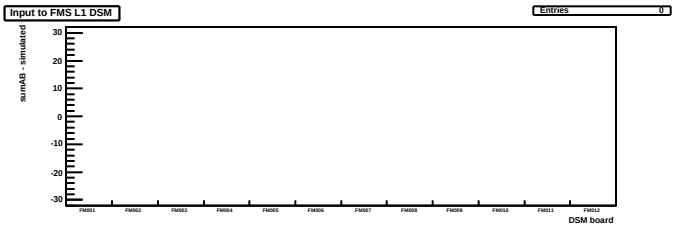

Input to FMS L1 DSM
Entries 6855612
sumBC
DSM board

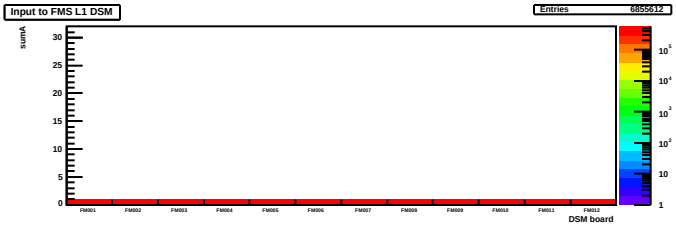

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

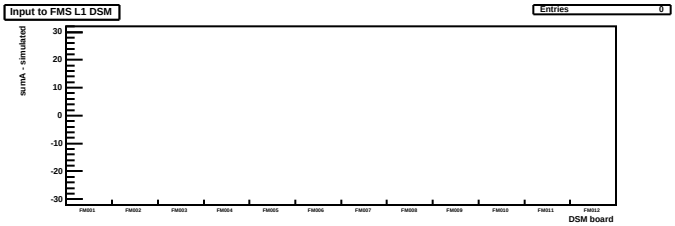

Input to FMS L1 DSM
Entries 6855612
sumB
DSM board

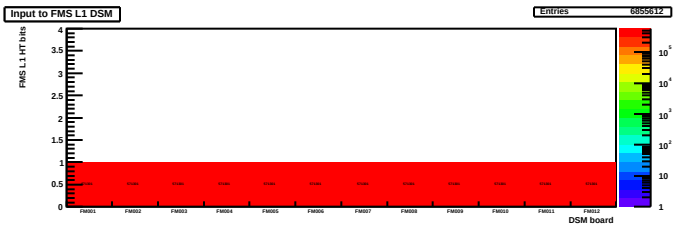

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

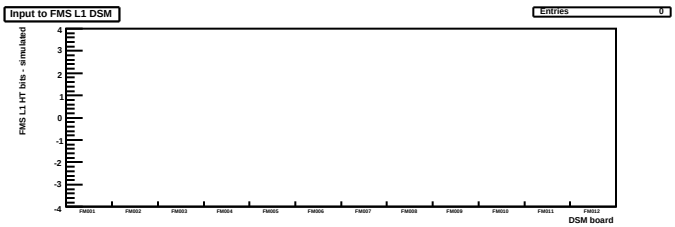

Input to FMS L1 DSM
Entries 6855612
sumAB
DSM board

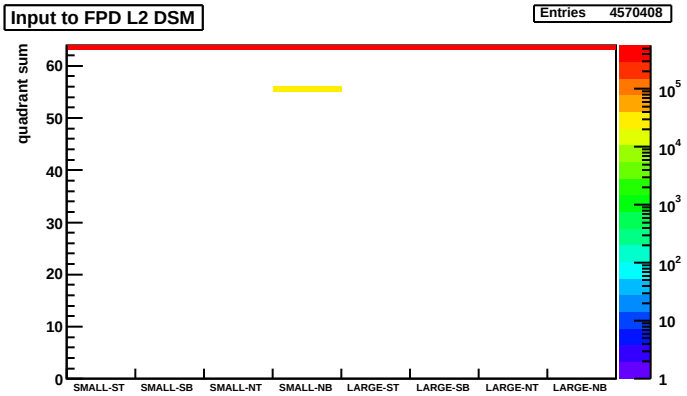

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

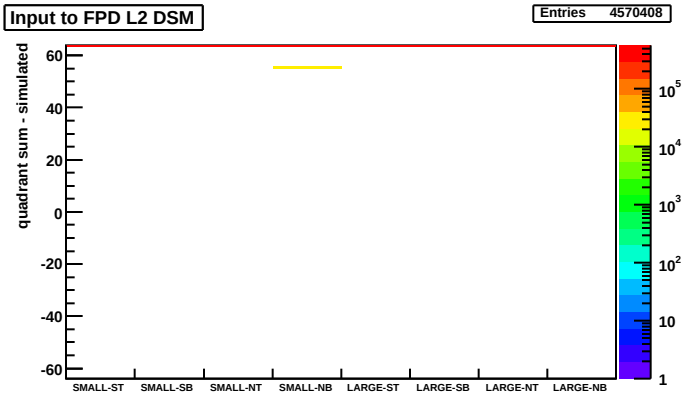

Input to FMS L1 DSM
Entries 6855612
sumA
DSM board

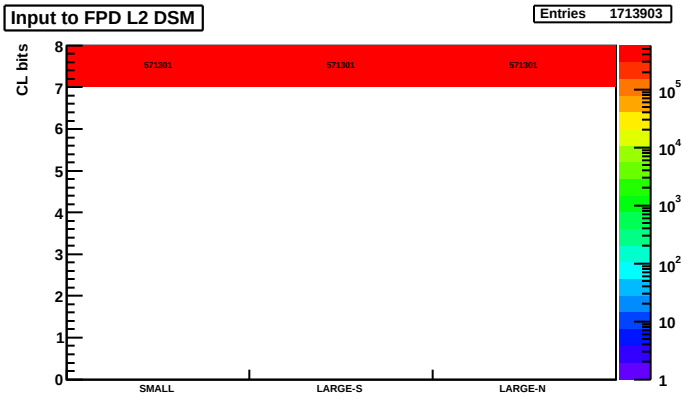

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

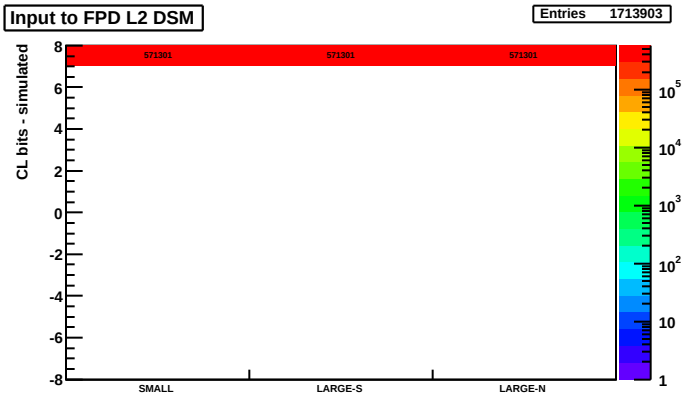

Input to FMS L1 DSM
Entries 6855612
FMS L1 HT bits
DSM board

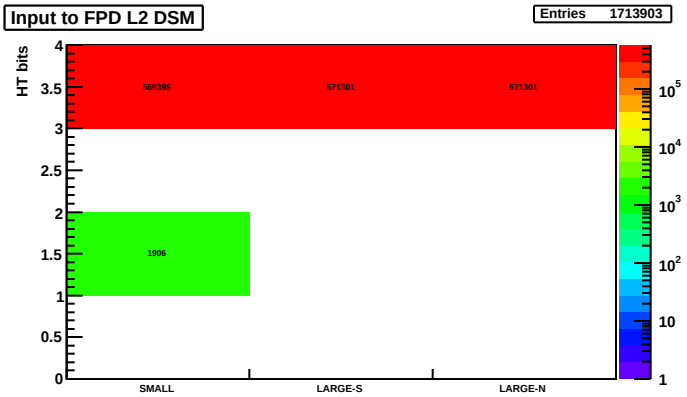

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

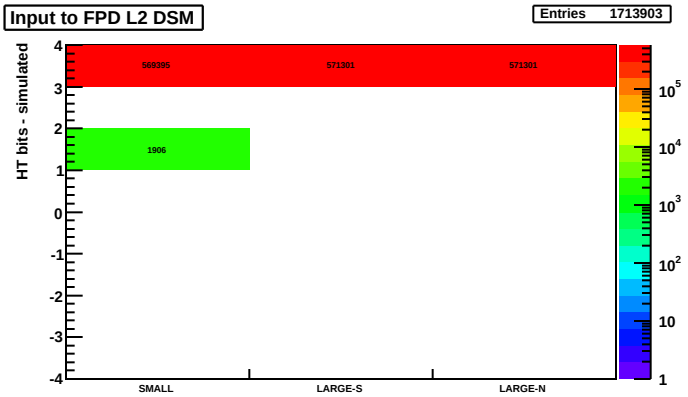

Input to FPD L2 DSM
Entries 4570408
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

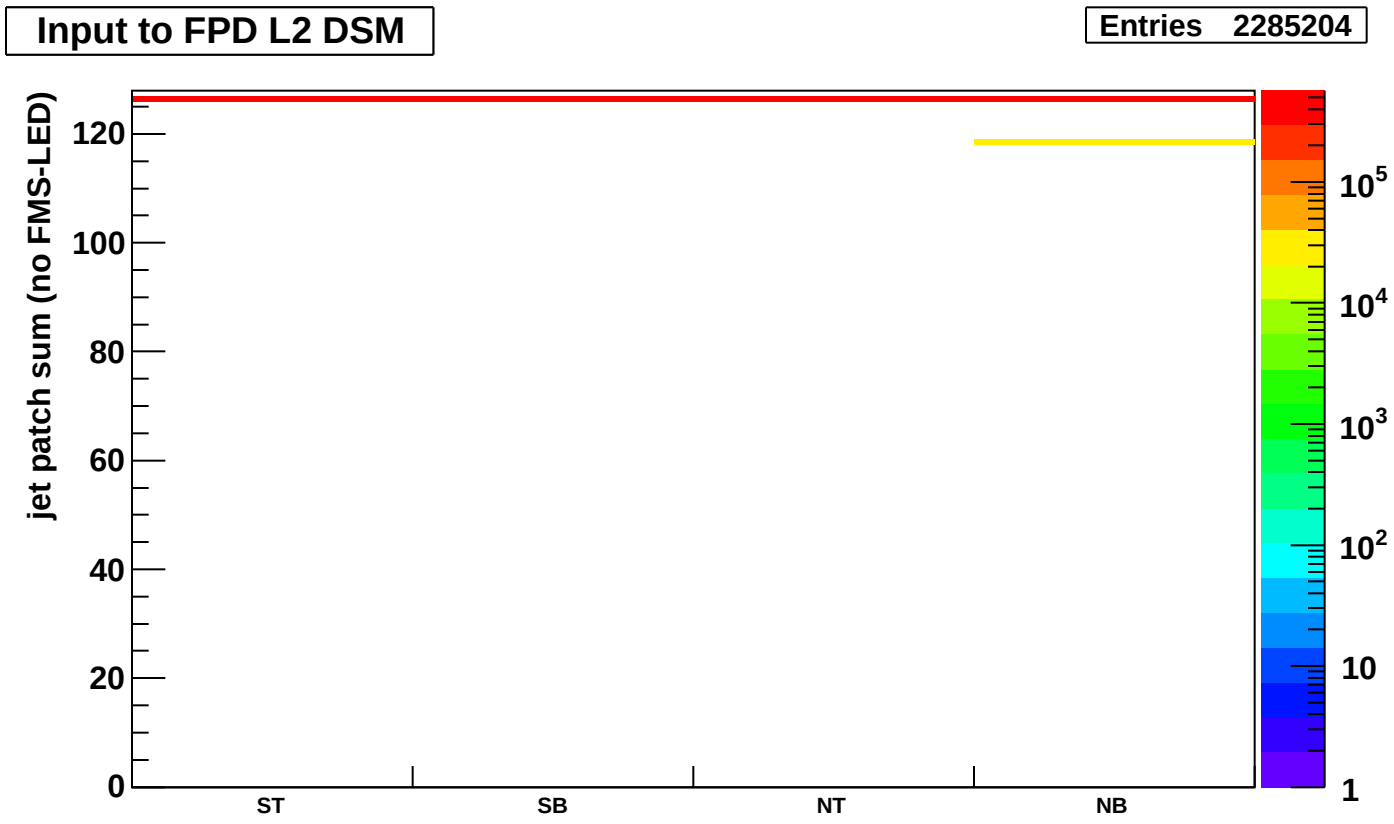

Input to FPD L2 DSM
Entries 4570408
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

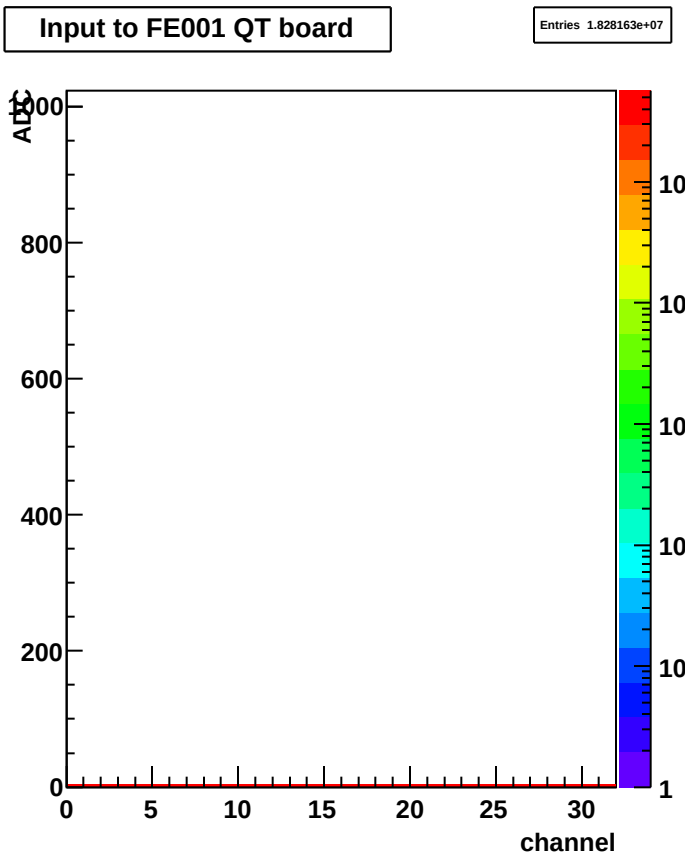

Input to FPD L2 DSM
Entries 1713903
CL bits
571301
571301
571301
SMALL
LARGE-S
LARGE-N

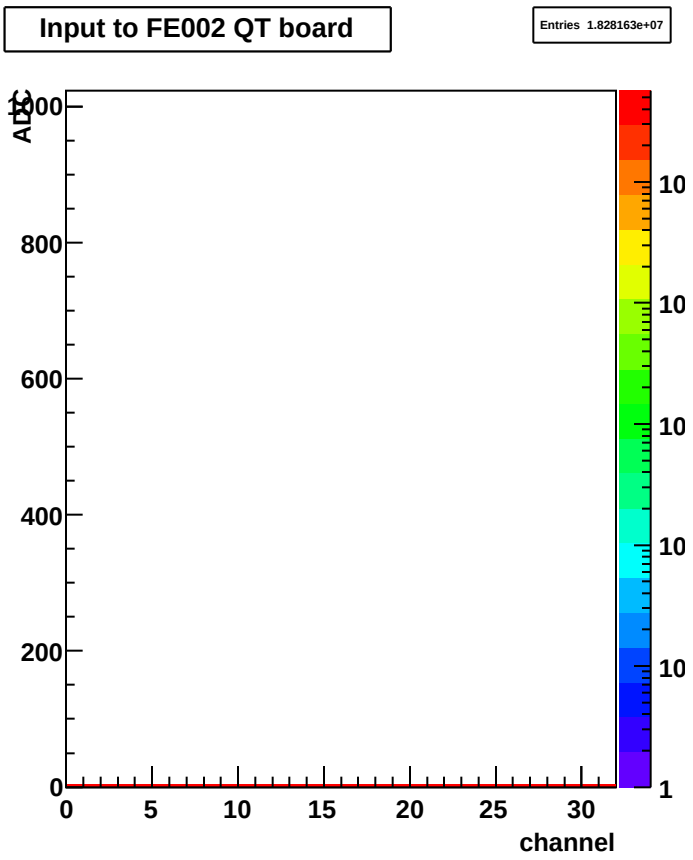

Input to FPD L2 DSM
Entries 1713903
CL bits - simulated
571301
571301
571301
SMALL
LARGE-S
LARGE-N

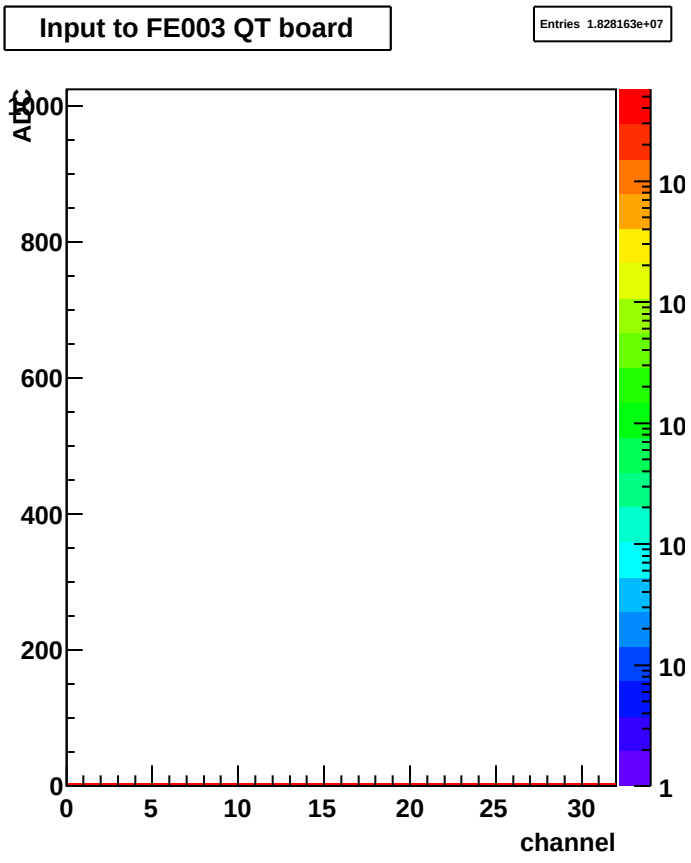

Input to FPD L2 DSM
Entries 1713903
HT bits
569395
571301
571301
1906
SMALL
LARGE-S
LARGE-N

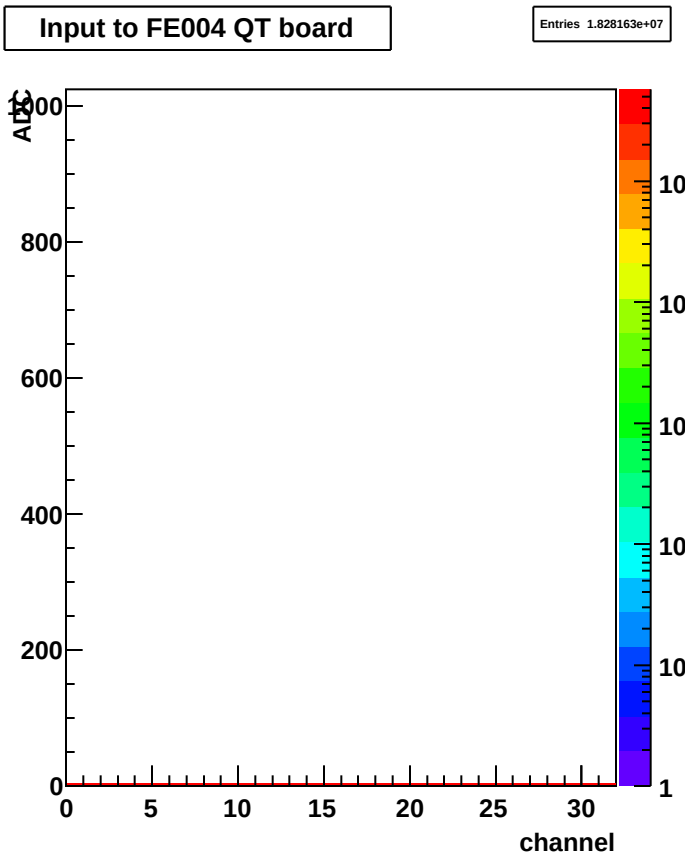

Input to FPD L2 DSM
Entries 1713903
HT bits - simulated
569395
571301
571301
1906
SMALL
LARGE-S
LARGE-N


Input to FPD L2 DSM
Entries 2285204
jet patch sum (no FMS-LED)
120
100
80
60
40
20
0
ST
SB
NT
NB
10^5
10^4
10^3
10^2
10
1

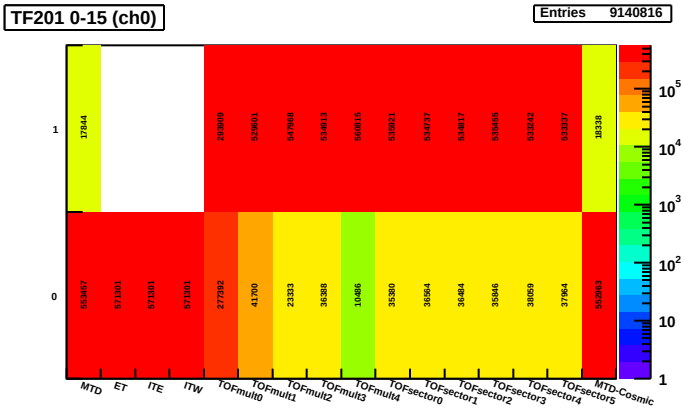

Input to FE001 QT board
Entries 1.828163e+07
ADC
channel


Input to FE002 QT board
Entries 1.828163e+07
ADC
channel

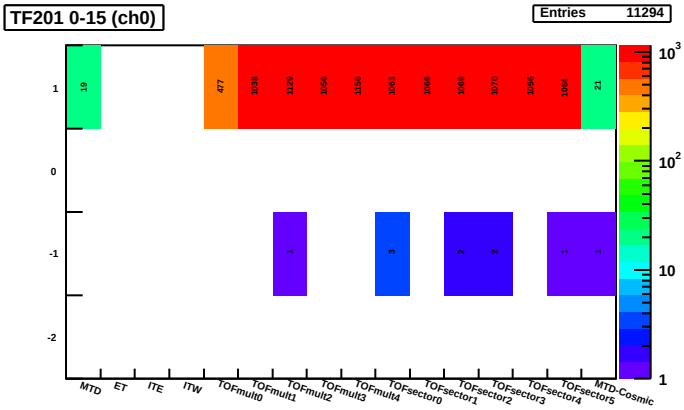

Input to FE003 QT board
Entries 1.828163e+07
ADC
channel


Input to FE004 QT board
Entries 1.828163e+07
ADC
channel

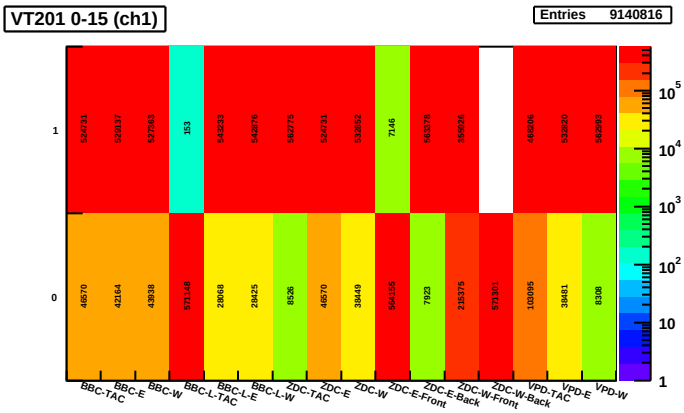
TF201 0-15 (ch0)



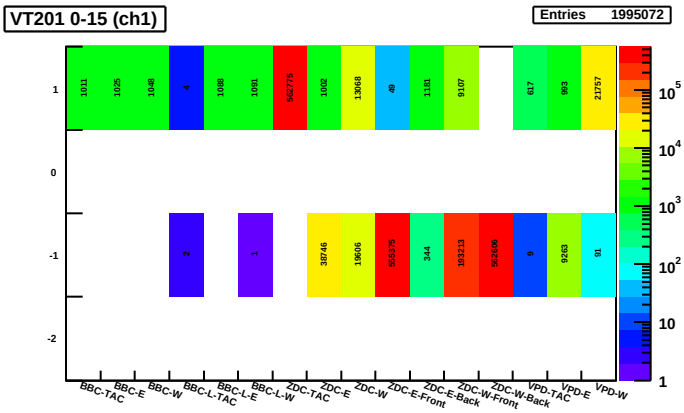
TF201 0-15 (ch0)



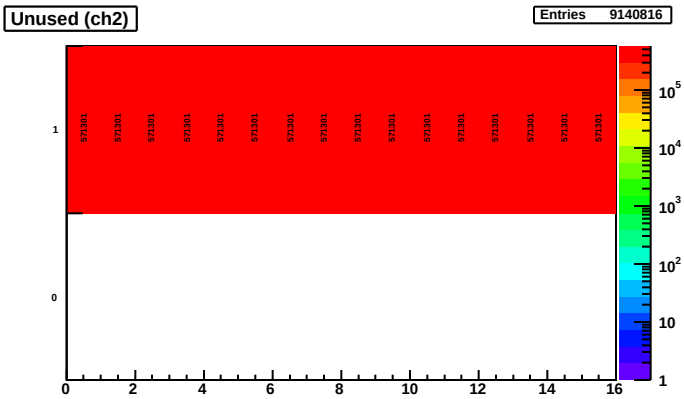
VT201 0-15 (ch1)

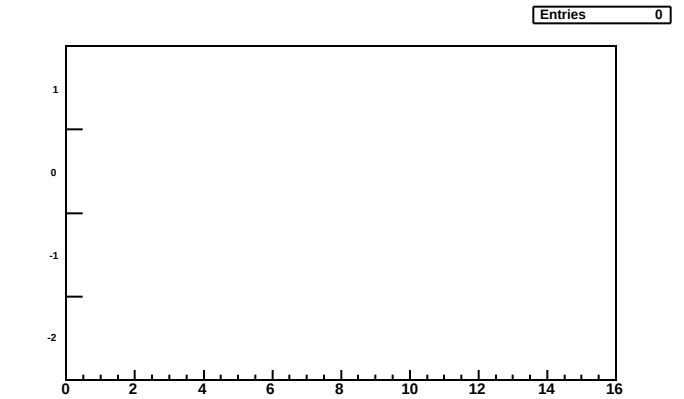


VT201 0-15 (ch1)

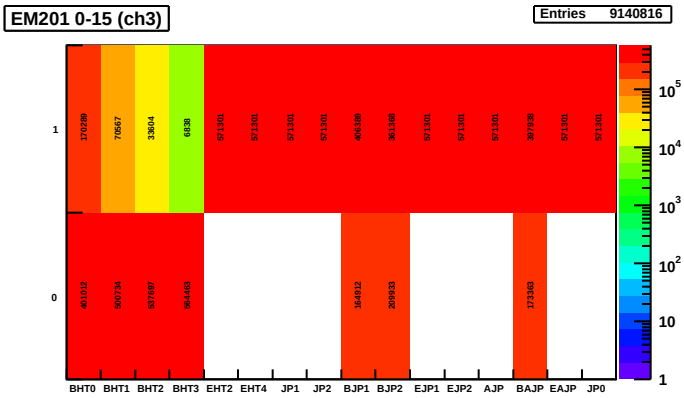


Unused (ch2)

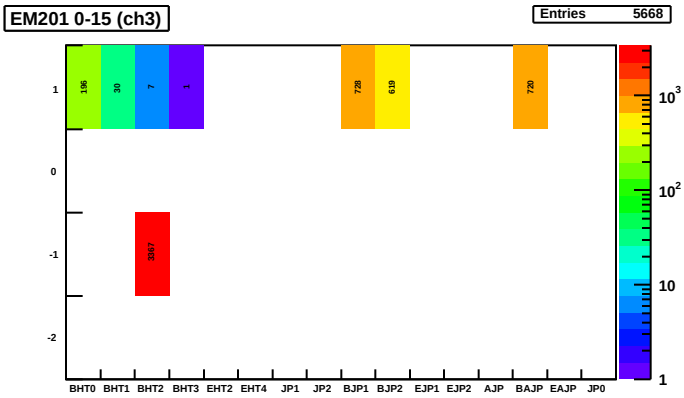




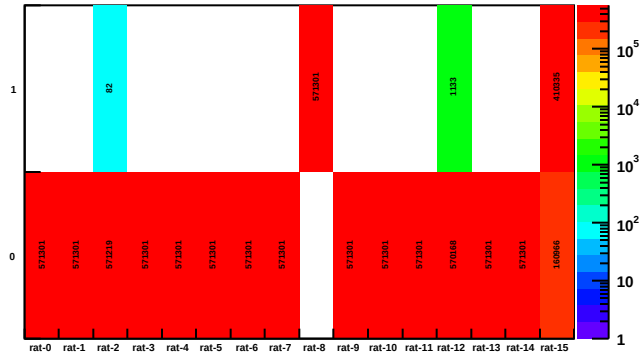
EM201 0-15 (ch3)

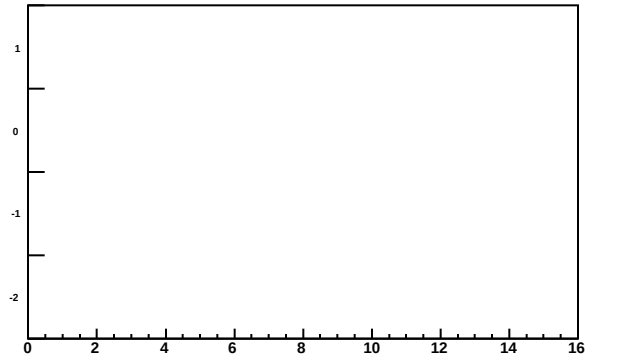


EM201 0-15 (ch3)

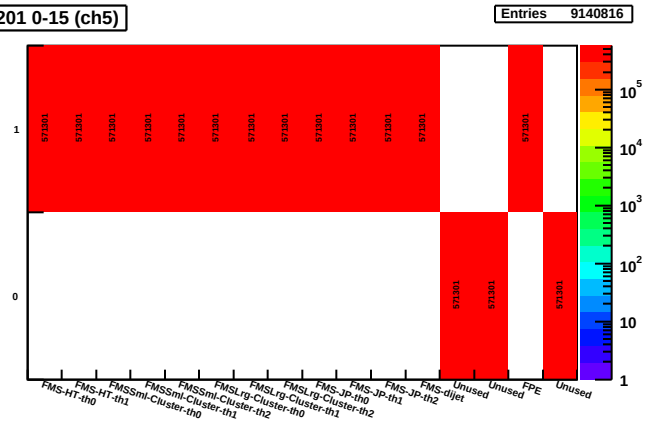


RAT board (ch4)

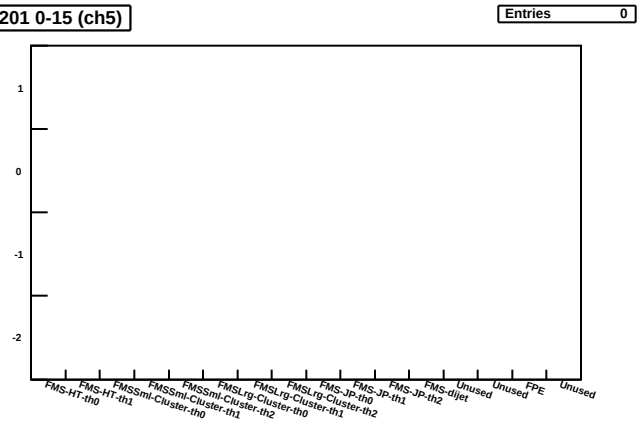




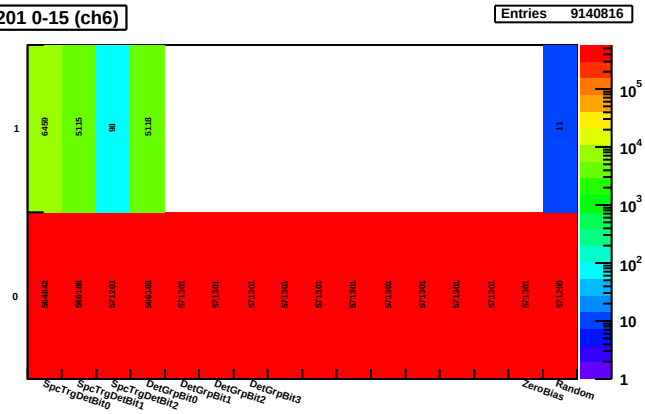
FP201 0-15 (ch5)

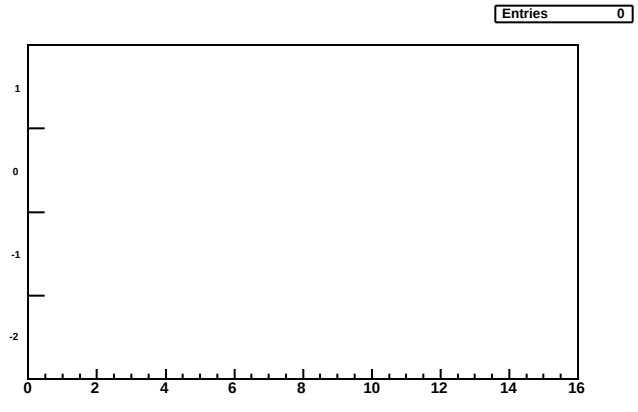
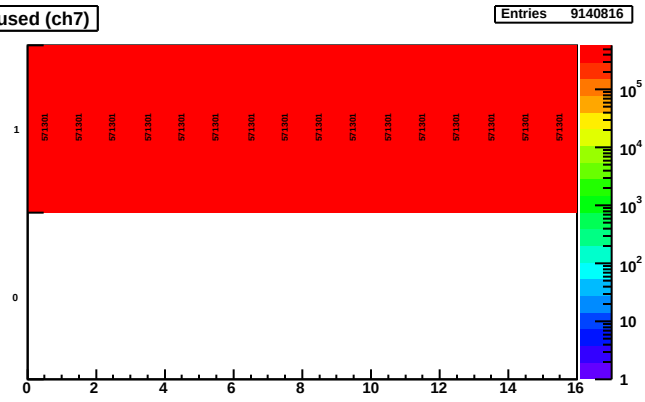
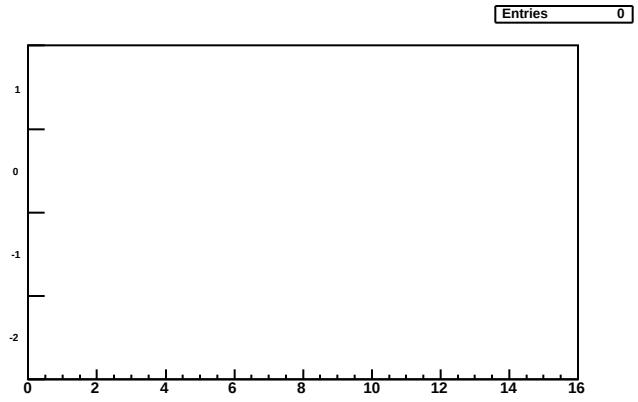


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)