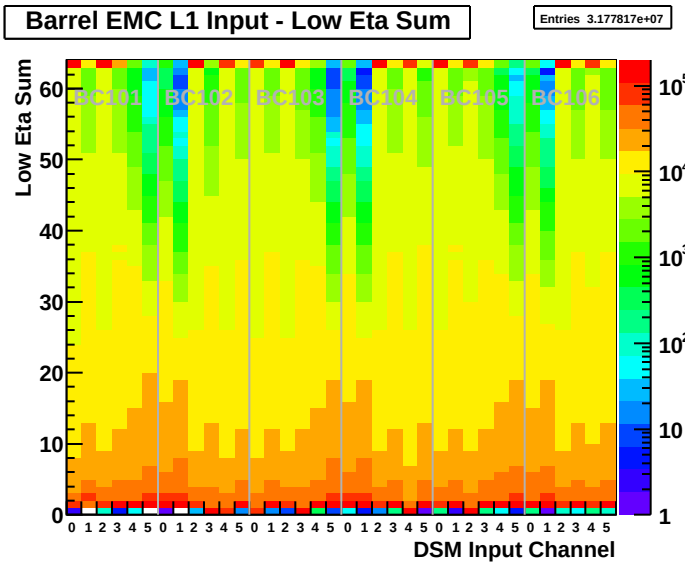
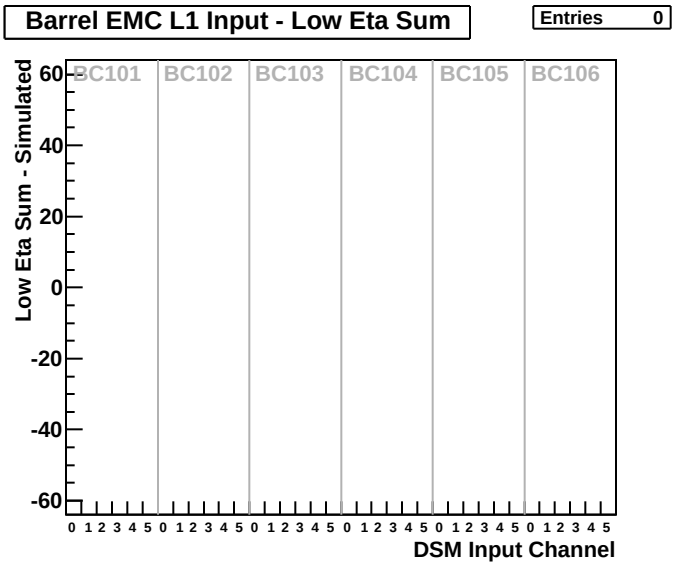
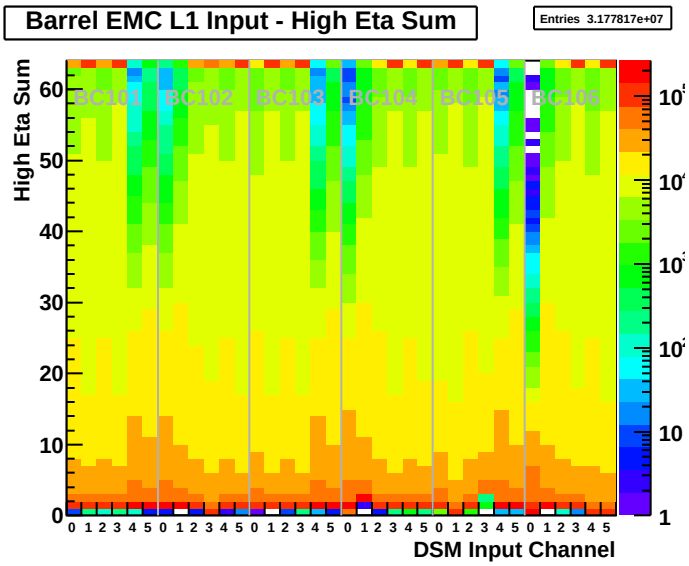
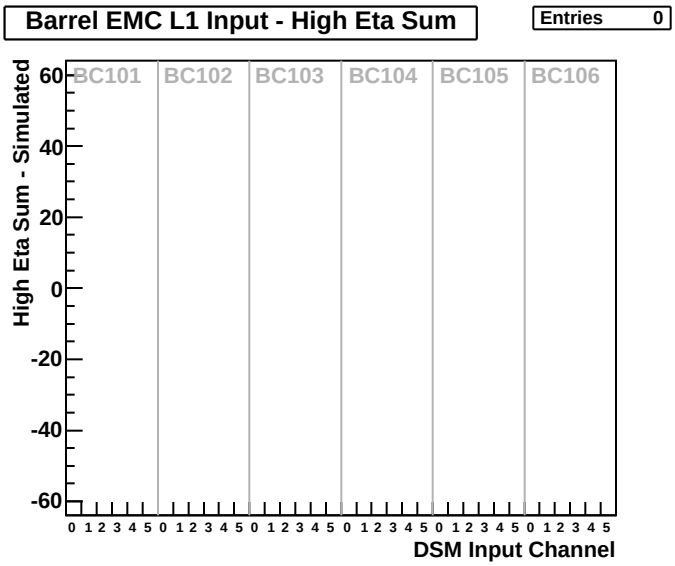
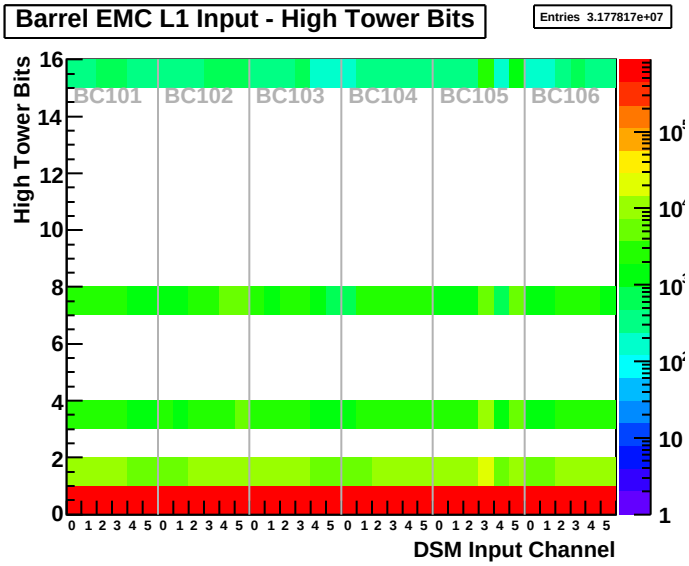
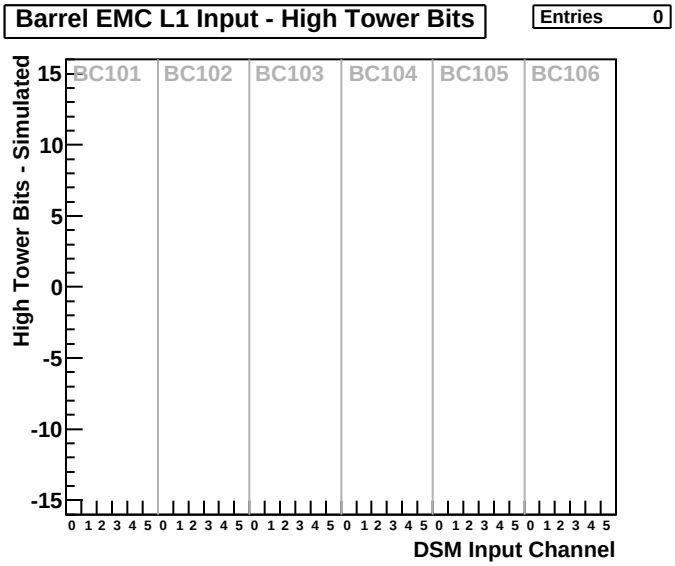

Barrel EMC L1 Input - Low Eta Sum
Entries 3.177817e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

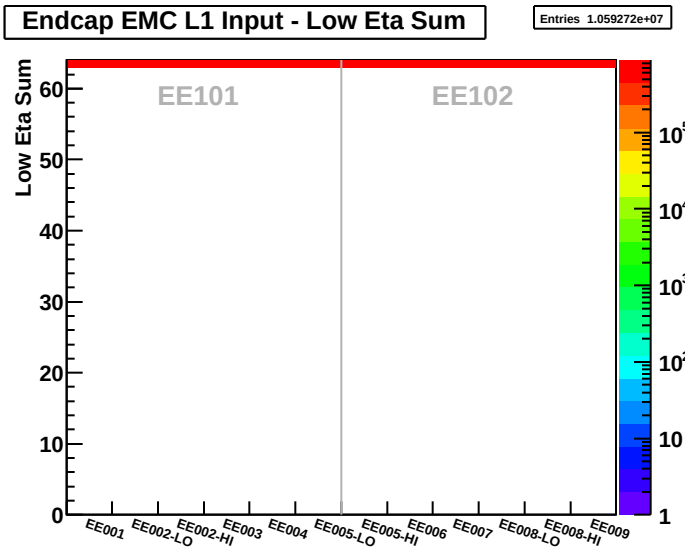

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

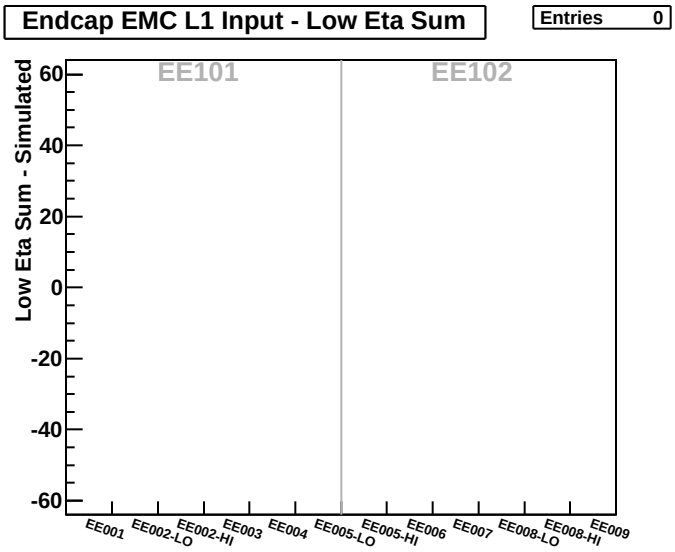

Barrel EMC L1 Input - High Eta Sum
Entries 3.177817e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

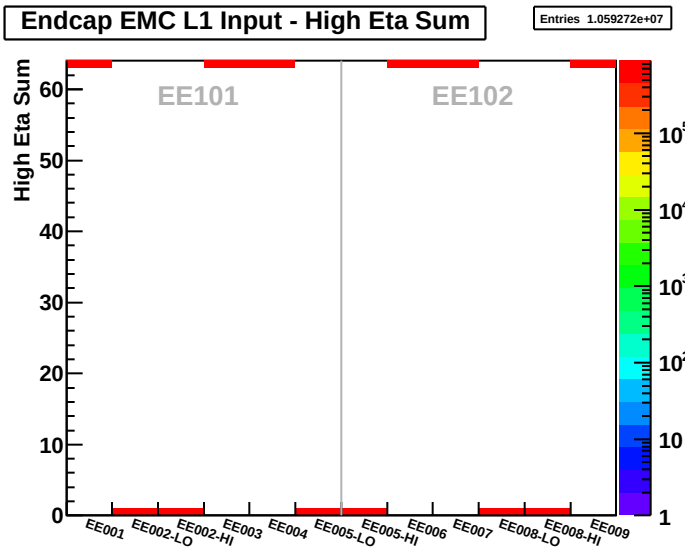

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

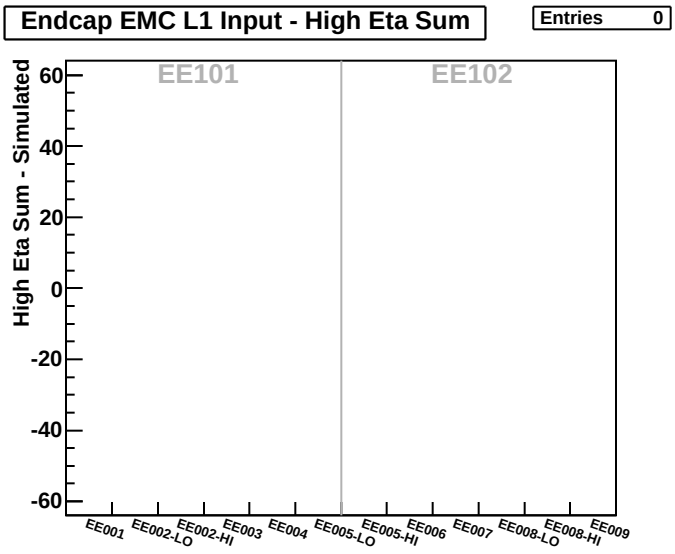

Barrel EMC L1 Input - High Tower Bits
Entries 3.177817e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

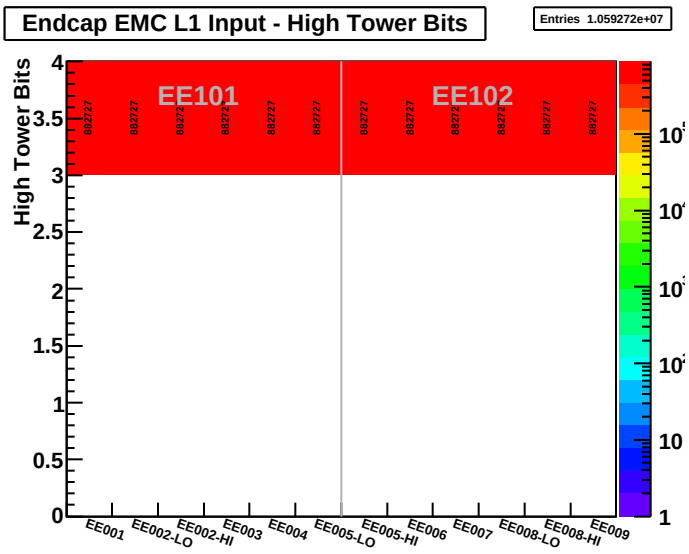

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

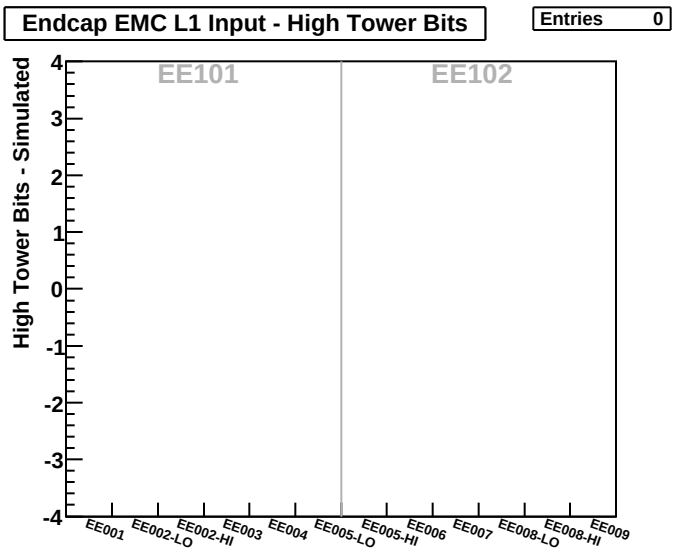

Endcap EMC L1 Input - Low Eta Sum
Entries 1.059272e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

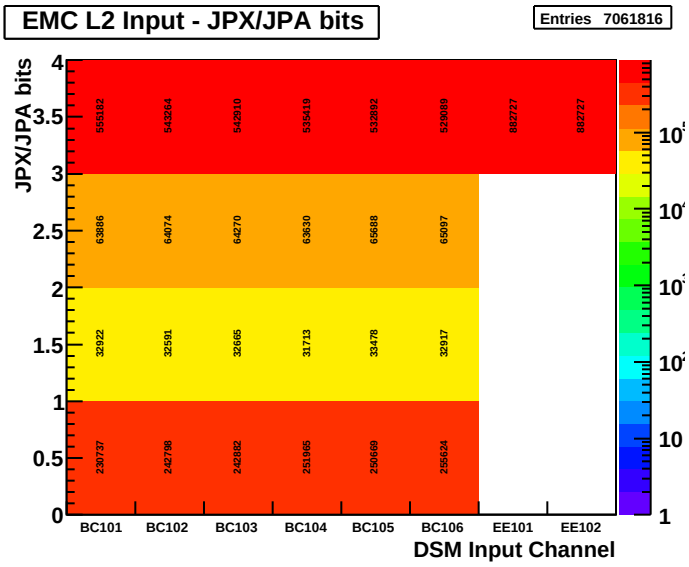

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

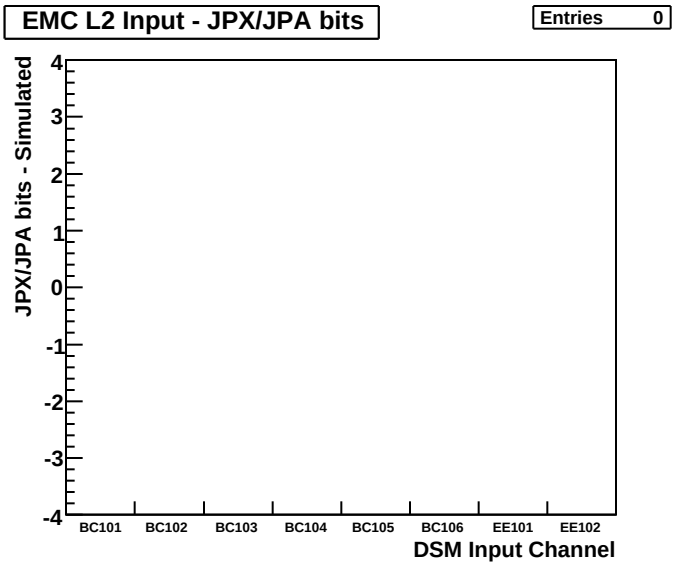

Endcap EMC L1 Input - High Eta Sum
Entries 1.059272e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

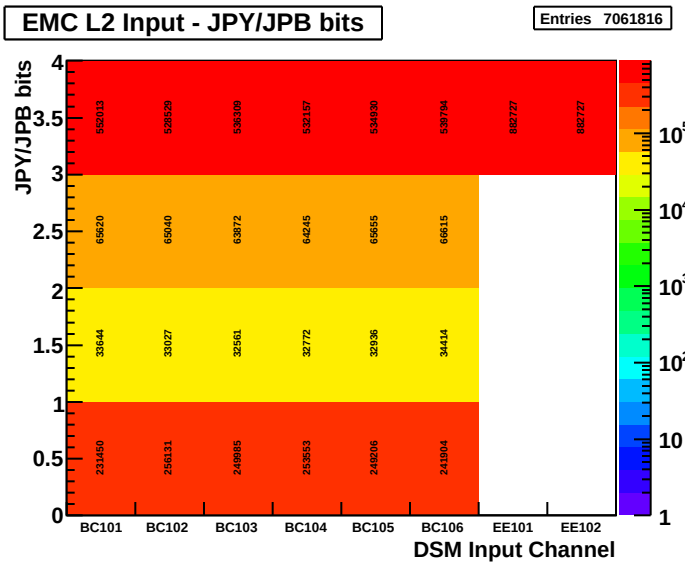

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

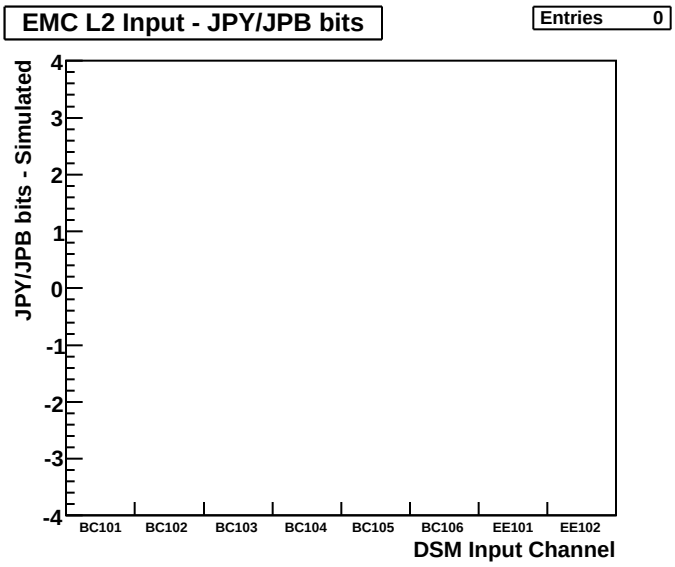

Endcap EMC L1 Input - High Tower Bits
Entries 1.059272e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

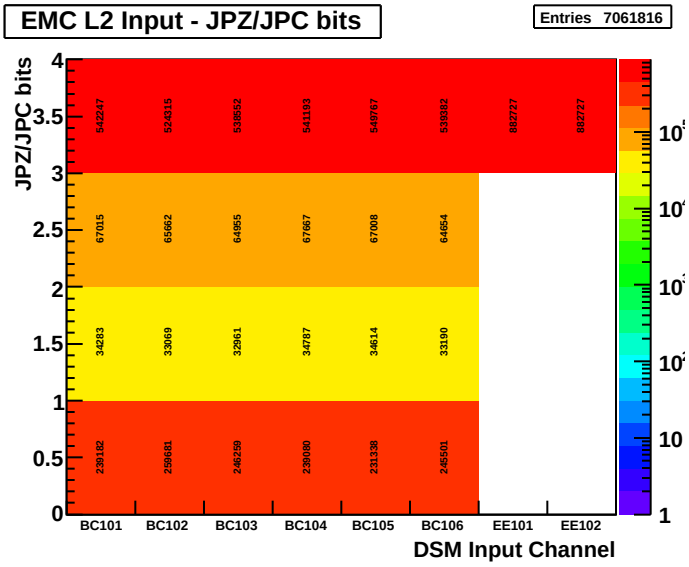

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

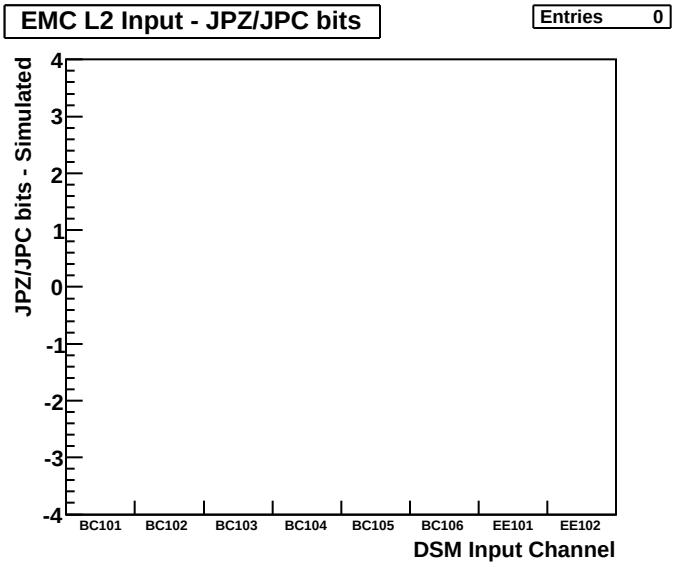

EMC L2 Input - JPX/JPA bits
Entries 7061816
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
555182 543264 542810 535419 532892 529089 882727 882727
63886 64074 64270 63630 65688 65097
32922 32591 32665 31713 33478 32917
230737 242798 242882 251965 250669 255624

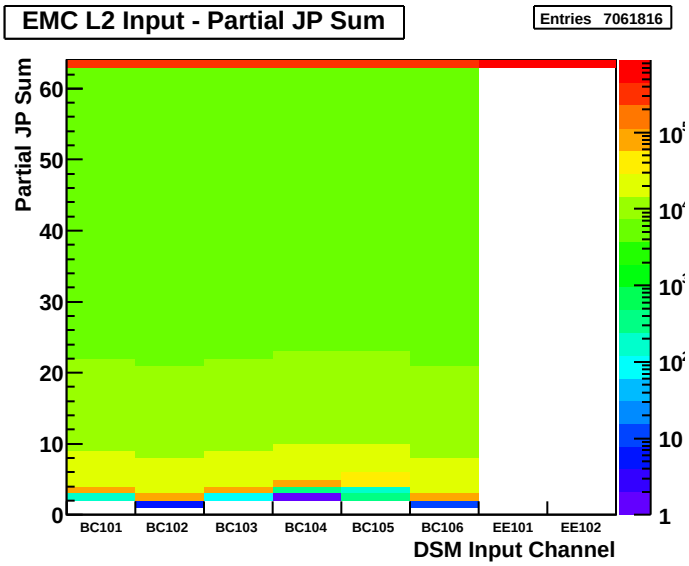

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

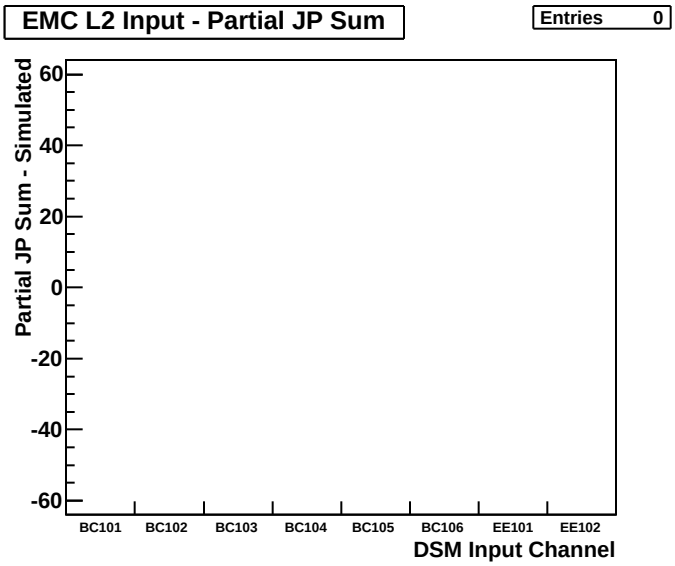

EMC L2 Input - JPY/JPB bits
Entries 7061816
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
552013 528529 536309 532157 534930 539794 882727 882727
65620 65040 63872 64245 65655 66615
33644 33027 32561 32772 32936 34414
231450 256131 249985 253553 249206 241904

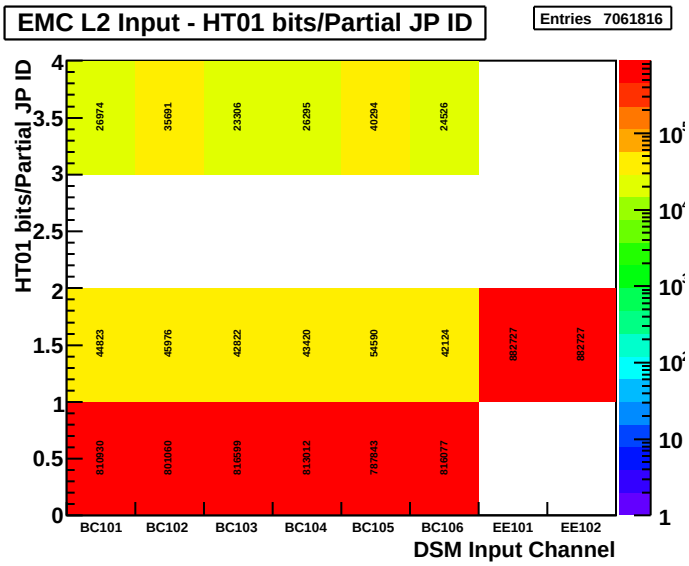

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

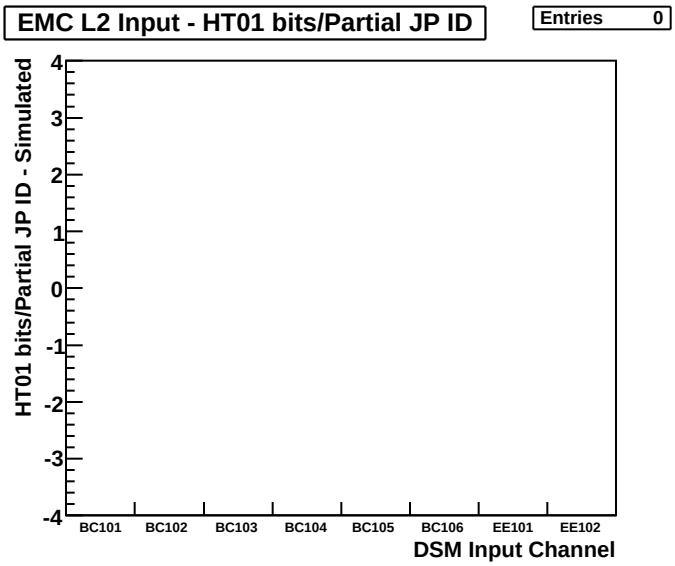

EMC L2 Input - JPZ/JPC bits
Entries 7061816
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
542247 524315 538552 541193 549767 539382 882727 882727
67015 65662 64955 67667 67008 64654
34283 33069 32961 34787 34614 33190
239182 259681 246259 239080 231338 245501

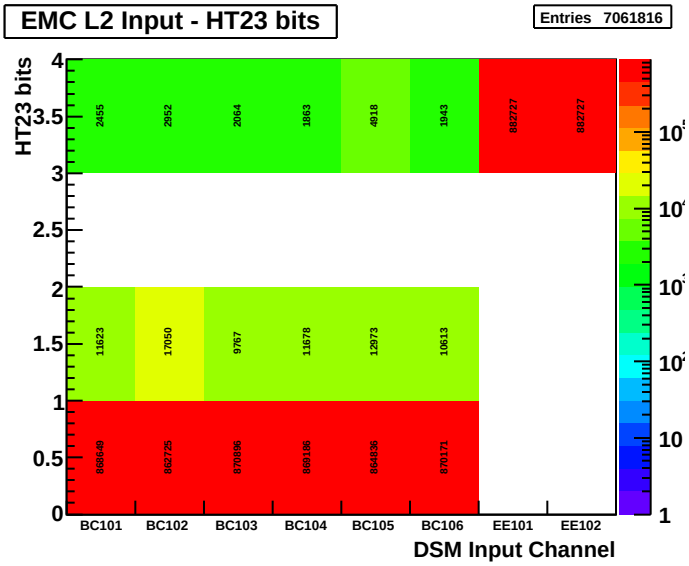

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

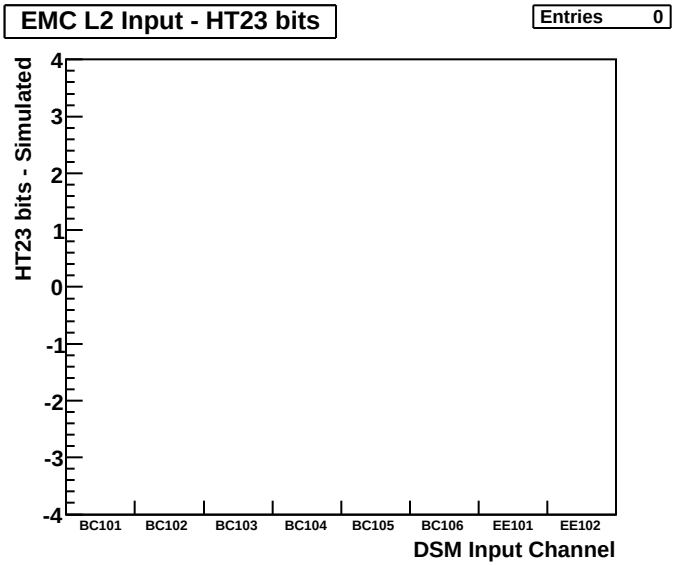

EMC L2 Input - Partial JP Sum
Entries 7061816
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

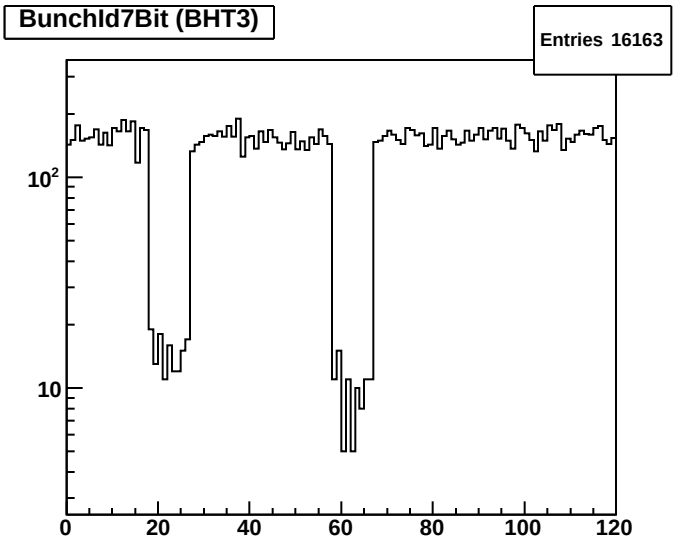

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

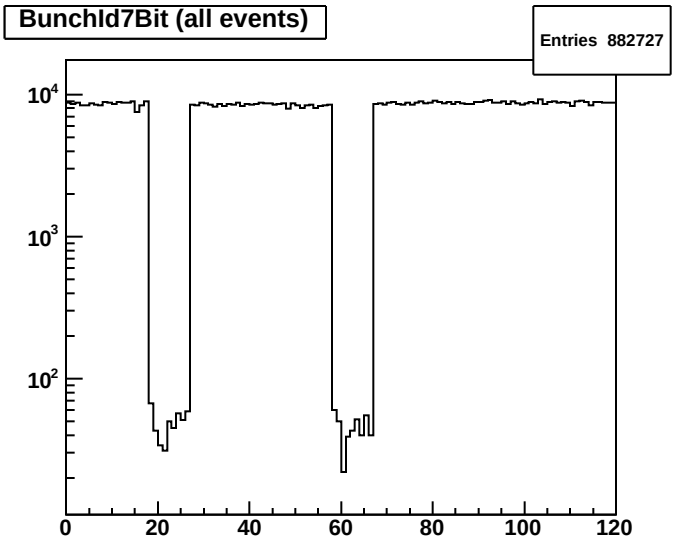

EMC L2 Input - HT01 bits/Partial JP ID
Entries 7061816
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

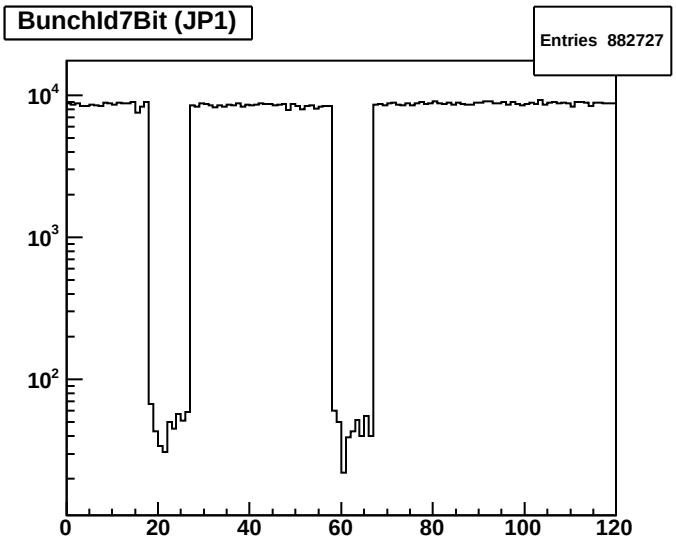

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

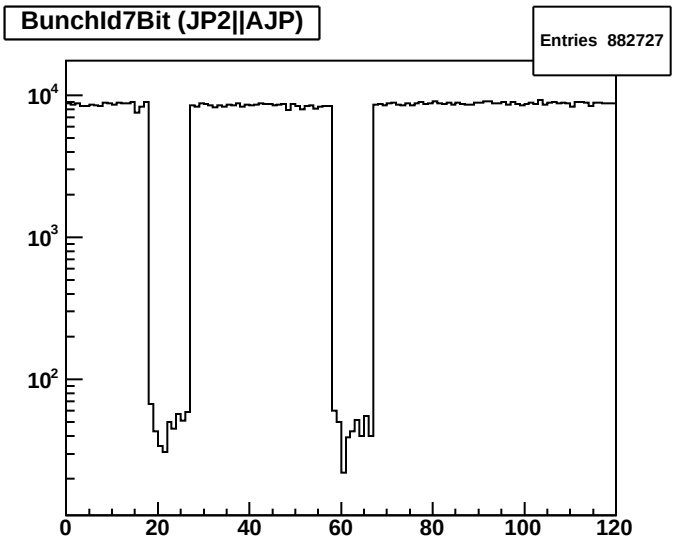

EMC L2 Input - HT23 bits
Entries 7061816
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

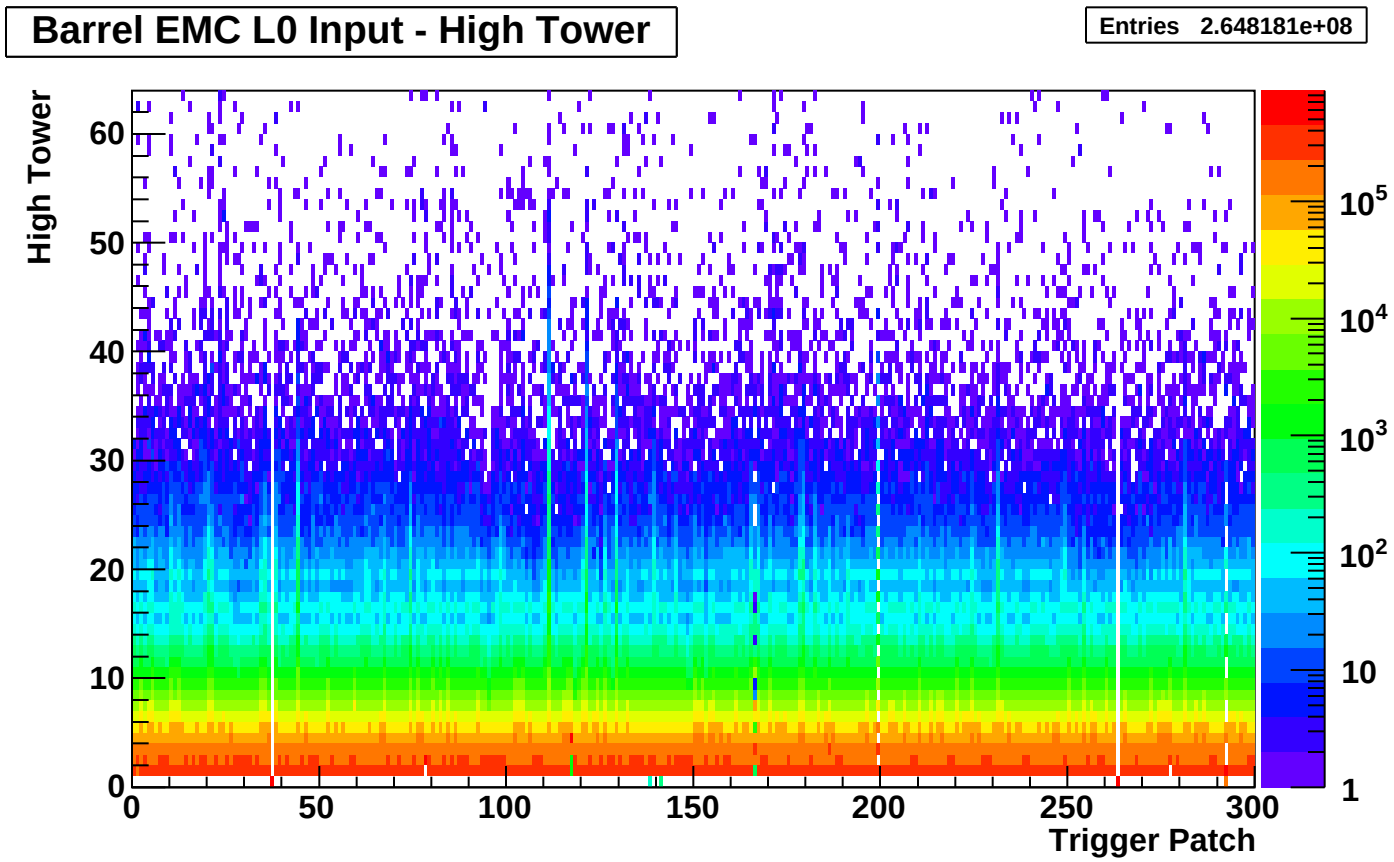

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

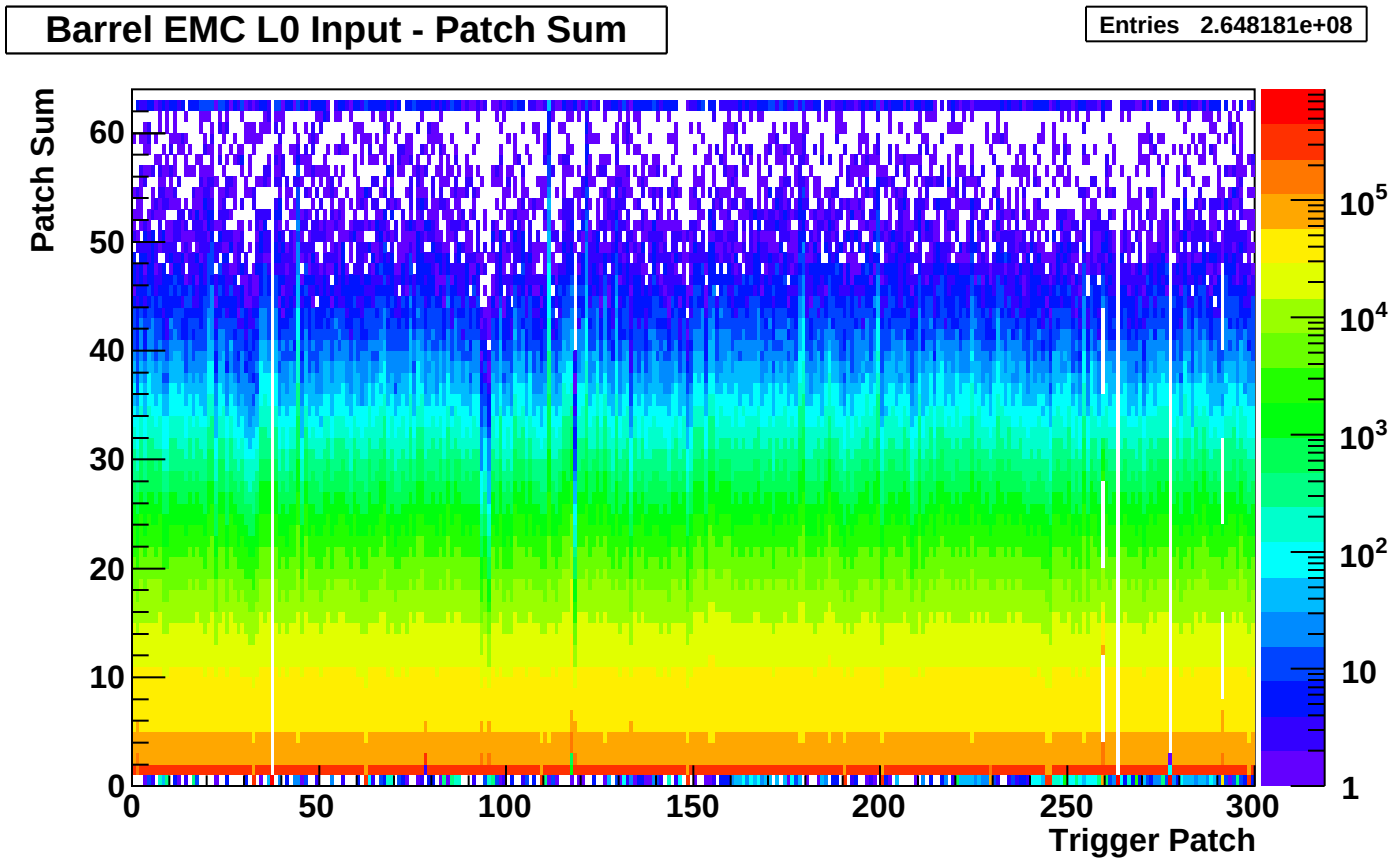

BunchId7Bit (BHT3)
Entries 16163
10^2
10
0
20
40
60
80
100
120

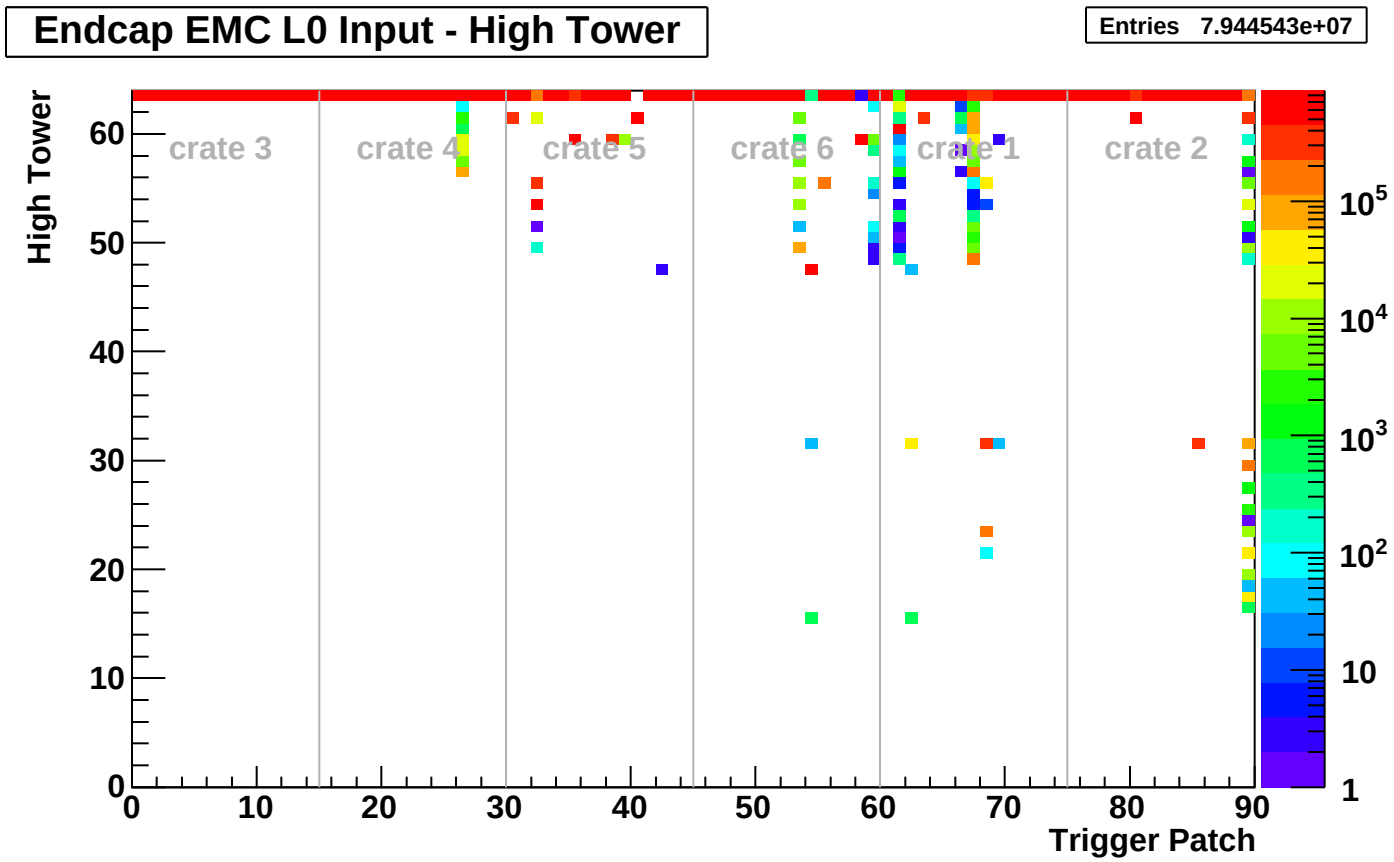

BunchId7Bit (all events)
Entries 882727
10^4
10^3
10^2
0
20
40
60
80
100
120

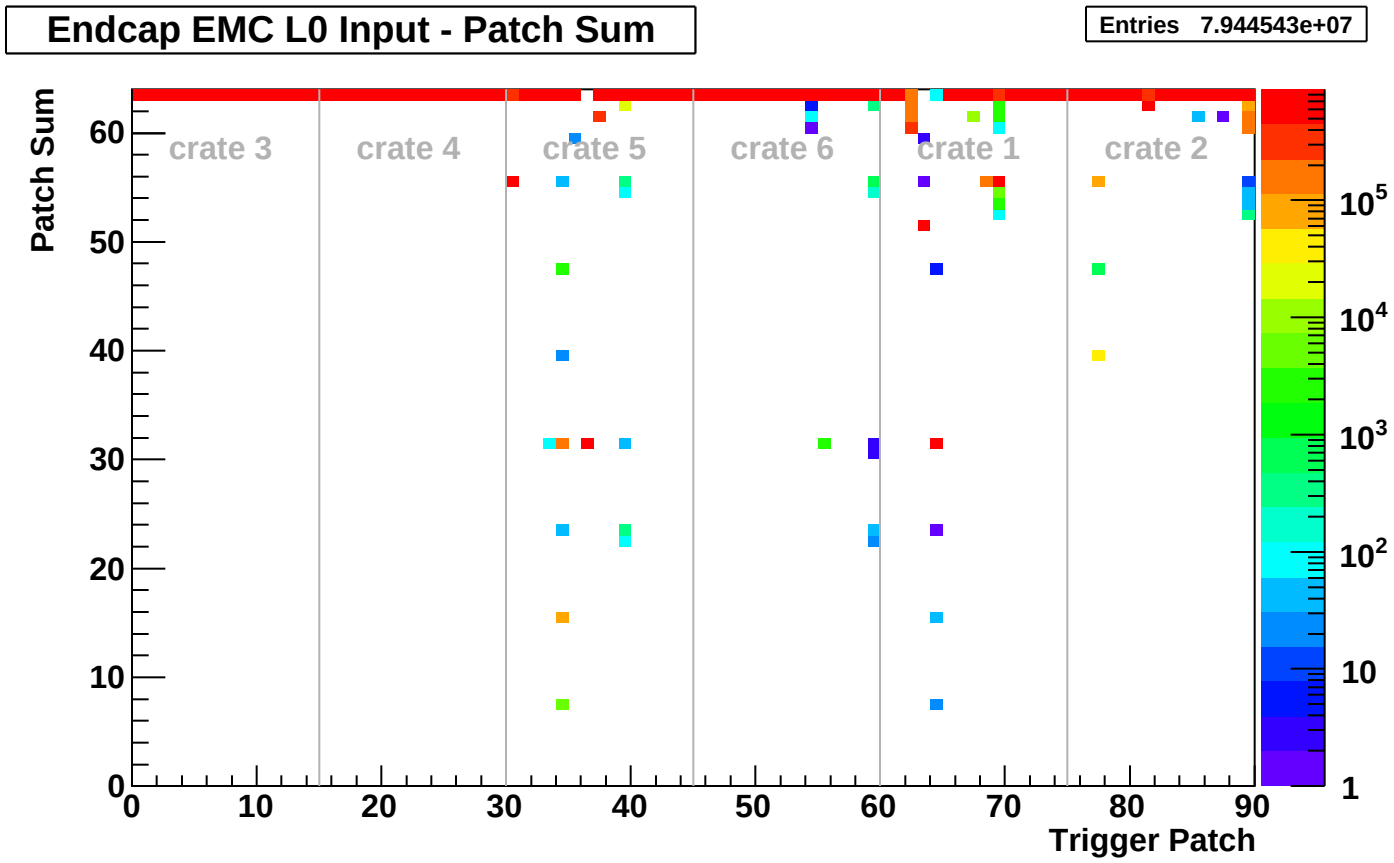

BunchId7Bit (JP1)
Entries 882727
10^4
10^3
10^2
0
20
40
60
80
100
120

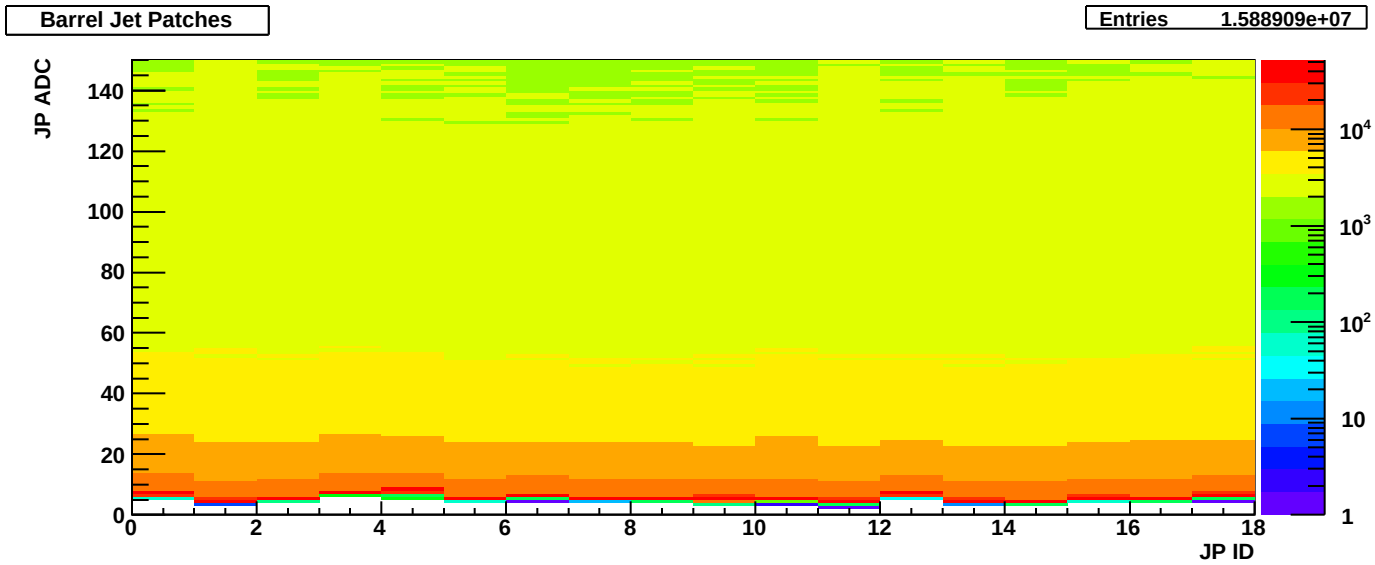

BunchId7Bit (JP2||AJP)
Entries 882727
10^4
10^3
10^2
0
20
40
60
80
100
120

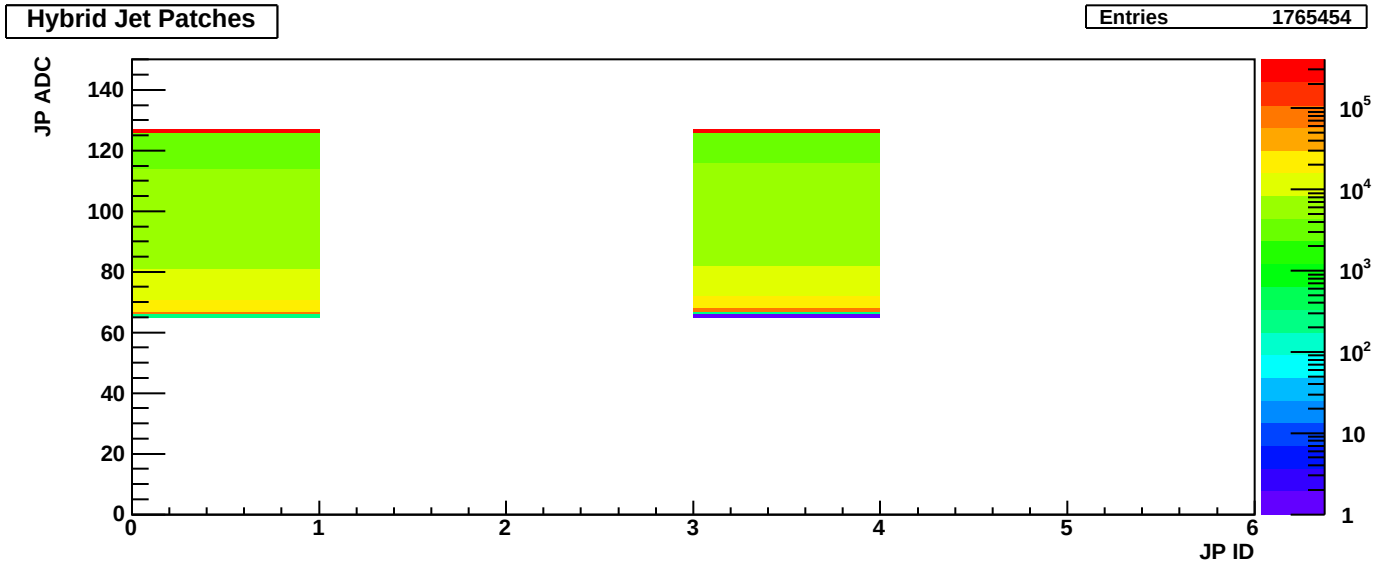

Barrel EMC L0 Input - High Tower
Entries 2.648181e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

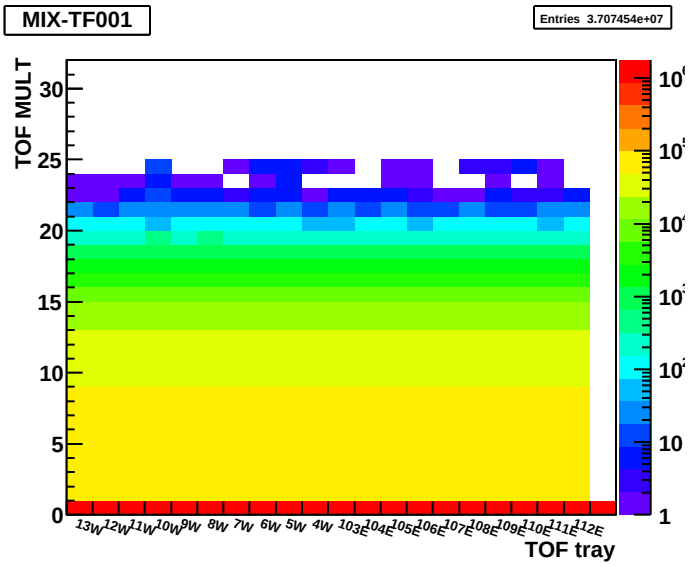

Barrel EMC L0 Input - Patch Sum
Entries 2.648181e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

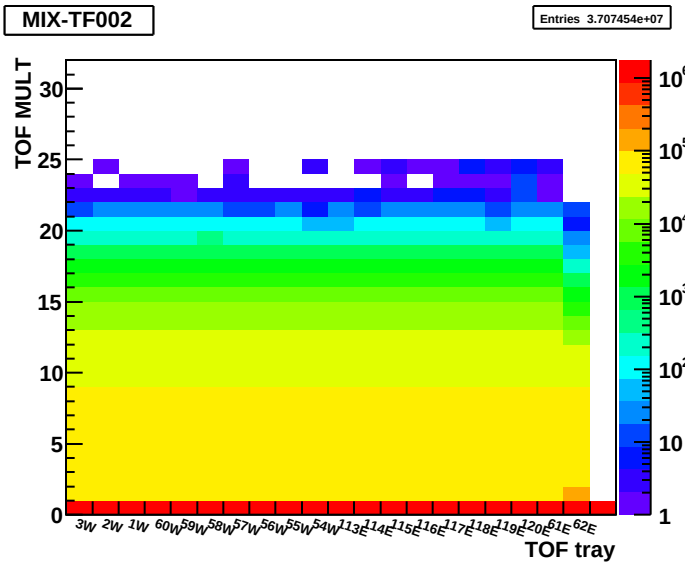

Endcap EMC L0 Input - High Tower
Entries 7.944543e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

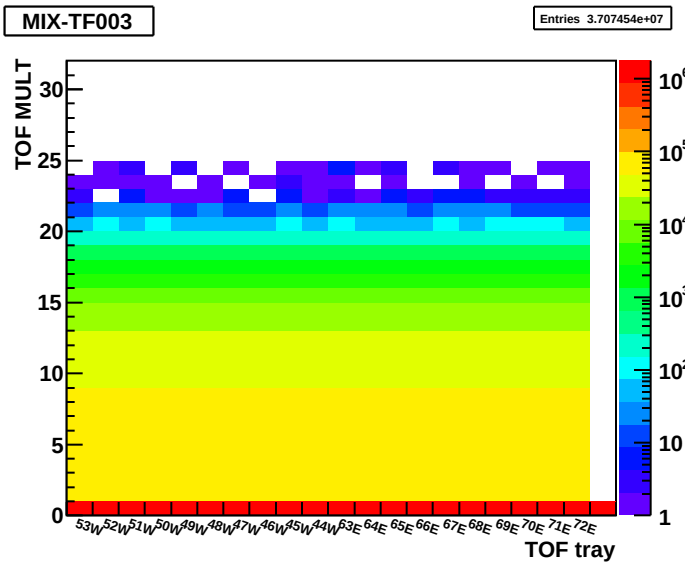

Endcap EMC L0 Input - Patch Sum
Entries 7.944543e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

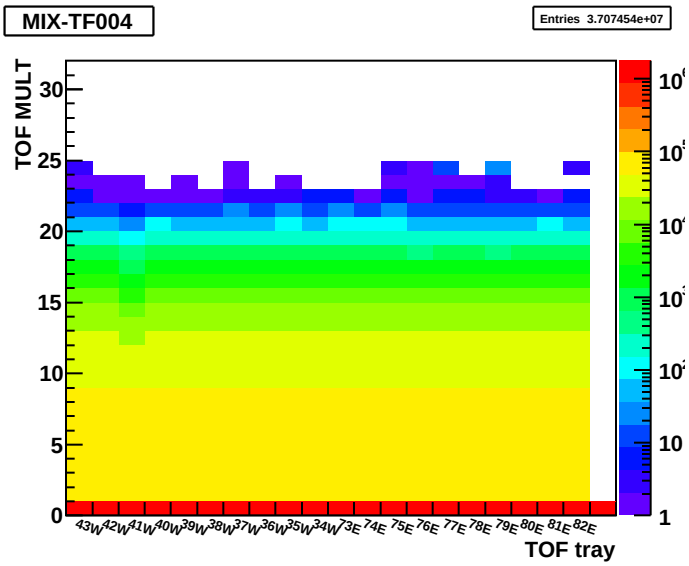

Barrel Jet Patches
Entries 1.588909e+07
JP ADC
JP ID

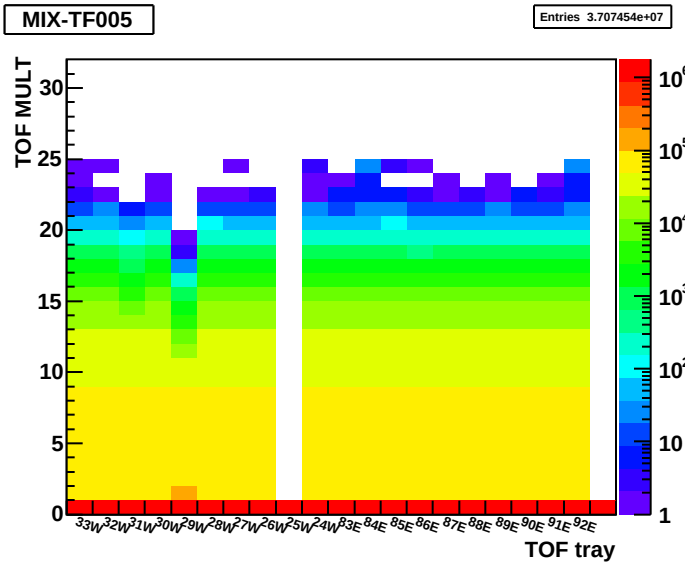

Hybrid Jet Patches
Entries 1765454
JP ADC
JP ID

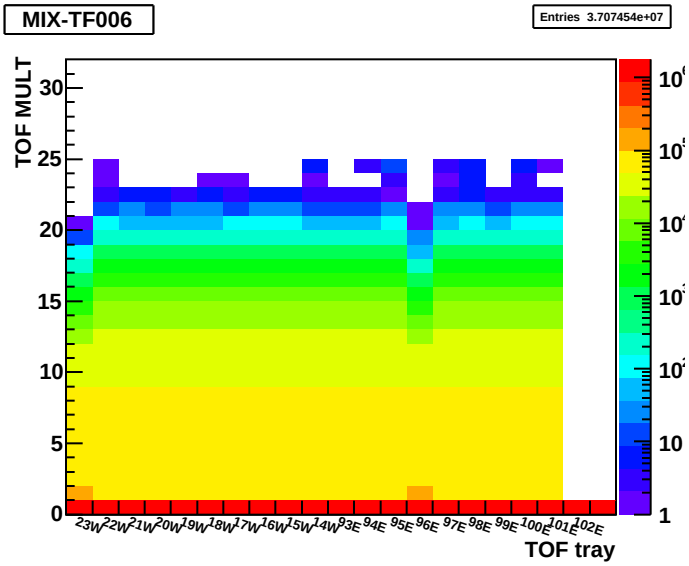

MIX-TF001
Entries 3.707454e+07
TOF MULT
TOF tray

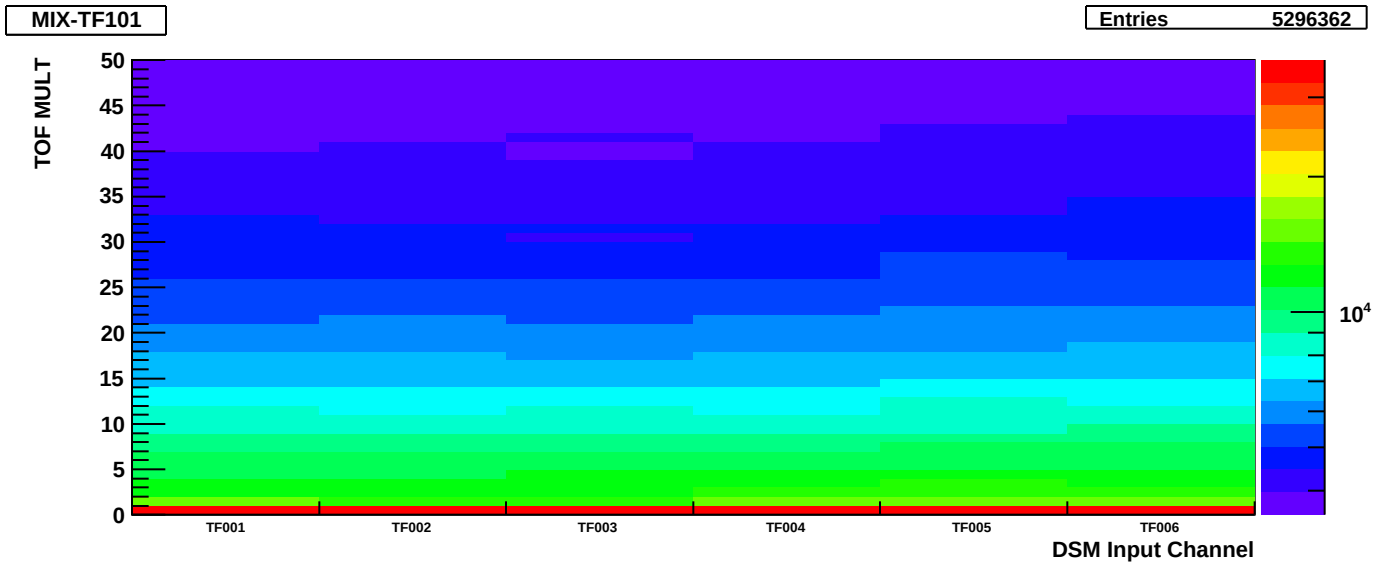

MIX-TF002
Entries 3.707454e+07
TOF MULT
TOF tray

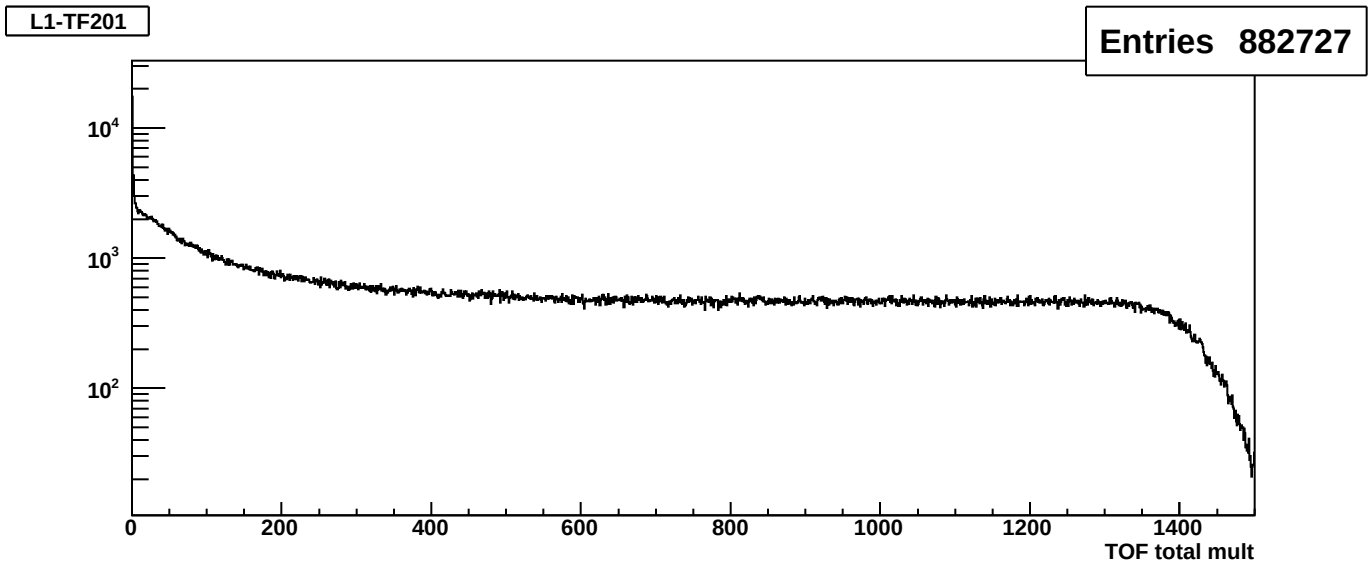

MIX-TF003
Entries 3.707454e+07
TOF MULT
TOF tray

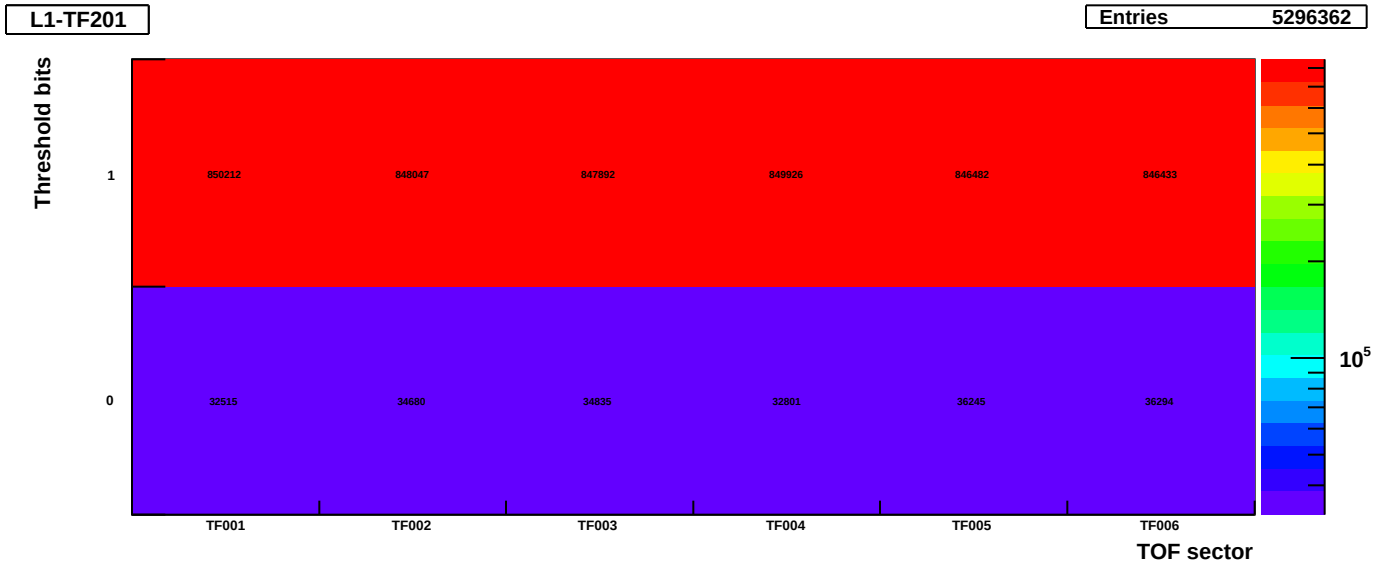

MIX-TF004
Entries 3.707454e+07
TOF MULT
TOF tray

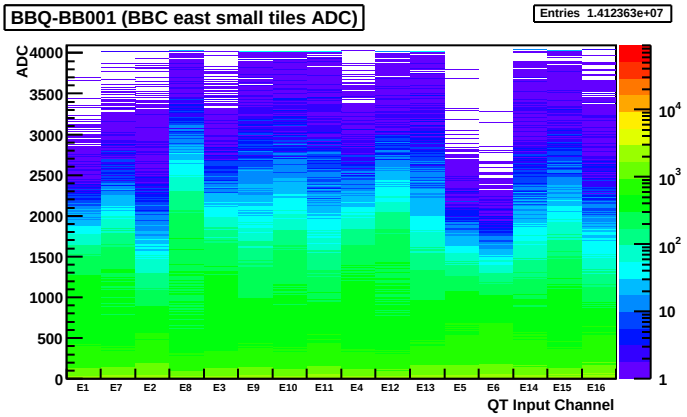

MIX-TF005
Entries 3.707454e+07
TOF MULT
TOF tray

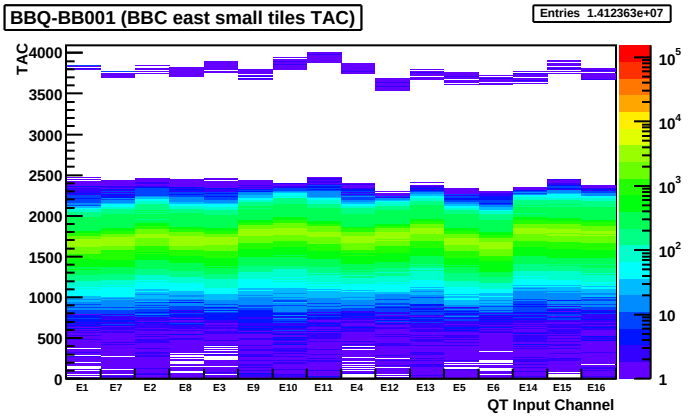

MIX-TF006
Entries 3.707454e+07
TOF MULT
TOF tray

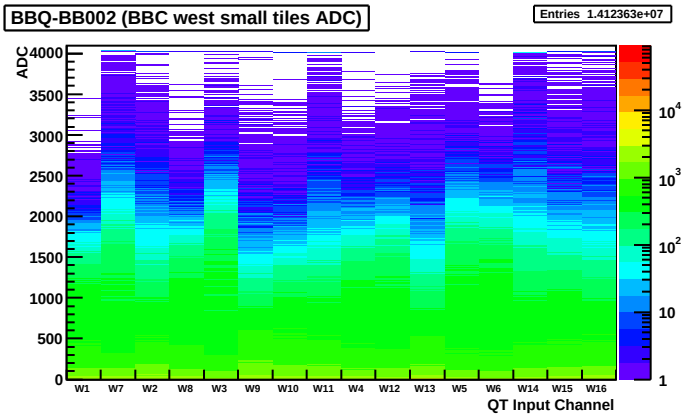

MIX-TF101
Entries 5296362
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

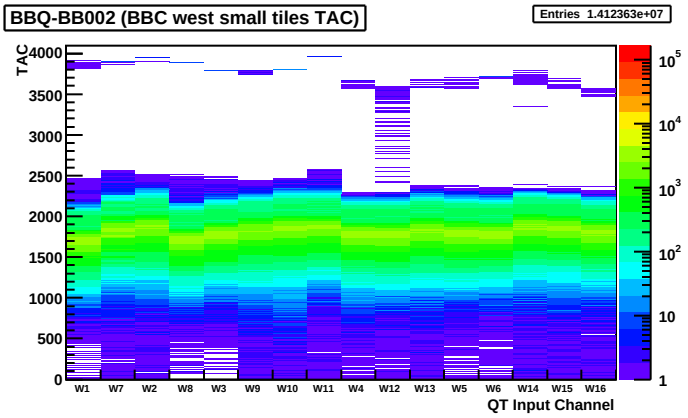

L1-TF201
Entries 882727
10^4
10^3
10^2
0
200
400
600
800
1000
1200
1400
TOF total mult

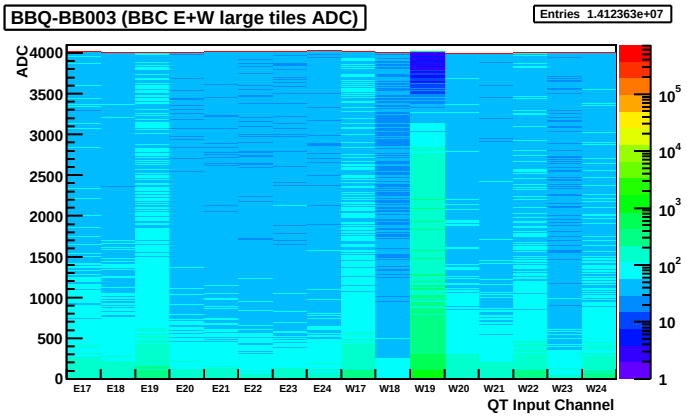

L1-TF201
Entries 5296362
Threshold bits
1
0
850212
848047
847892
849926
846482
846433
32515
34680
34835
32801
36245
36294
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

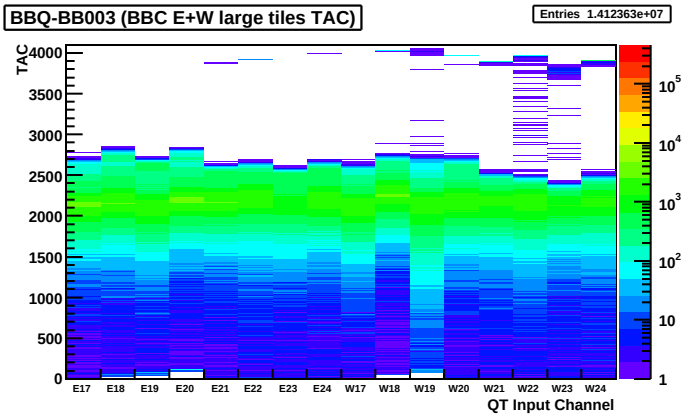

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.412363e+07
ADC
QT Input Channel

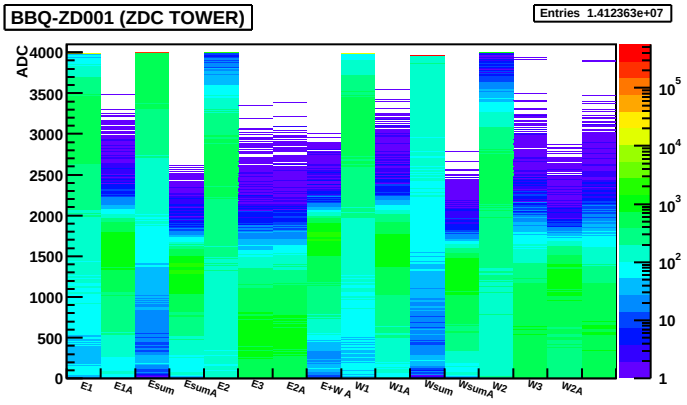

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.412363e+07
TAC
QT Input Channel

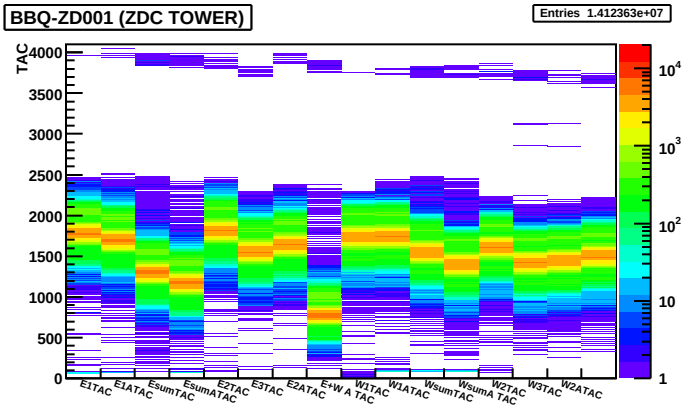

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.412363e+07
ADC
QT Input Channel

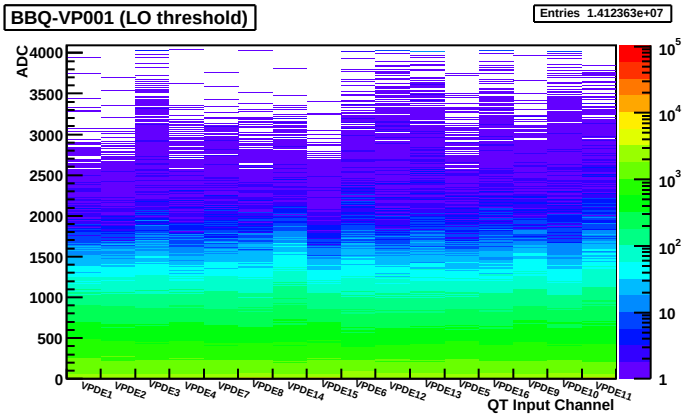

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.412363e+07
TAC
QT Input Channel

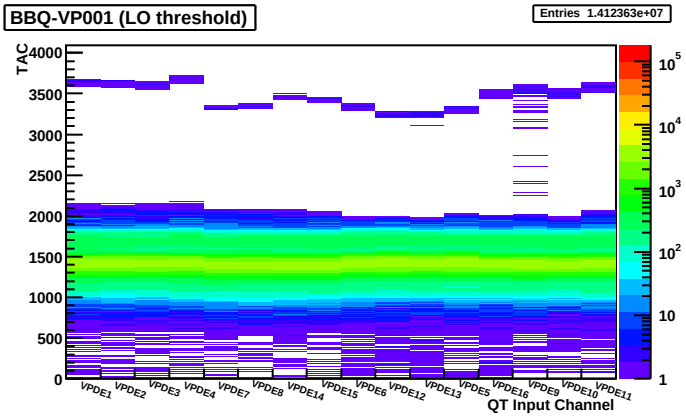

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.412363e+07
ADC
QT Input Channel

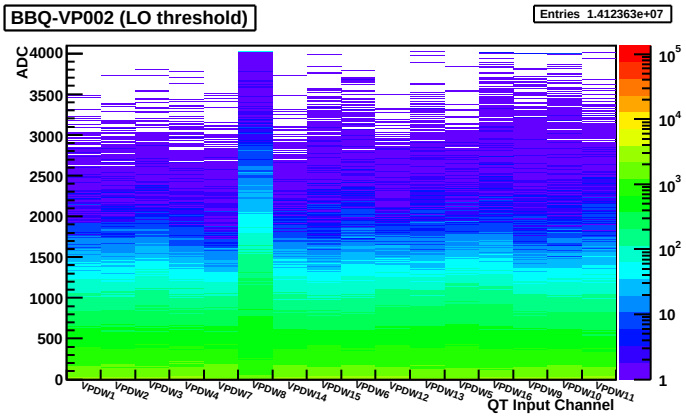

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.412363e+07
TAC
QT Input Channel

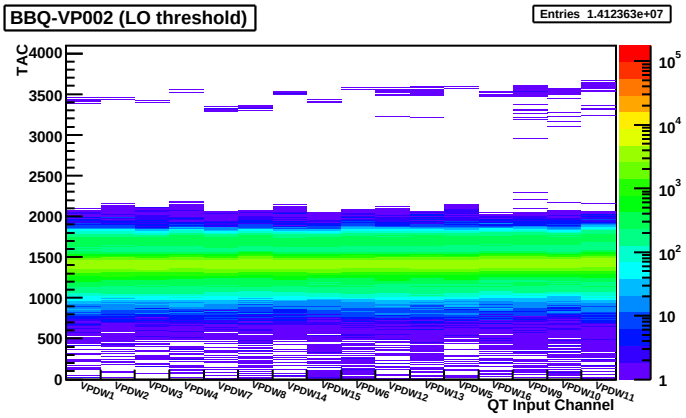

BBQ-ZD001 (ZDC TOWER)
Entries 1.412363e+07
ADC

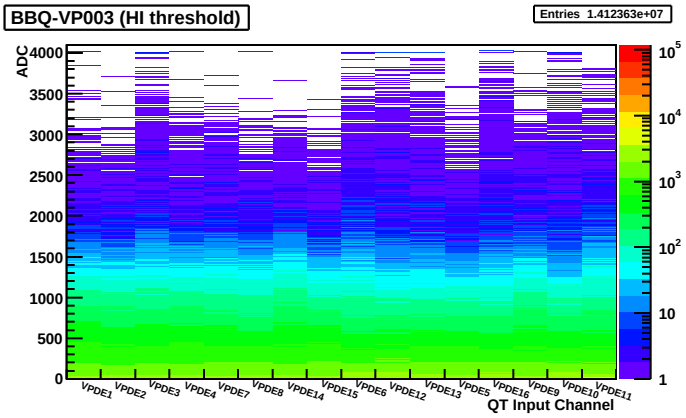

BBQ-ZD001 (ZDC TOWER)
Entries 1.412363e+07
TAC

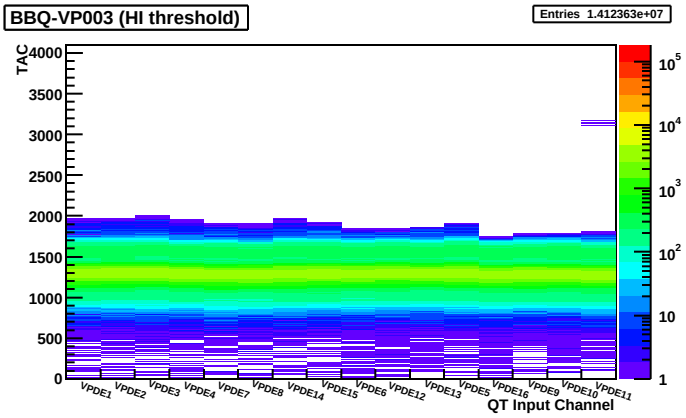

BBQ-VP001 (LO threshold)
Entries 1.412363e+07
ADC
QT Input Channel

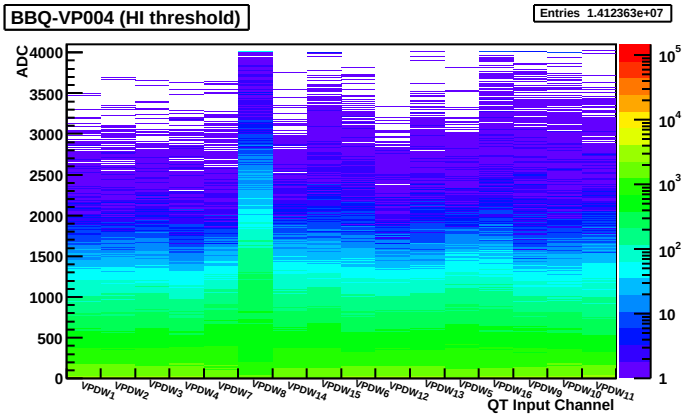

BBQ-VP001 (LO threshold)
Entries 1.412363e+07
TAC
QT Input Channel

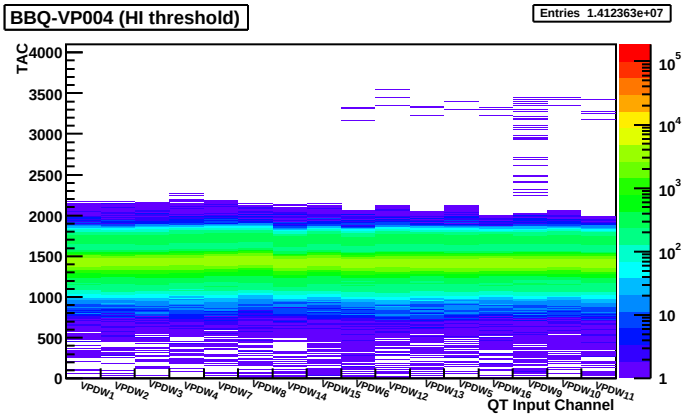

BBQ-VP002 (LO threshold)
Entries 1.412363e+07
ADC
QT Input Channel

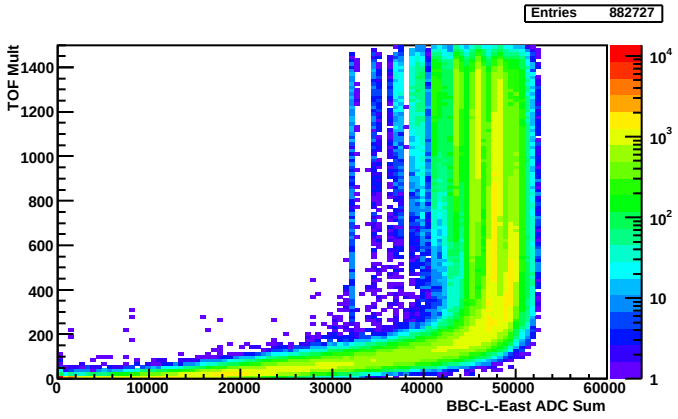

BBQ-VP002 (LO threshold)
Entries 1.412363e+07
TAC
QT Input Channel

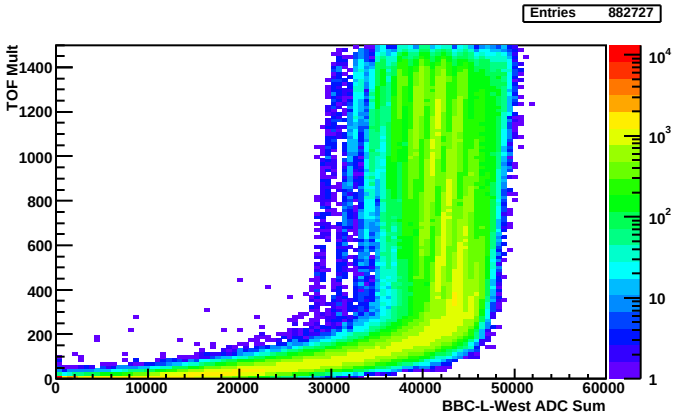

BBQ-VP003 (HI threshold)
Entries 1.412363e+07
ADC
QT Input Channel

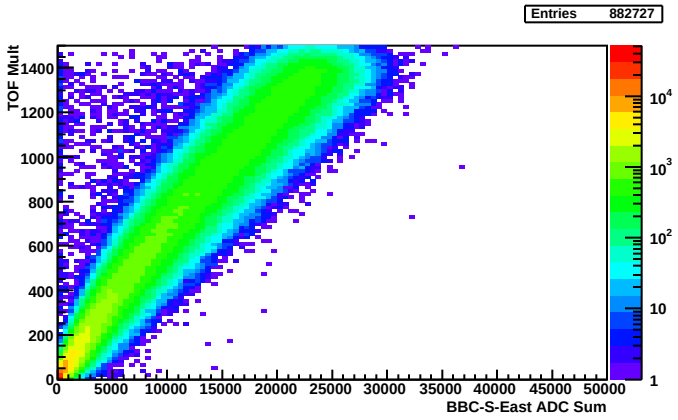

BBQ-VP003 (HI threshold)
Entries 1.412363e+07
TAC
QT Input Channel

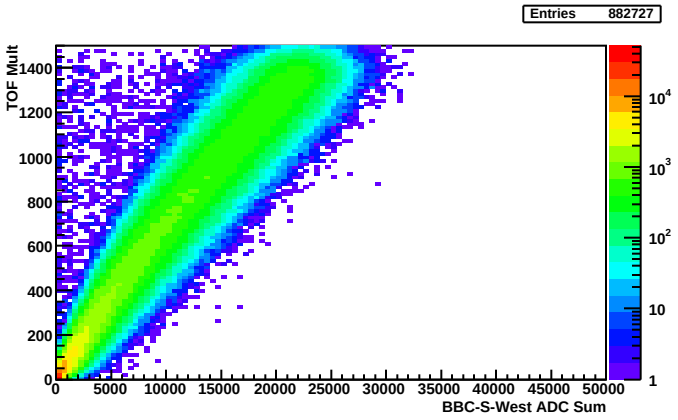

BBQ-VP004 (HI threshold)
Entries 1.412363e+07
ADC
QT Input Channel

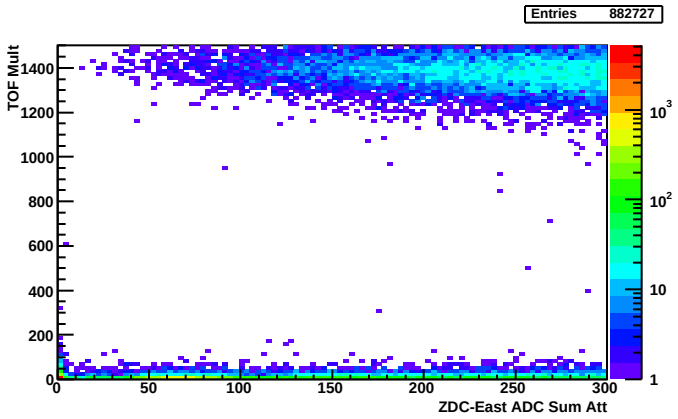

BBQ-VP004 (HI threshold)
Entries 1.412363e+07
TAC
QT Input Channel

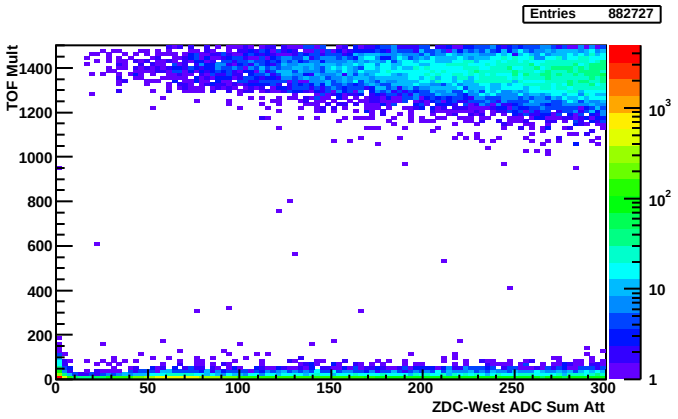

Entries 882727
TOF Mult
BBC-L-East ADC Sum

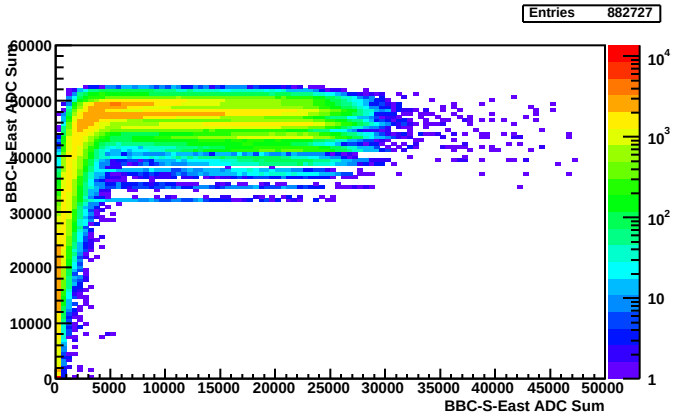

Entries 882727
TOF Mult
BBC-L-West ADC Sum

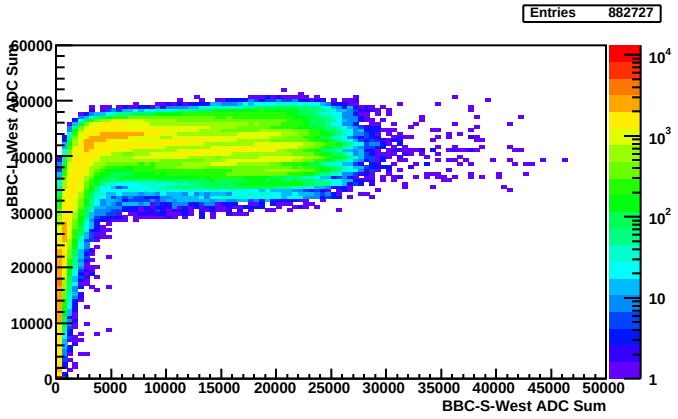

Entries 882727
TOF Mult
BBC-S-East ADC Sum

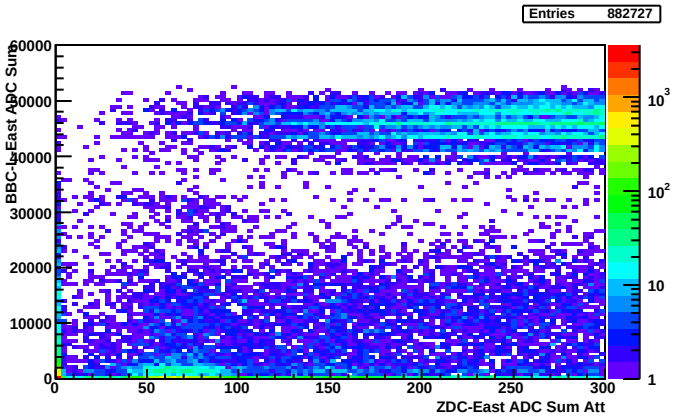

Entries 882727
TOF Mult
BBC-S-West ADC Sum

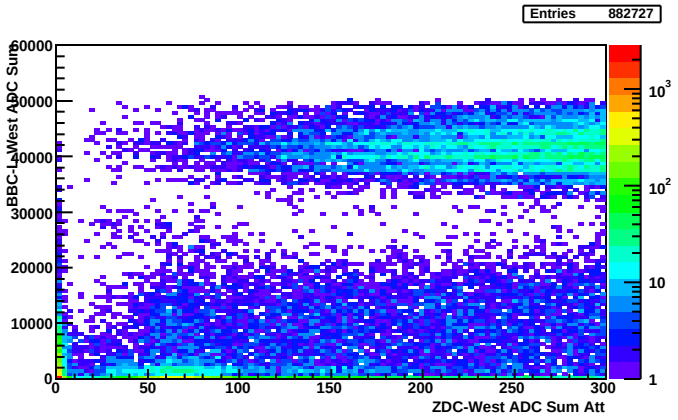

Entries 882727
TOF Mult
ZDC-East ADC Sum Att

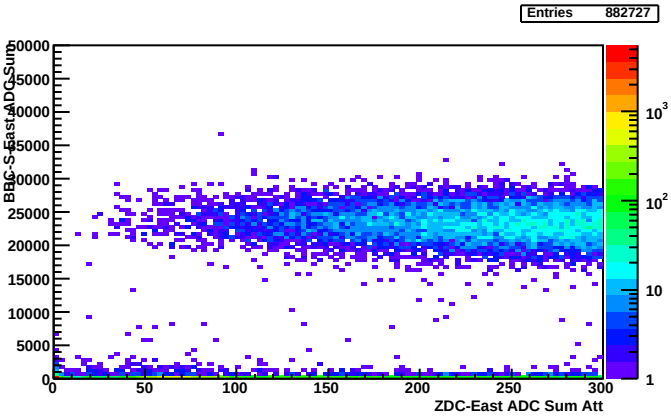

Entries 882727
TOF Mult
ZDC-West ADC Sum Att

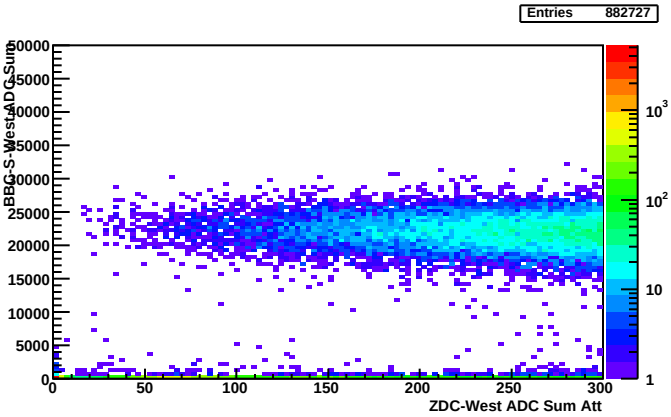

Entries 882727
BBC-L-East ADC Sum
BBC-S-East ADC Sum

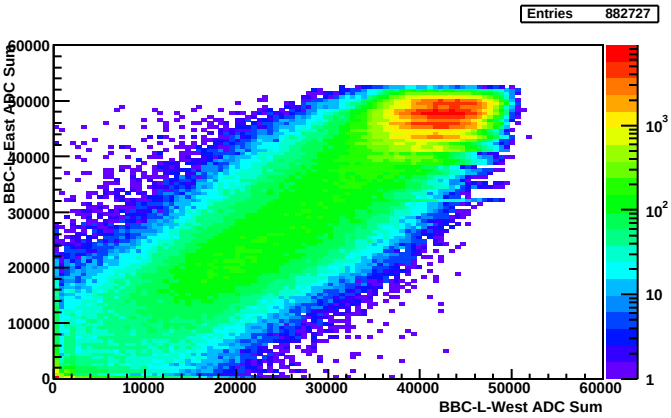

Entries 882727
BBC-L-West ADC Sum
BBC-S-West ADC Sum

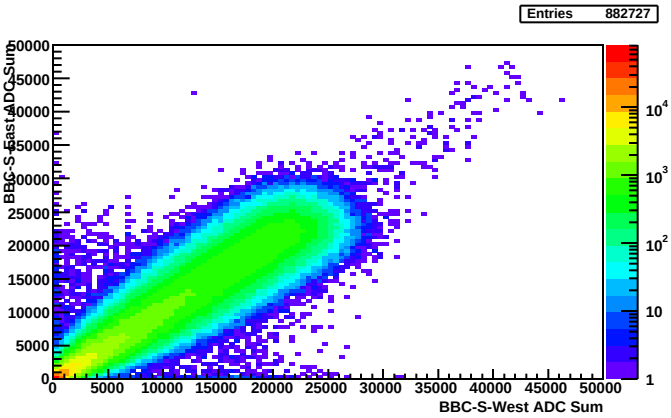

Entries 882727
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

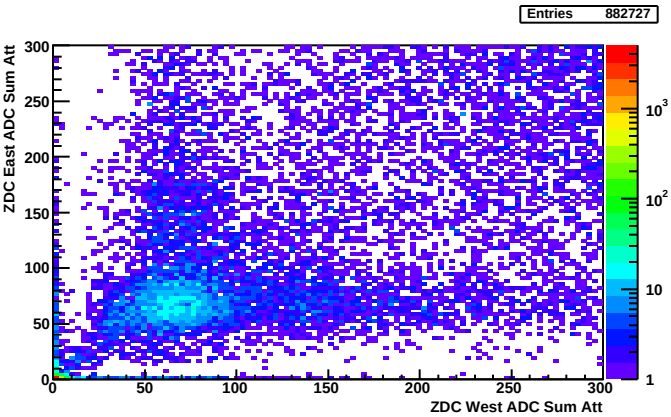

Entries 882727
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

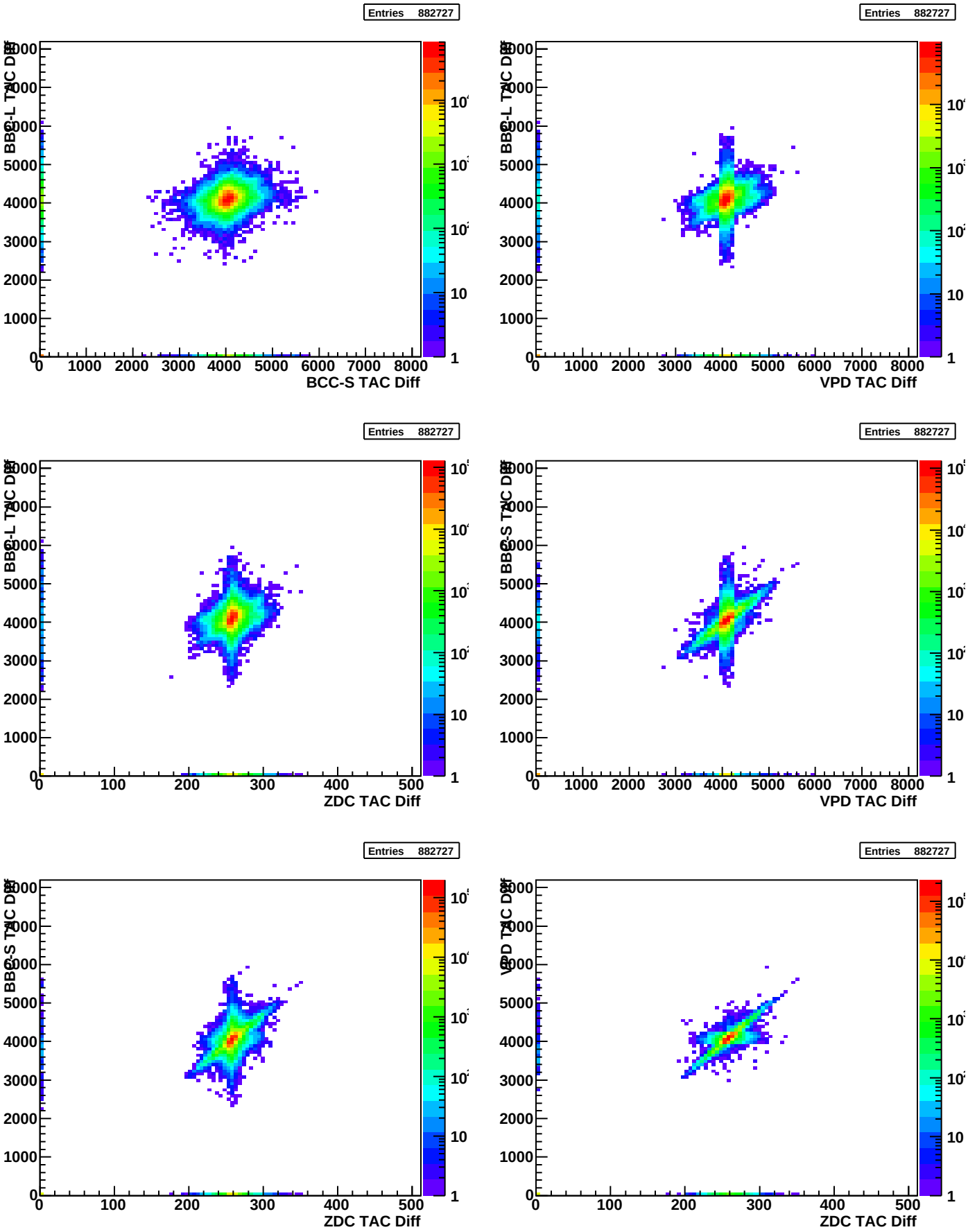

Entries 882727
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

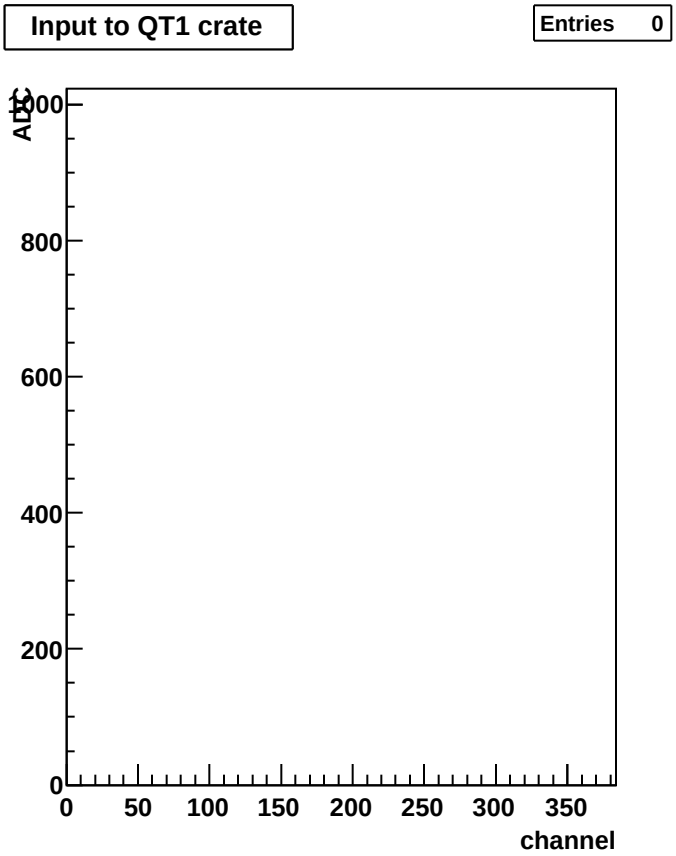

Entries 882727
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

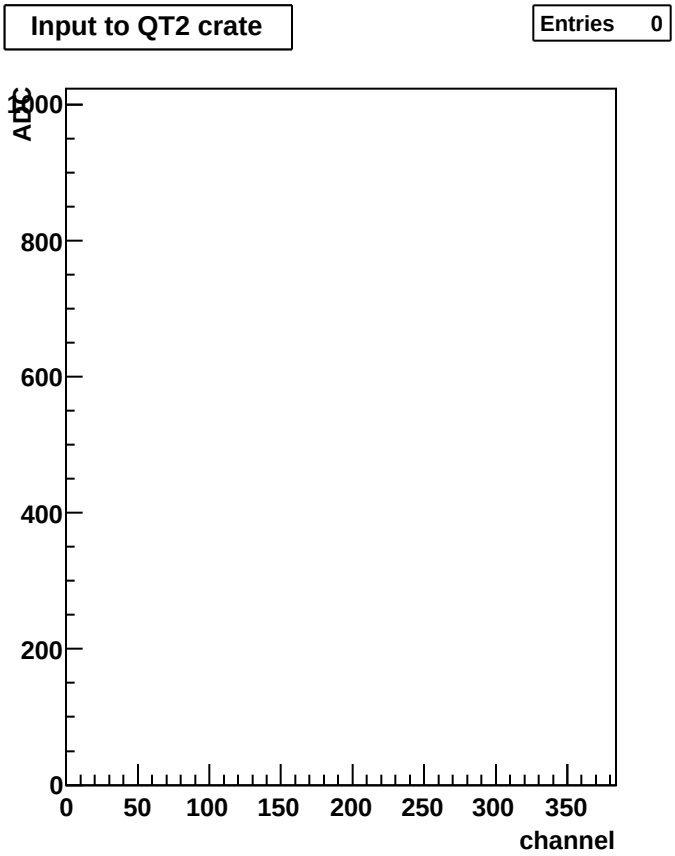

Entries 882727
BBC-L-East ADC Sum
BBC-L-West ADC Sum

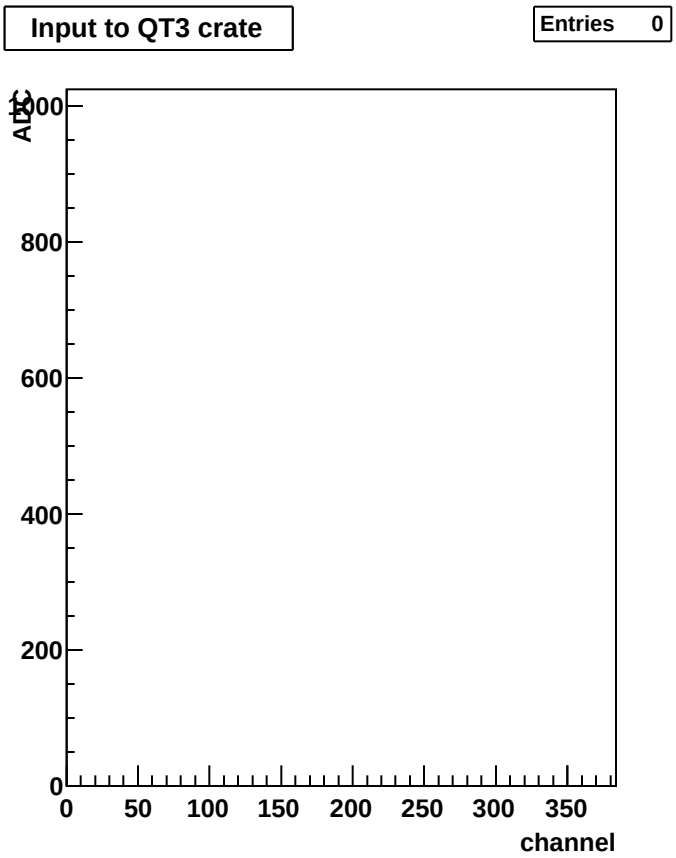

Entries 882727
BBC-S-East ADC Sum
BBC-S-West ADC Sum

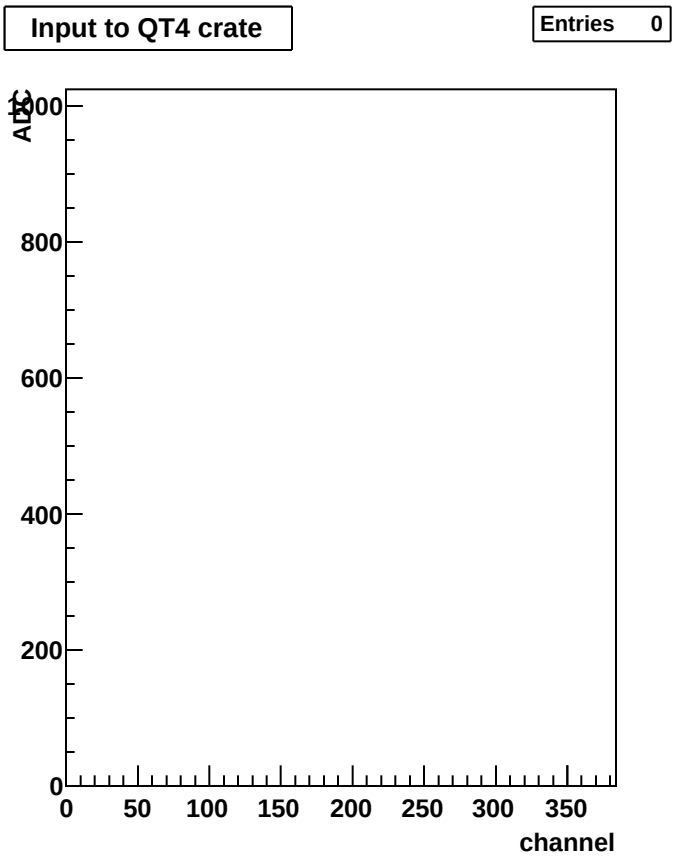

Entries 882727
ZDC East ADC Sum Att
ZDC West ADC Sum Att

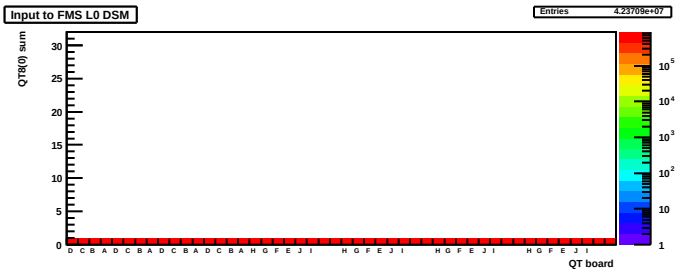

Entries 882727
BBC-L TAC Diff
BCC-S TAC Diff
Entries 882727
BBC-L TAC Diff
VPD TAC Diff
Entries 882727
BBC-L TAC Diff
ZDC TAC Diff
Entries 882727
BBC-S TAC Diff
VPD TAC Diff
Entries 882727
BBC-S TAC Diff
ZDC TAC Diff
Entries 882727
VPD TAC Diff
ZDC TAC Diff

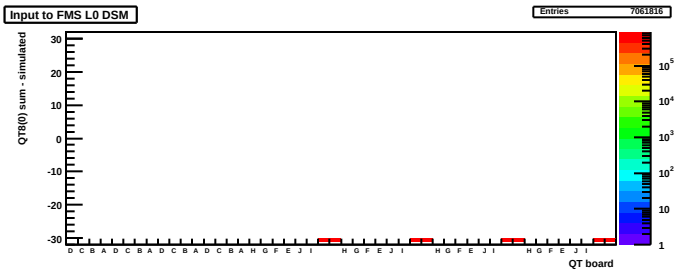

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

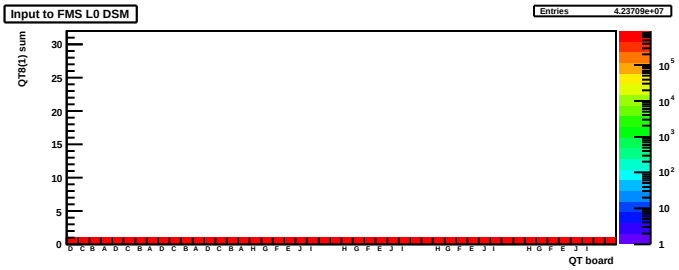

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

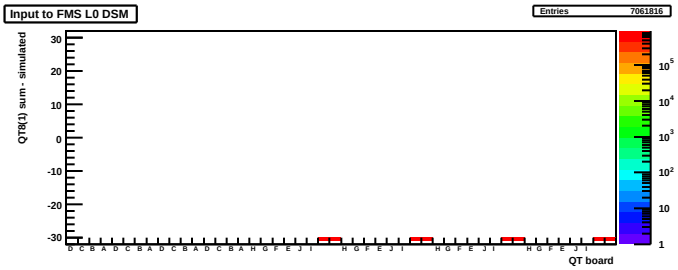

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

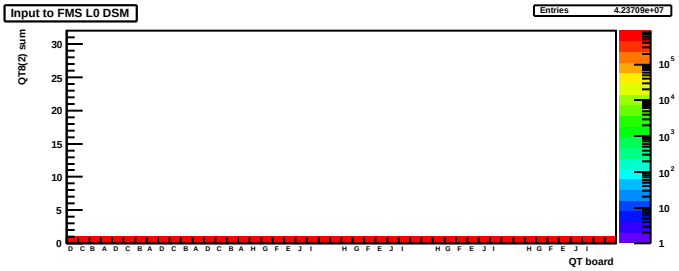

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

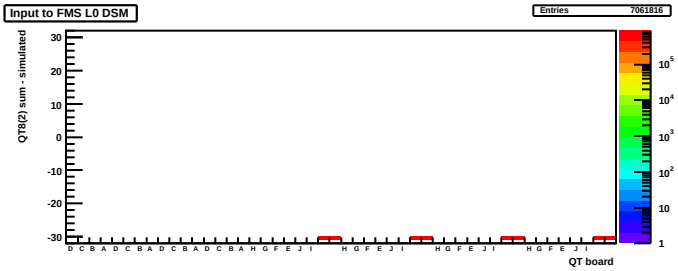

Input to FMS L0 DSM
Entries 4.23709e+07
QT8(0) sum
QT board

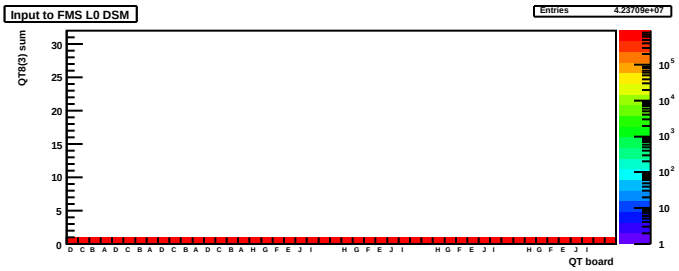

Input to FMS L0 DSM
Entries 7061816
QT8(0) sum - simulated
QT board

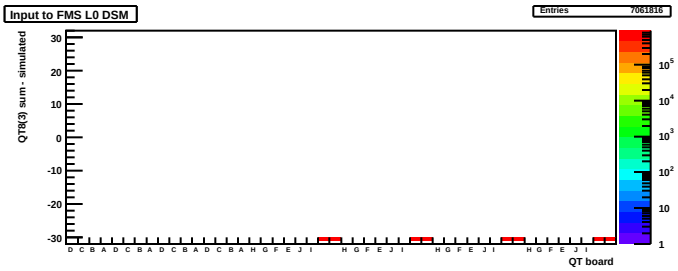

Input to FMS L0 DSM
Entries 4.23709e+07
QT8(1) sum
QT board

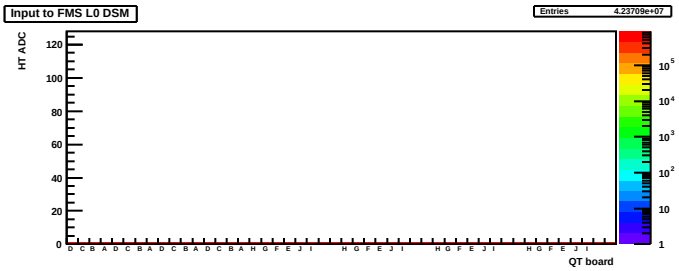

Input to FMS L0 DSM
Entries 7061816
QT8(1) sum - simulated
QT board

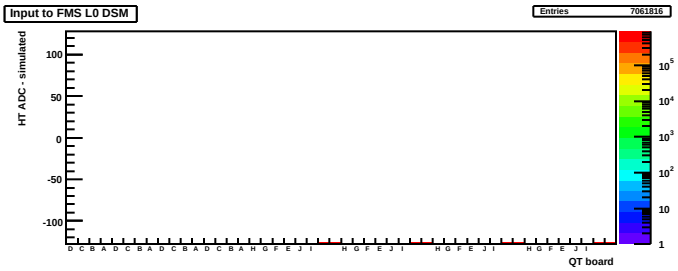

Input to FMS L0 DSM
Entries 4.23709e+07
QT8(2) sum
QT board

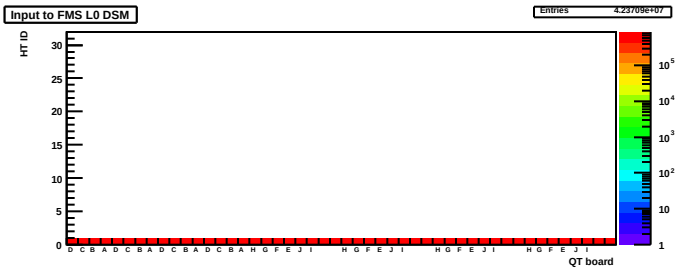

Input to FMS L0 DSM
Entries 7061816
QT8(2) sum - simulated
QT board

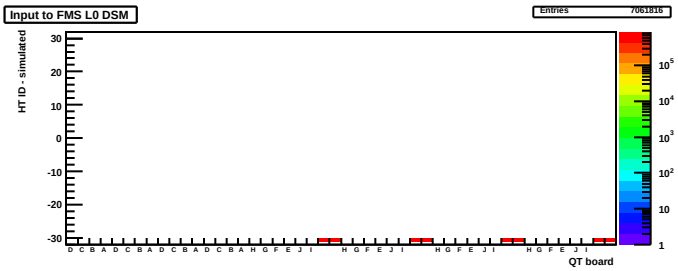

Input to FMS L0 DSM
Entries 4.23709e+07
QT8(3) sum
QT board

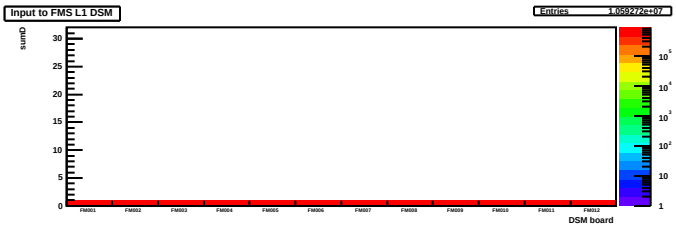

Input to FMS L0 DSM
Entries 7061816
QT8(3) sum - simulated
QT board

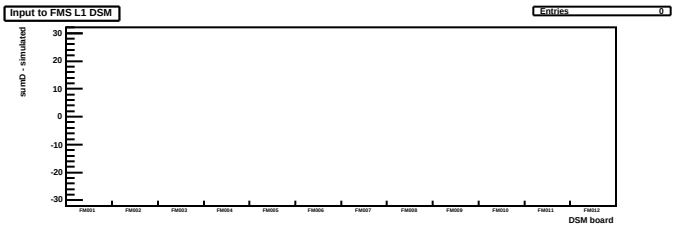

Input to FMS L0 DSM
Entries 4.23709e+07
HT ADC
QT board

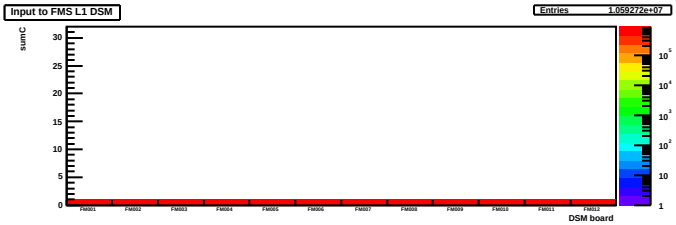

Input to FMS L0 DSM
Entries 7061816
HT ADC - simulated
QT board

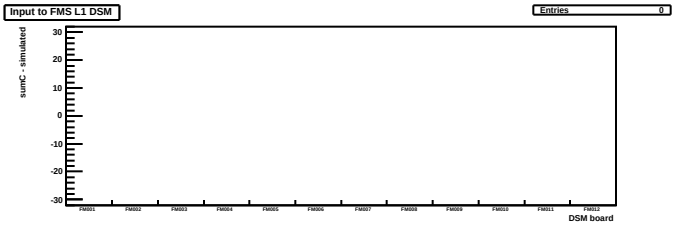

Input to FMS L0 DSM
Entries 4.23709e+07
HT ID
QT board

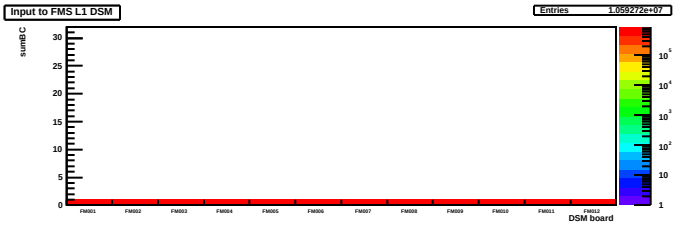

Input to FMS L0 DSM
Entries 7061816
HT ID - simulated
QT board

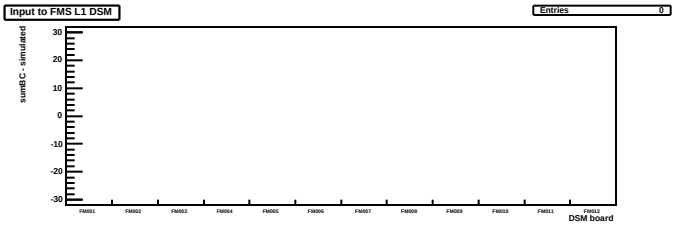

Input to FMS L1 DSM
Entries 1.059272e+07
sumD
DSM board

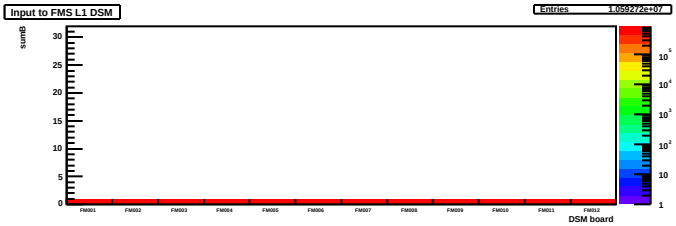

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

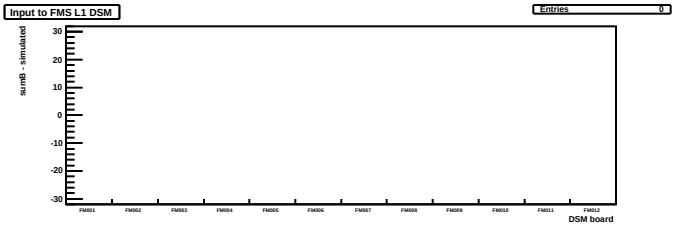

Input to FMS L1 DSM
Entries 1.059272e+07
sumC
DSM board

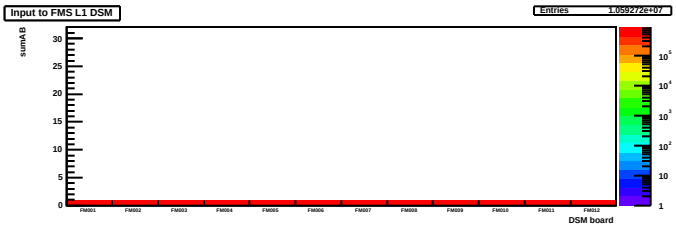

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

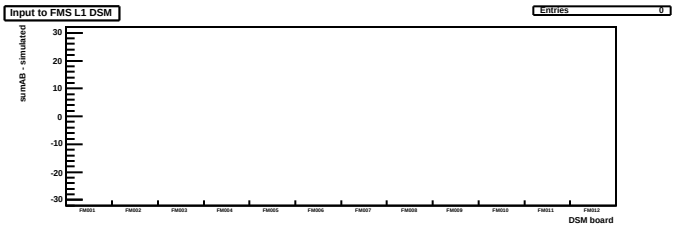

Input to FMS L1 DSM
Entries 1.059272e+07
sumBC
DSM board

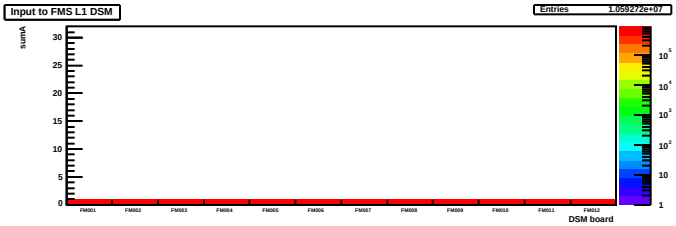

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

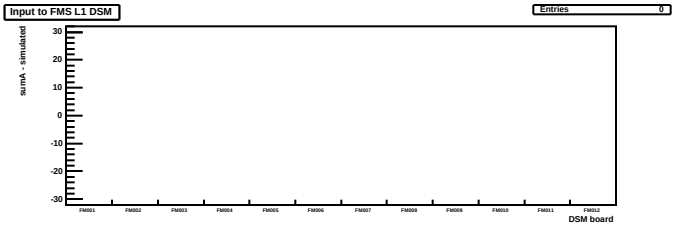

Input to FMS L1 DSM
Entries 1.059272e+07
sumB
DSM board

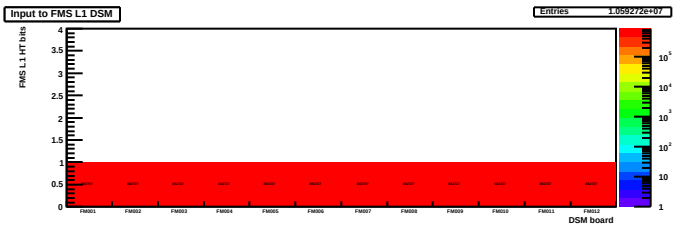

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

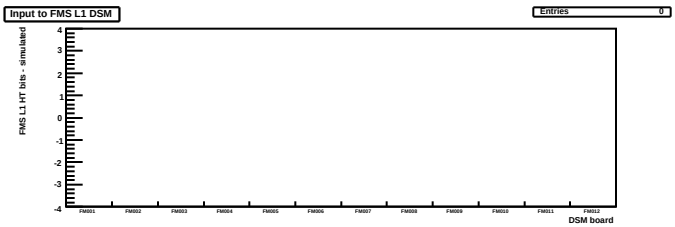

Input to FMS L1 DSM
Entries 1.059272e+07
sumAB
DSM board

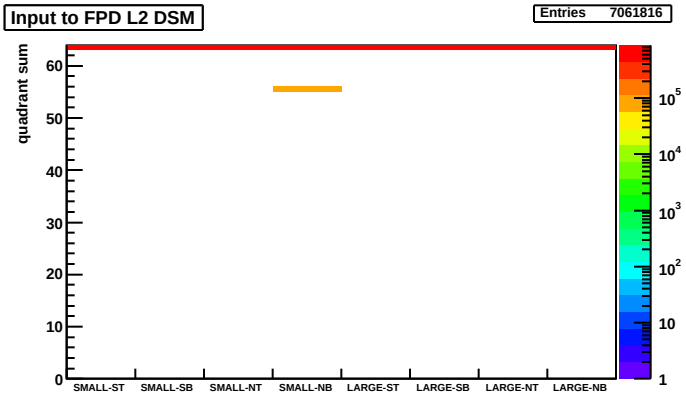

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

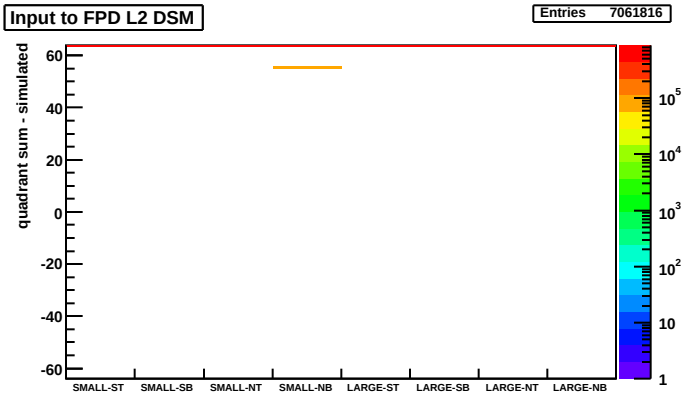

Input to FMS L1 DSM
Entries 1.059272e+07
sumA
DSM board

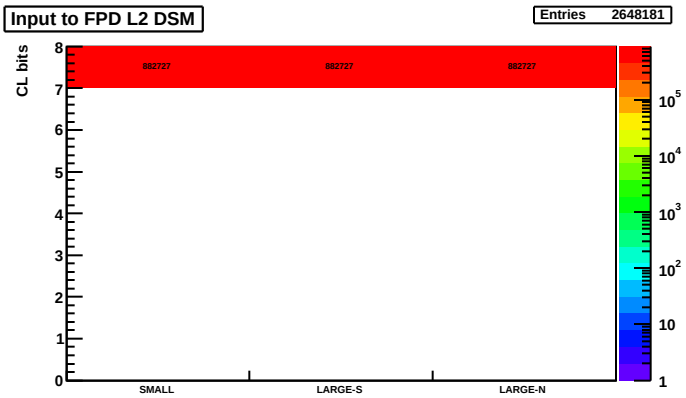

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

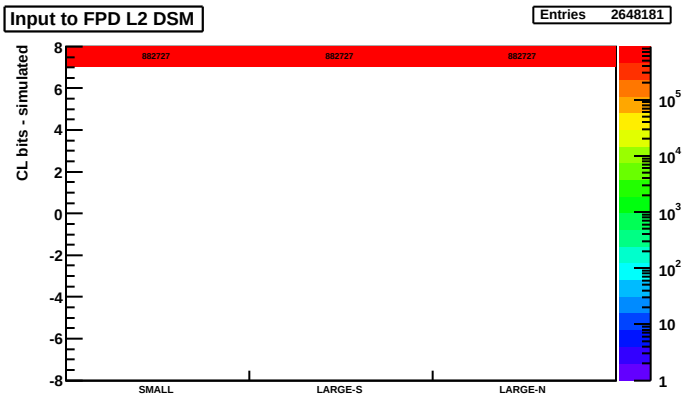

Input to FMS L1 DSM
Entries 1.059272e+07
FMS L1 HT bits
DSM board

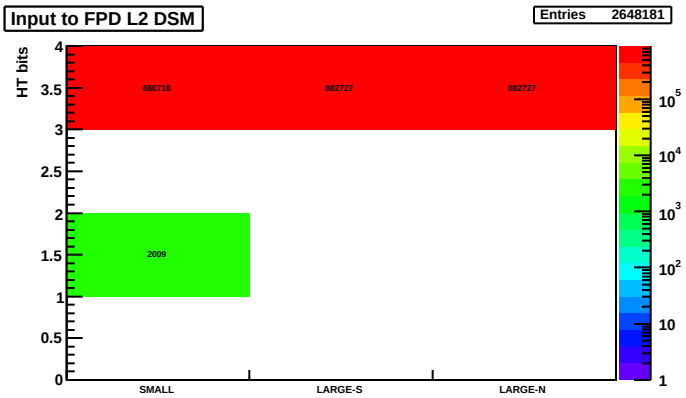

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

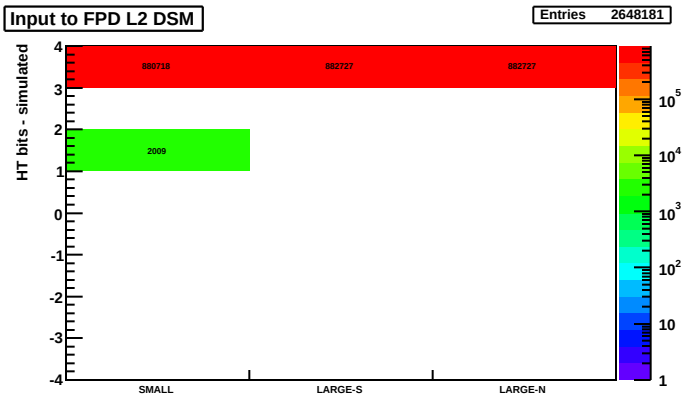

Input to FPD L2 DSM
Entries 7061816
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

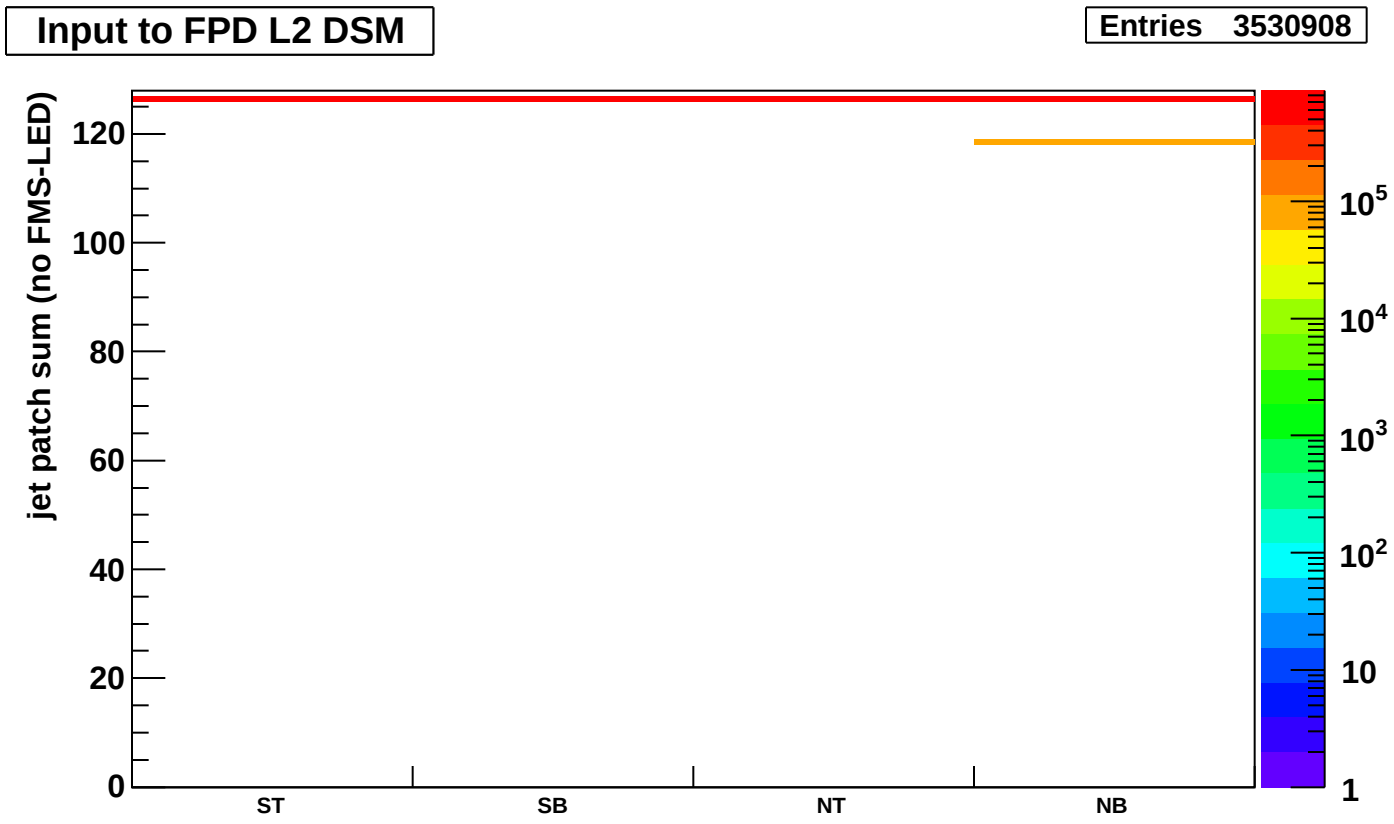

Input to FPD L2 DSM
Entries 7061816
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

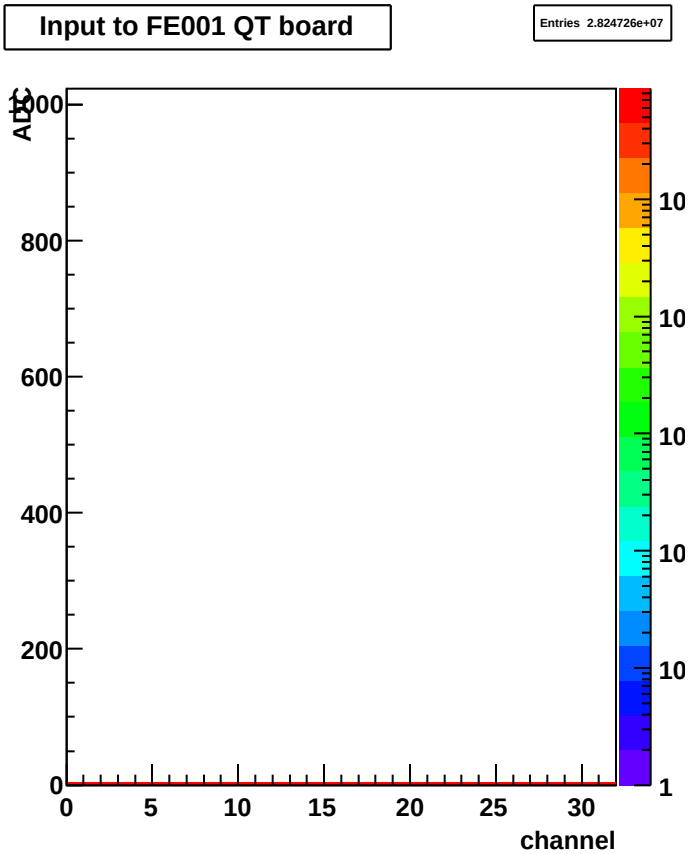

Input to FPD L2 DSM
Entries 2648181
CL bits
882727
882727
882727
SMALL
LARGE-S
LARGE-N

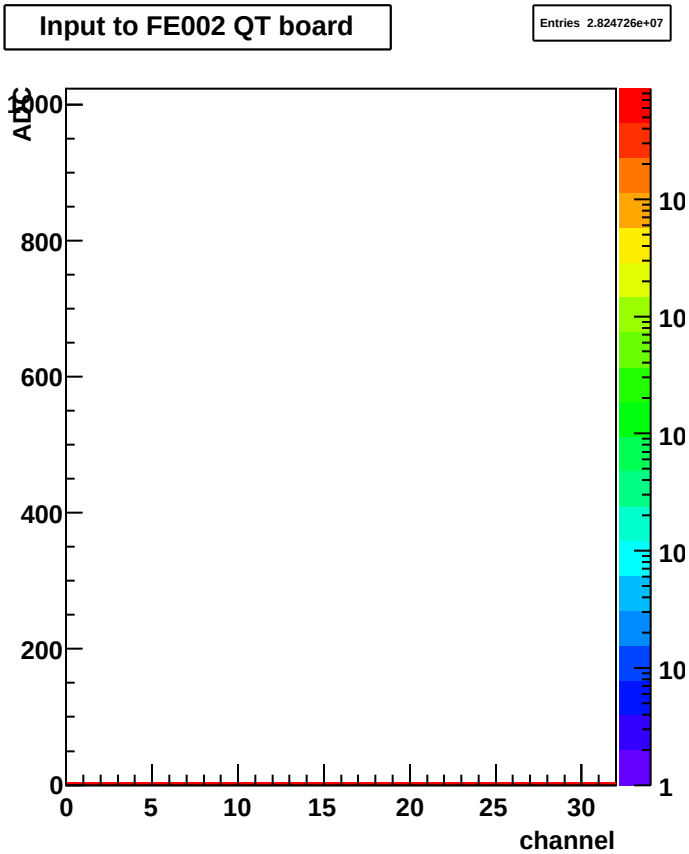

Input to FPD L2 DSM
Entries 2648181
CL bits - simulated
882727
882727
882727
SMALL
LARGE-S
LARGE-N

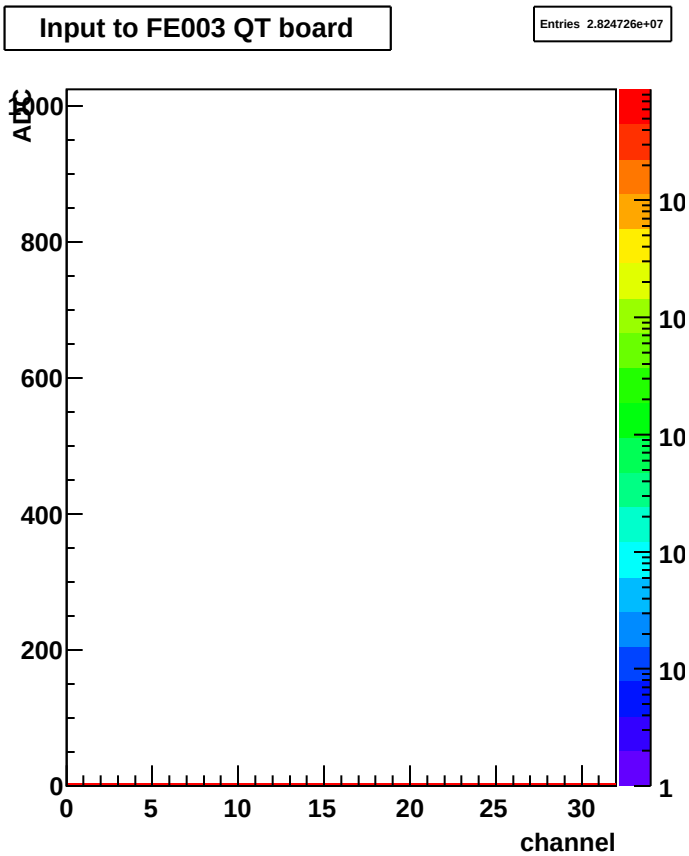

Input to FPD L2 DSM
Entries 2648181
HT bits
880718
882727
882727
2009
SMALL
LARGE-S
LARGE-N

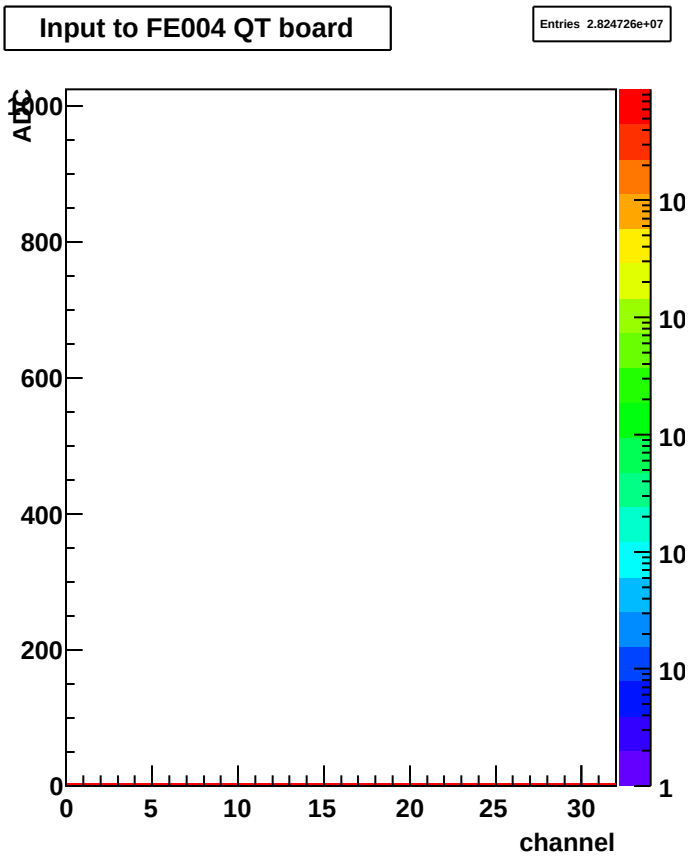

Input to FPD L2 DSM
Entries 2648181
HT bits - simulated
880718
882727
882727
2009
SMALL
LARGE-S
LARGE-N

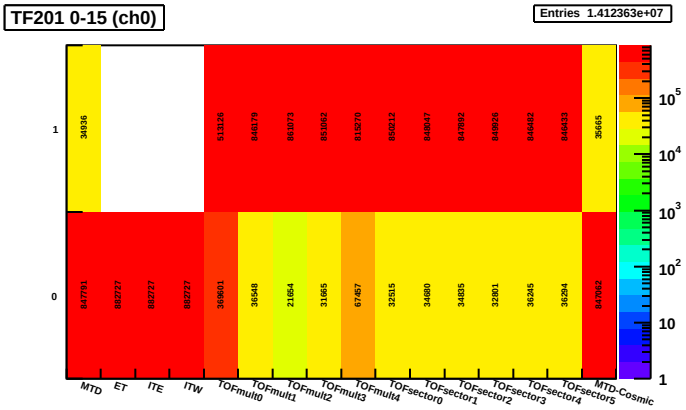

Input to FPD L2 DSM
Entries 3530908
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

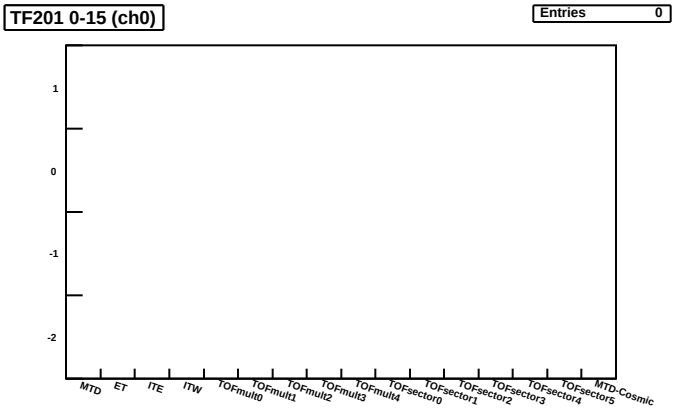

Input to FE001 QT board
Entries 2.824726e+07
ADC
channel

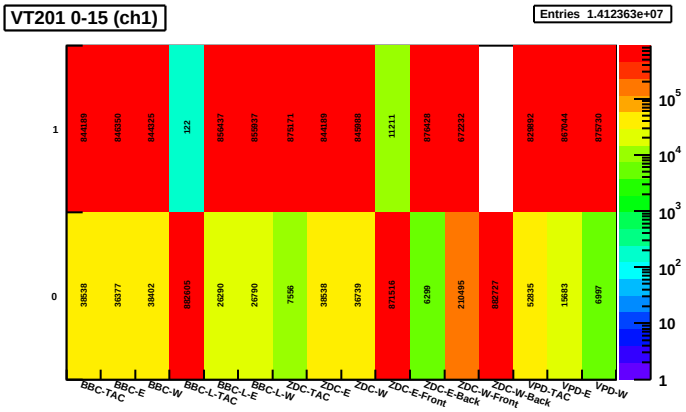

Input to FE002 QT board
Entries 2.824726e+07
ADC
channel

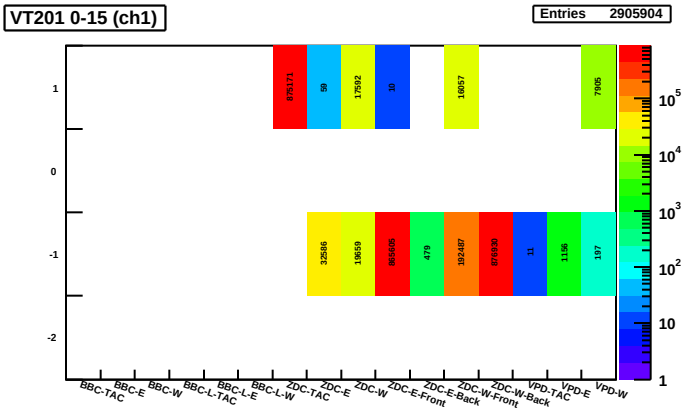

Input to FE003 QT board
Entries 2.824726e+07
ADC
channel

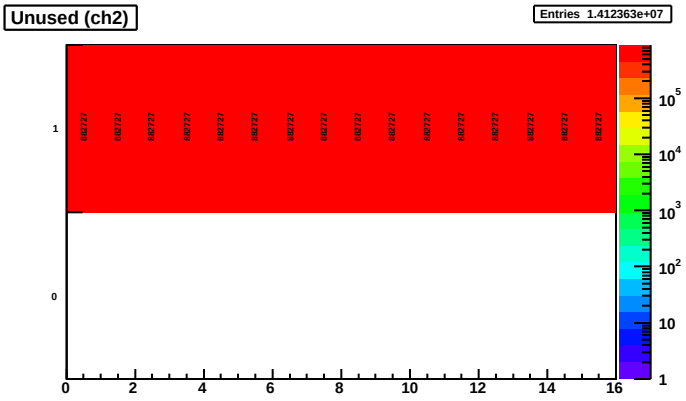

Input to FE004 QT board
Entries 2.824726e+07
ADC
channel

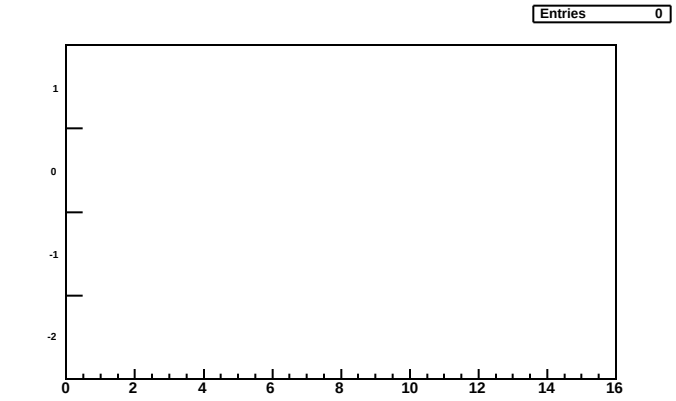

TF201 0-15 (ch0)
Entries 1.412363e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

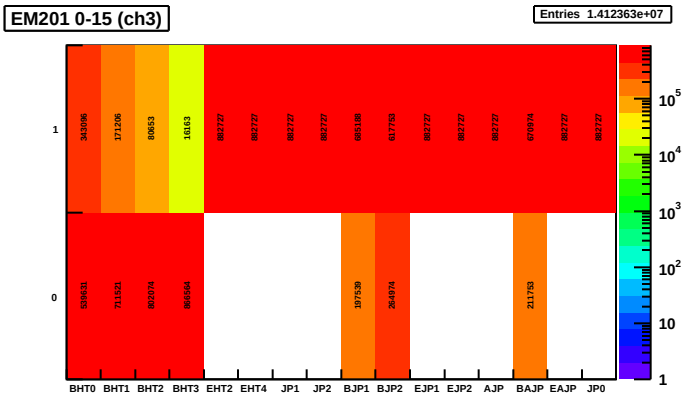

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

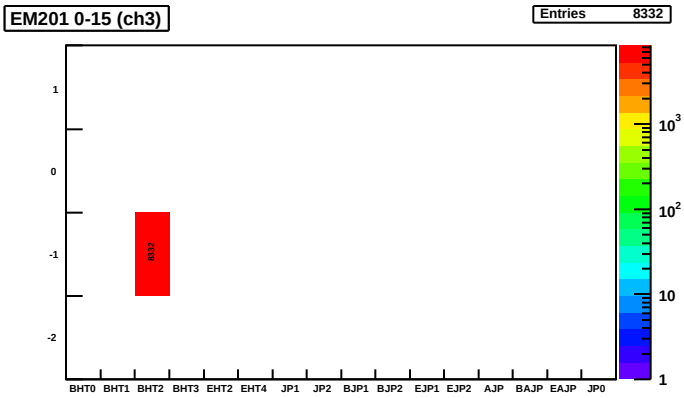

VT201 0-15 (ch1)
Entries 1.412363e+07
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 2905904
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

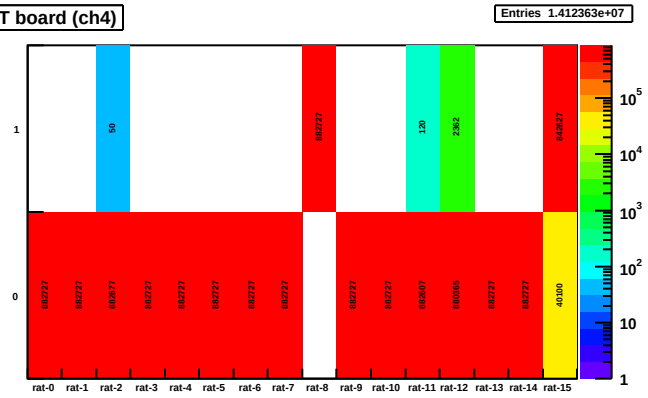

Unused (ch2)
Entries 1.412363e+07

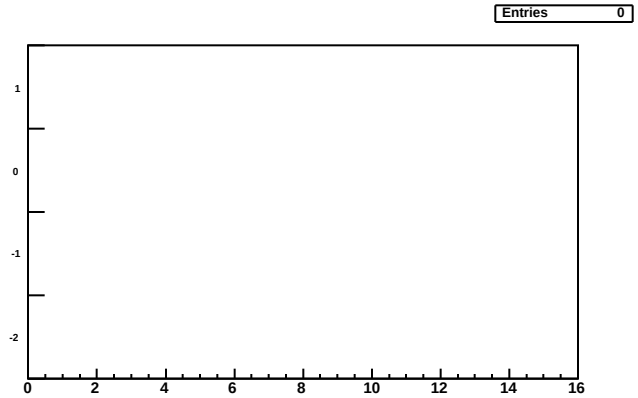

Entries 0


EM201 0-15 (ch3)
Entries 1.412363e+07
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

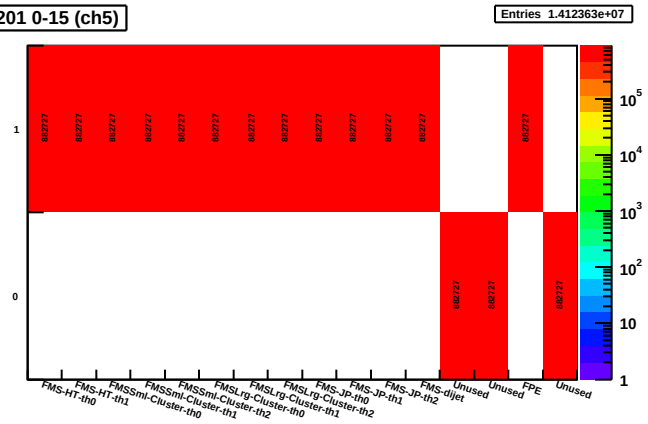

EM201 0-15 (ch3)
Entries 8332
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

RAT board (ch4)

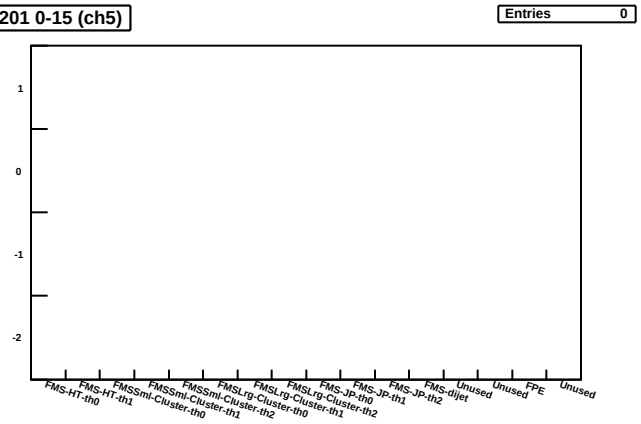




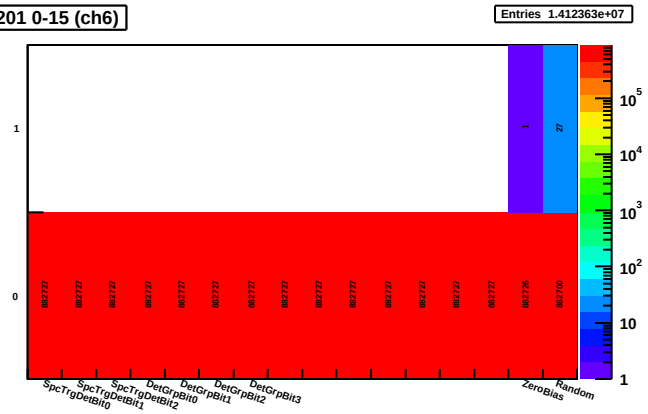
FP201 0-15 (ch5)

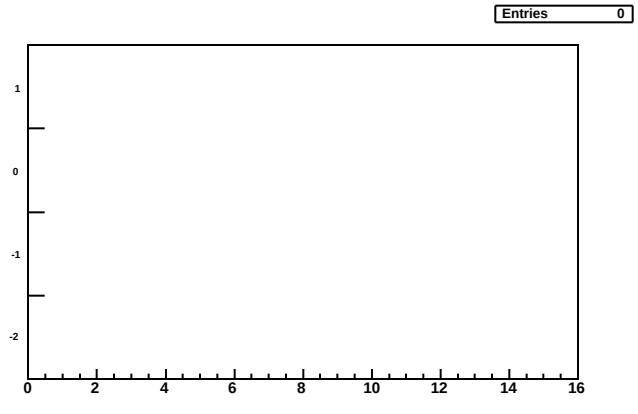
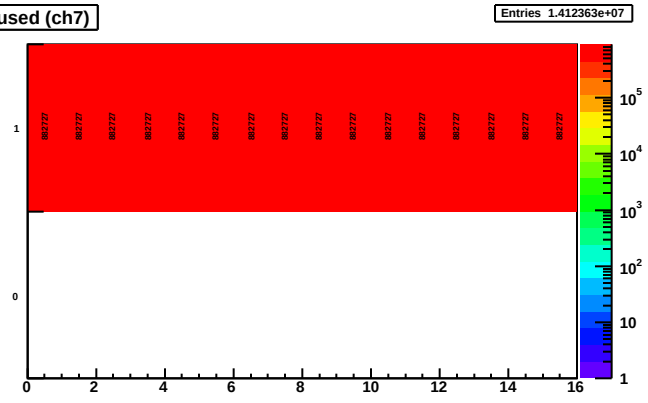
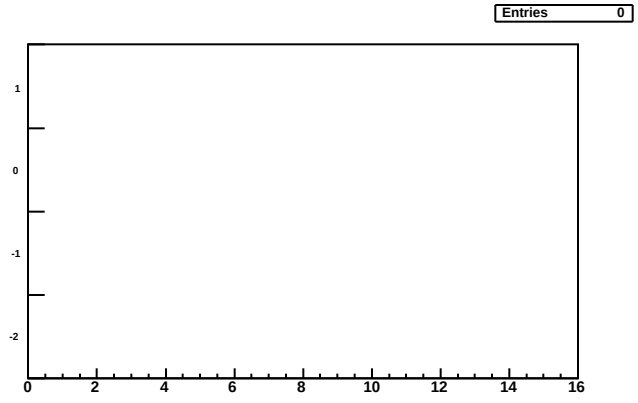


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)