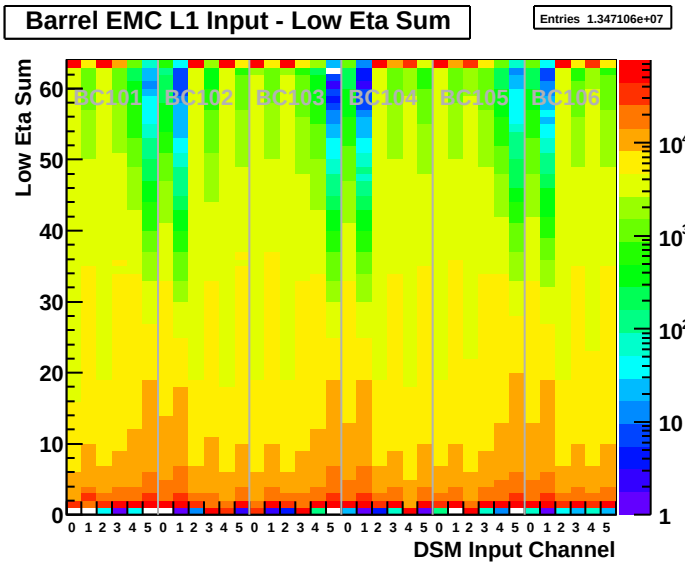
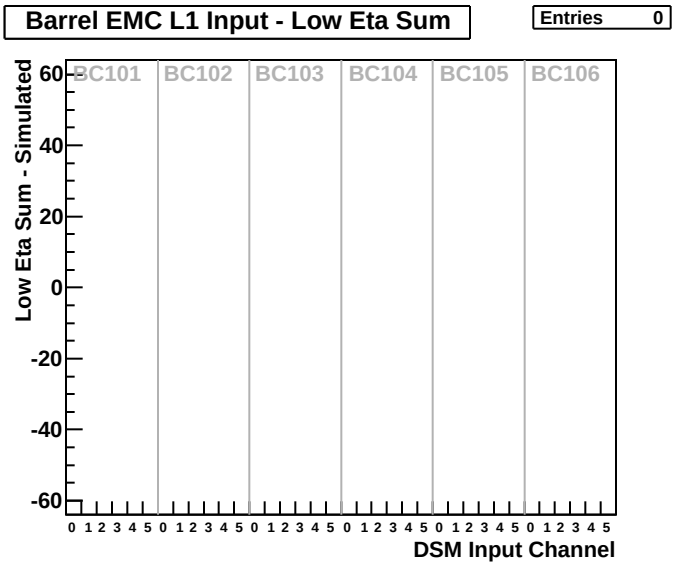
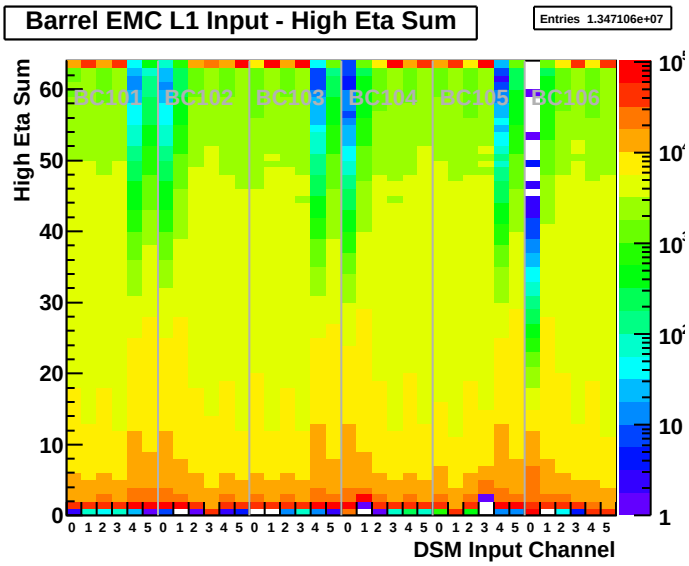
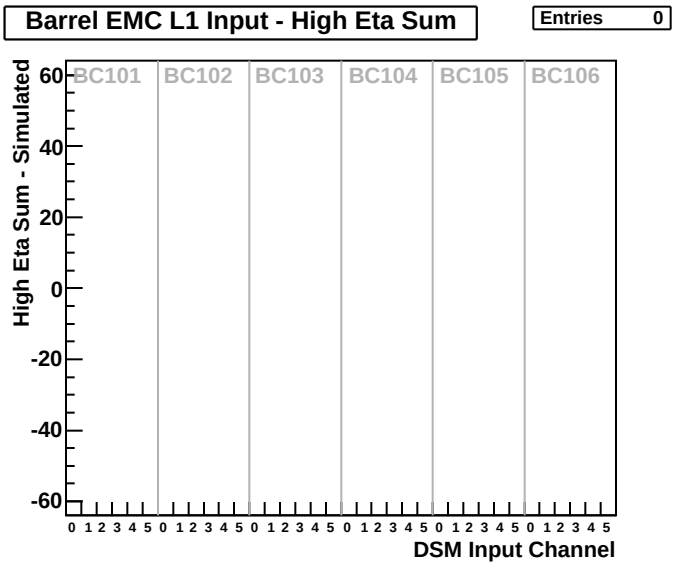
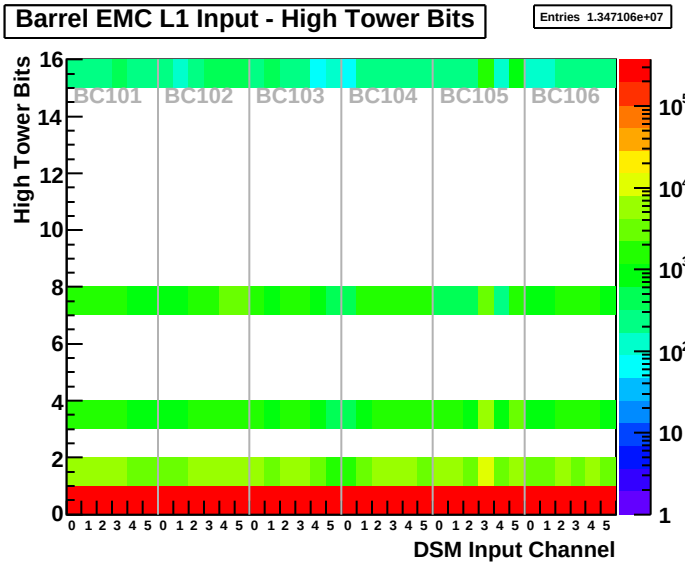
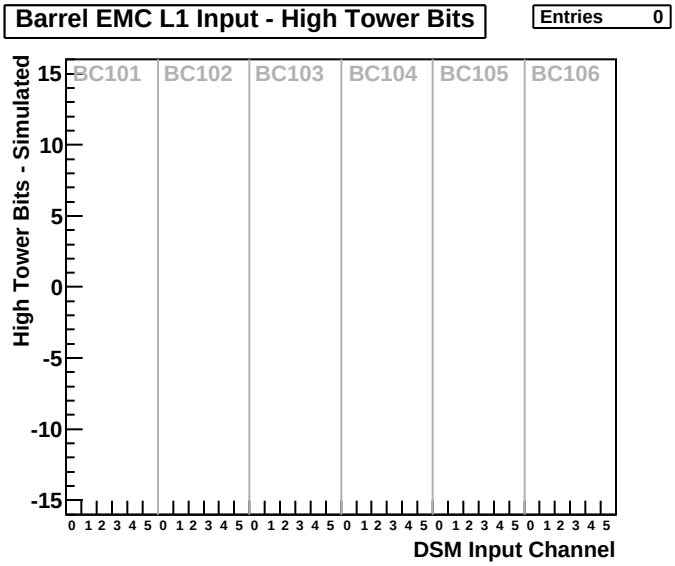

Barrel EMC L1 Input - Low Eta Sum
Entries 1.347106e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

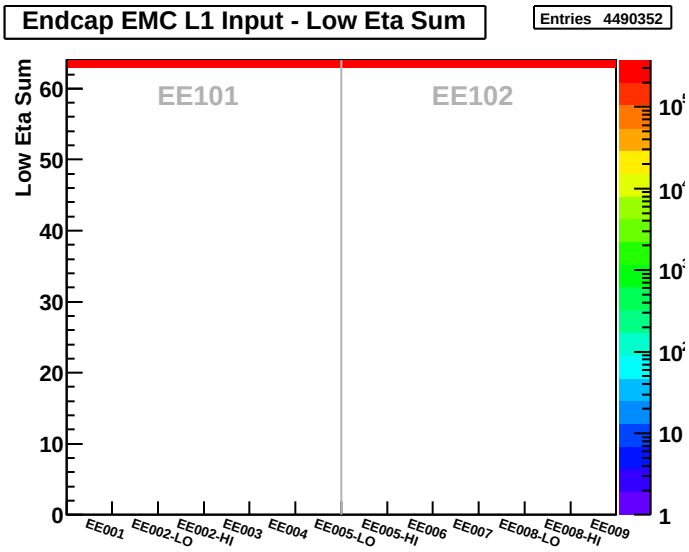

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

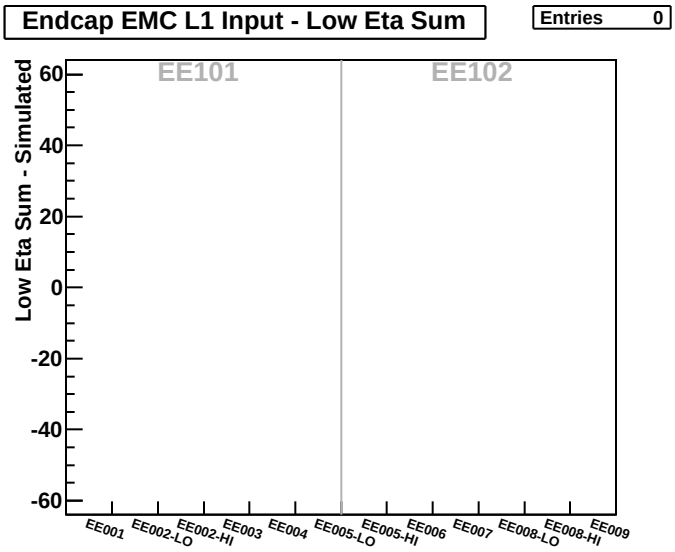

Barrel EMC L1 Input - High Eta Sum
Entries 1.347106e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

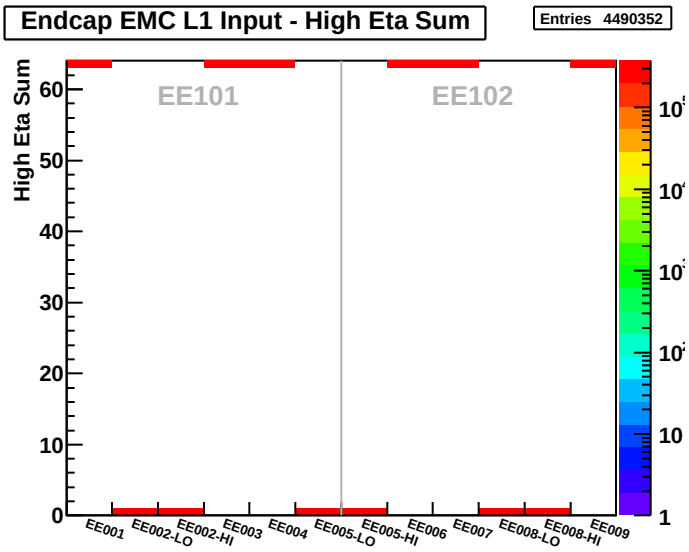

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

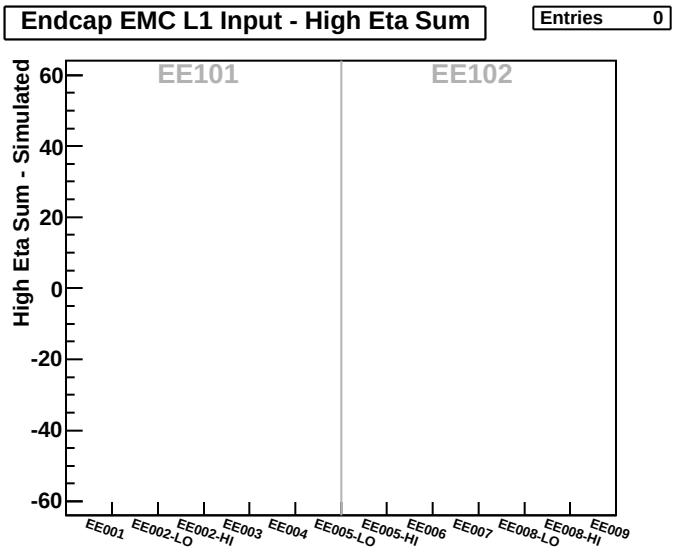

Barrel EMC L1 Input - High Tower Bits
Entries 1.347106e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

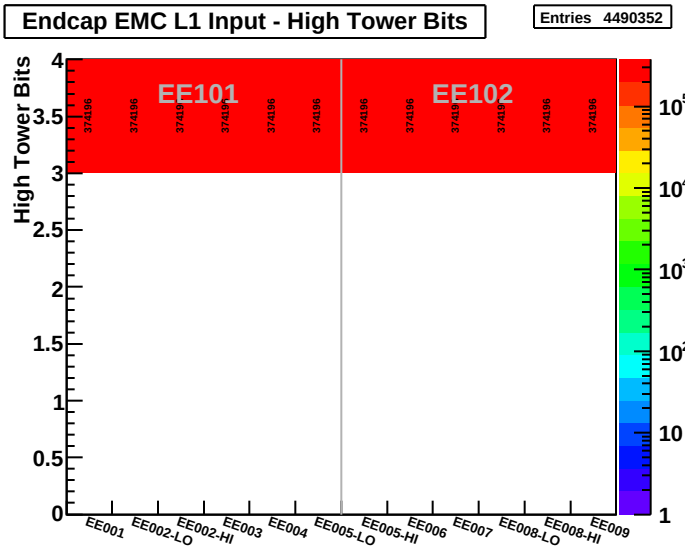

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

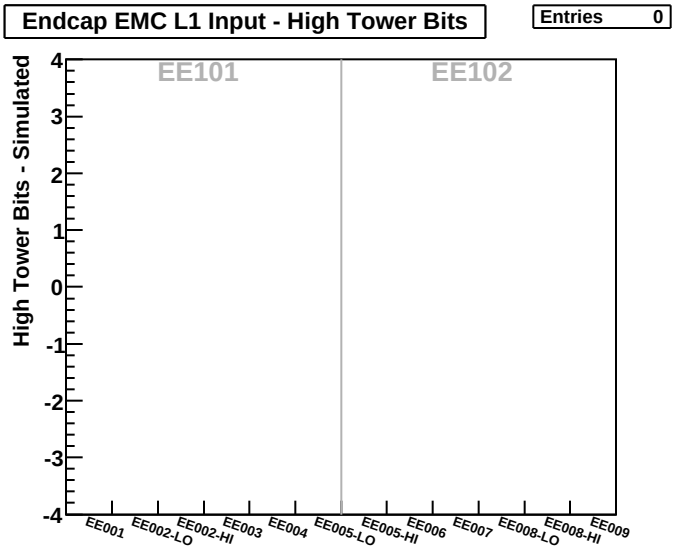

Endcap EMC L1 Input - Low Eta Sum
Entries 4490352
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

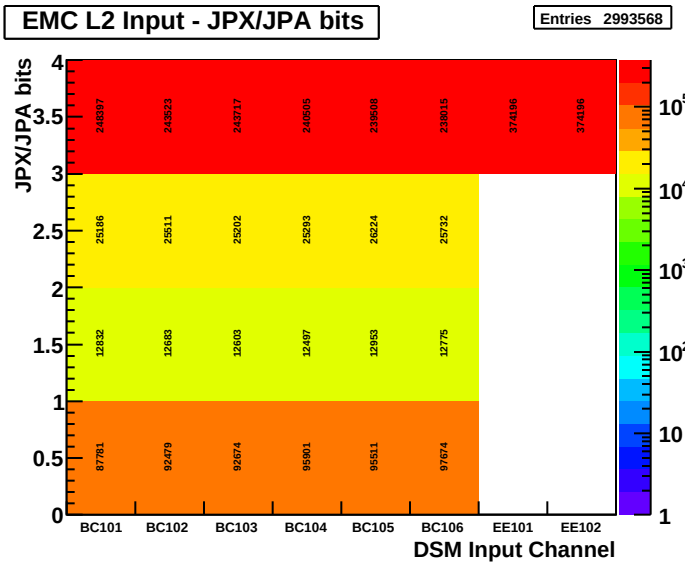

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

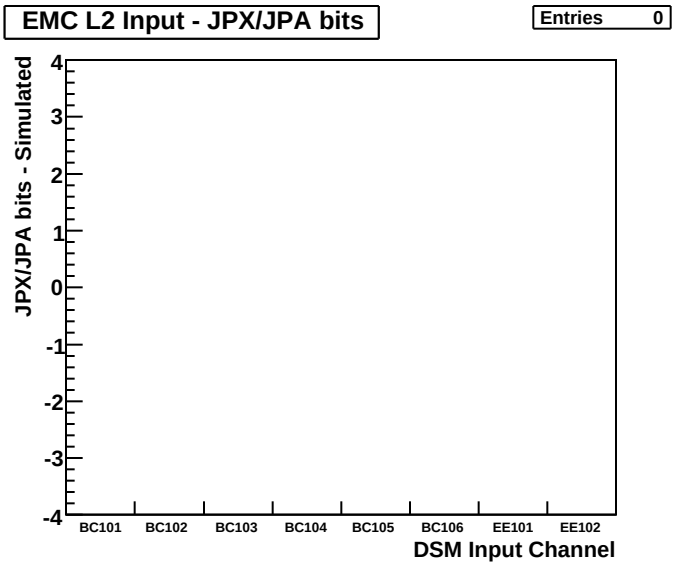

Endcap EMC L1 Input - High Eta Sum
Entries 4490352
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

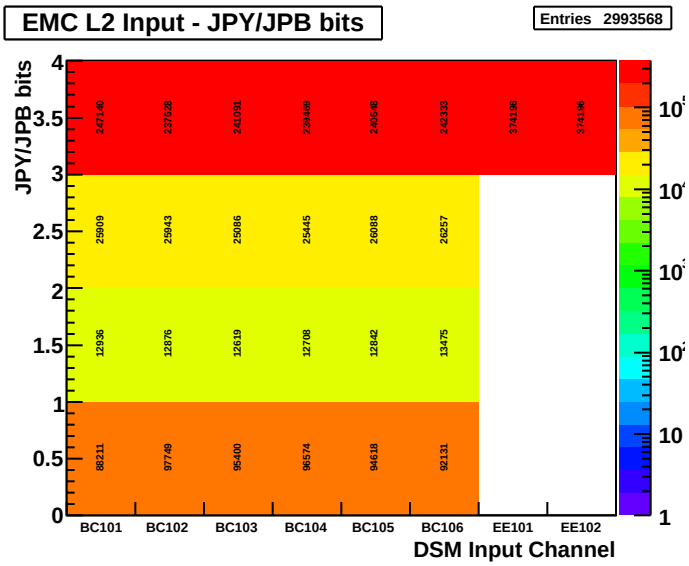

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

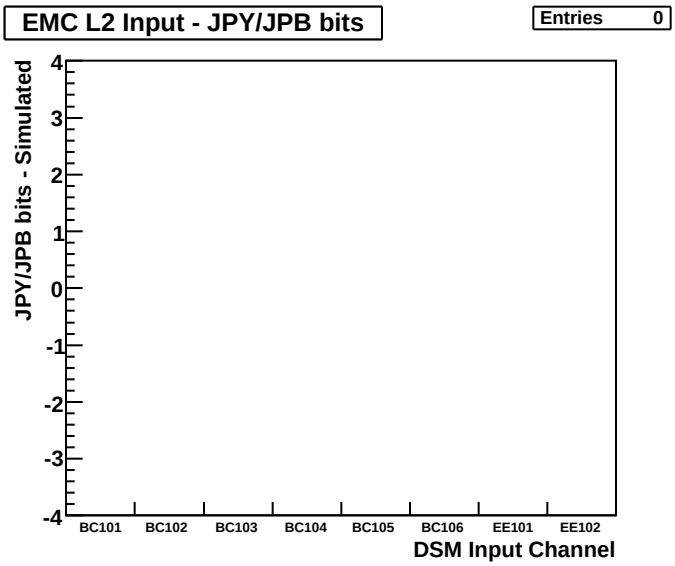

Endcap EMC L1 Input - High Tower Bits
Entries 4490352
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

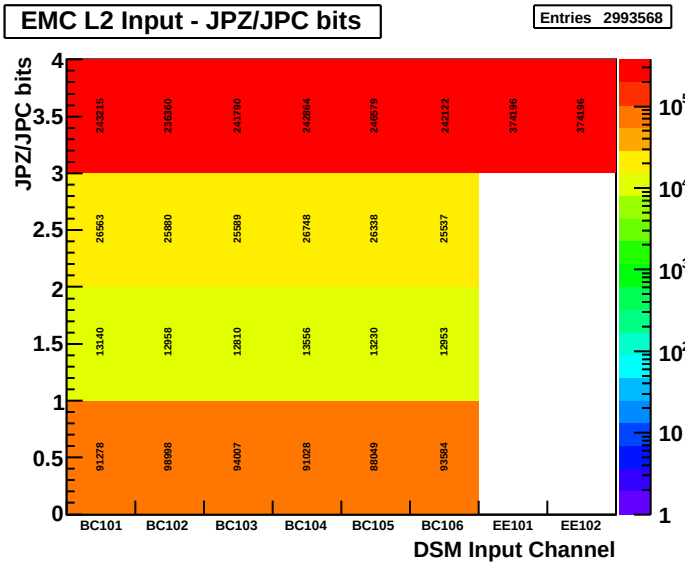

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

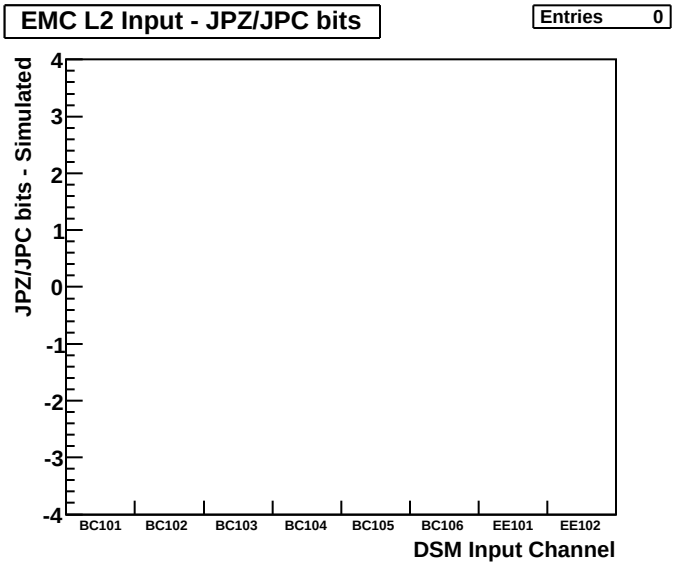

EMC L2 Input - JPX/JPA bits
Entries 2993568
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
248397 243823 243717 240505 239508 238015 374196 374196
25186 25511 25202 25293 26224 25732
12832 12663 12603 12497 12953 12775
87781 92479 92674 95901 95511 97674

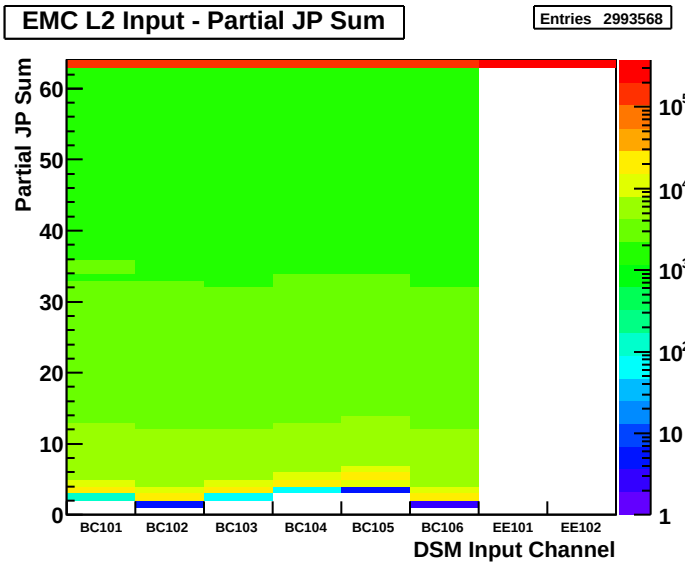

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

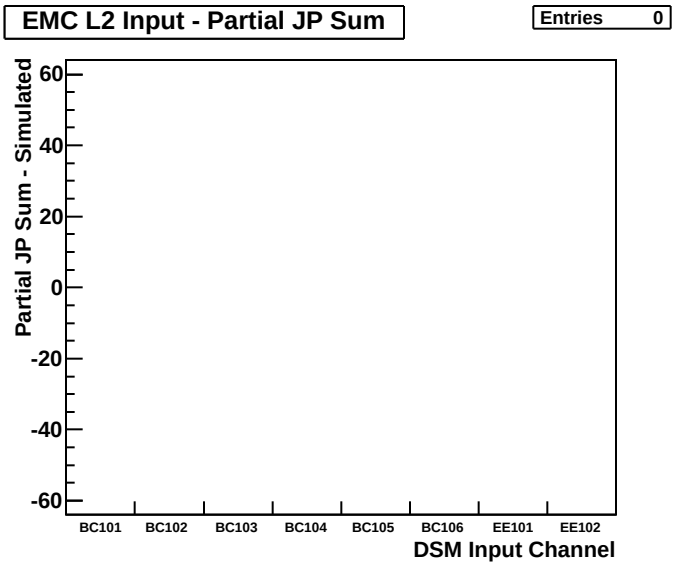

EMC L2 Input - JPY/JPB bits
Entries 2993568
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
247140 237628 241021 239469 240648 242333 374196 374196
25909 25943 25086 25445 26088 26257
12936 12876 12619 12708 12842 13475
88211 97749 95400 96574 94618 92131

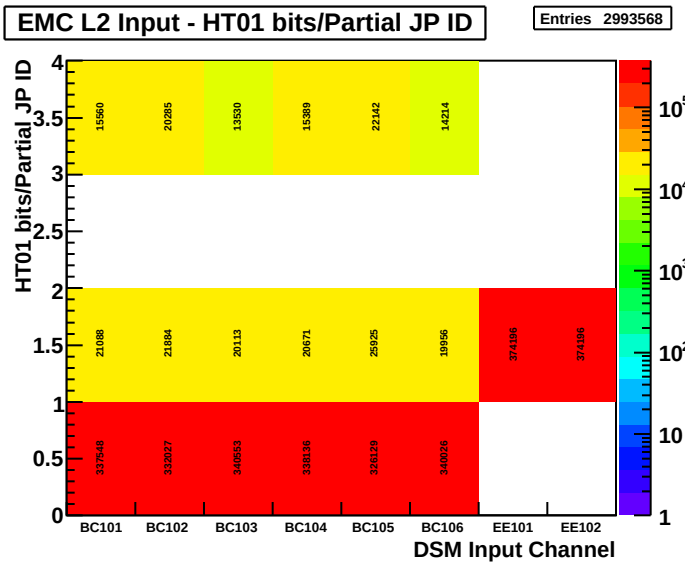

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

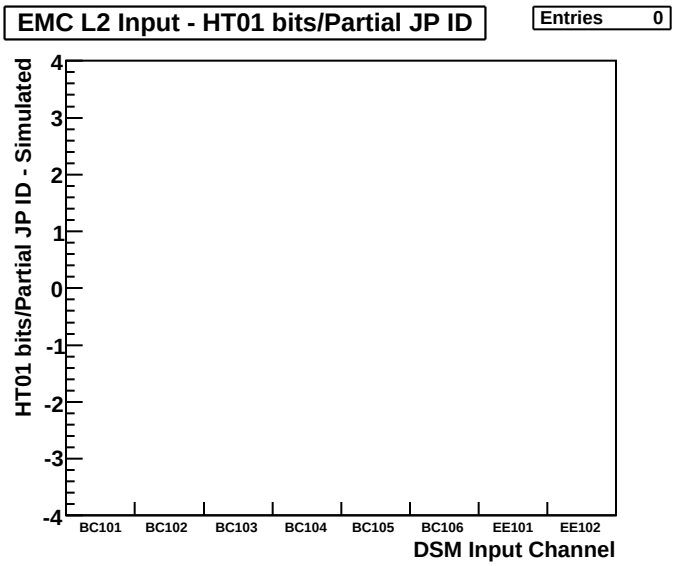

EMC L2 Input - JPZ/JPC bits
Entries 2993568
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
243215 236360 241790 242864 246579 242122 374196 374196
26563 25880 25589 26748 26338 25537
13140 12958 12810 13556 13230 12953
91278 98898 94007 91028 88049 93584

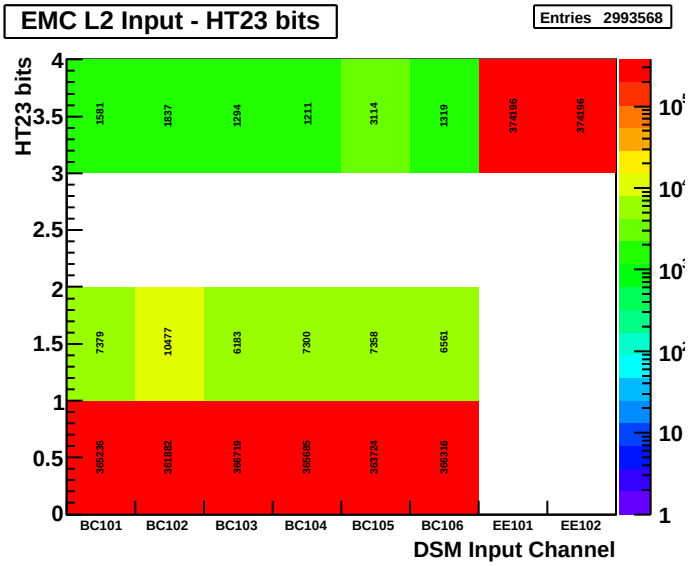

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

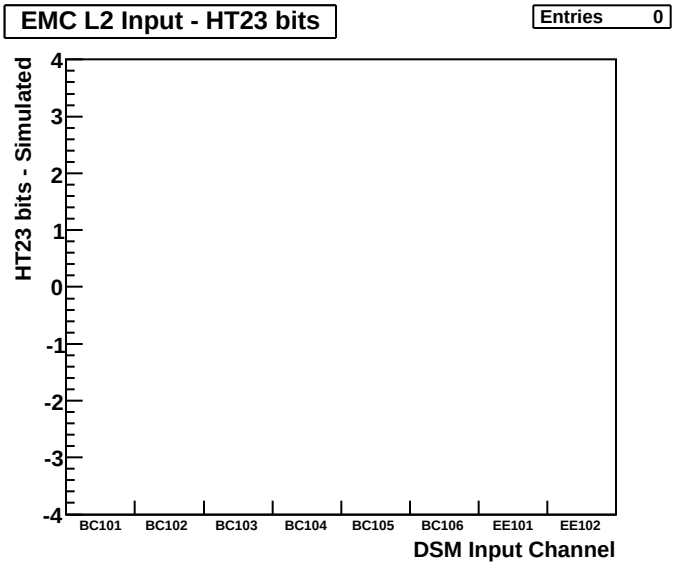

EMC L2 Input - Partial JP Sum
Entries 2993568
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

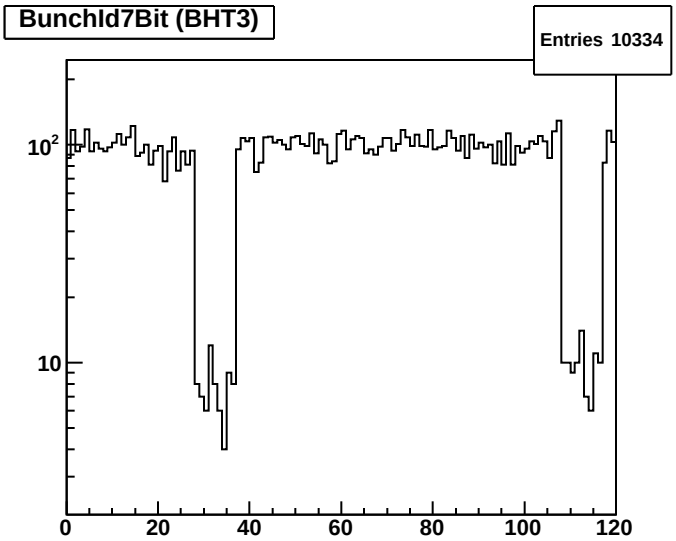

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

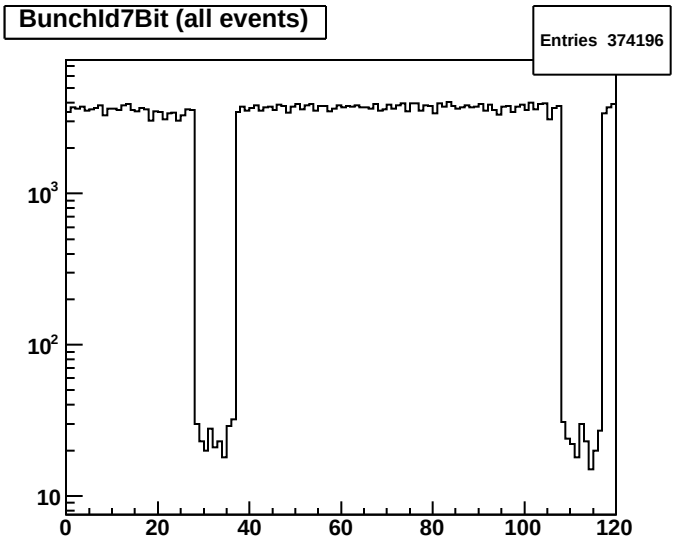

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2993568
HT01 bits/Partial JP ID
15560 20285 13530 15389 22142 14214
21088 21884 20113 20671 25925 19956 374196 374196
337548 332027 340553 338136 326129 340026
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

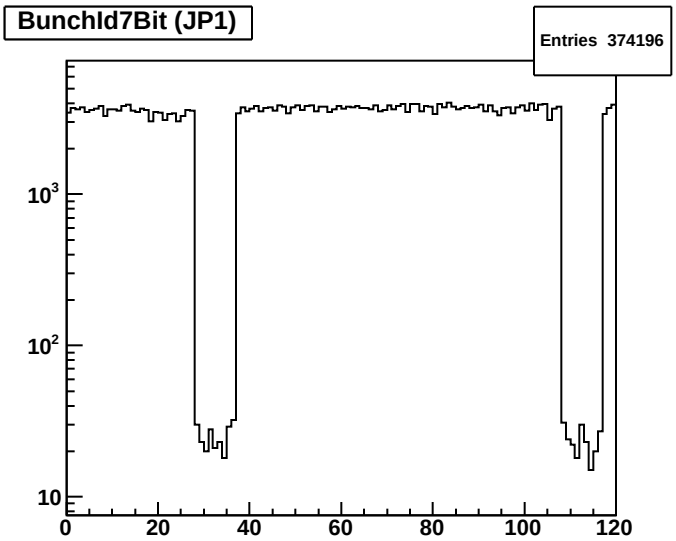

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

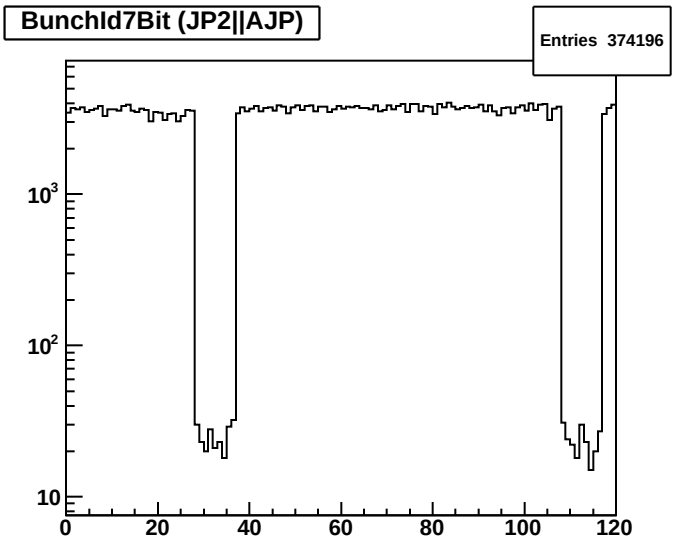

EMC L2 Input - HT23 bits
Entries 2993568
HT23 bits
1581 1837 1294 1211 3114 1319 374196 374196
7379 10477 6183 7300 7358 6561
365236 361882 366719 365685 363724 366316
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

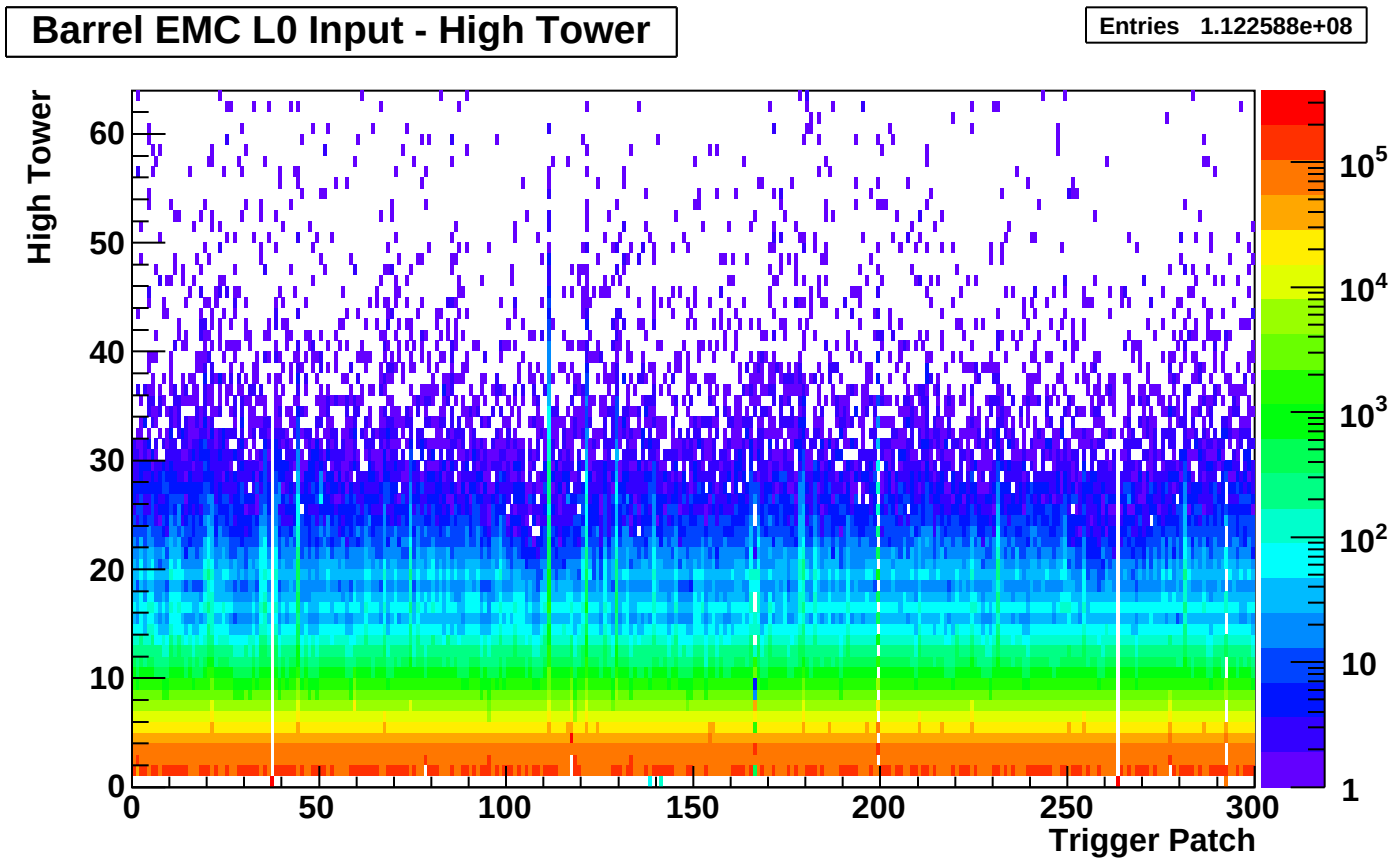

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

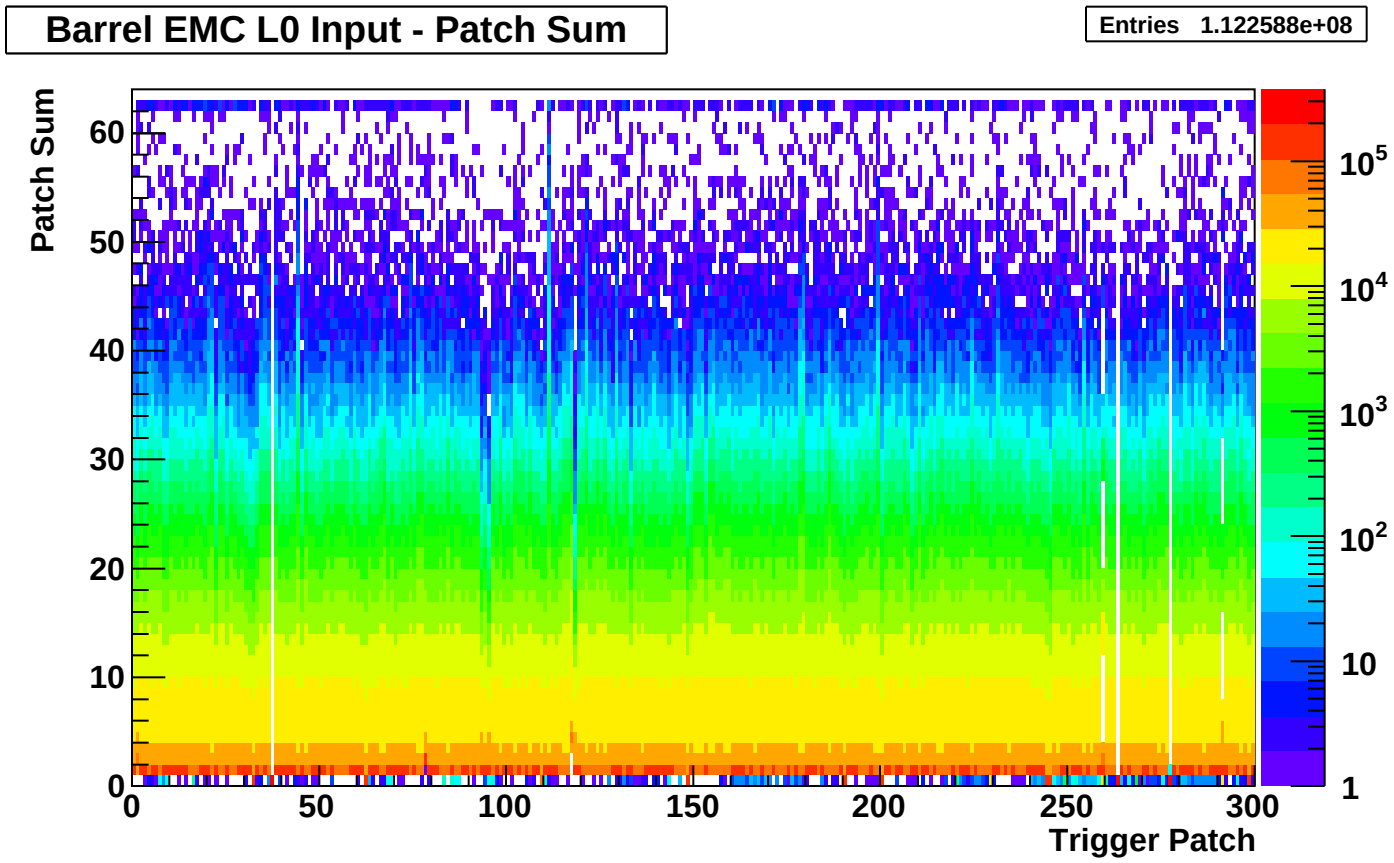

BunchId7Bit (BHT3)
Entries 10334
10^2
10
0
20
40
60
80
100
120

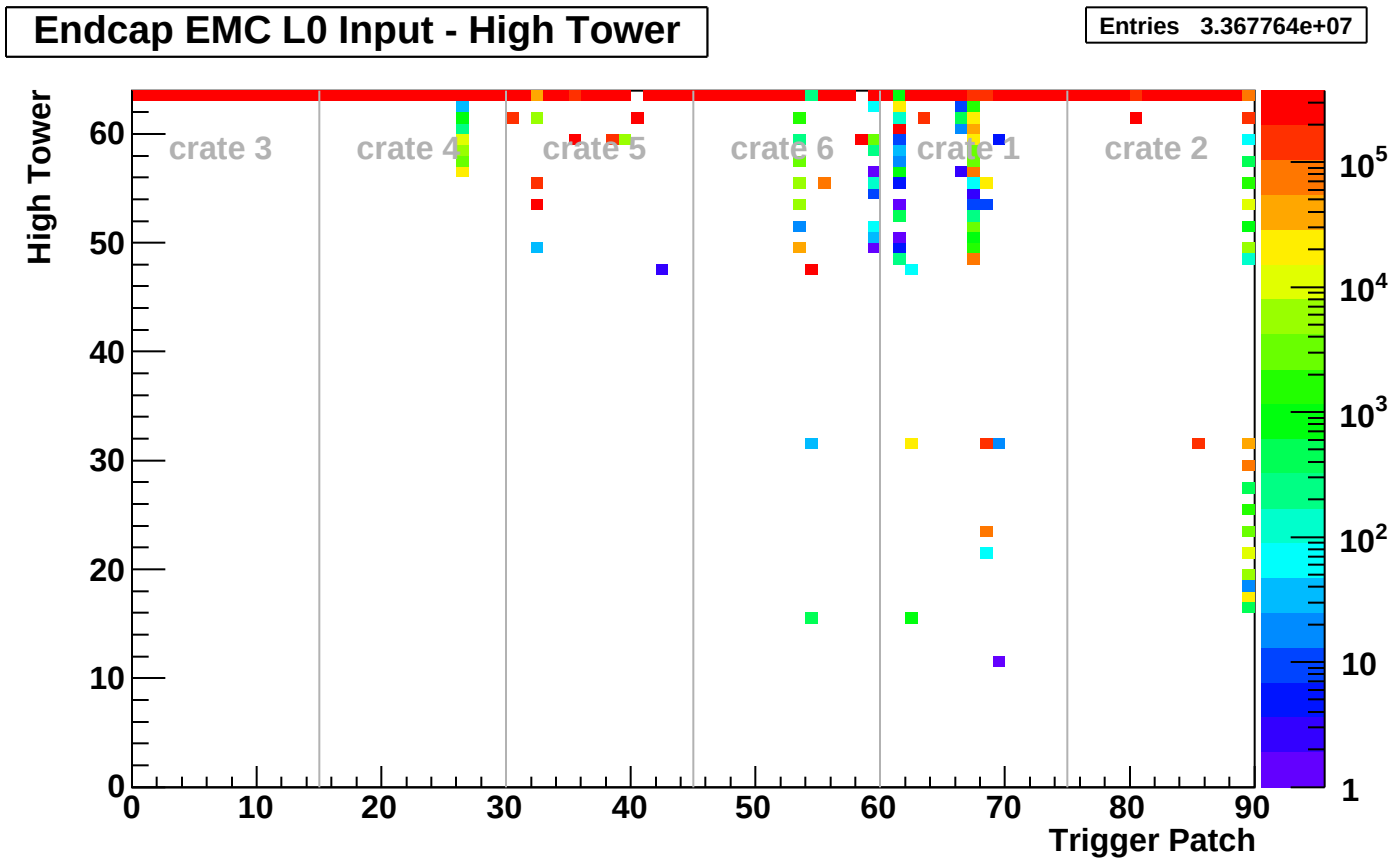

BunchId7Bit (all events)
Entries 374196
10^3
10^2
10
0
20
40
60
80
100
120

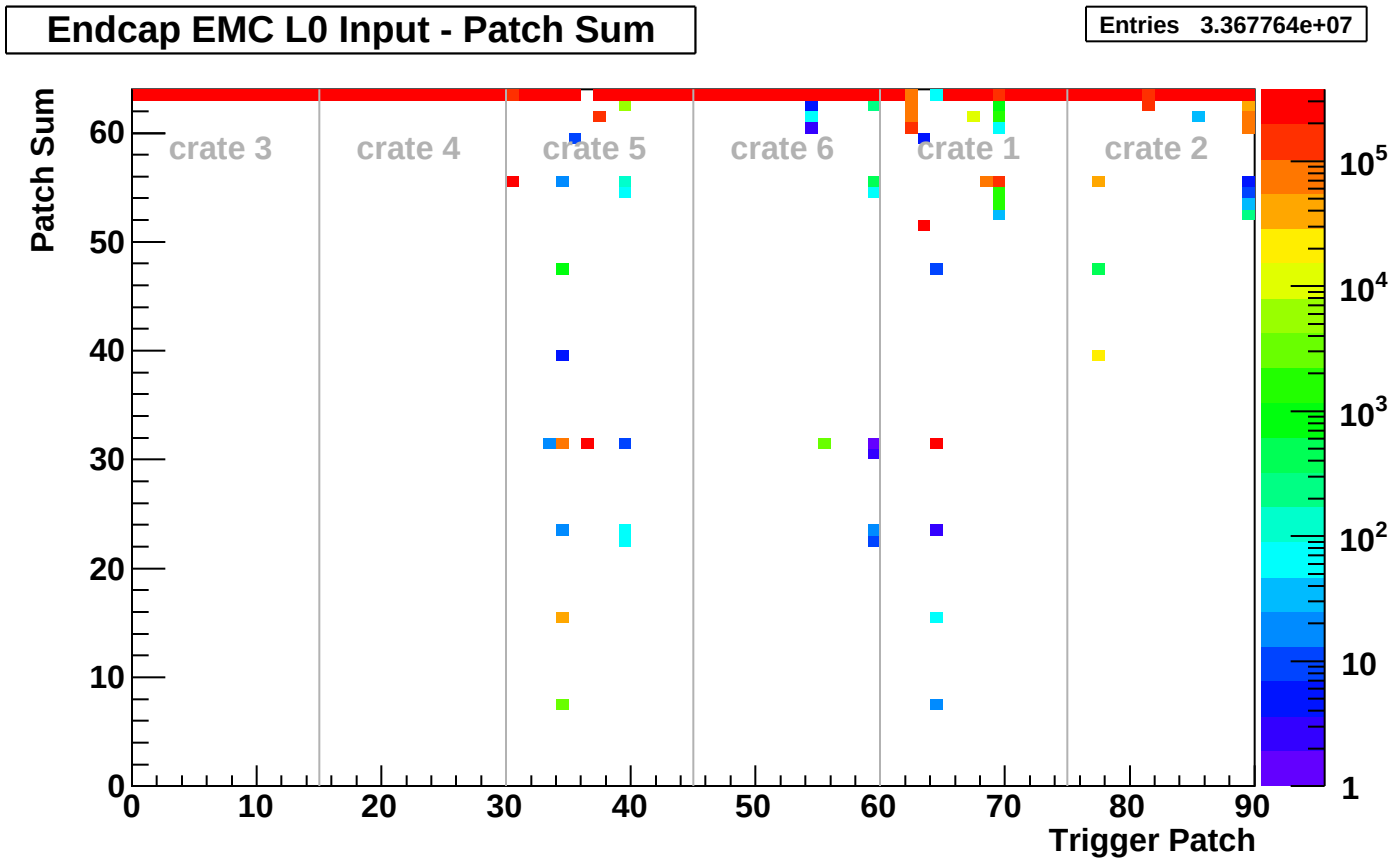

BunchId7Bit (JP1)
Entries 374196
10^3
10^2
10
0
20
40
60
80
100
120

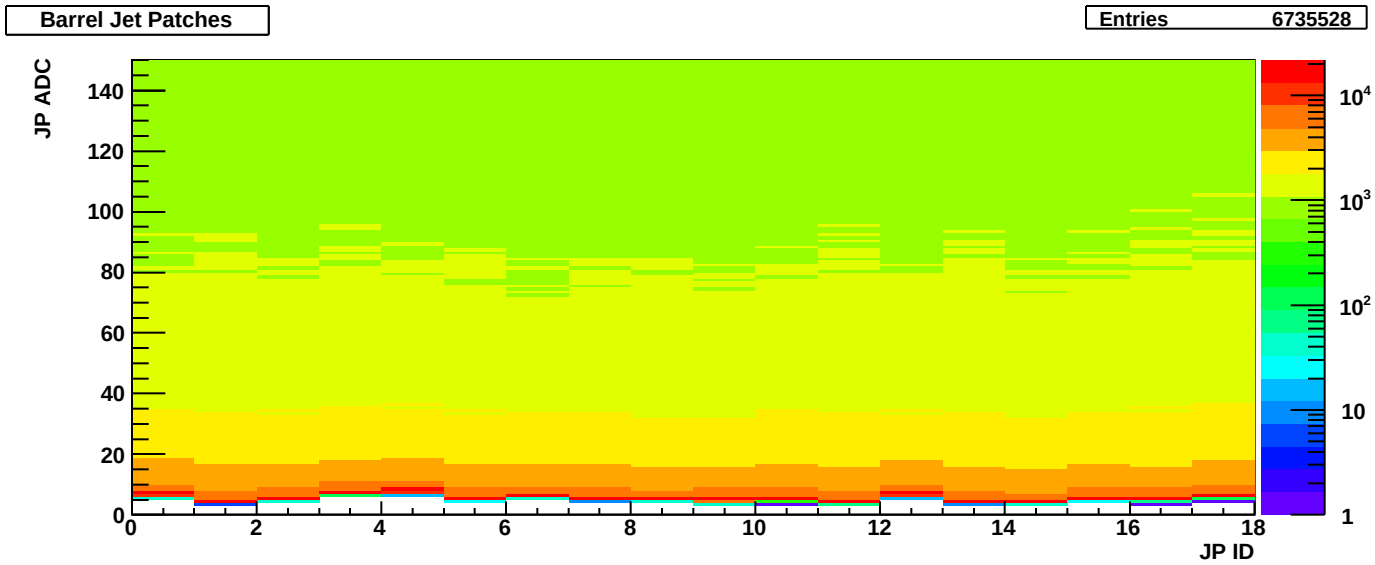

BunchId7Bit (JP2||AJP)
Entries 374196
10^3
10^2
10
0
20
40
60
80
100
120

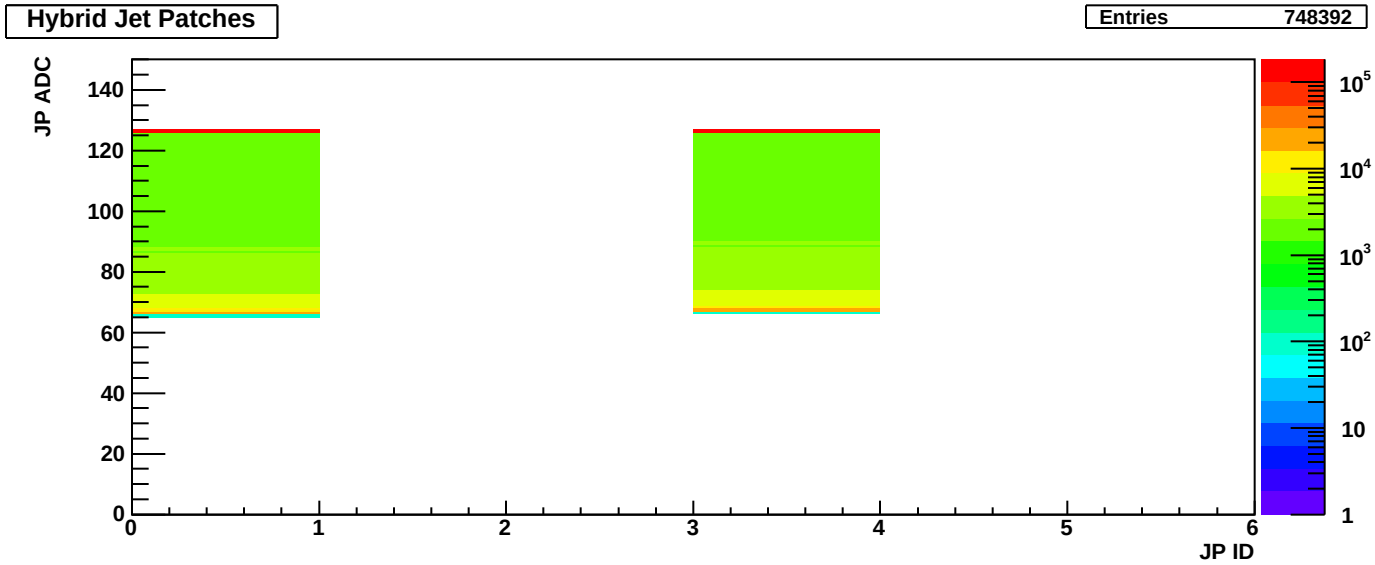

Barrel EMC L0 Input - High Tower
Entries 1.122588e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

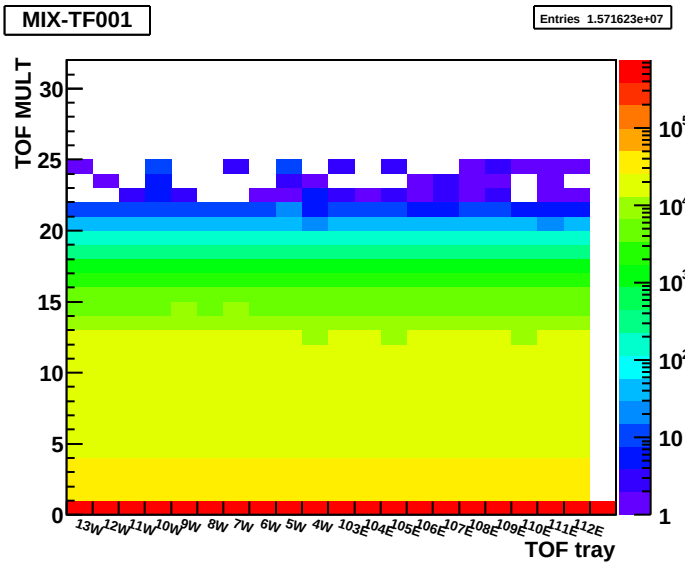

Barrel EMC L0 Input - Patch Sum
Entries 1.122588e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

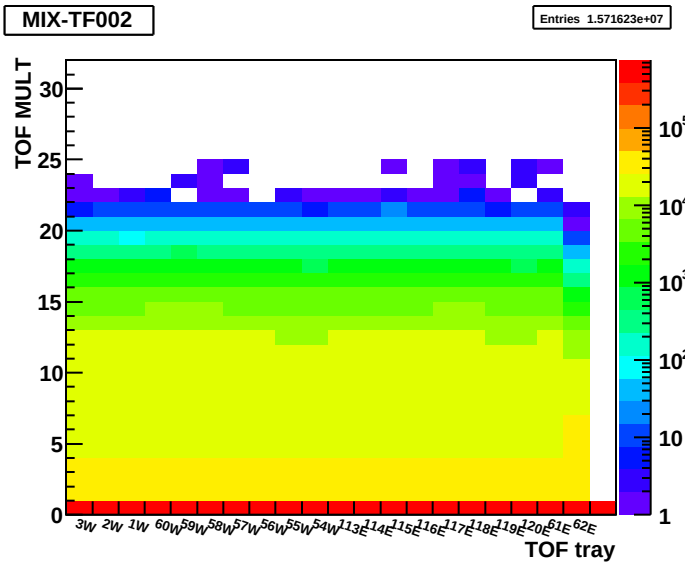

Endcap EMC L0 Input - High Tower
Entries 3.367764e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

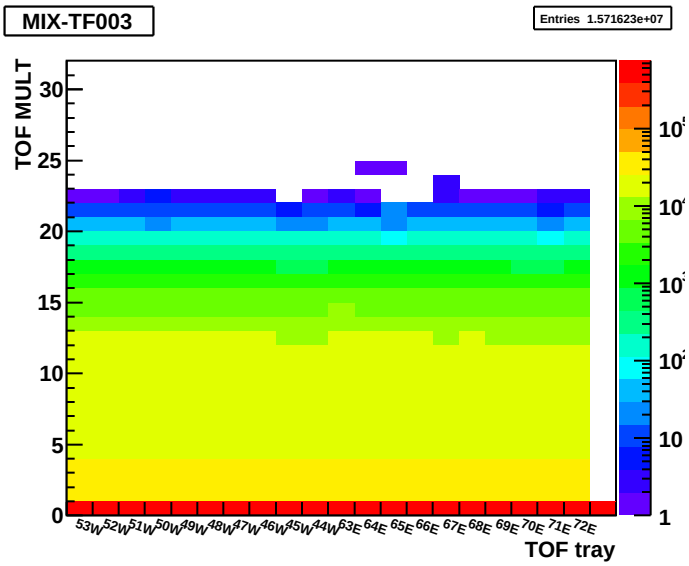

Endcap EMC L0 Input - Patch Sum
Entries 3.367764e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

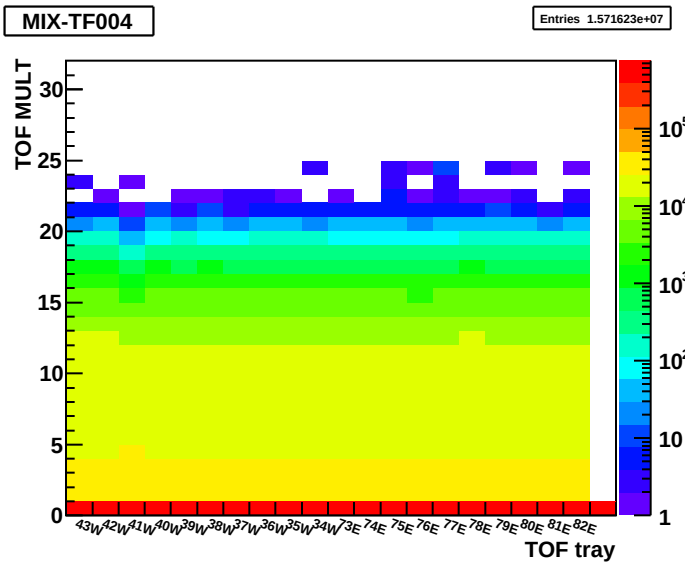

Barrel Jet Patches
Entries 6735528
JP ADC
JP ID

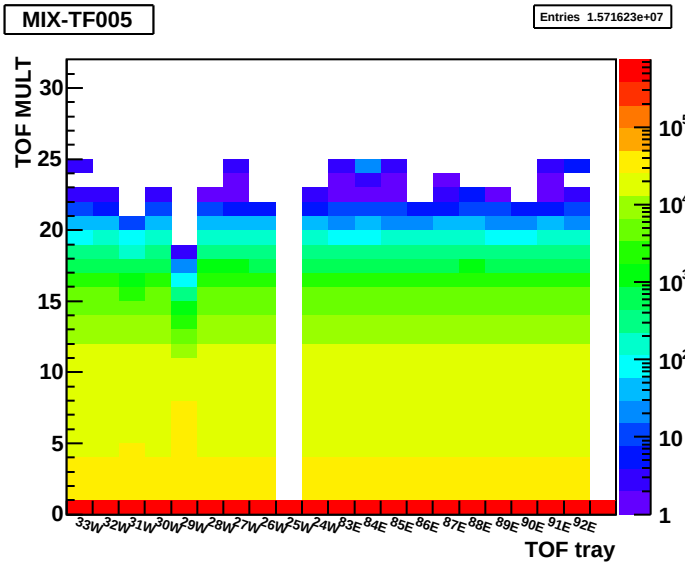

Hybrid Jet Patches
Entries 748392
JP ADC
JP ID

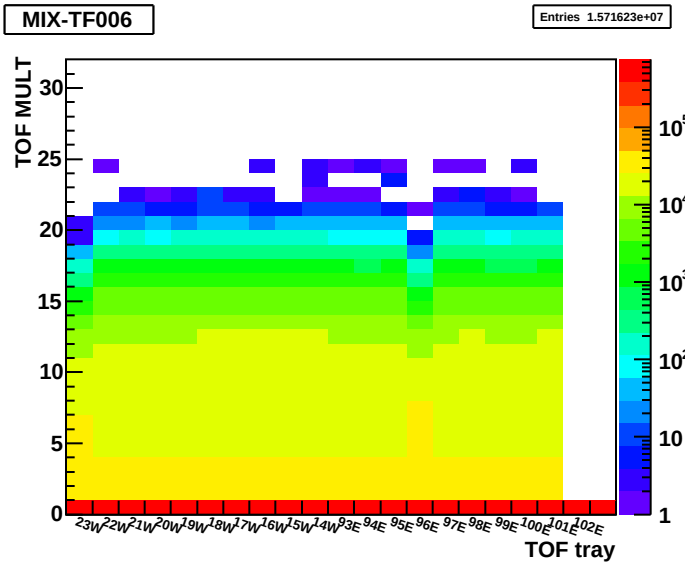

MIX-TF001
Entries 1.571623e+07
TOF MULT
TOF tray

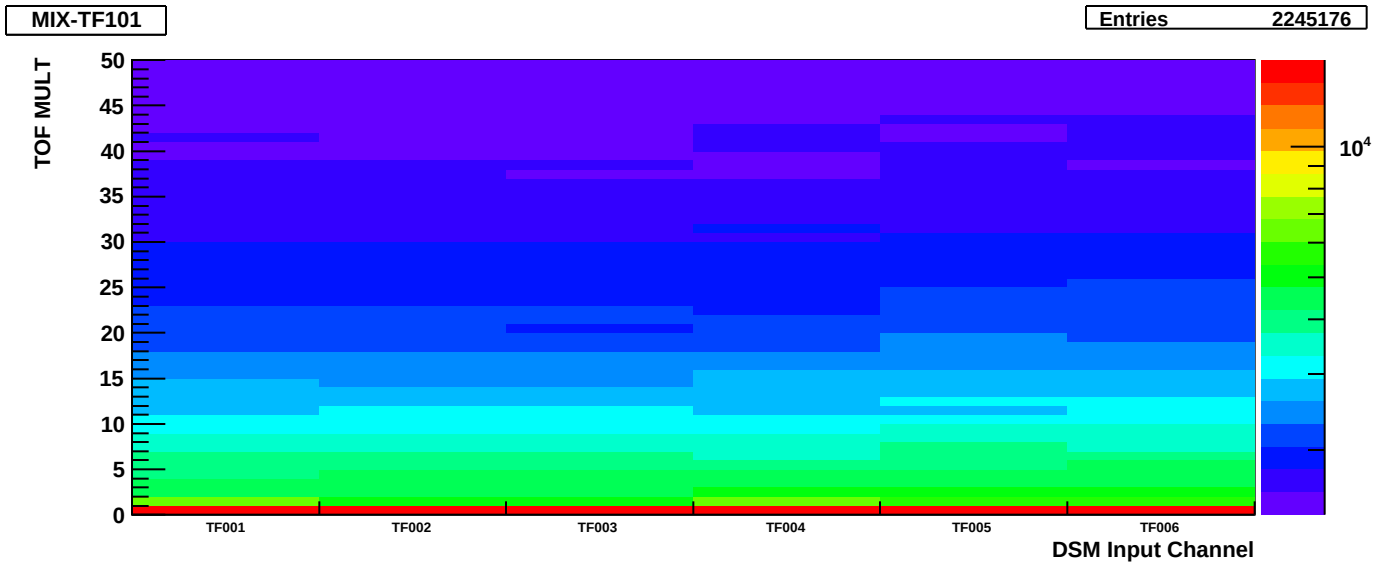

MIX-TF002
Entries 1.571623e+07
TOF MULT
TOF tray

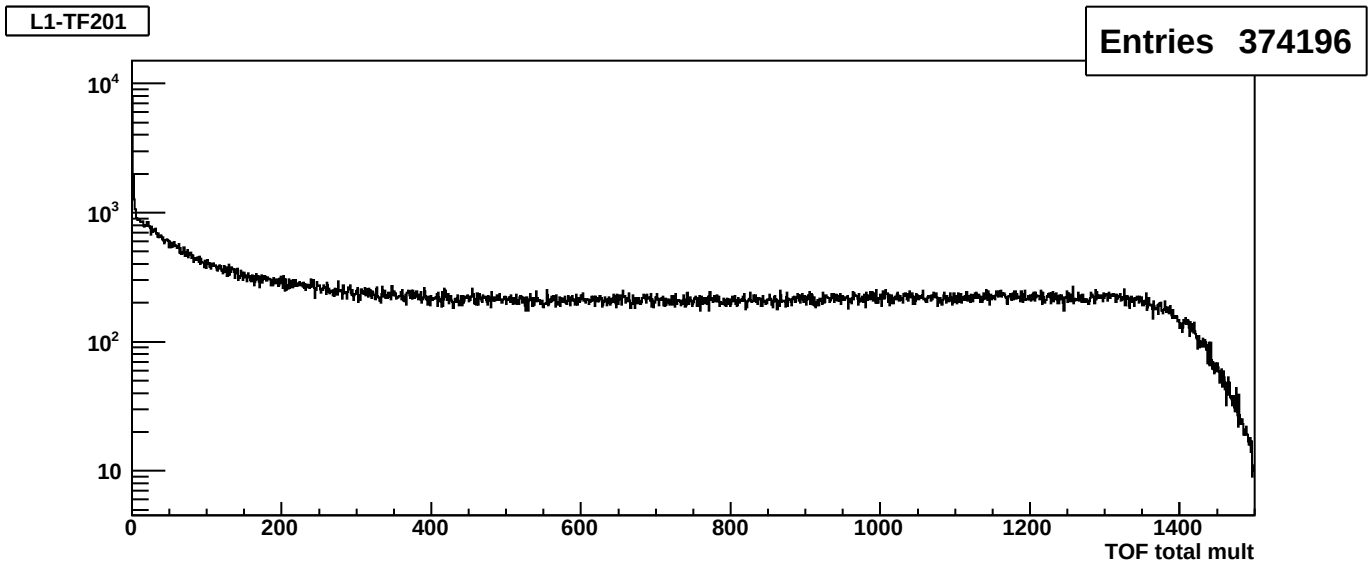

MIX-TF003
Entries 1.571623e+07
TOF MULT
TOF tray

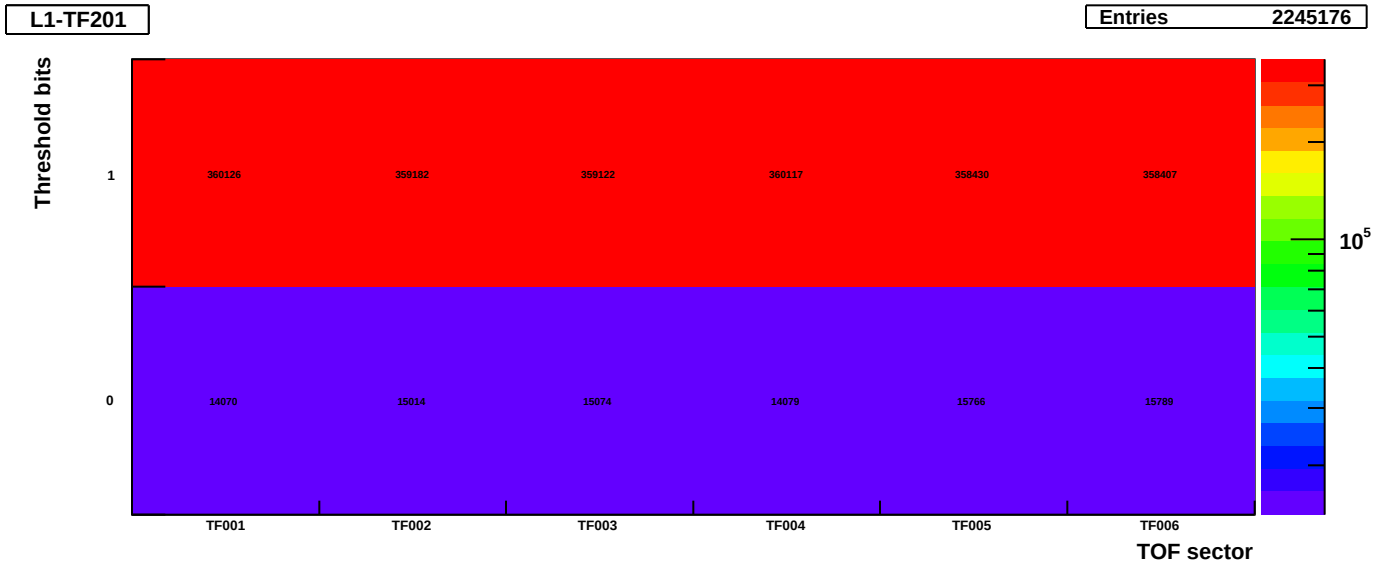

MIX-TF004
Entries 1.571623e+07
TOF MULT
TOF tray

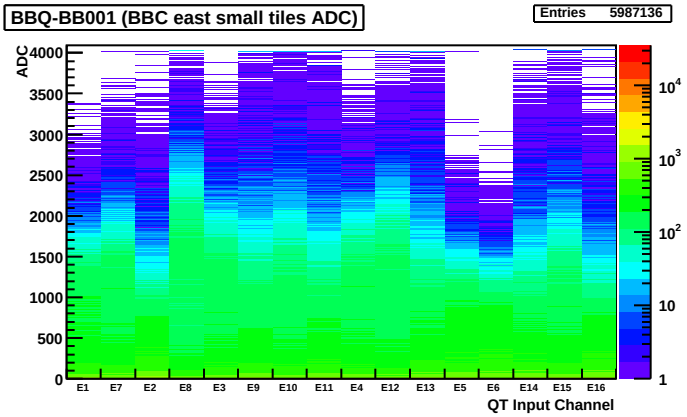

MIX-TF005
Entries 1.571623e+07
TOF MULT
TOF tray

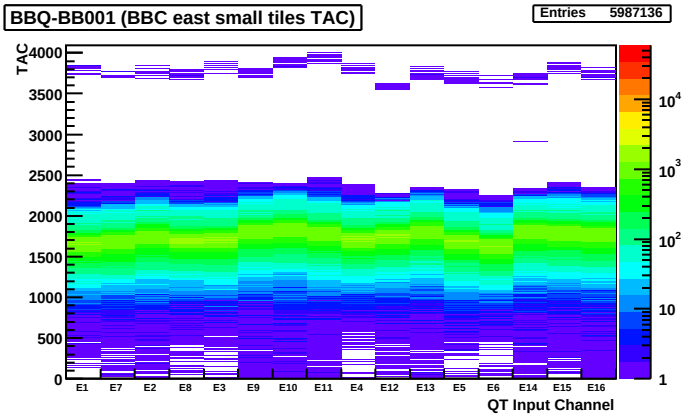

MIX-TF006
Entries 1.571623e+07
TOF MULT
TOF tray

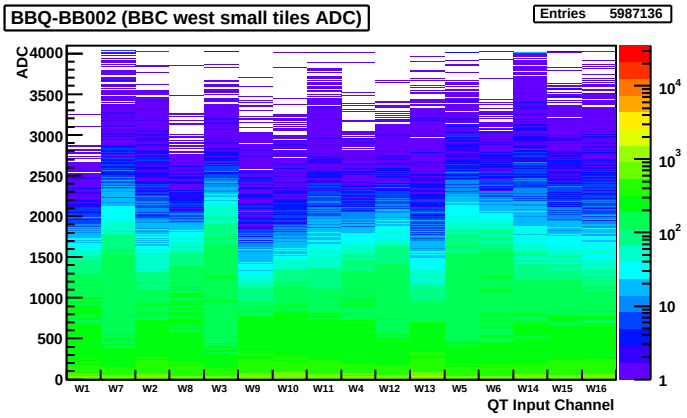

MIX-TF101
Entries 2245176
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

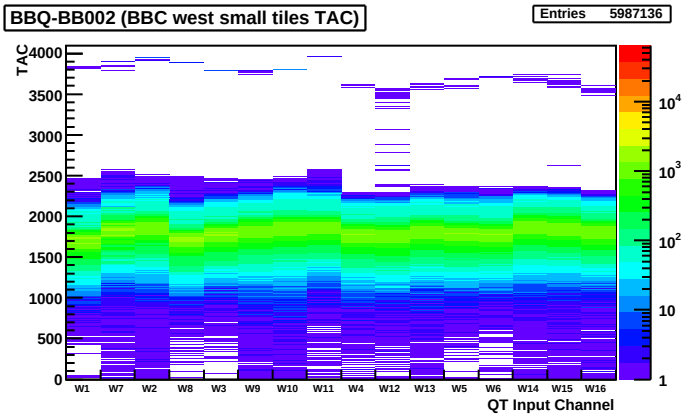

L1-TF201
Entries 374196
10^4
10^3
10^2
10
0
200
400
600
800
1000
1200
1400
TOF total mult

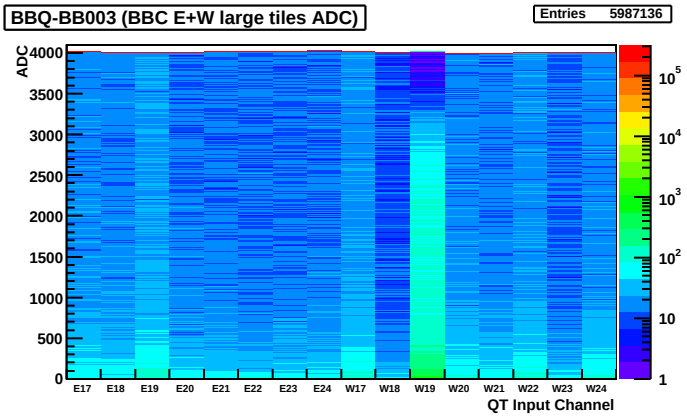

L1-TF201
Entries 2245176
Threshold bits
1
0
360126
359182
359122
360117
358430
358407
14070
15014
15074
14079
15766
15789
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

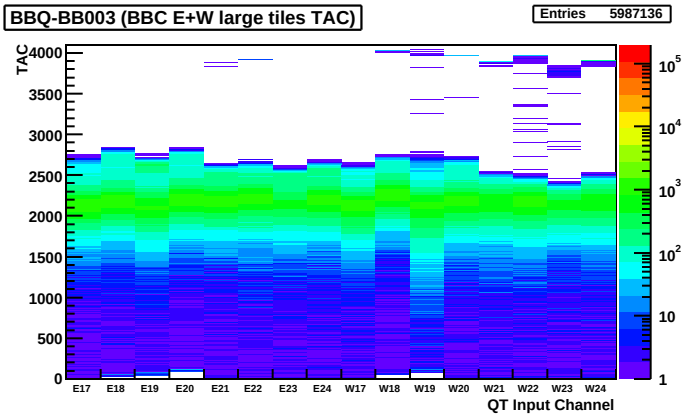

BBQ-BB001 (BBC east small tiles ADC)
Entries 5987136
ADC
QT Input Channel

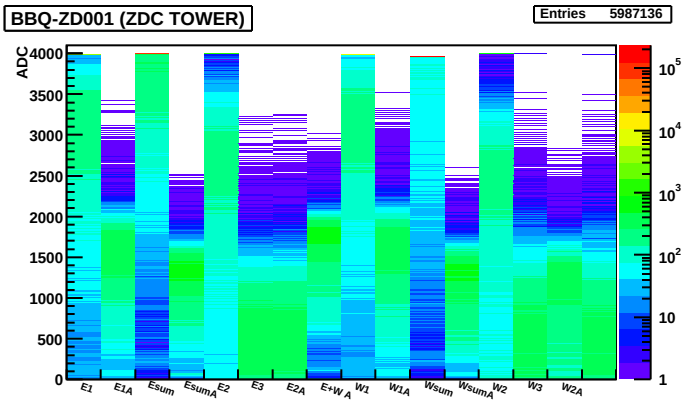

BBQ-BB001 (BBC east small tiles TAC)
Entries 5987136
TAC
QT Input Channel

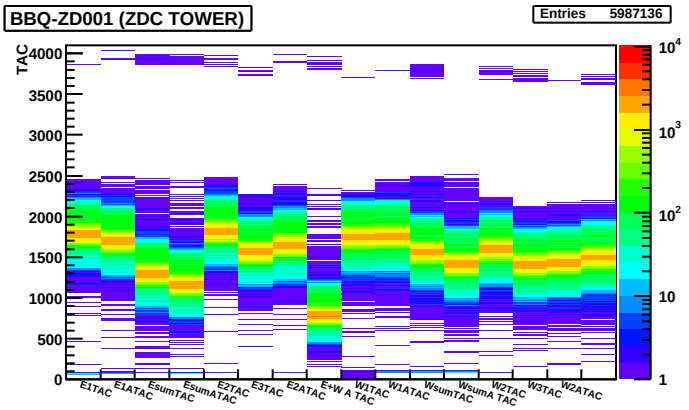

BBQ-BB002 (BBC west small tiles ADC)
Entries 5987136
ADC
QT Input Channel

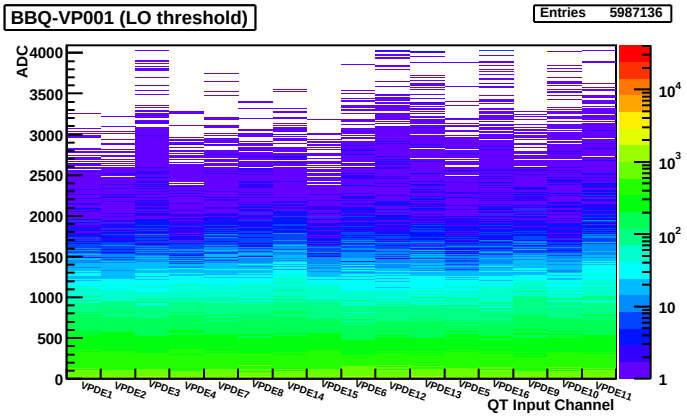

BBQ-BB002 (BBC west small tiles TAC)
Entries 5987136
TAC
QT Input Channel

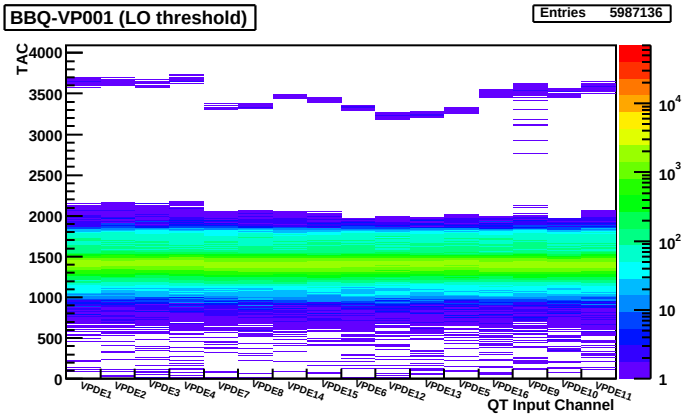

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 5987136
ADC
QT Input Channel

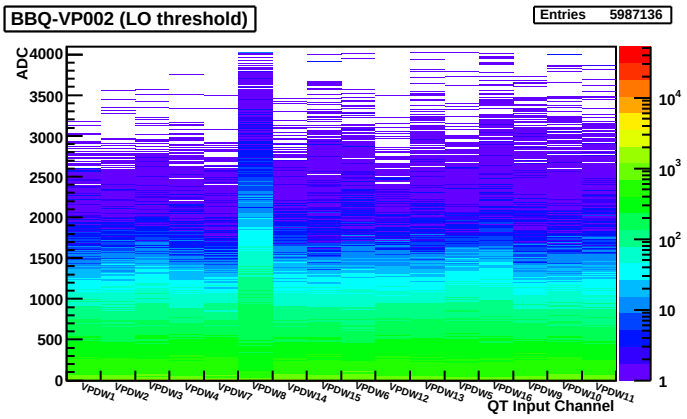

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 5987136
TAC
QT Input Channel

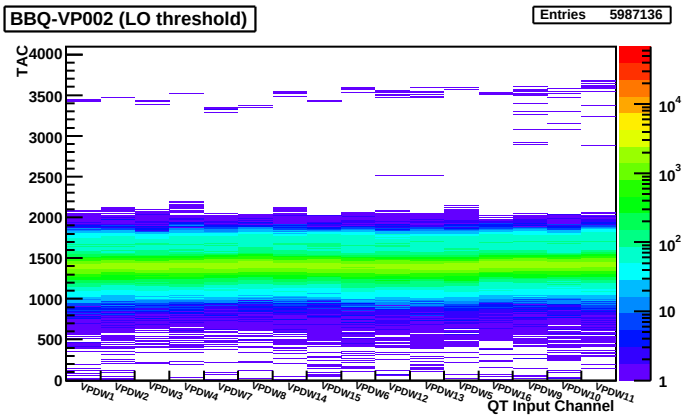

BBQ-ZD001 (ZDC TOWER)
Entries 5987136
ADC

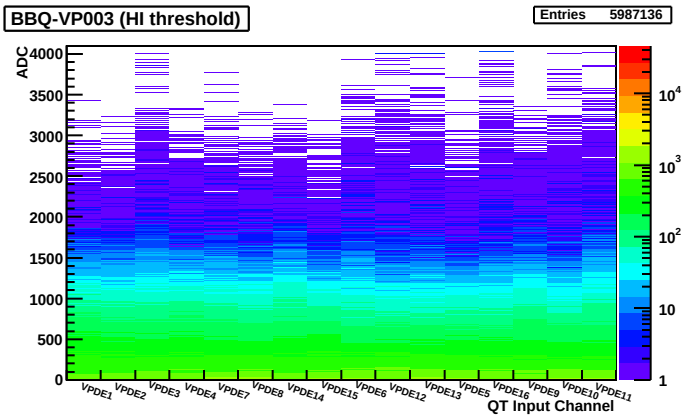

BBQ-ZD001 (ZDC TOWER)
Entries 5987136
TAC

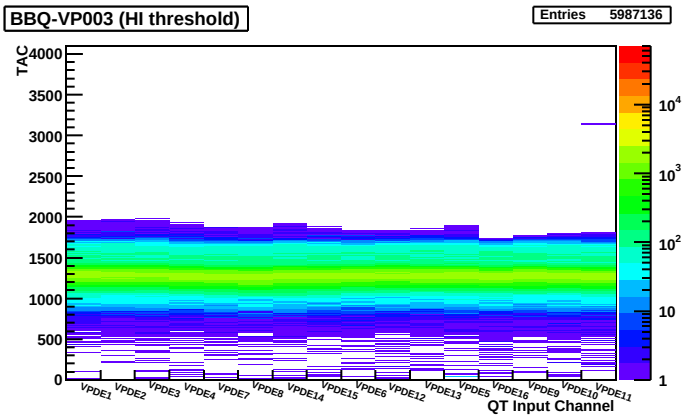

BBQ-VP001 (LO threshold)
Entries 5987136
ADC
QT Input Channel

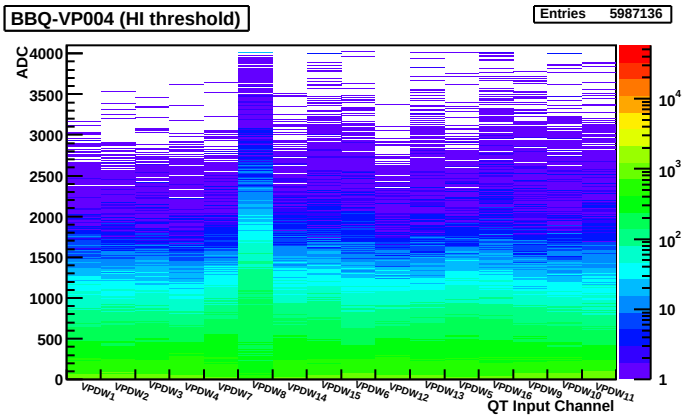

BBQ-VP001 (LO threshold)
Entries 5987136
TAC
QT Input Channel

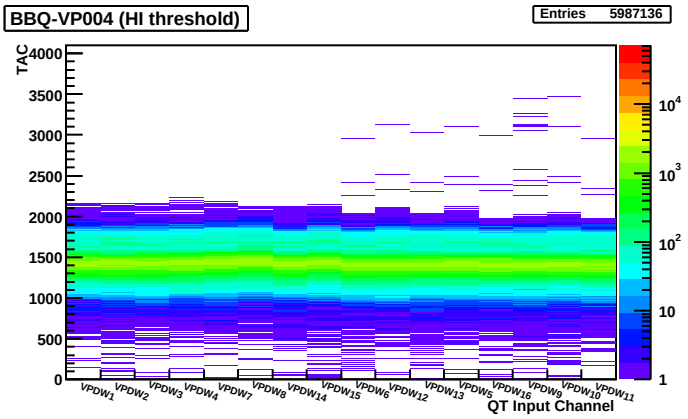

BBQ-VP002 (LO threshold)
Entries 5987136
ADC
QT Input Channel

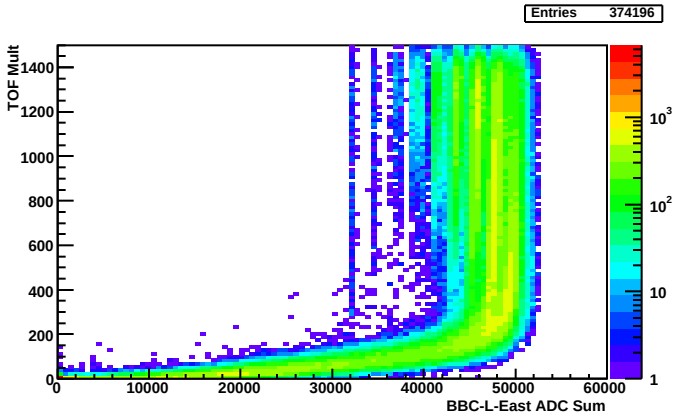

BBQ-VP002 (LO threshold)
Entries 5987136
TAC
QT Input Channel

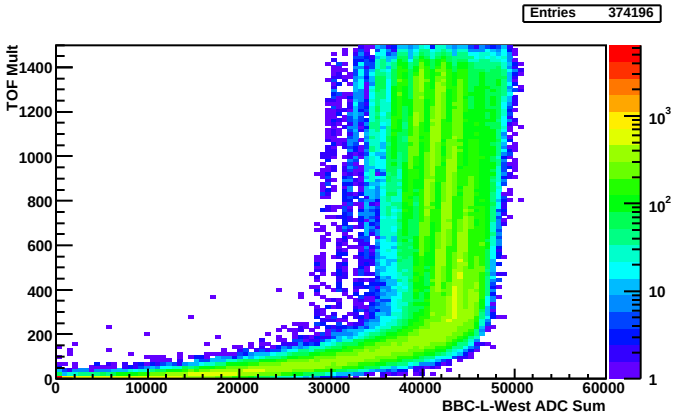

BBQ-VP003 (HI threshold)
Entries 5987136
ADC
QT Input Channel

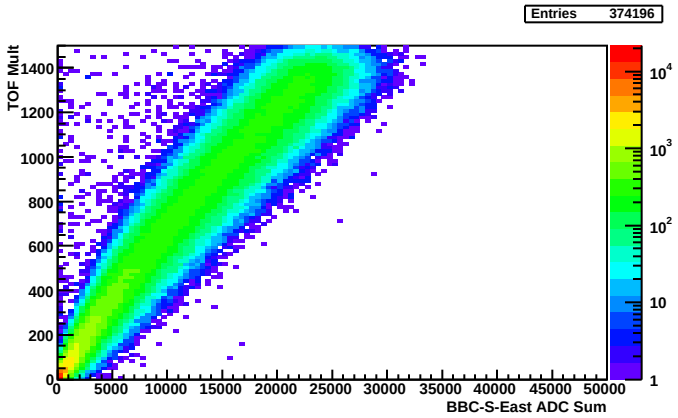

BBQ-VP003 (HI threshold)
Entries 5987136
TAC
QT Input Channel

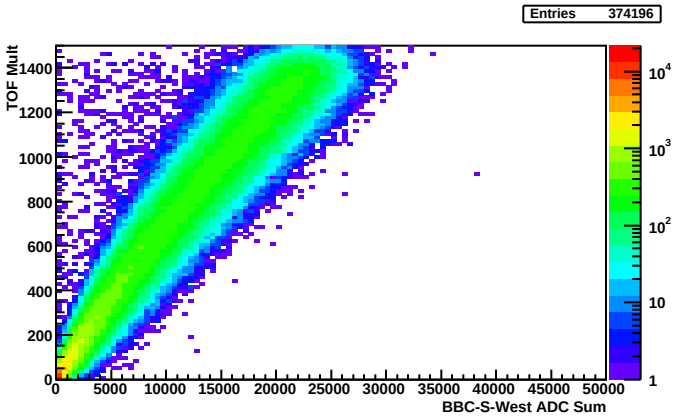

BBQ-VP004 (HI threshold)
Entries 5987136
ADC
QT Input Channel

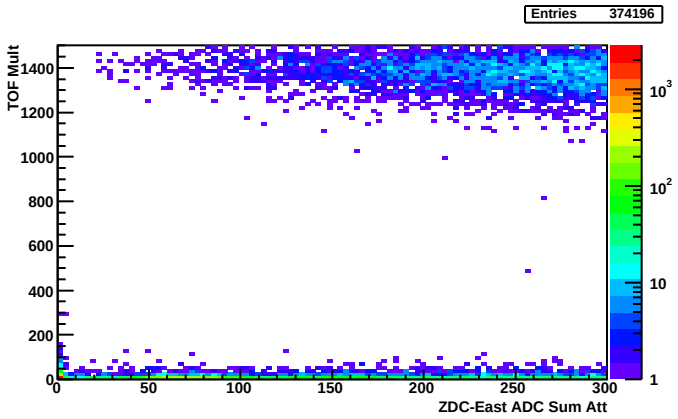

BBQ-VP004 (HI threshold)
Entries 5987136
TAC
QT Input Channel

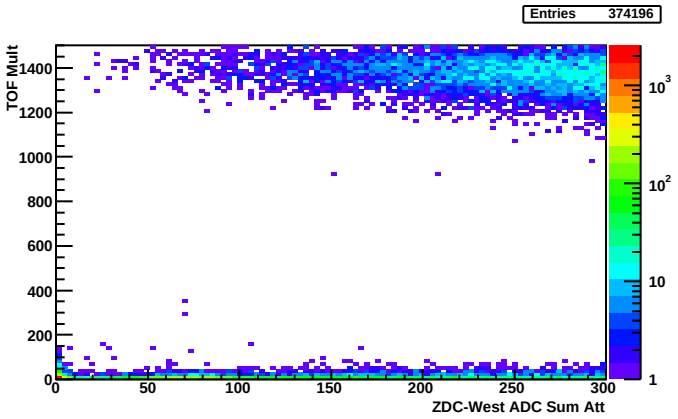

Entries 374196
TOF Mult
BBC-L-East ADC Sum

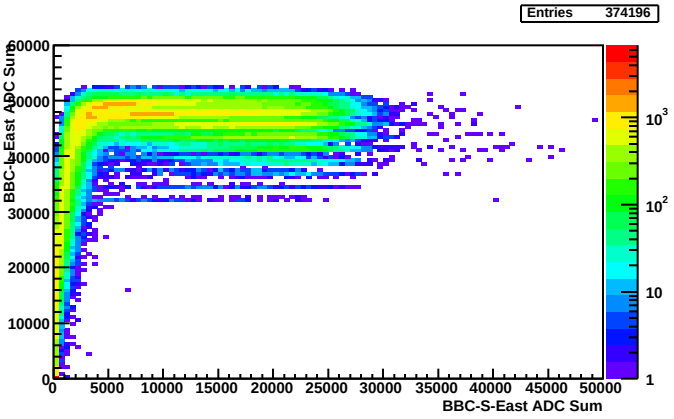

Entries 374196
TOF Mult
BBC-L-West ADC Sum

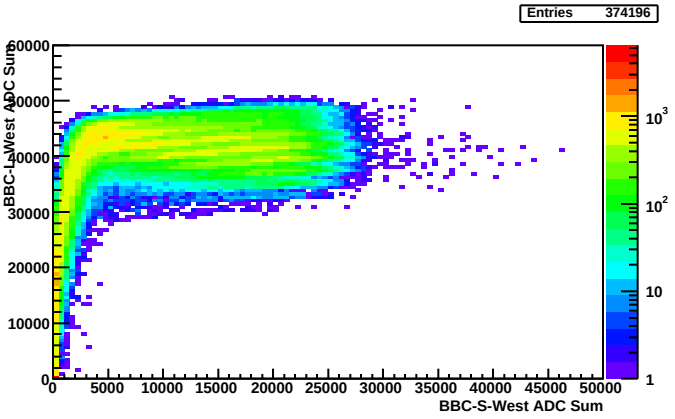

Entries 374196
TOF Mult
BBC-S-East ADC Sum

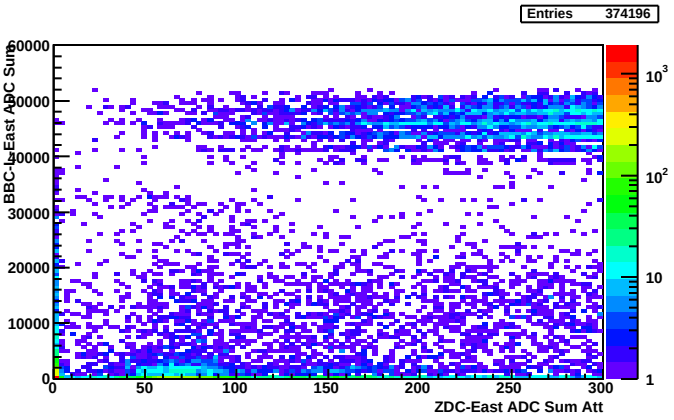

Entries 374196
TOF Mult
BBC-S-West ADC Sum

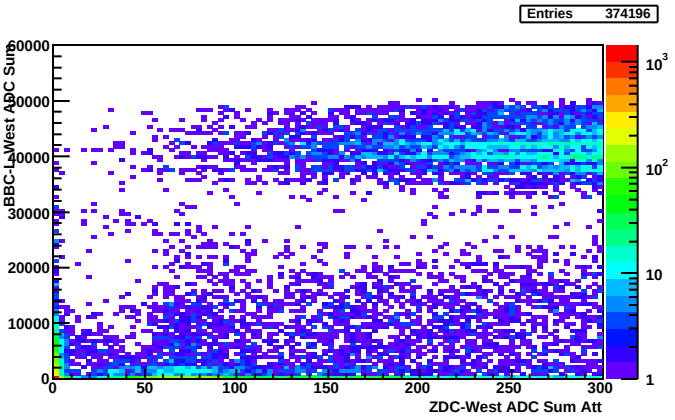

Entries 374196
TOF Mult
ZDC-East ADC Sum Att

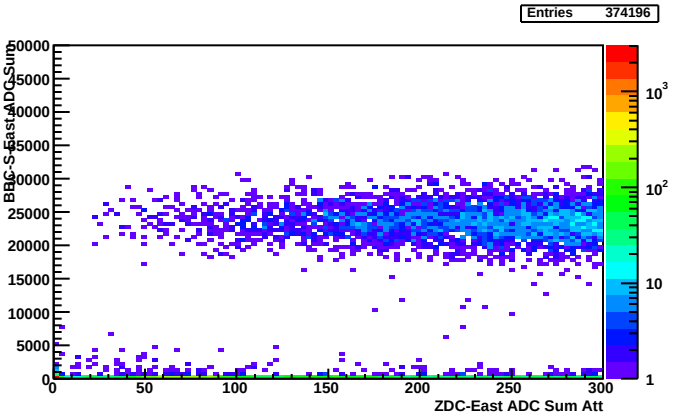

Entries 374196
TOF Mult
ZDC-West ADC Sum Att


Entries 374196
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 374196
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 374196
BBC-L-East ADC Sum
ZDC-East ADC Sum Att


Entries 374196
BBC-L-West ADC Sum
ZDC-West ADC Sum Att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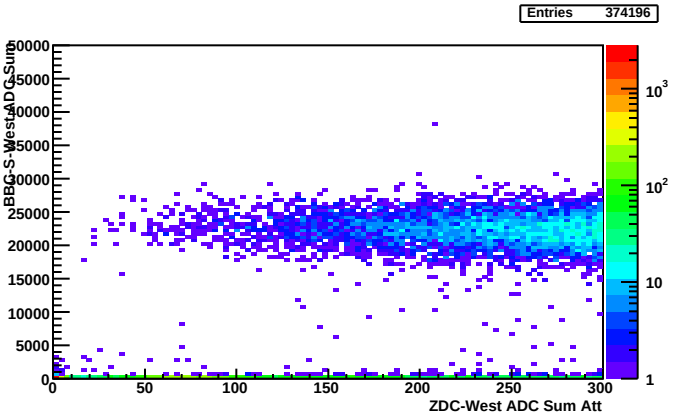
BBC-S-East ADC Sum
ZDC-East ADC Sum Att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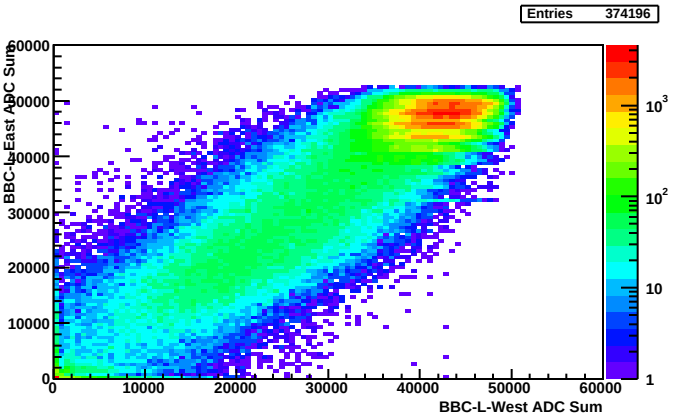
BBC-S-West ADC Sum
ZDC-West ADC Sum Att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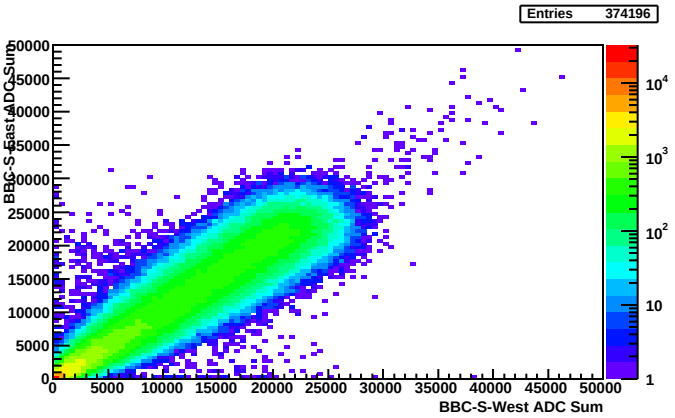
BBC-L-East ADC Sum
BBC-L-West ADC Sum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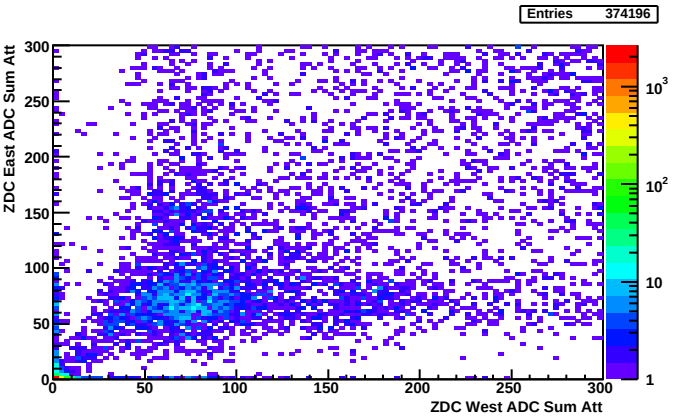
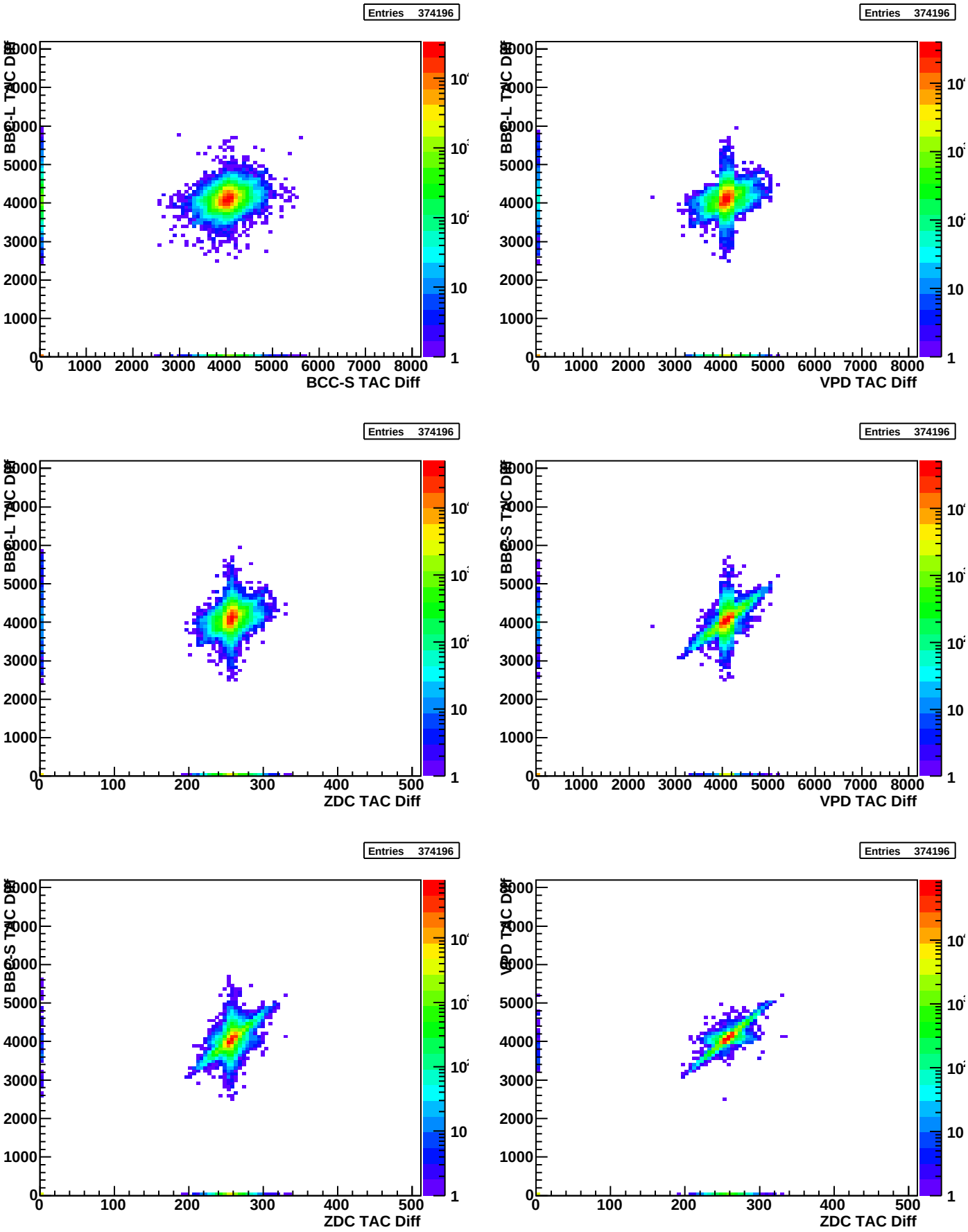
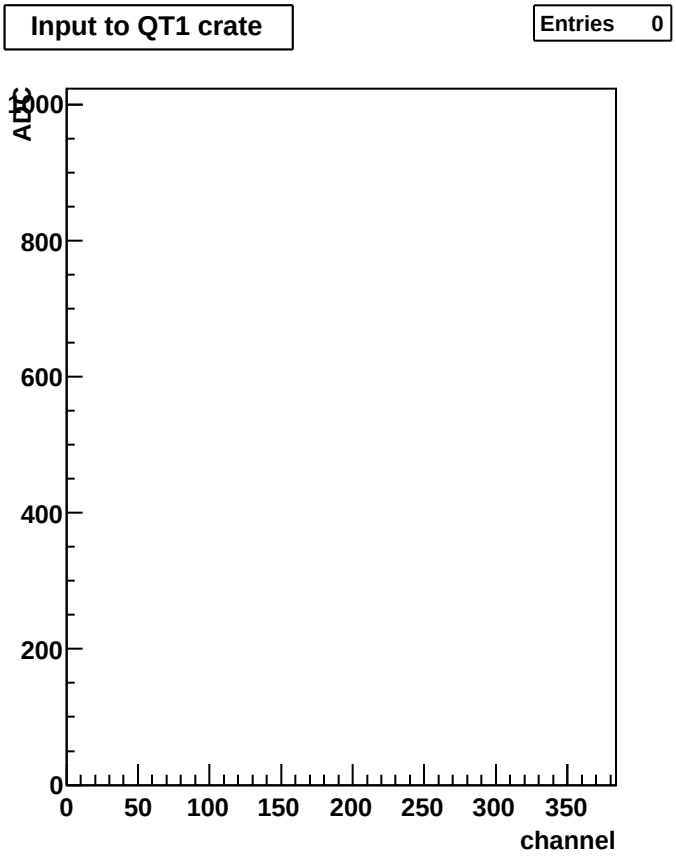
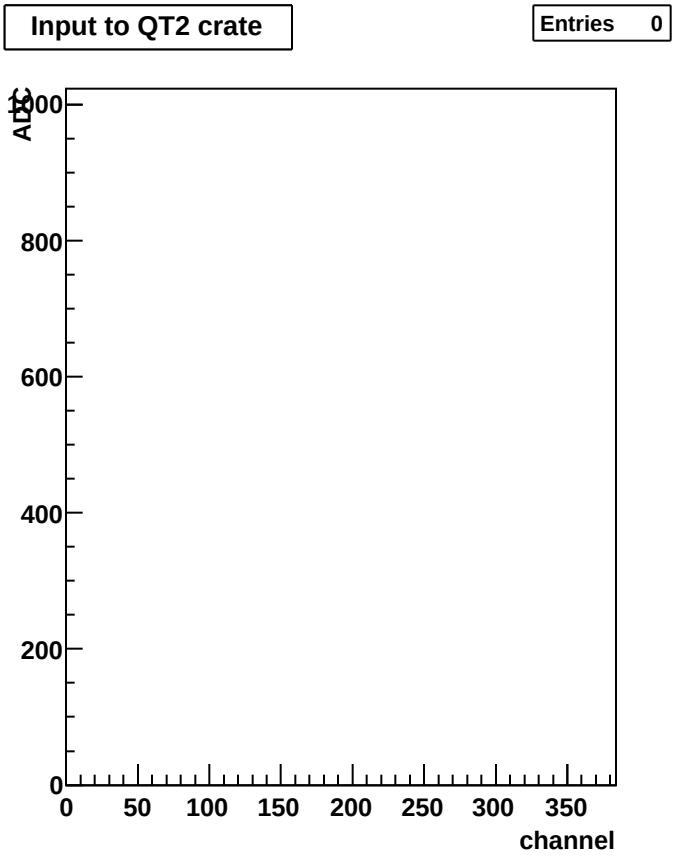
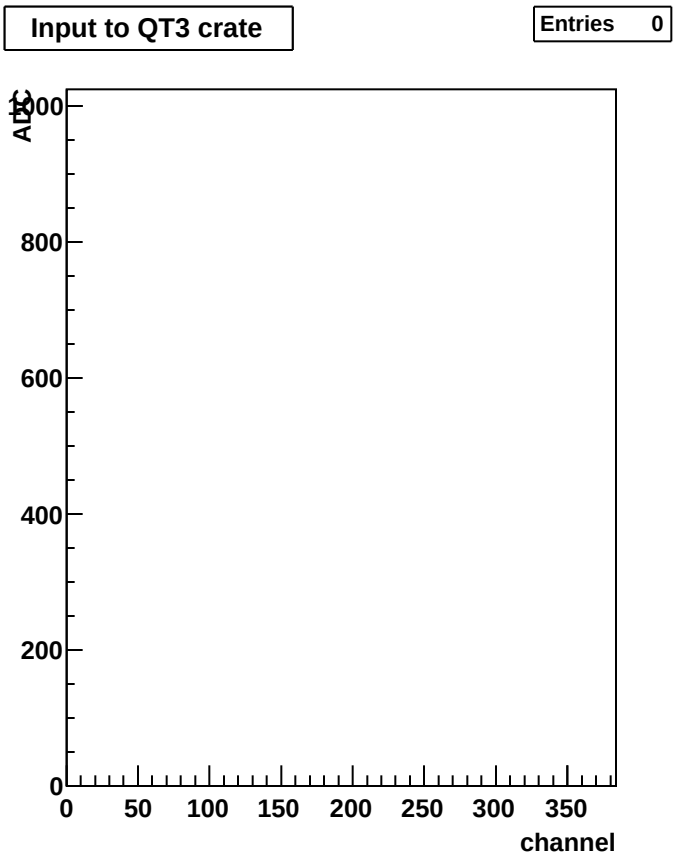
BBC-S-East ADC Sum
BBC-S-West ADC Sum

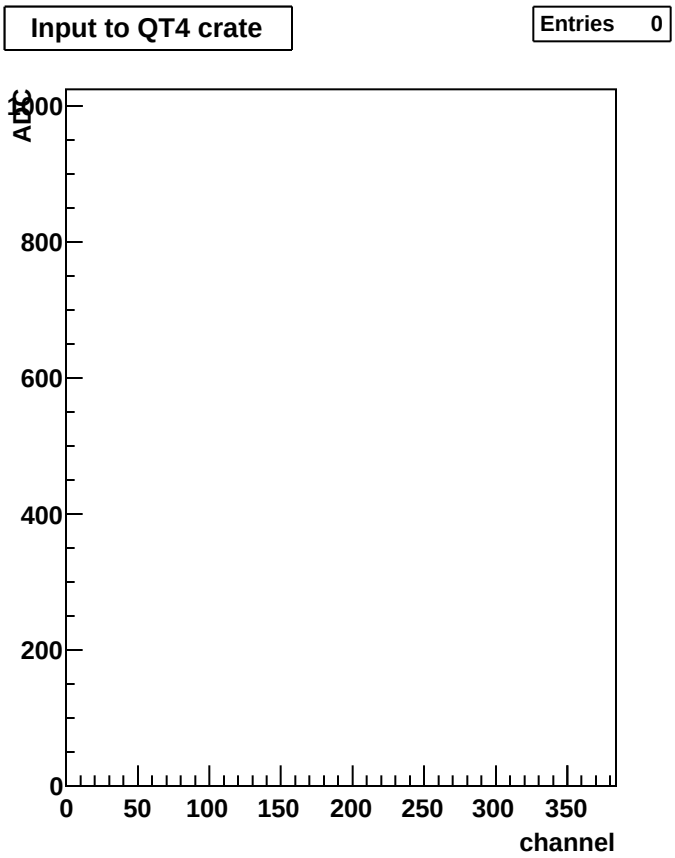

Entries 374196
ZDC East ADC Sum Att
ZDC West ADC Sum Att

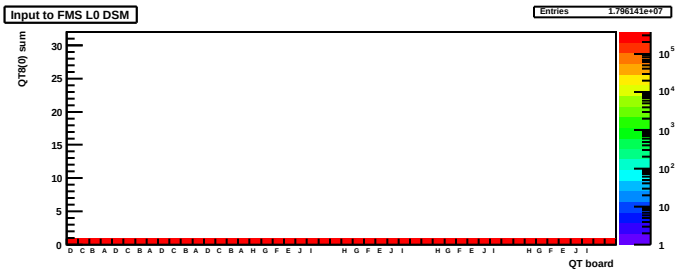

Entries 374196
BBC-L TAC Diff
BCC-S TAC Diff
Entries 374196
BBC-L TAC Diff
VPD TAC Diff
Entries 374196
BBC-L TAC Diff
ZDC TAC Diff
Entries 374196
BBC-S TAC Diff
VPD TAC Diff
Entries 374196
BBC-S TAC Diff
ZDC TAC Diff
Entries 374196
VPD TAC Diff
ZDC TAC Diff

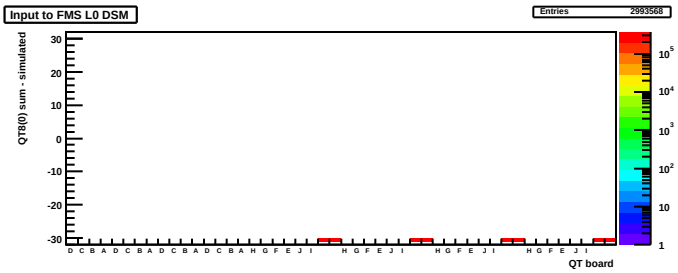

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

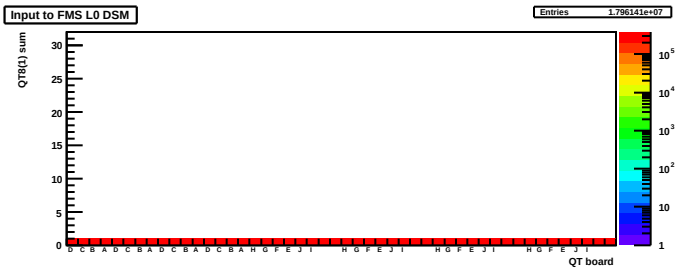

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

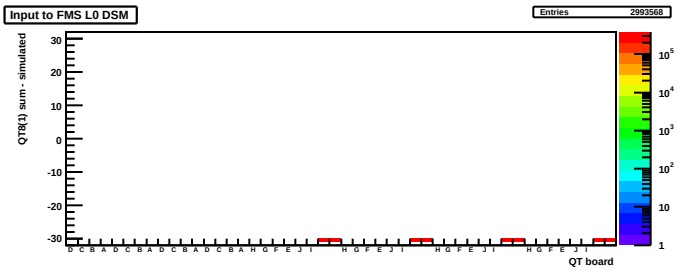

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

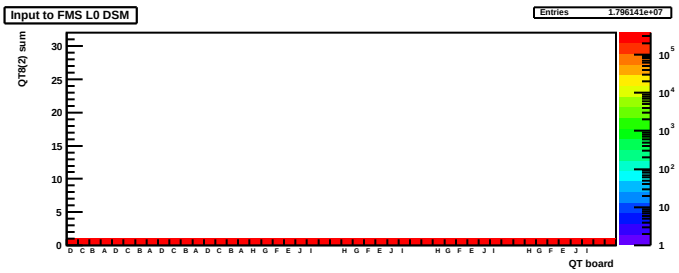

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

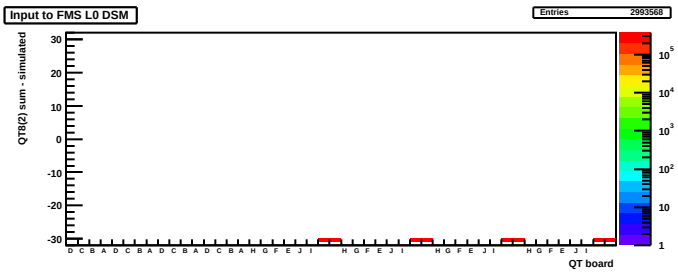

Input to FMS L0 DSM
Entries 1.796141e+07
QT8(0) sum
QT board

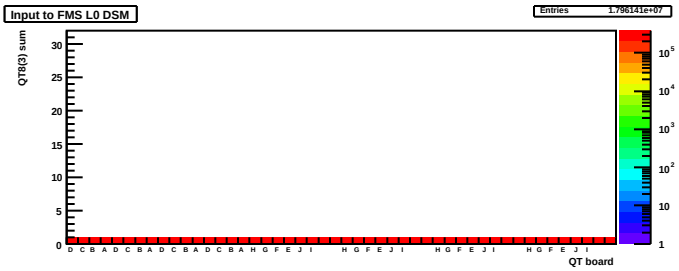

Input to FMS L0 DSM
Entries 2993568
QT8(0) sum - simulated
QT board

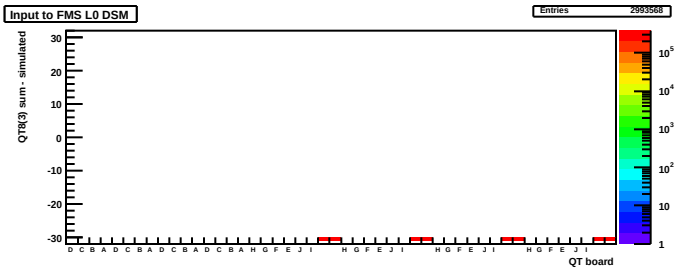

Input to FMS L0 DSM
Entries 1.796141e+07
QT8(1) sum
QT board

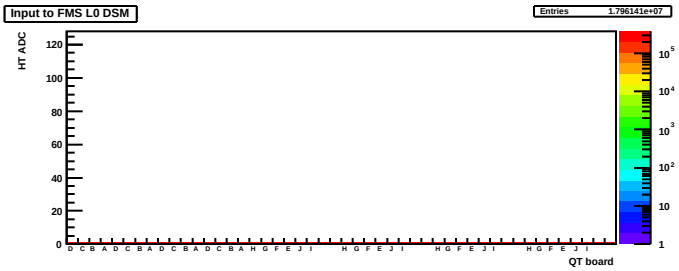

Input to FMS L0 DSM
Entries 2993568
QT8(1) sum - simulated
QT board

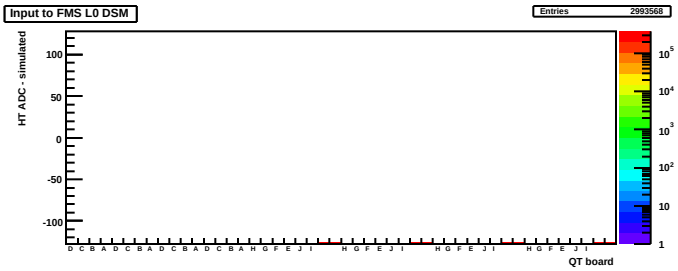

Input to FMS L0 DSM
Entries 1.796141e+07
QT8(2) sum
QT board

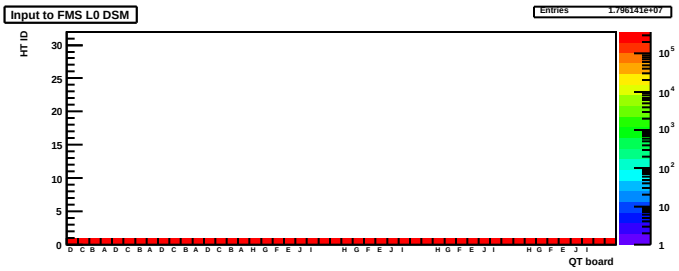

Input to FMS L0 DSM
Entries 2993568
QT8(2) sum - simulated
QT board

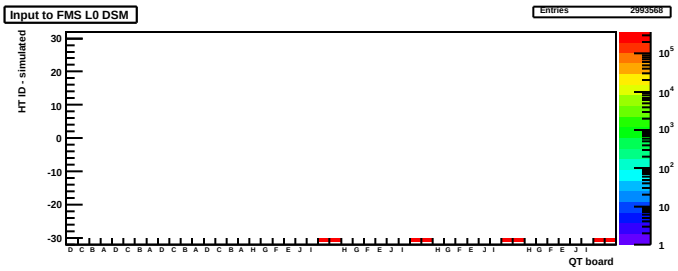

Input to FMS L0 DSM
Entries 1.796141e+07
QT8(3) sum
QT board

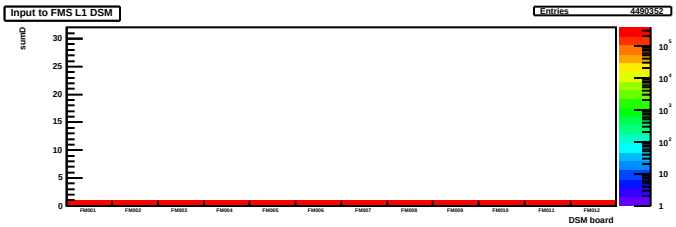

Input to FMS L0 DSM
Entries 2993568
QT8(3) sum - simulated
QT board

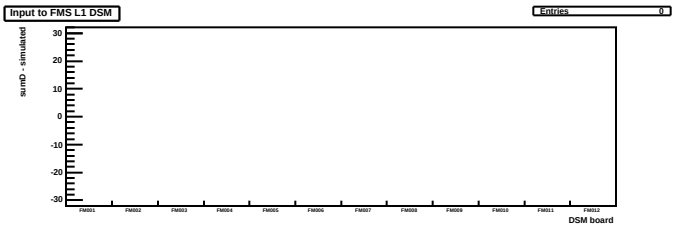

Input to FMS L0 DSM
Entries 1.796141e+07
HT ADC
QT board

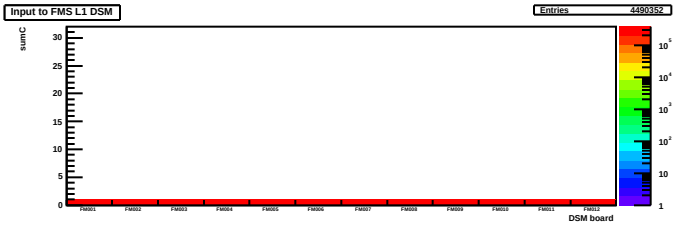

Input to FMS L0 DSM
Entries 2993568
HT ADC - simulated
QT board

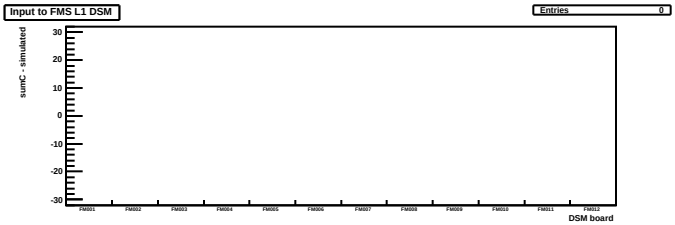

Input to FMS L0 DSM
Entries 1.796141e+07
HT ID
QT board

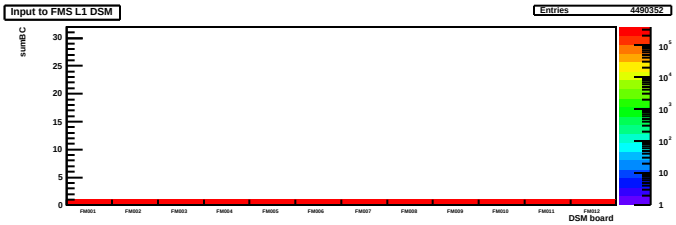

Input to FMS L0 DSM
Entries 2993568
HT ID - simulated
QT board

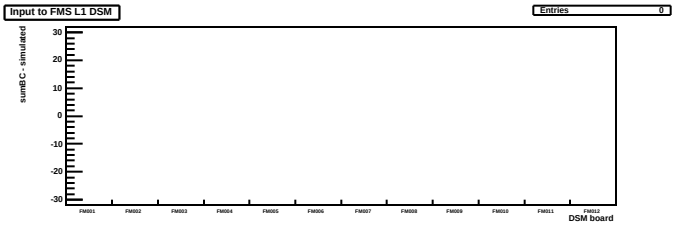

Input to FMS L1 DSM
Entries 4490352
sumD
DSM board

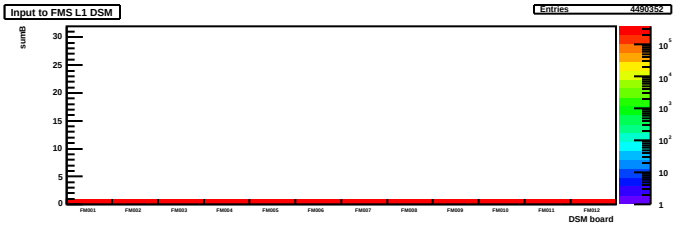

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

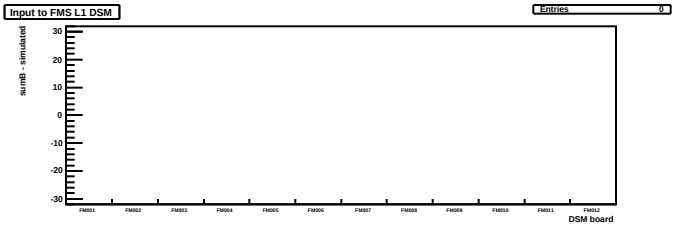

Input to FMS L1 DSM
Entries 4490352
sumC
DSM board

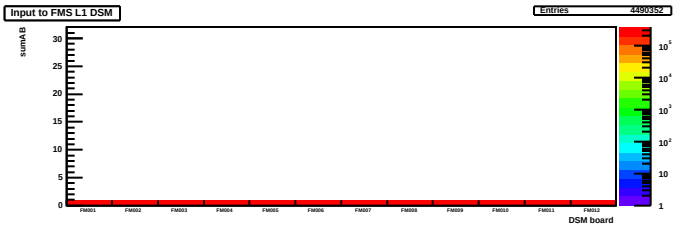

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

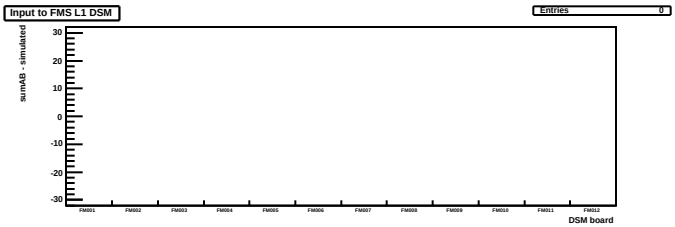

Input to FMS L1 DSM
Entries 4490352
sumBC
DSM board

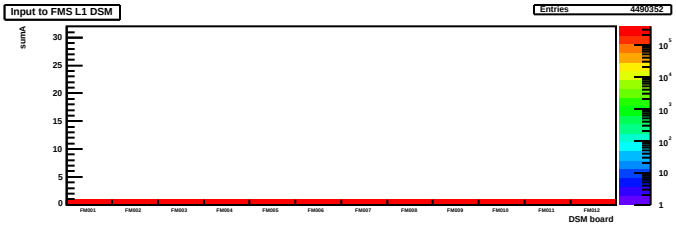

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

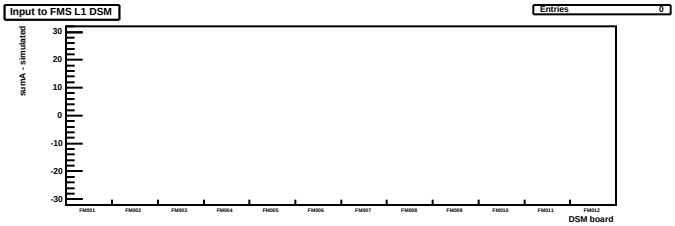

Input to FMS L1 DSM
Entries 4490352
sumB
DSM board

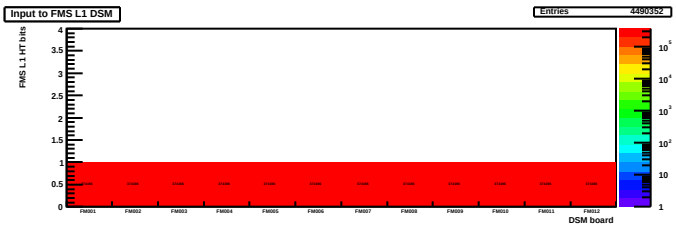

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

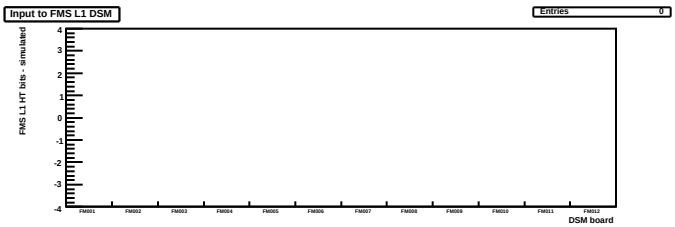

Input to FMS L1 DSM
Entries 4490352
sumAB
DSM board

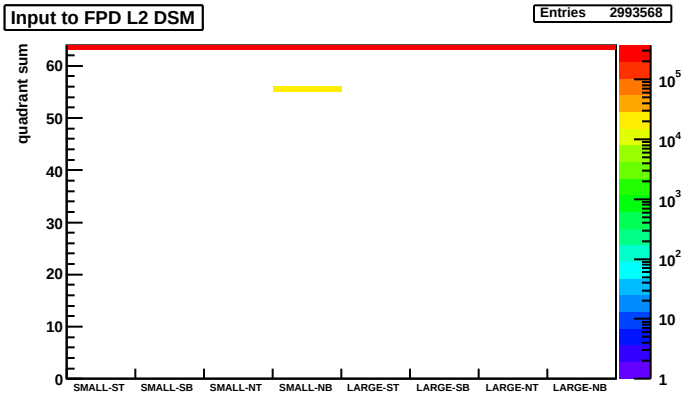

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

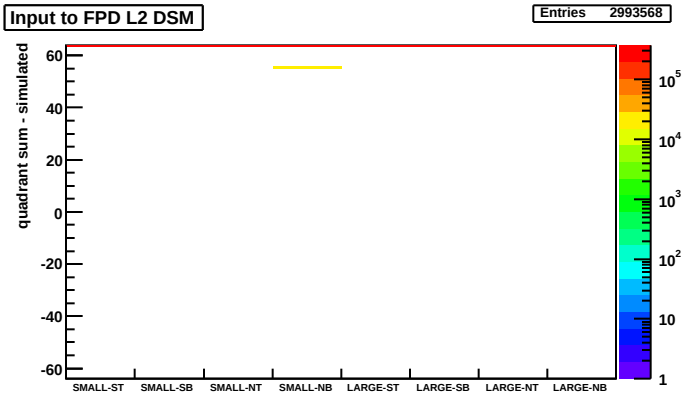

Input to FMS L1 DSM
Entries 4490352
sumA
DSM board

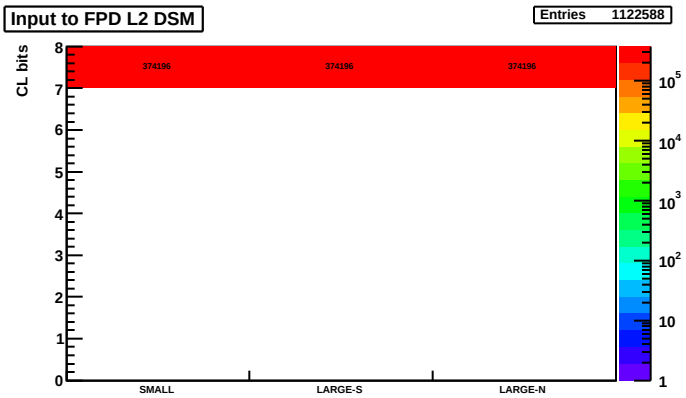

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

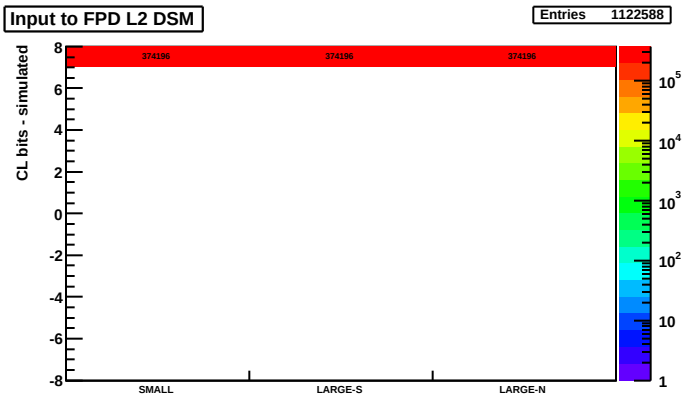

Input to FMS L1 DSM
Entries 4490352
FMS L1 HT bits
DSM board

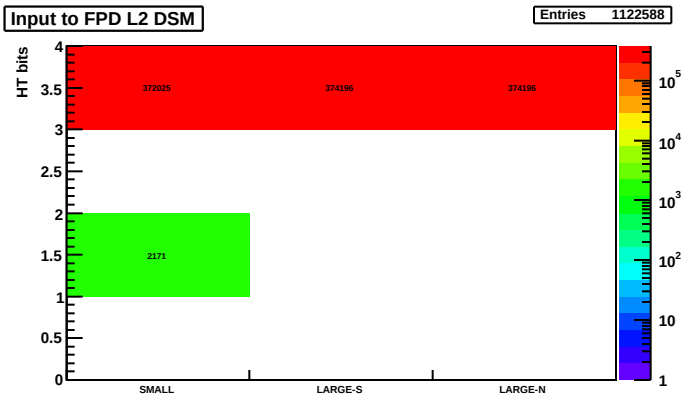

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

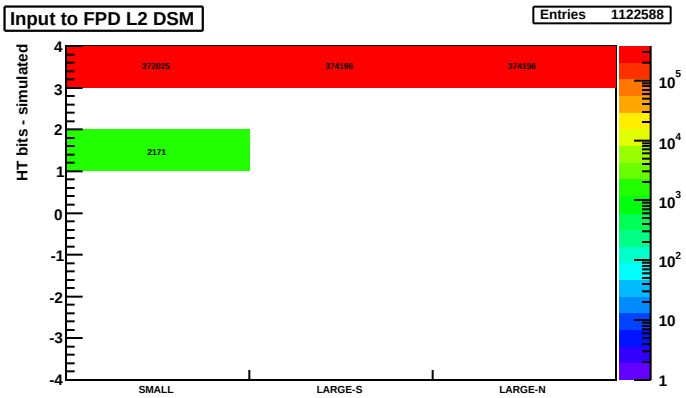

Input to FPD L2 DSM
Entries 2993568
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

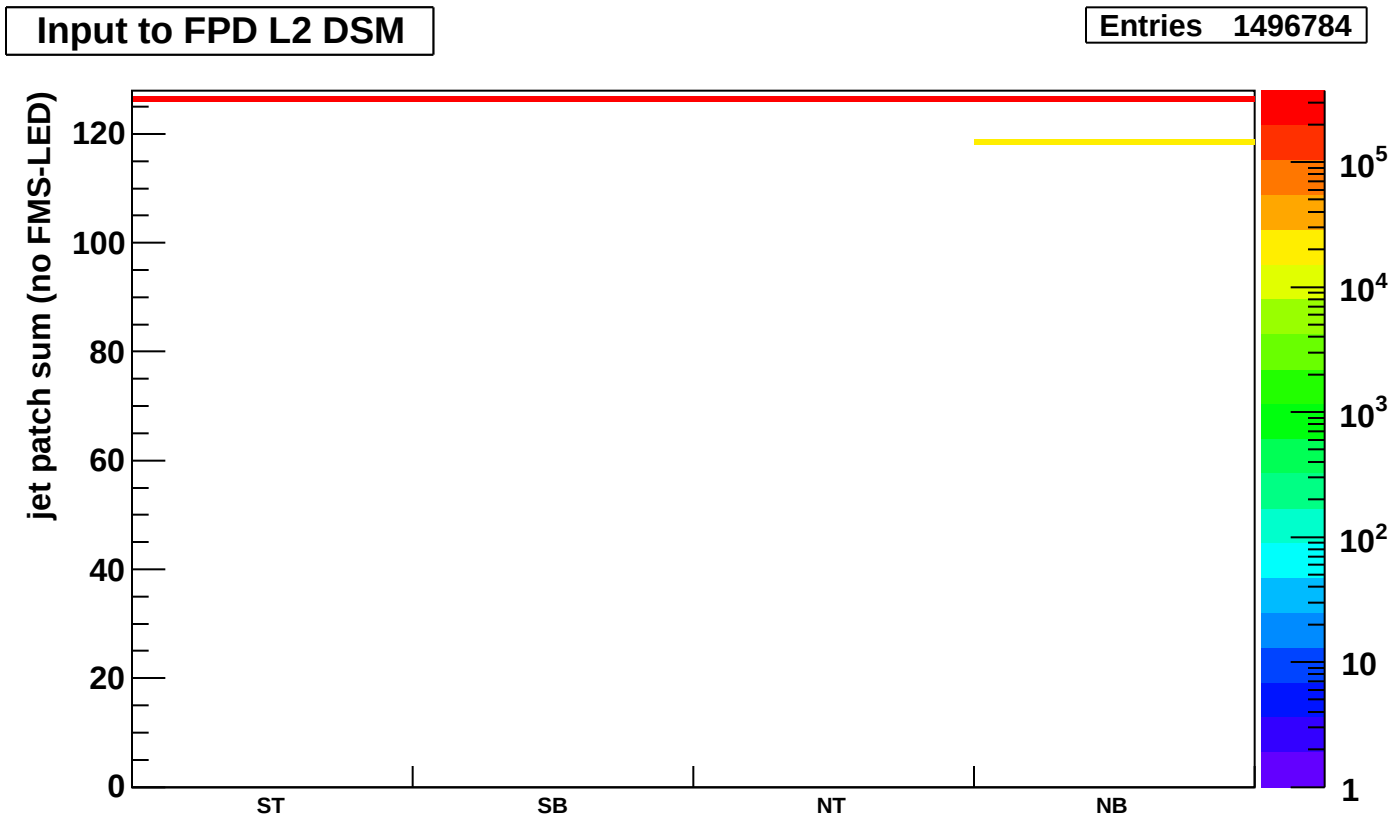

Input to FPD L2 DSM
Entries 2993568
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

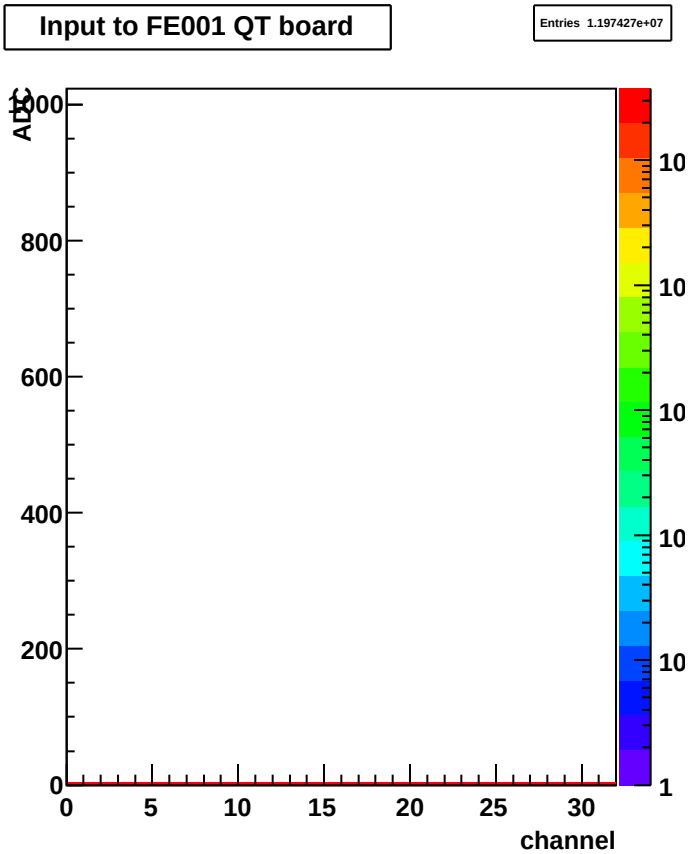

Input to FPD L2 DSM
Entries 1122588
CL bits
374196
374196
374196
SMALL
LARGE-S
LARGE-N

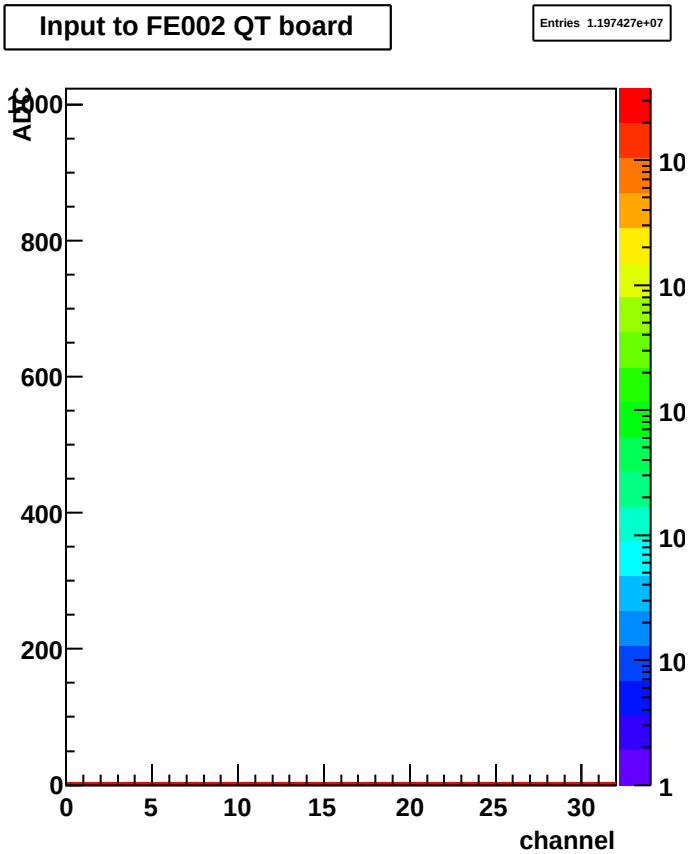

Input to FPD L2 DSM
Entries 1122588
CL bits - simulated
374196
374196
374196
SMALL
LARGE-S
LARGE-N

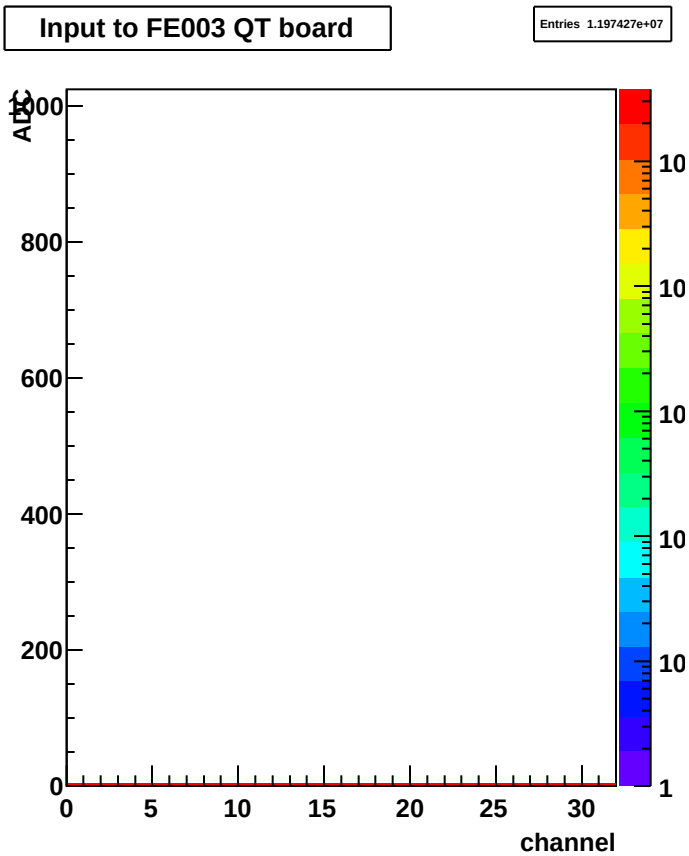

Input to FPD L2 DSM
Entries 1122588
HT bits
372025
374196
374196
2171
SMALL
LARGE-S
LARGE-N

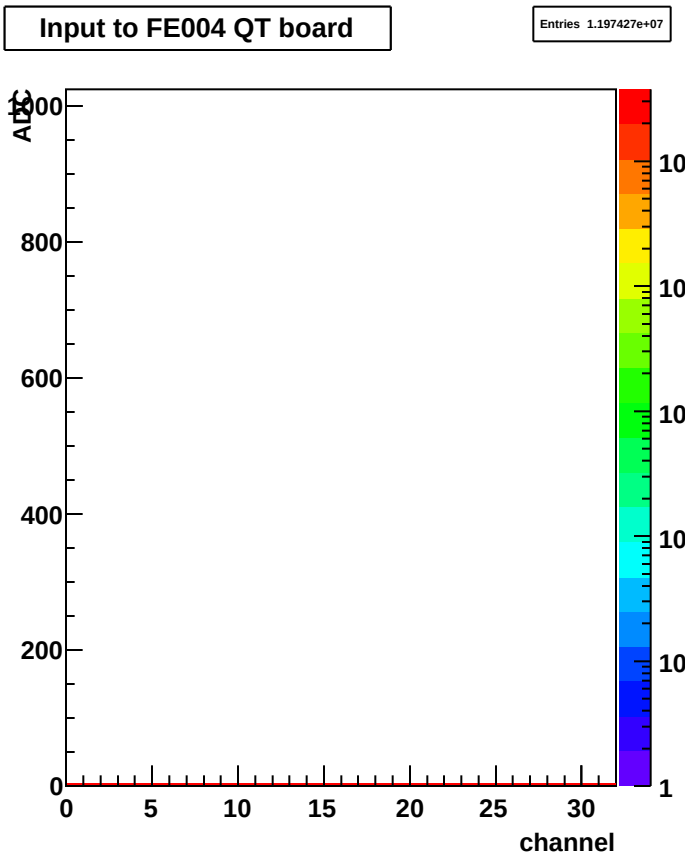

Input to FPD L2 DSM
Entries 1122588
HT bits - simulated
372025
374196
374196
2171
SMALL
LARGE-S
LARGE-N

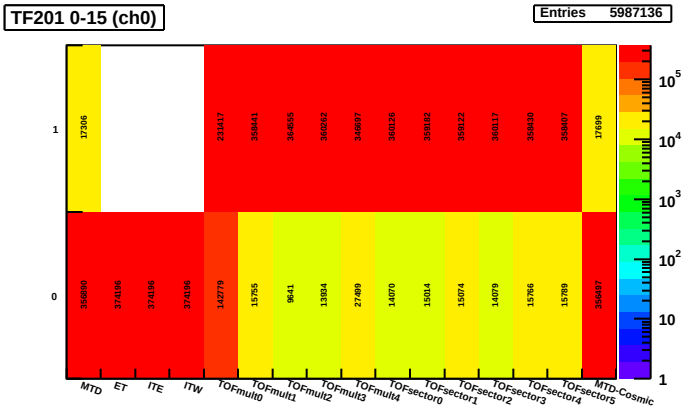

Input to FPD L2 DSM
Entries 1496784
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

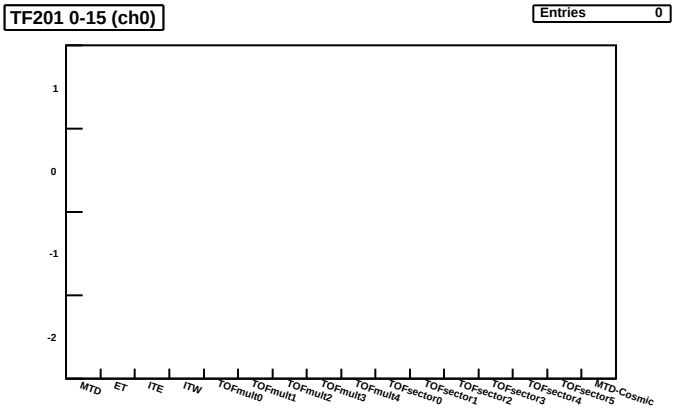

Input to FE001 QT board
Entries 1.197427e+07
ADC
channel

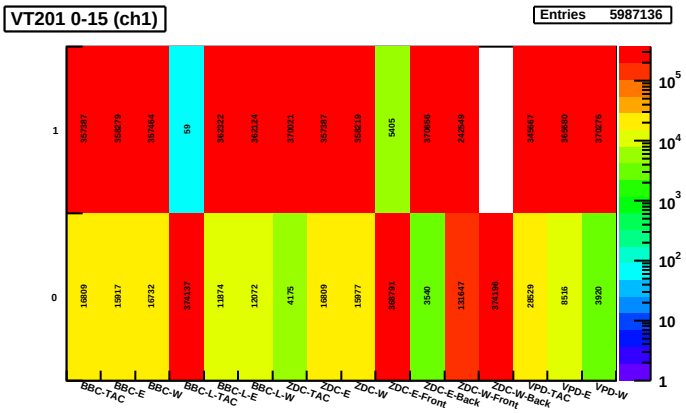

Input to FE002 QT board
Entries 1.197427e+07
ADC
channel

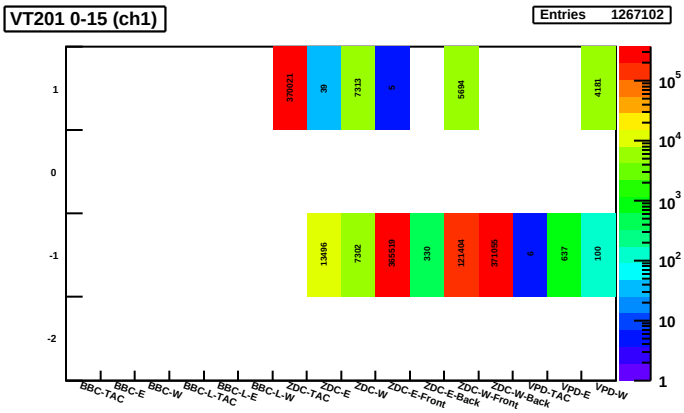

Input to FE003 QT board
Entries 1.197427e+07
ADC
channel

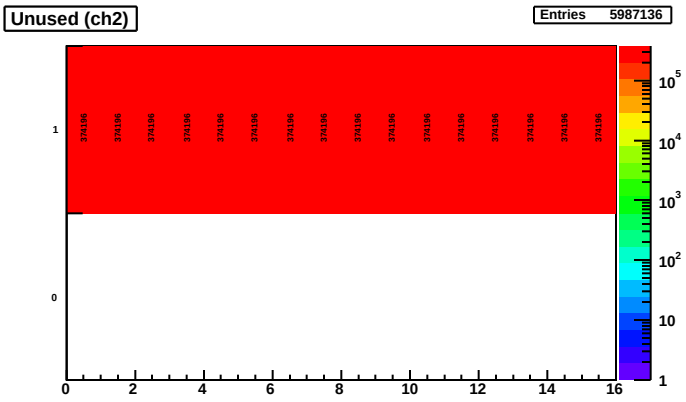

Input to FE004 QT board
Entries 1.197427e+07
ADC
channel

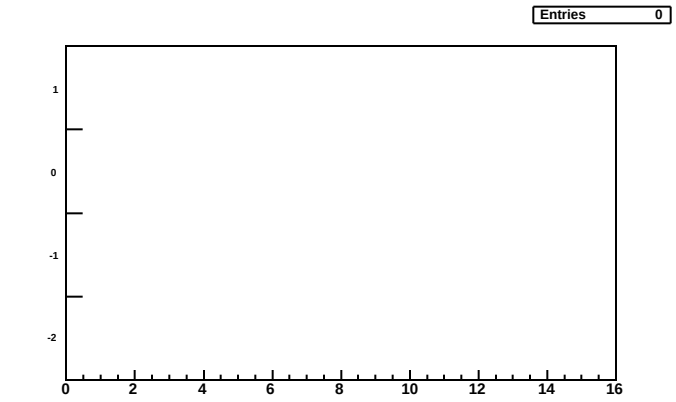

TF201 0-15 (ch0)
Entries 5987136
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

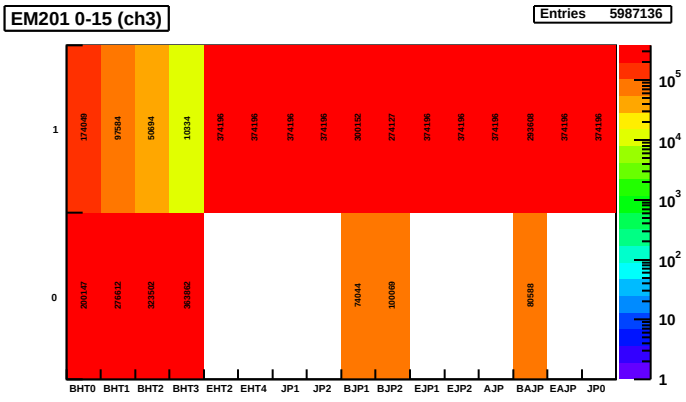

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

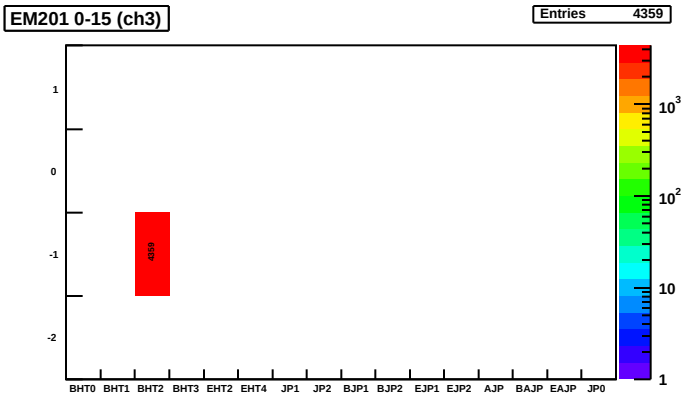

VT201 0-15 (ch1)
Entries 5987136
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 1267102
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

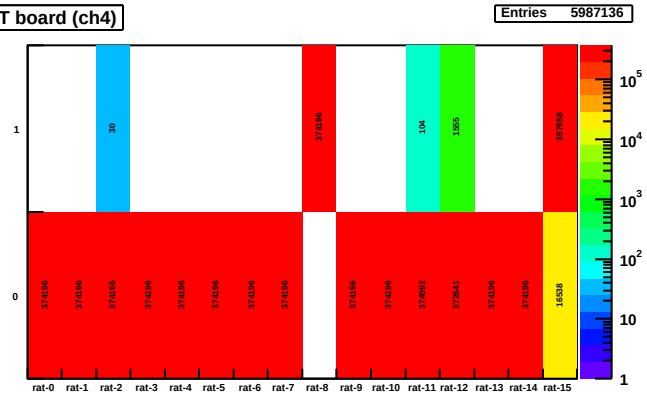

Unused (ch2)
Entries 5987136

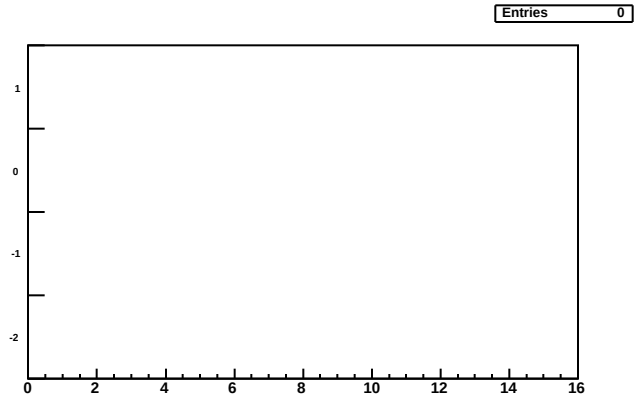

Entries 0


EM201 0-15 (ch3)
Entries 5987136
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

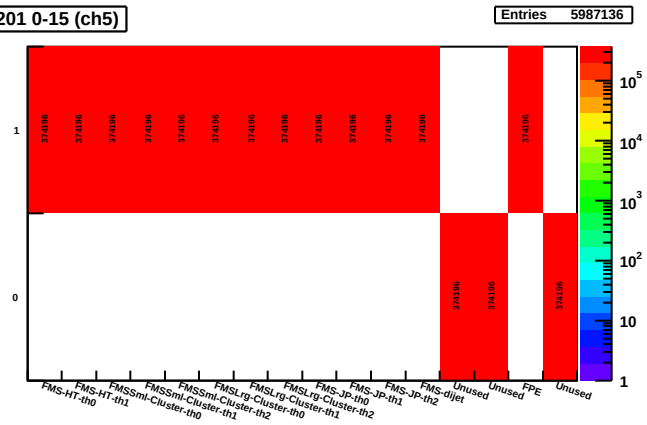

EM201 0-15 (ch3)
Entries 4359
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

RAT board (ch4)

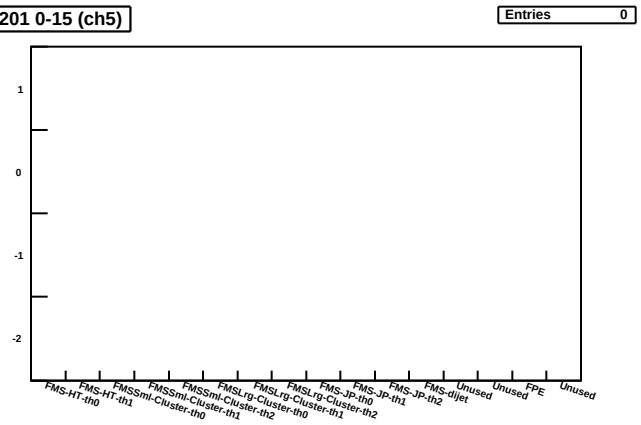




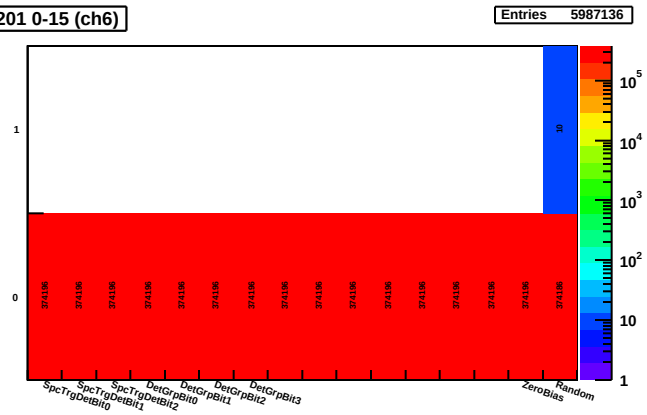
FP201 0-15 (ch5)

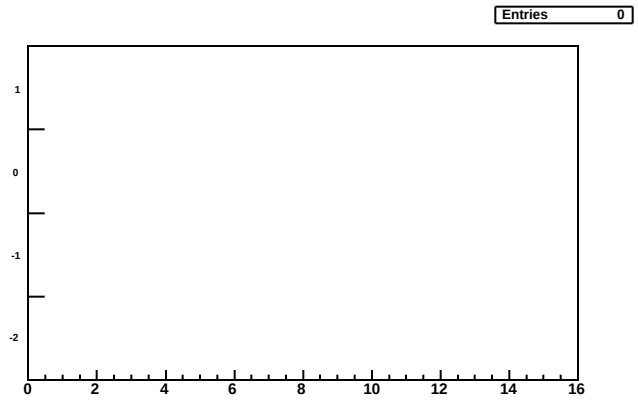
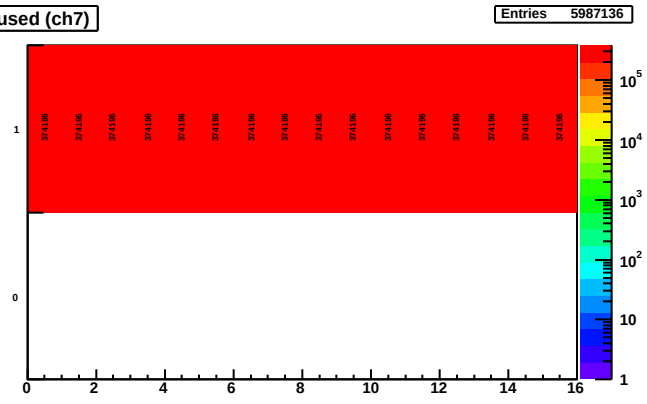
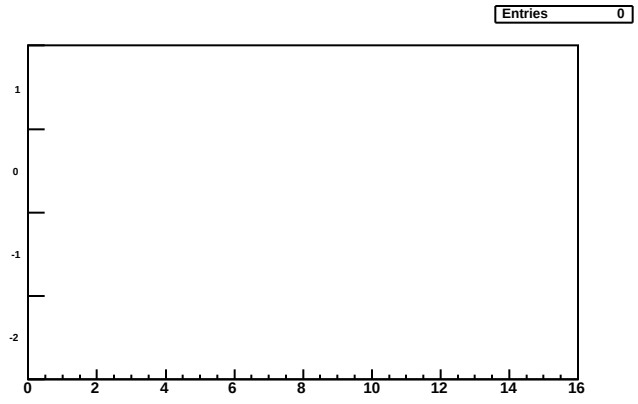


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)