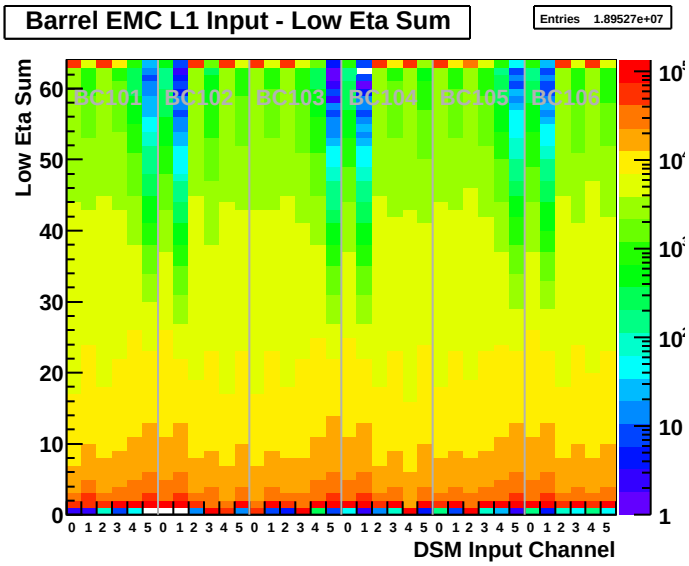
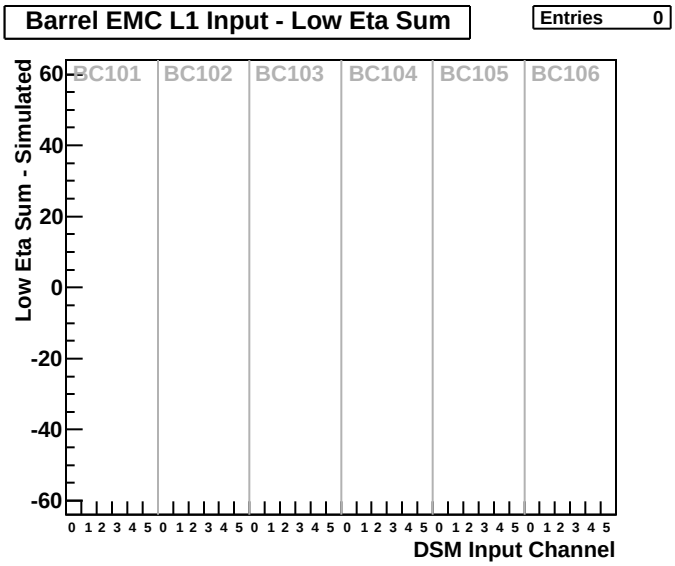
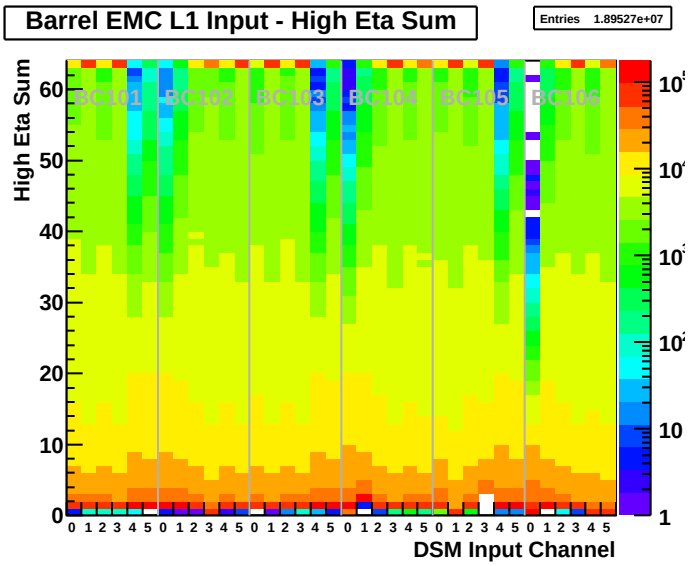
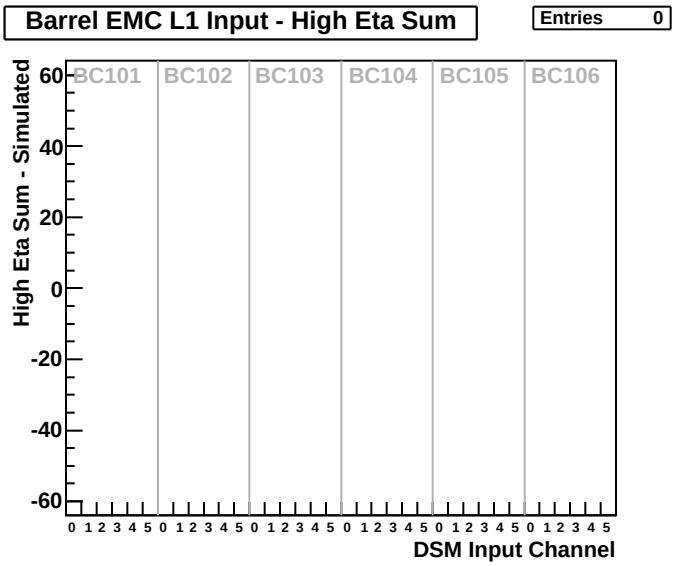
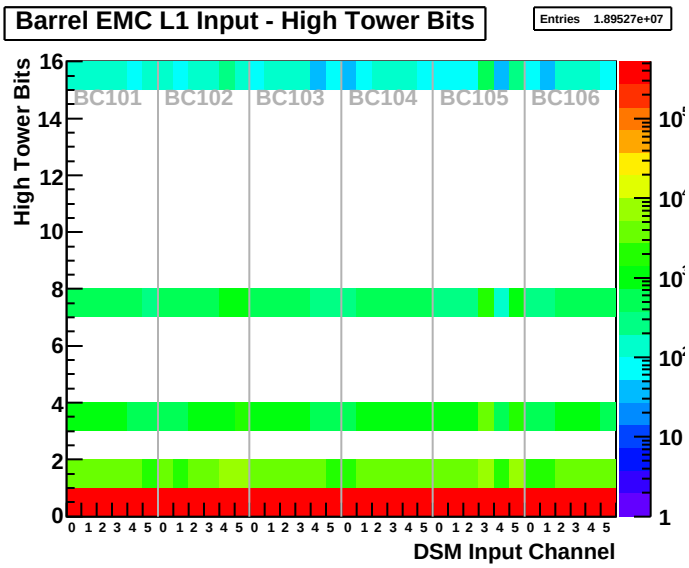
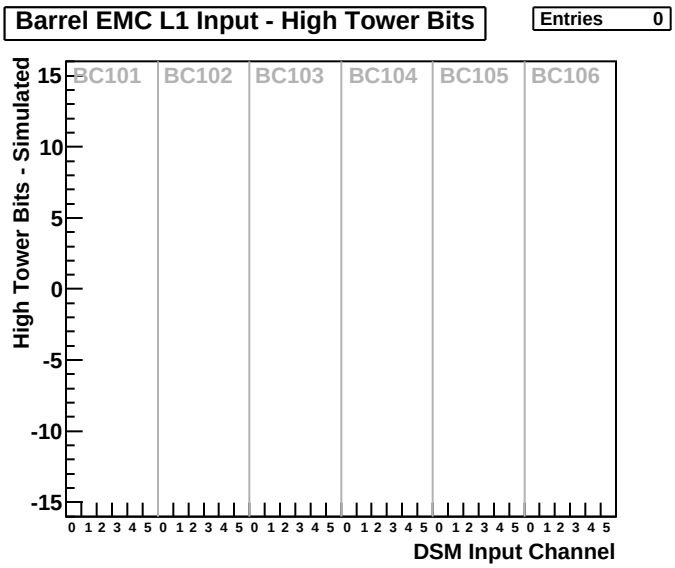

Barrel EMC L1 Input - Low Eta Sum
Entries 1.89527e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

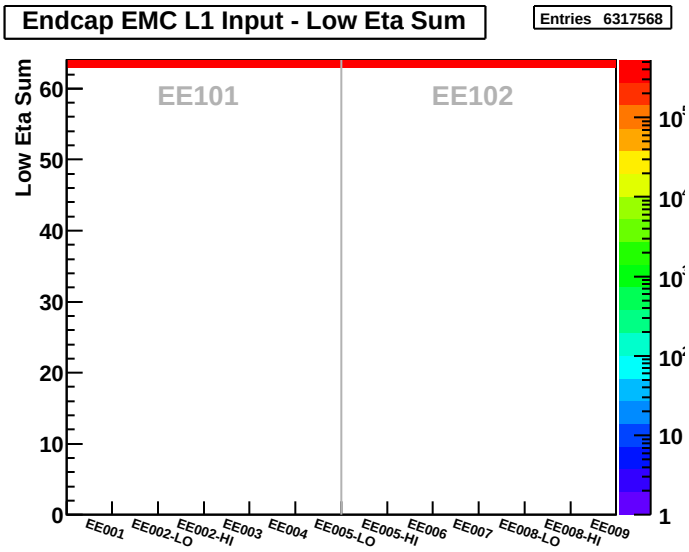

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

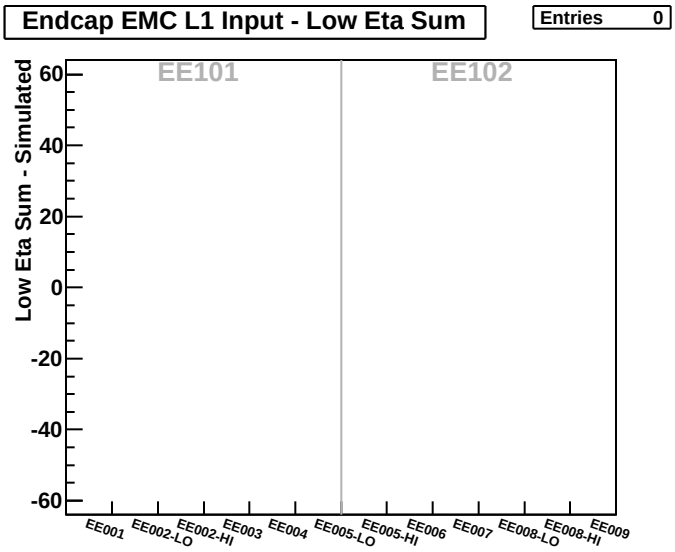

Barrel EMC L1 Input - High Eta Sum
Entries 1.89527e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

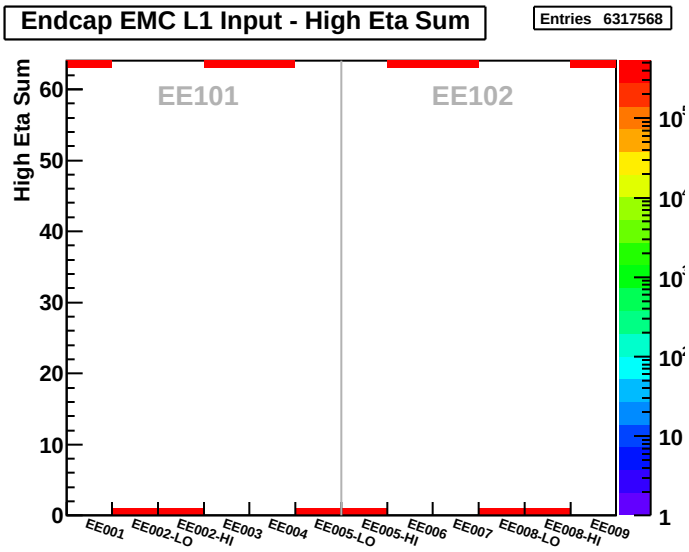

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

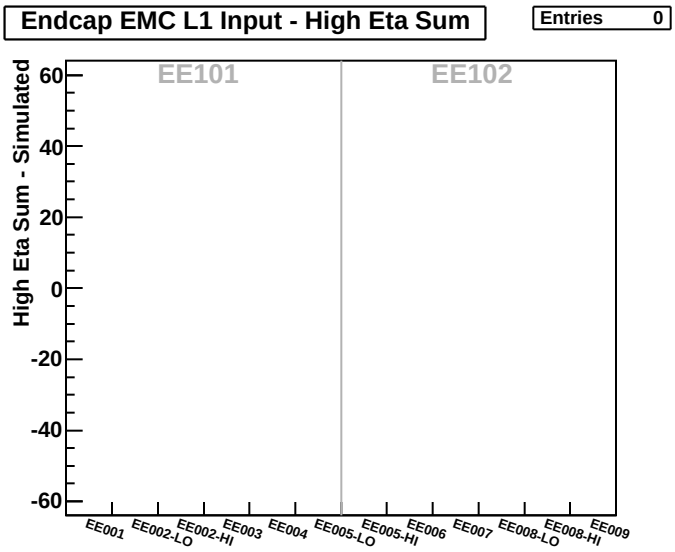

Barrel EMC L1 Input - High Tower Bits
Entries 1.89527e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

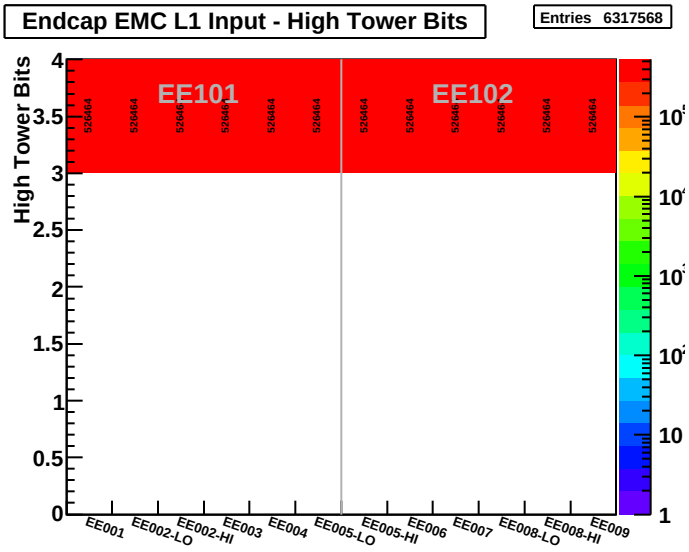

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

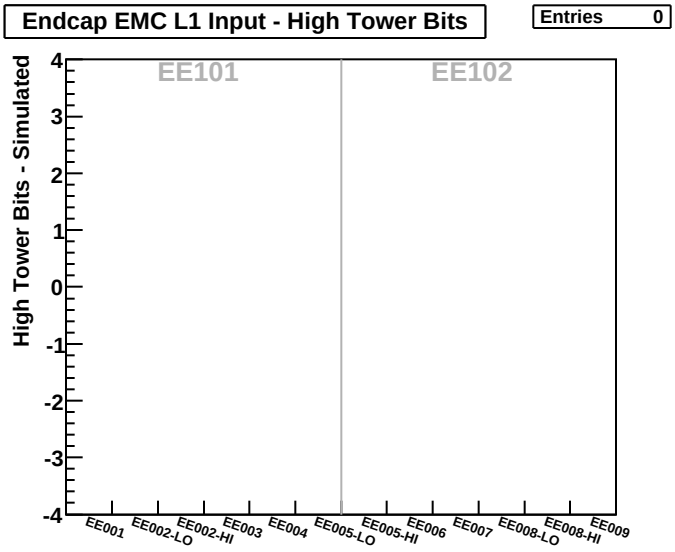

Endcap EMC L1 Input - Low Eta Sum
Entries 6317568
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

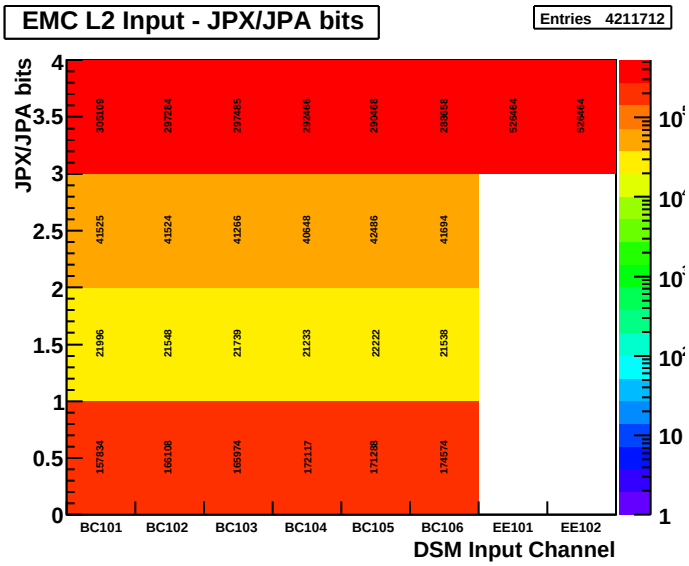

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

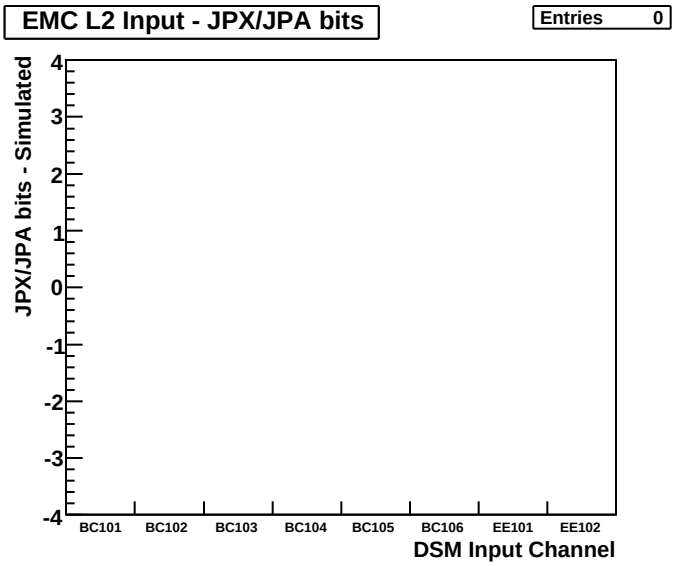

Endcap EMC L1 Input - High Eta Sum
Entries 6317568
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

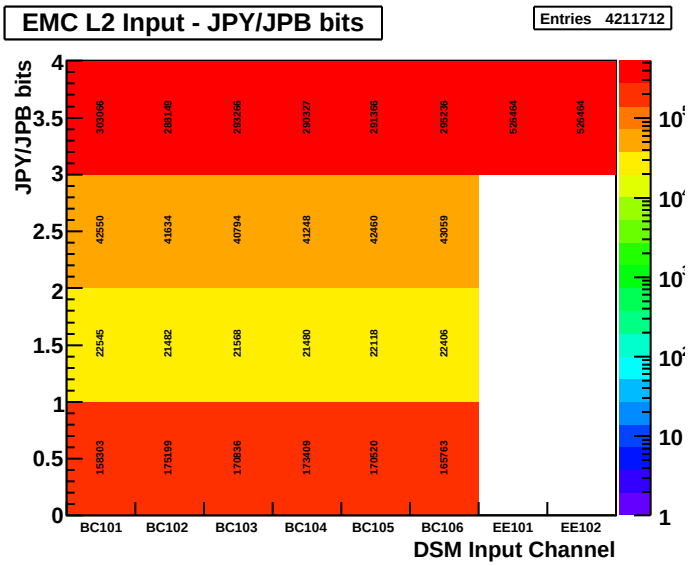

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

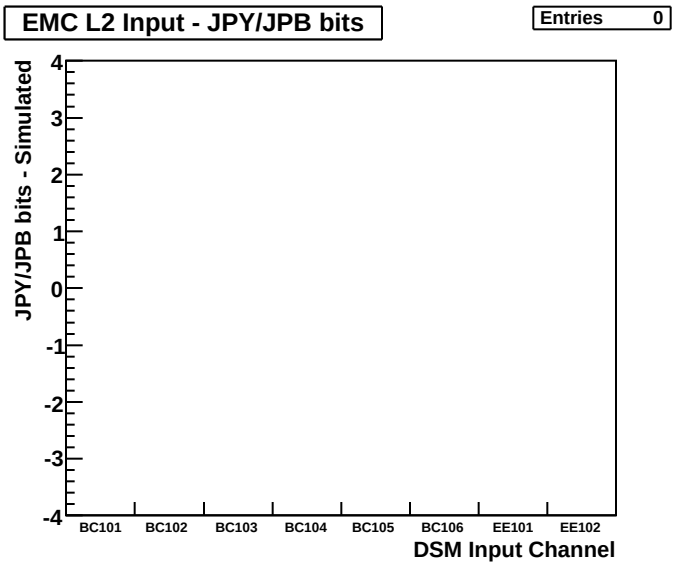

Endcap EMC L1 Input - High Tower Bits
Entries 6317568
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

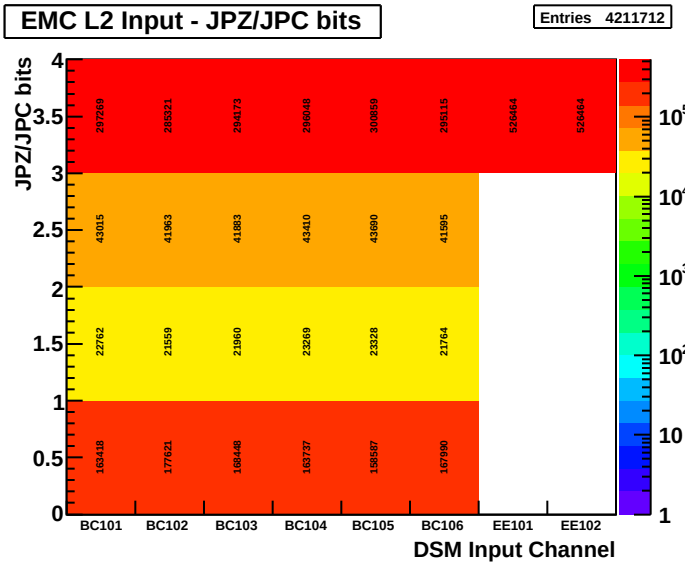

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

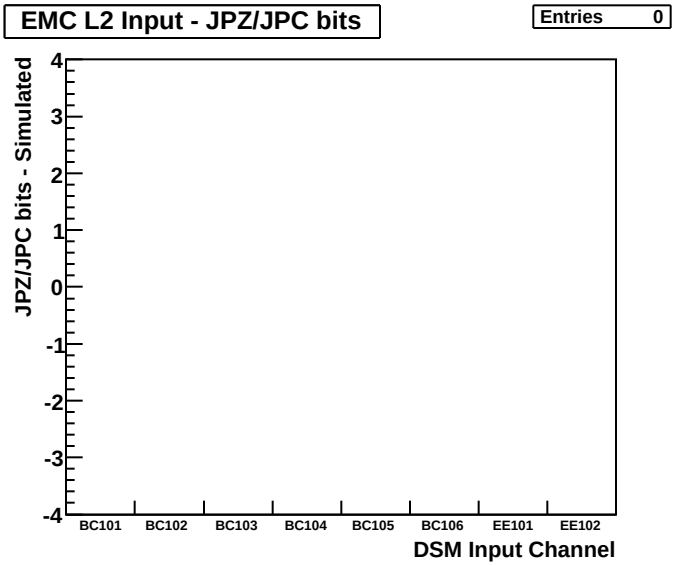

EMC L2 Input - JPX/JPA bits
Entries 4211712
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
305109 297284 297485 292466 290468 288658 526464 526464
41525 41524 41266 40648 42486 41694
21996 21548 21739 21233 22222 21538
157834 166108 165974 172117 171288 174574

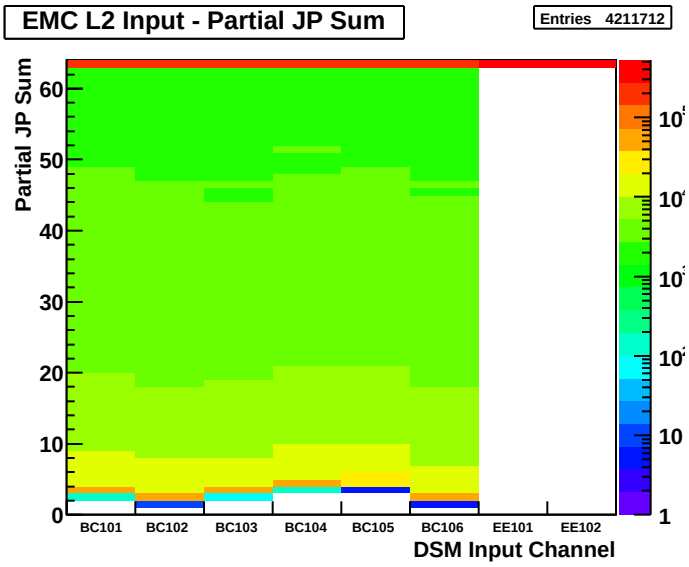

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

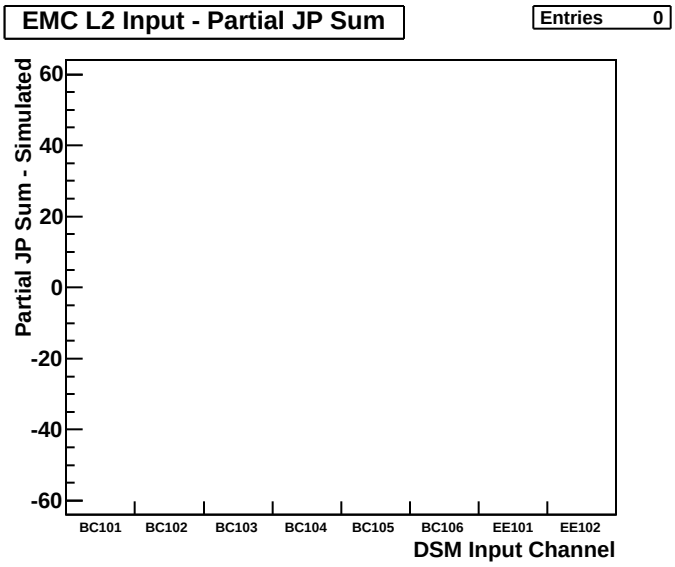

EMC L2 Input - JPY/JPB bits
Entries 4211712
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
303066 280149 293266 290327 291366 295236 526464 526464
42550 41634 40794 41248 42460 43059
22545 21482 21568 21480 22118 22406
158303 175199 170836 173409 170520 165763

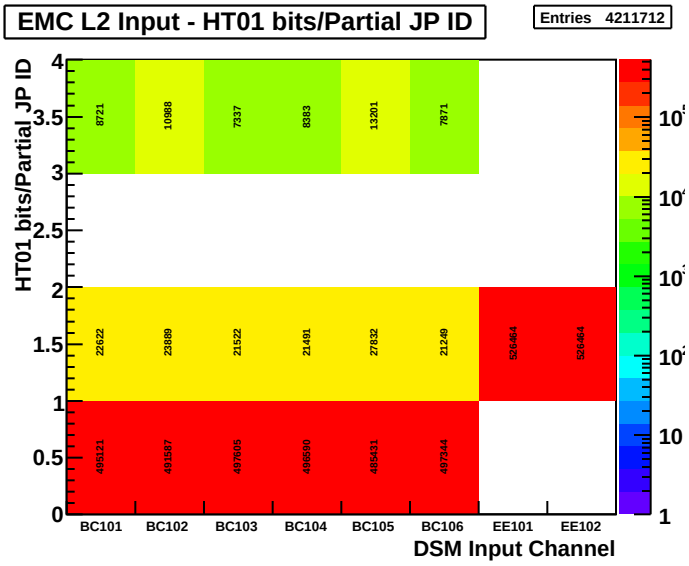

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

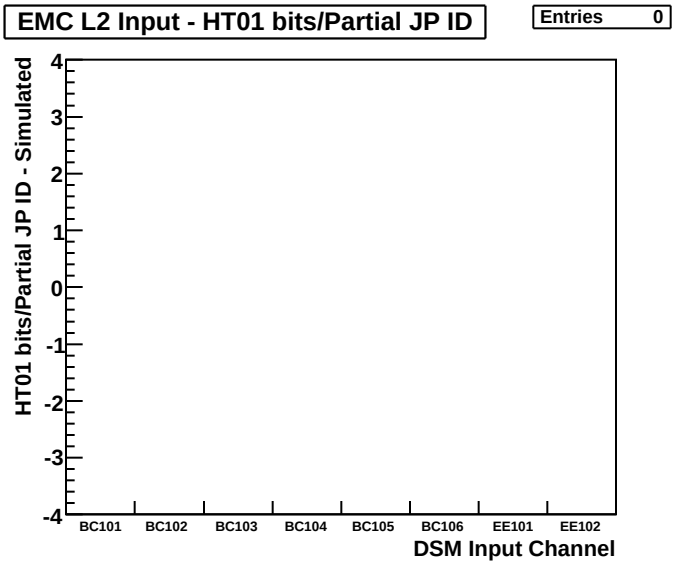

EMC L2 Input - JPZ/JPC bits
Entries 4211712
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
297269 283321 294173 290048 300859 295115 526464 526464
43015 41963 41883 43410 43690 41595
22762 21559 21960 23269 23328 21764
163418 177621 168448 163737 158587 167990

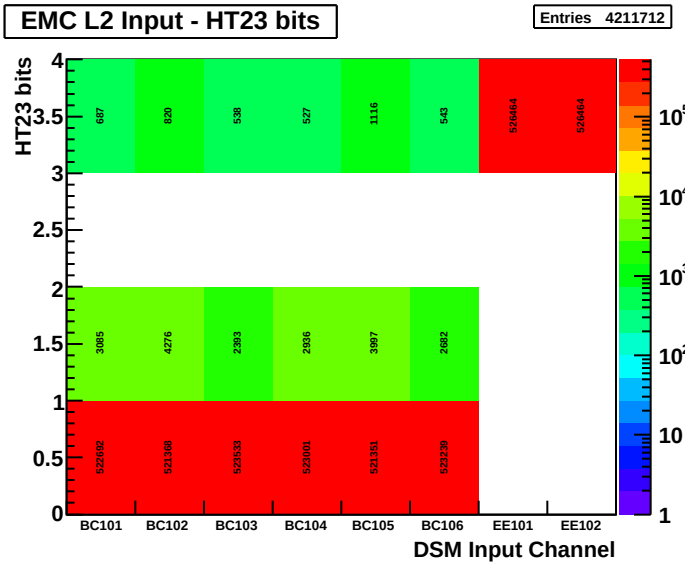

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

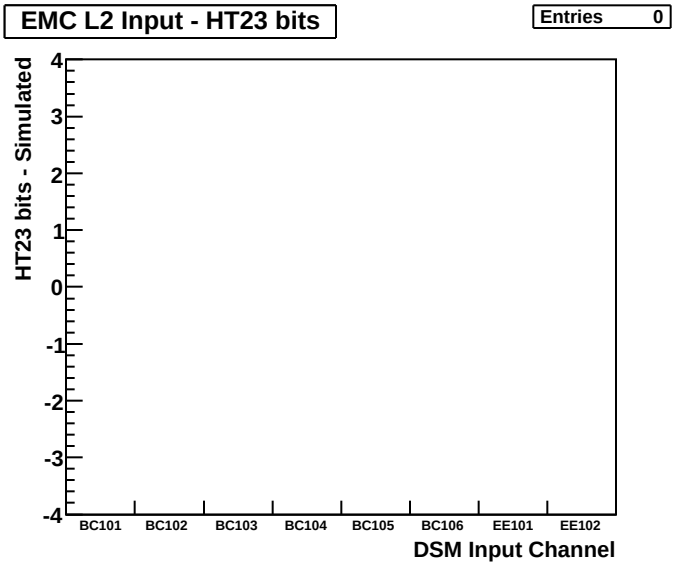

EMC L2 Input - Partial JP Sum
Entries 4211712
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

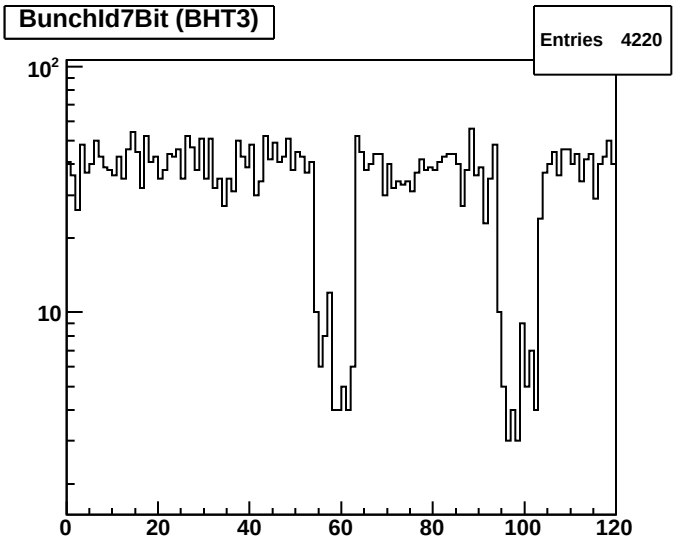

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

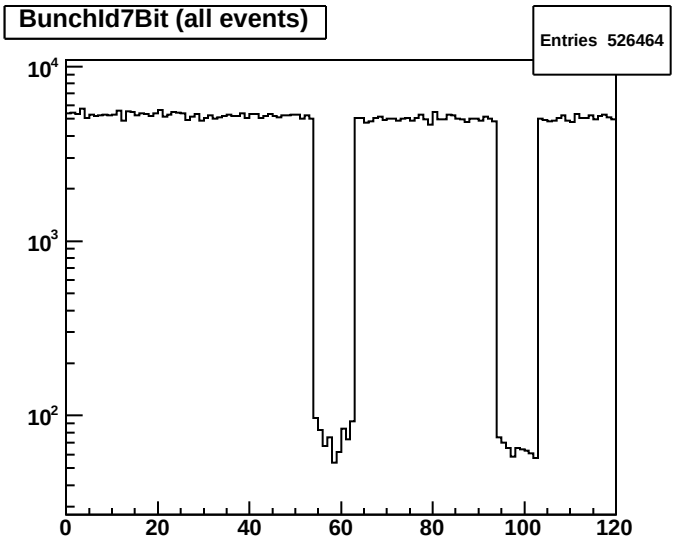

EMC L2 Input - HT01 bits/Partial JP ID
Entries 4211712
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

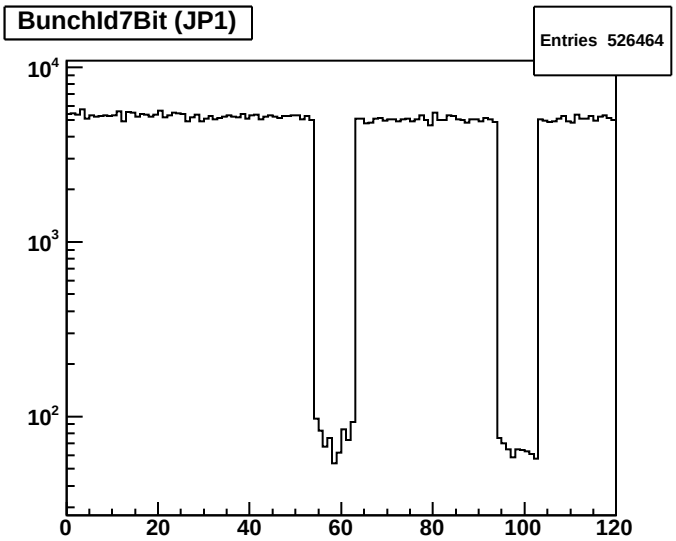

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

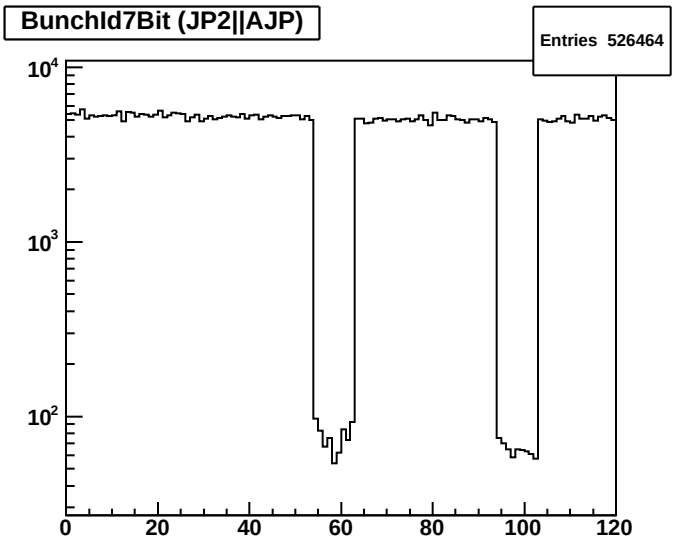

EMC L2 Input - HT23 bits
Entries 4211712
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

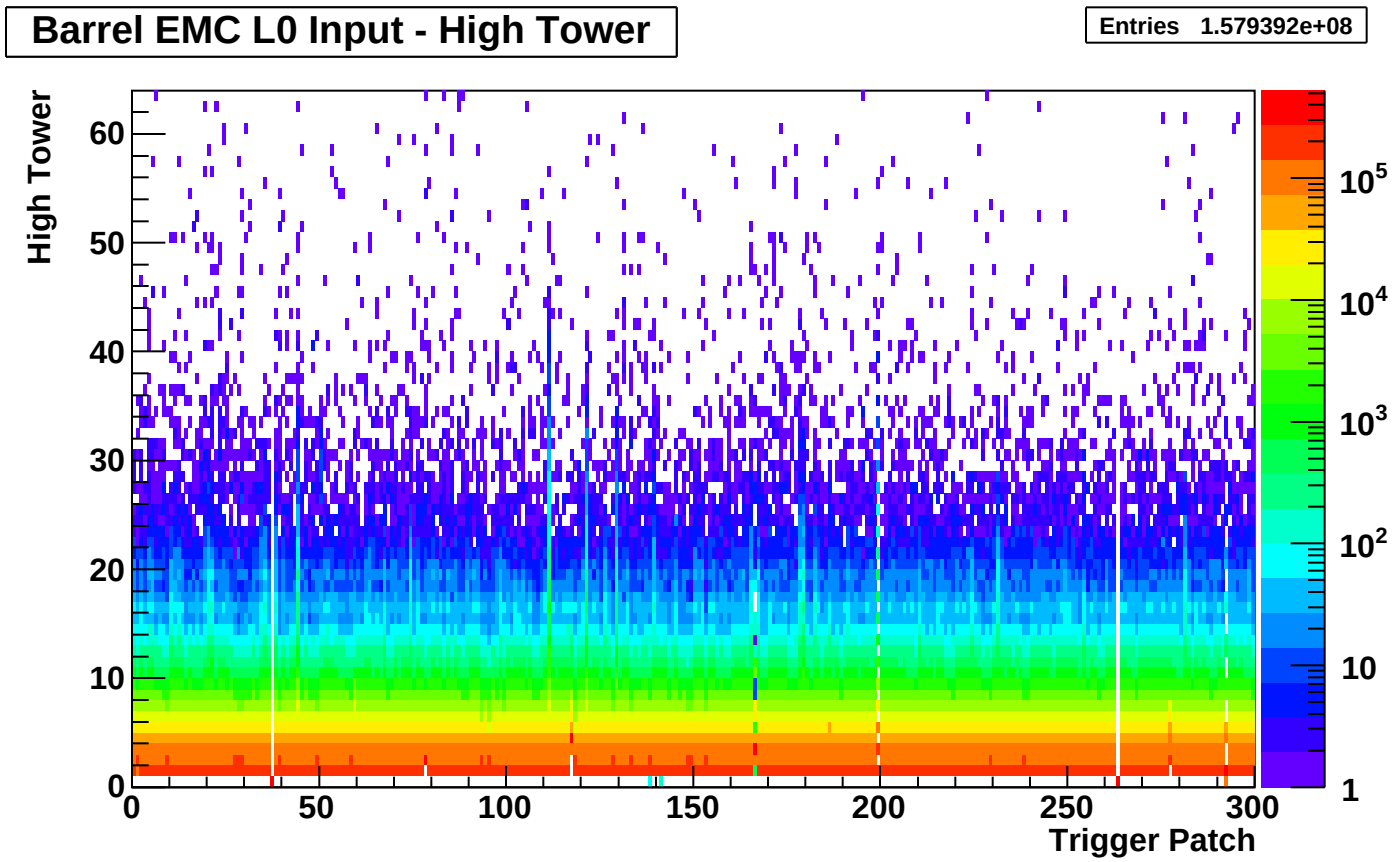

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

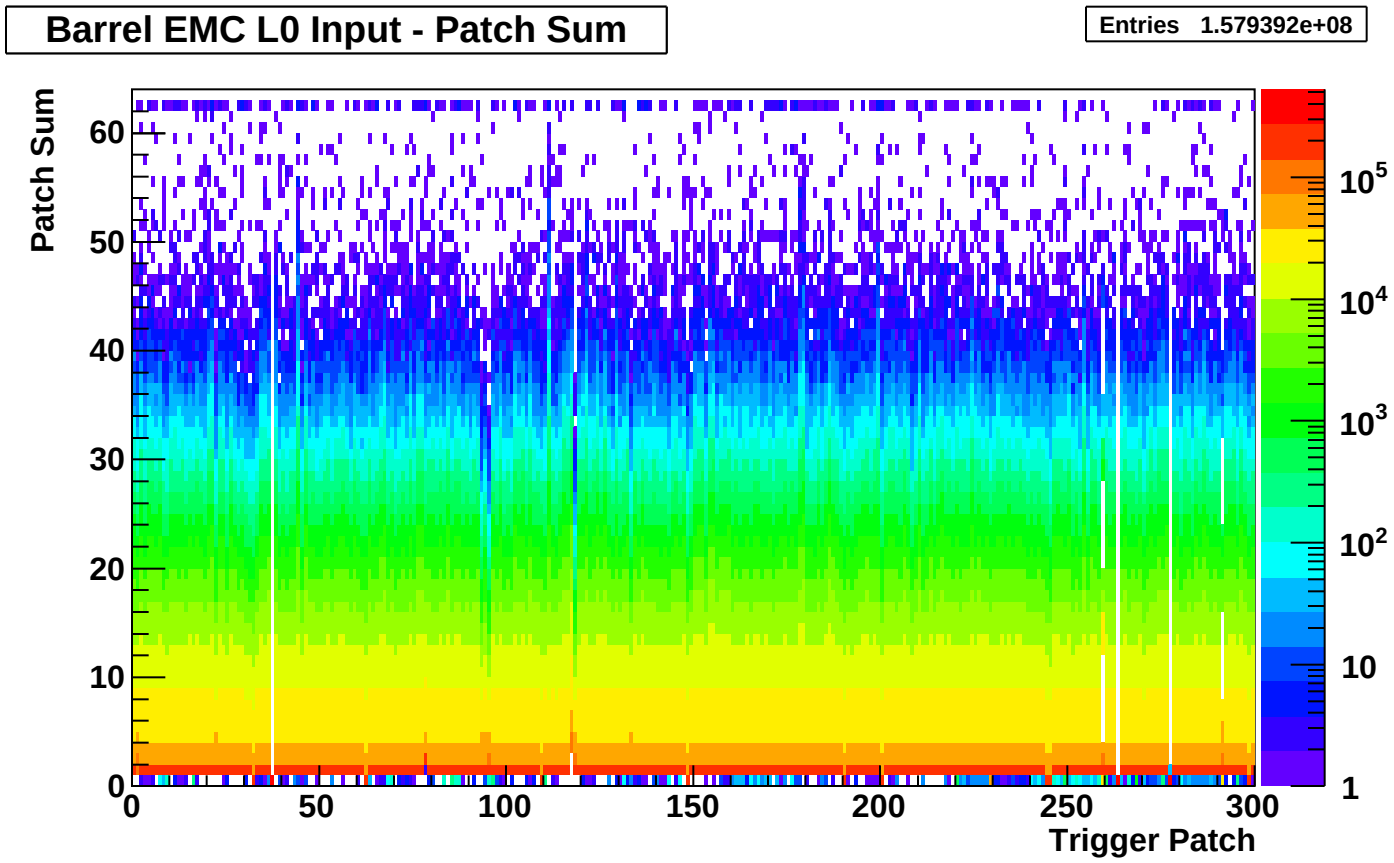

BunchId7Bit (BHT3)
Entries 4220
$10^2$
10
0
20
40
60
80
100
120

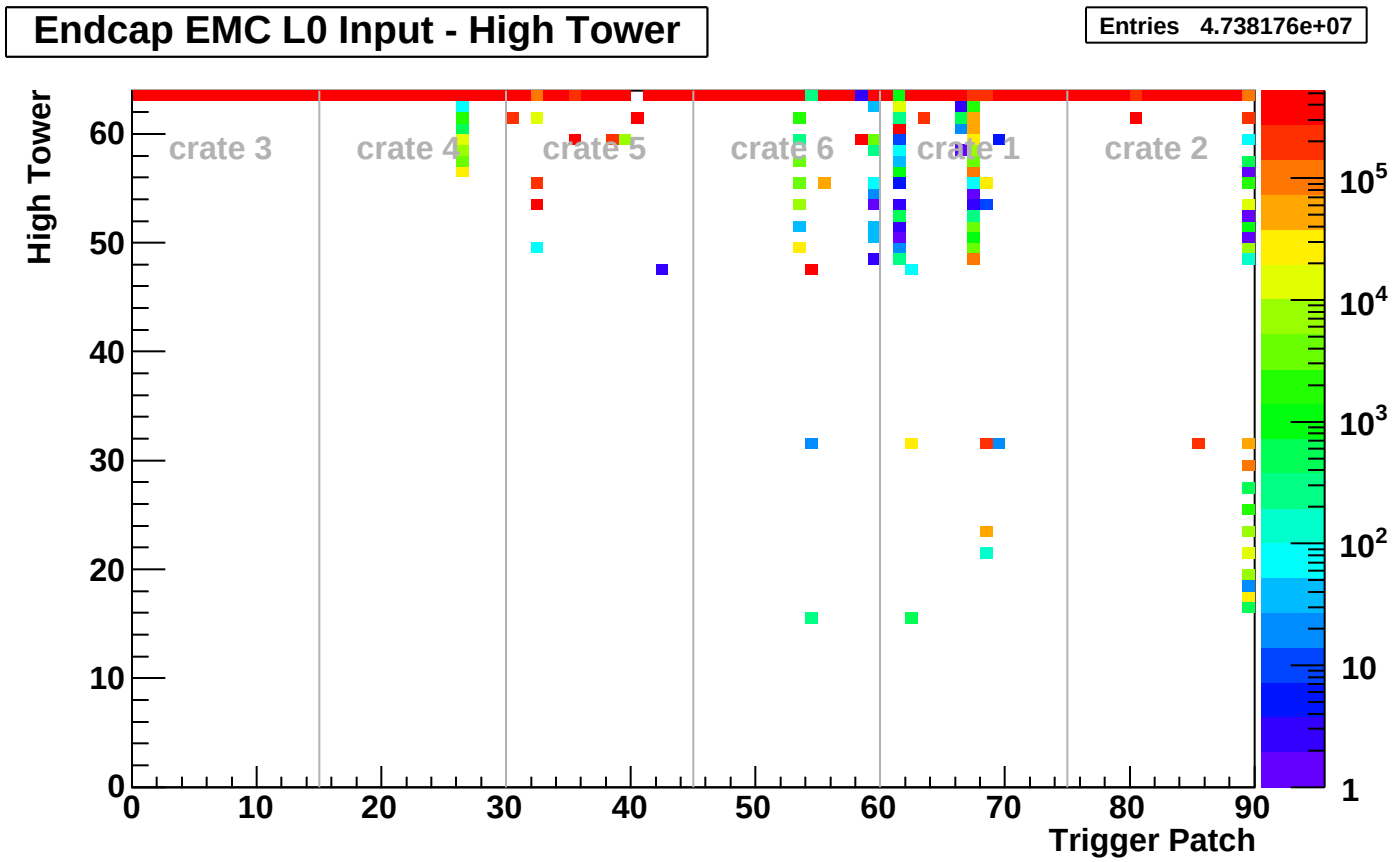

BunchId7Bit (all events)
Entries 526464
$10^4$
$10^3$
$10^2$
0
20
40
60
80
100
120

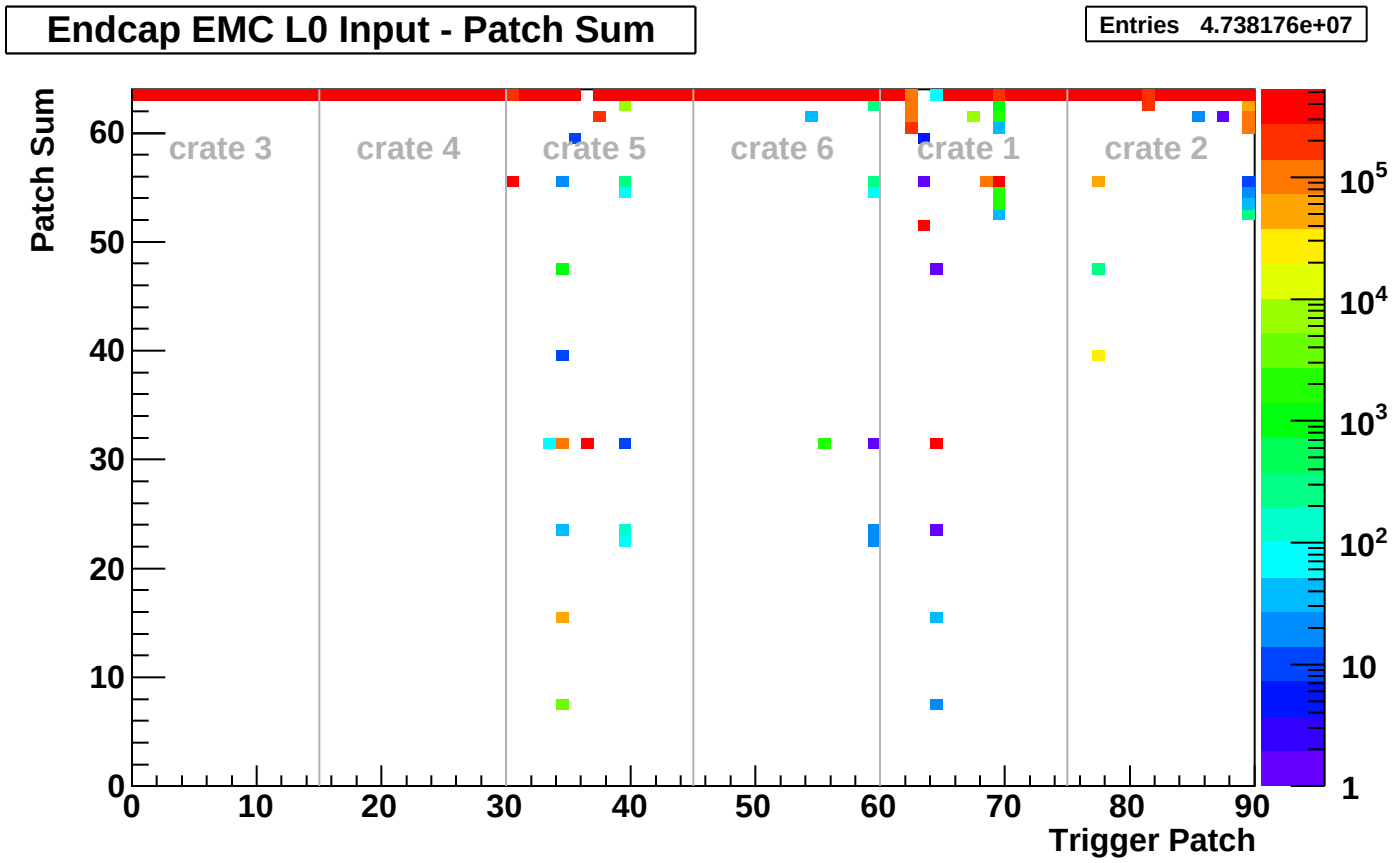

BunchId7Bit (JP1)
Entries 526464
$10^4$
$10^3$
$10^2$
0
20
40
60
80
100
120

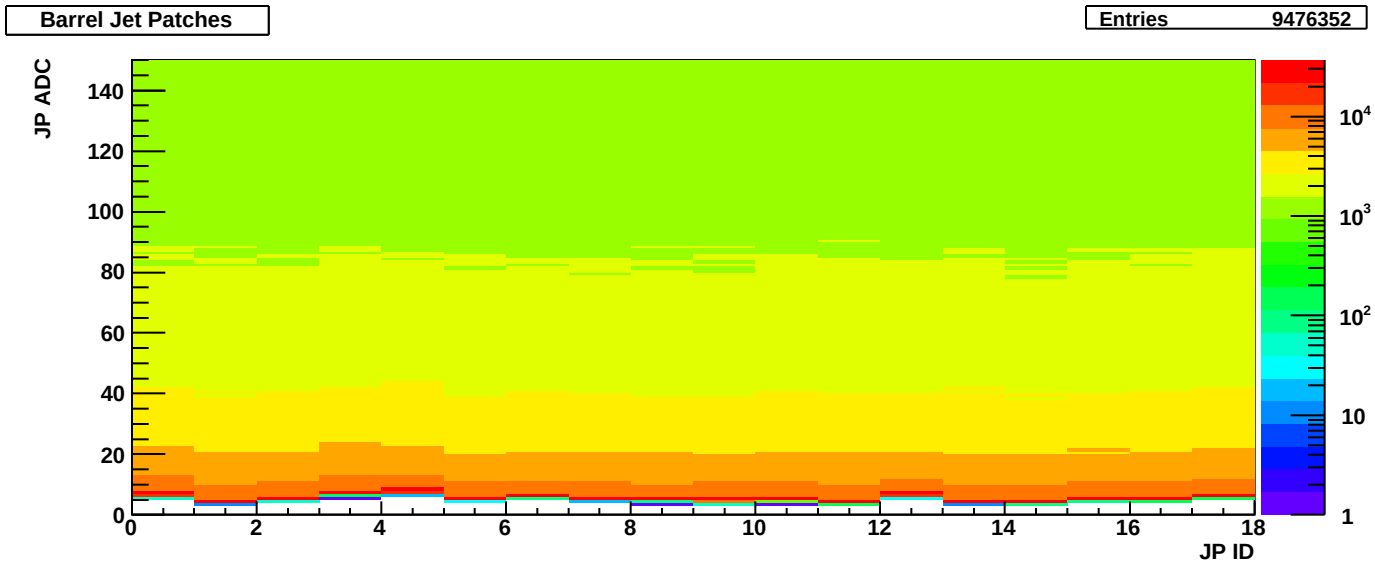

BunchId7Bit (JP2||AJP)
Entries 526464
$10^4$
$10^3$
$10^2$
0
20
40
60
80
100
120

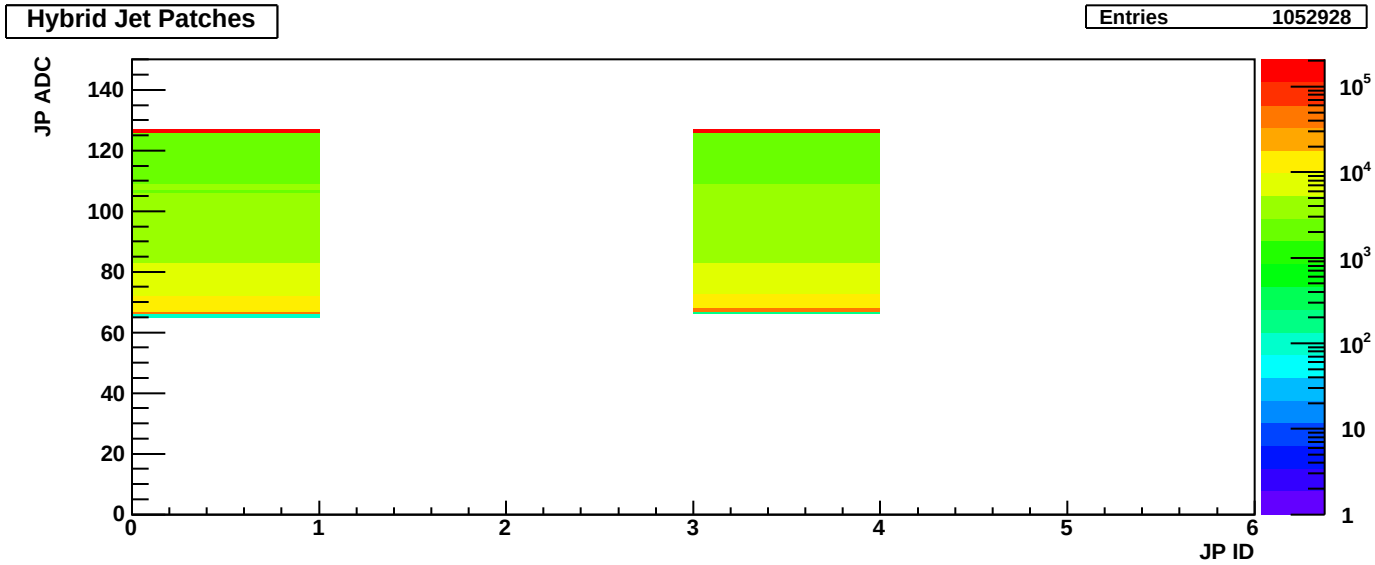

Barrel EMC L0 Input - High Tower
Entries 1.579392e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$
$10^4$
$10^5$

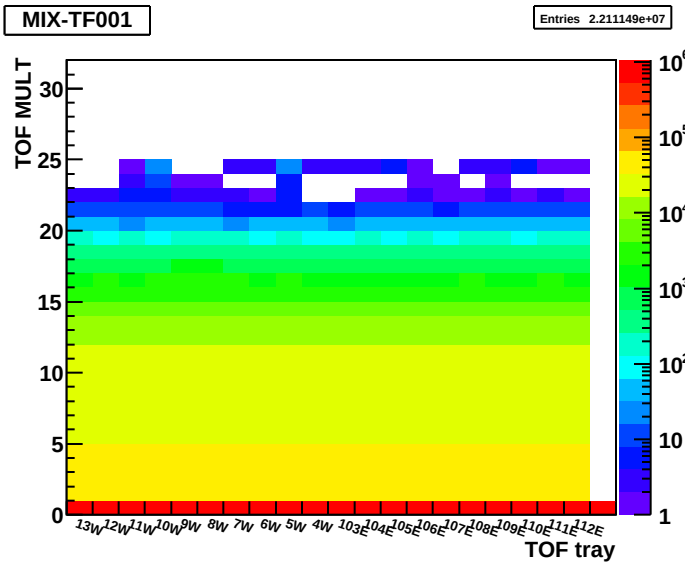

Barrel EMC L0 Input - Patch Sum
Entries 1.579392e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$
$10^4$
$10^5$

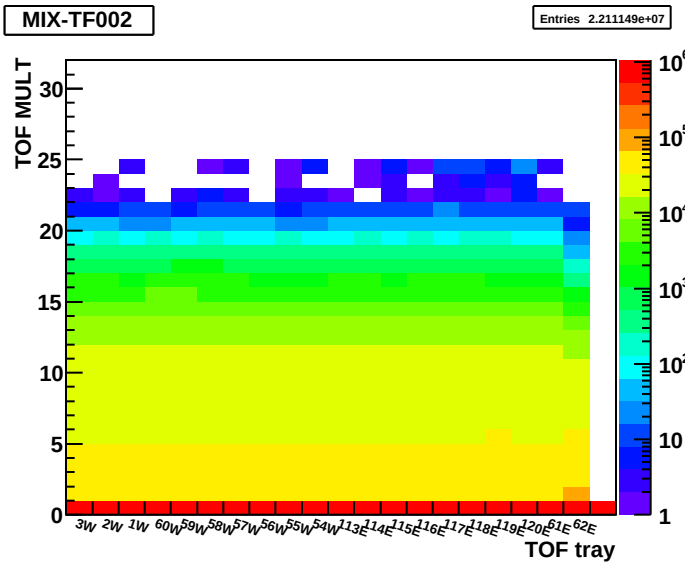

Endcap EMC L0 Input - High Tower
Entries 4.738176e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

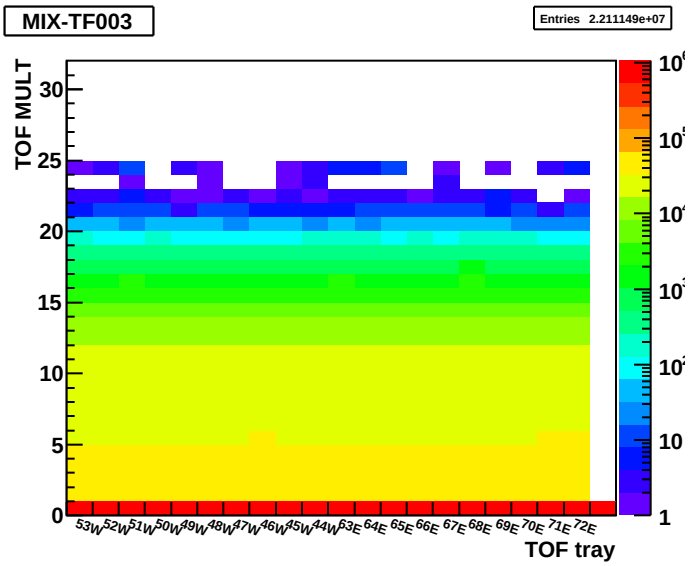

Endcap EMC L0 Input - Patch Sum
Entries 4.738176e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

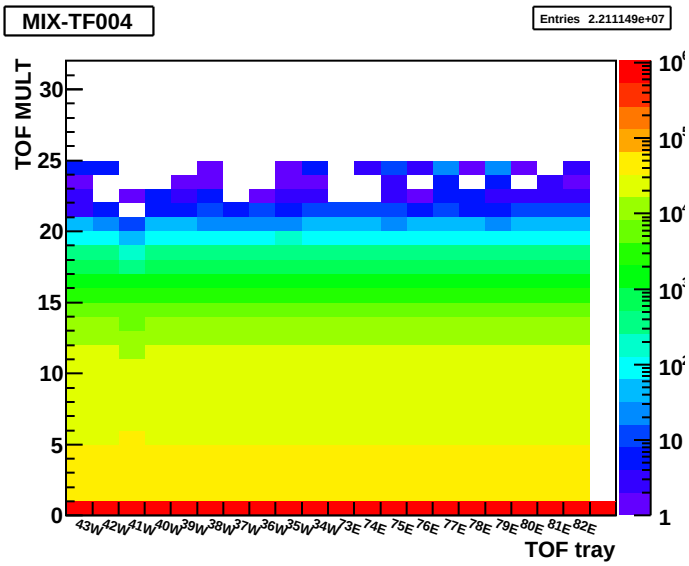

Barrel Jet Patches
Entries 9476352
JP ADC
JP ID

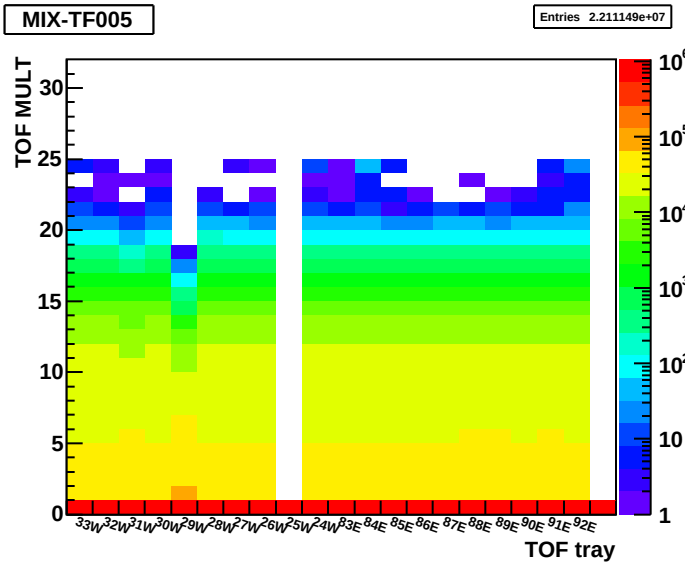

Hybrid Jet Patches
Entries 1052928
JP ADC
JP ID

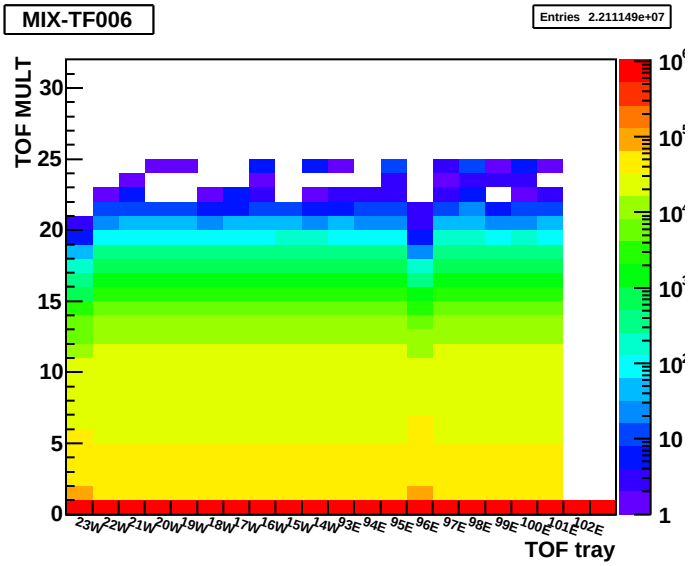

MIX-TF001
Entries 2.211149e+07
TOF MULT
TOF tray

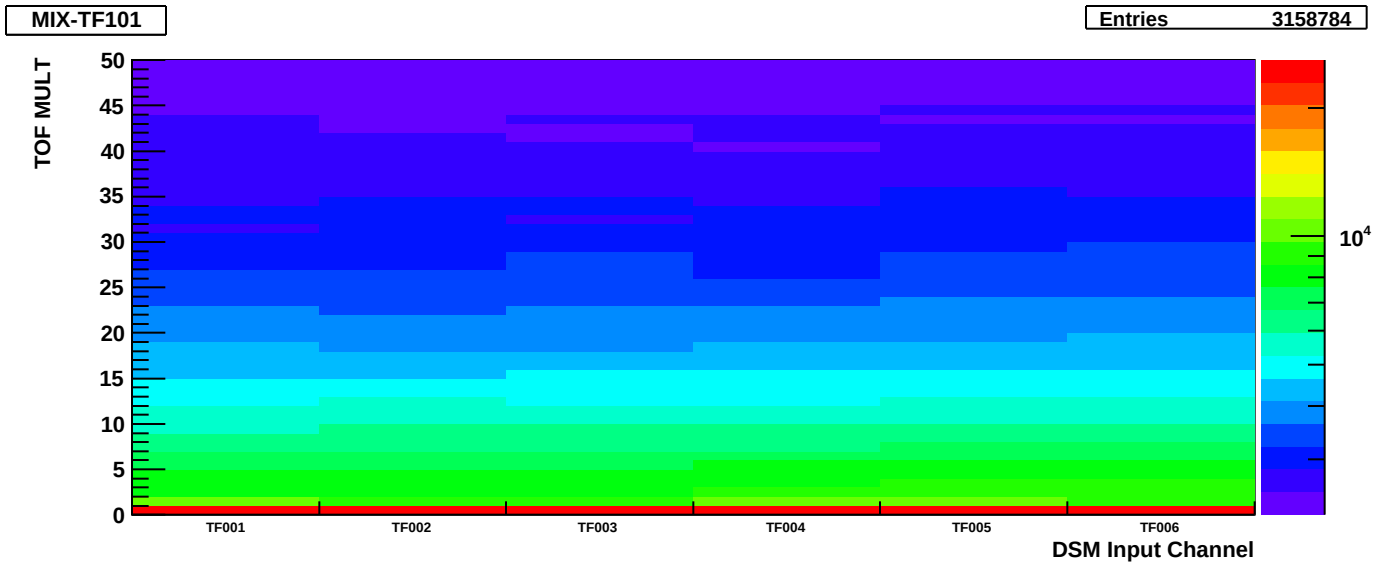

MIX-TF002
Entries 2.211149e+07
TOF MULT
TOF tray

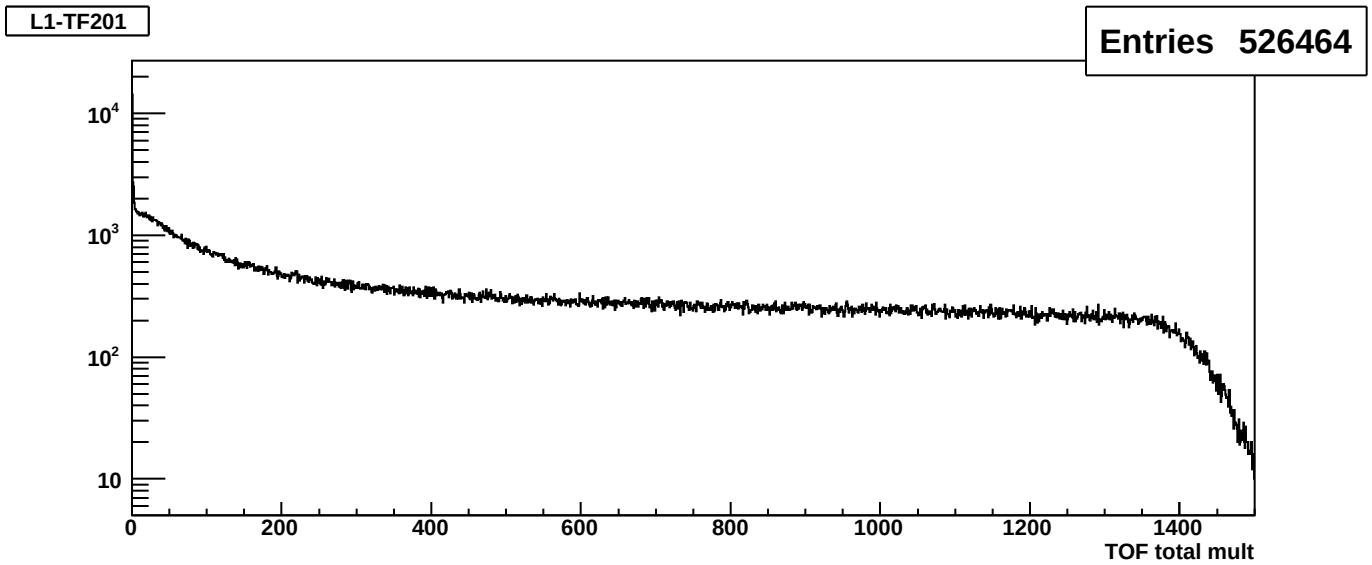

MIX-TF003
Entries 2.211149e+07
TOF MULT
TOF tray

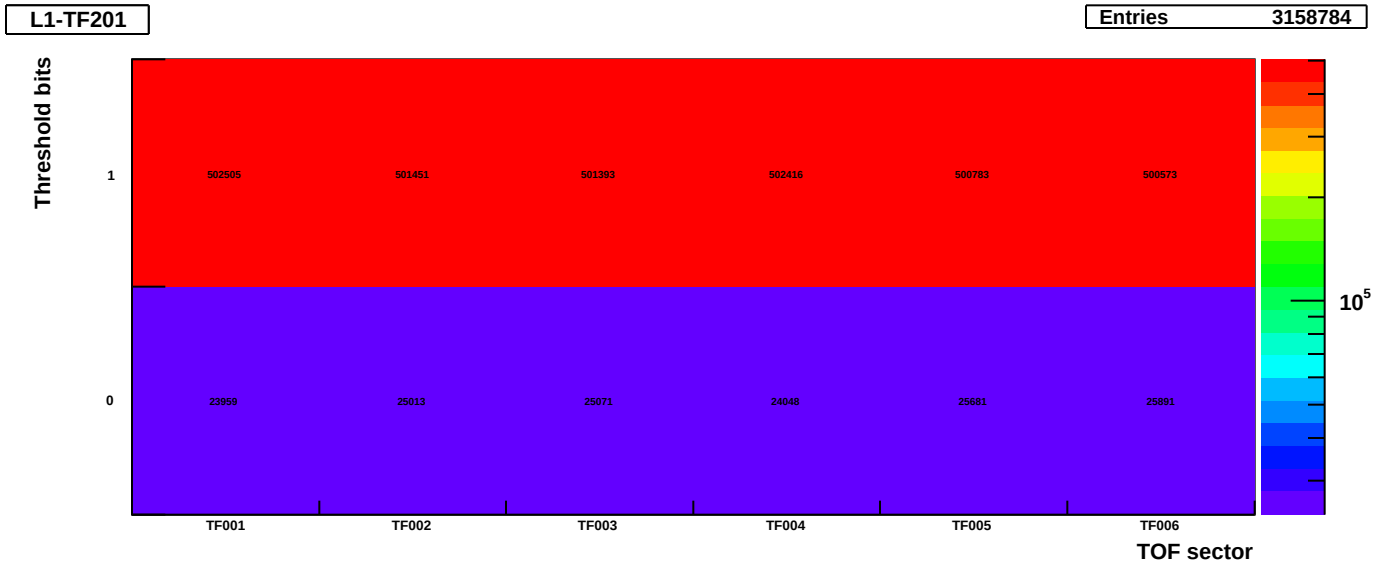

MIX-TF004
Entries 2.211149e+07
TOF MULT
TOF tray

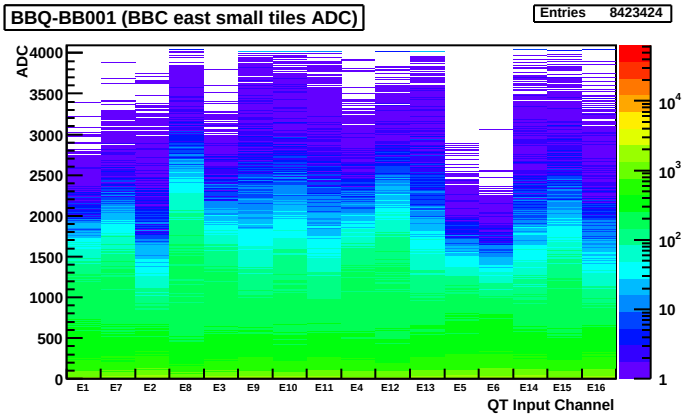

MIX-TF005
Entries 2.211149e+07
TOF MULT
TOF tray

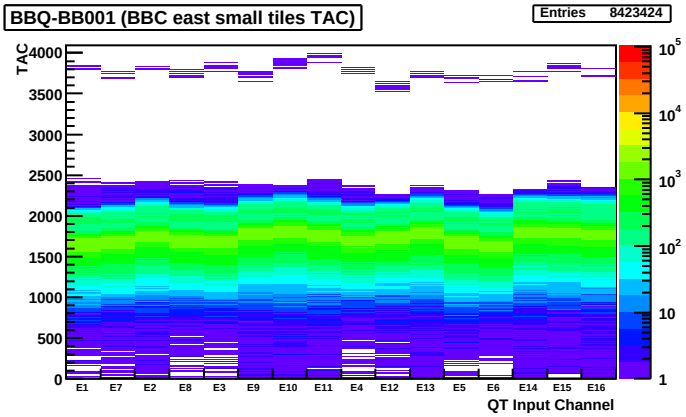

MIX-TF006
Entries 2.211149e+07
TOF MULT
TOF tray

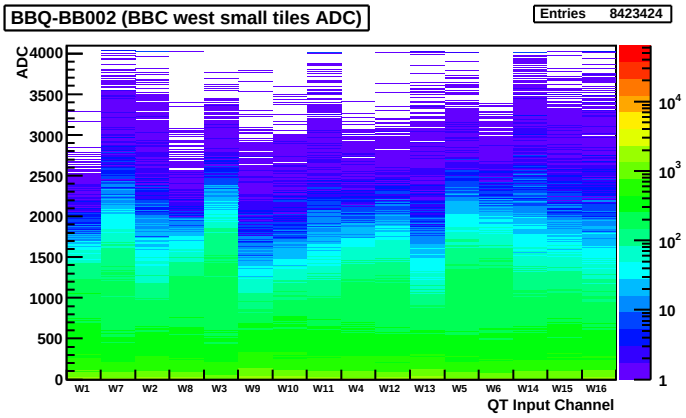

MIX-TF101
Entries 3158784
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
$10^4$

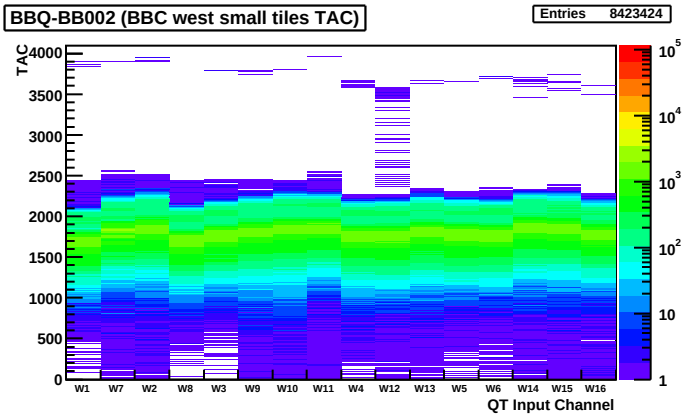

L1-TF201
Entries 526464
$10^4$
$10^3$
$10^2$
10
0
200
400
600
800
1000
1200
1400
TOF total mult

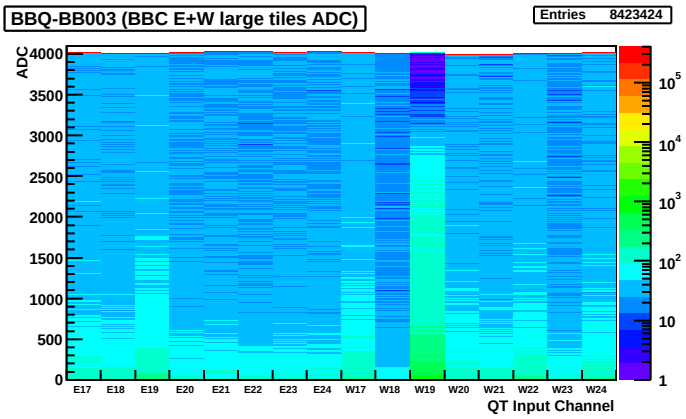

L1-TF201
Entries 3158784
Threshold bits
1
0
502505
501451
501393
502416
500783
500573
23959
25013
25071
24048
25681
25891
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
$10^5$

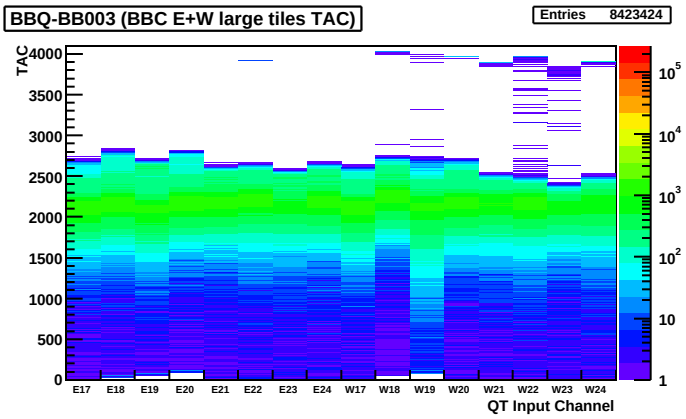

BBQ-BB001 (BBC east small tiles ADC)
Entries 8423424
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

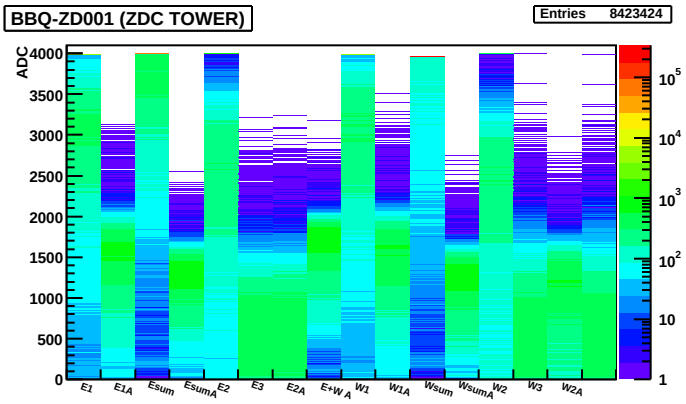

BBQ-BB001 (BBC east small tiles TAC)
Entries 8423424
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

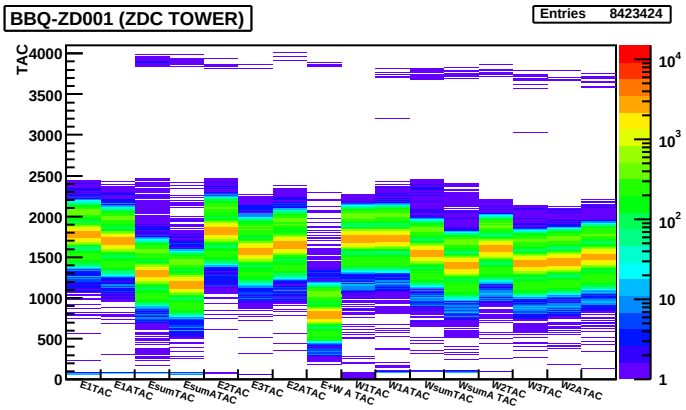

BBQ-BB002 (BBC west small tiles ADC)
Entries 8423424
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

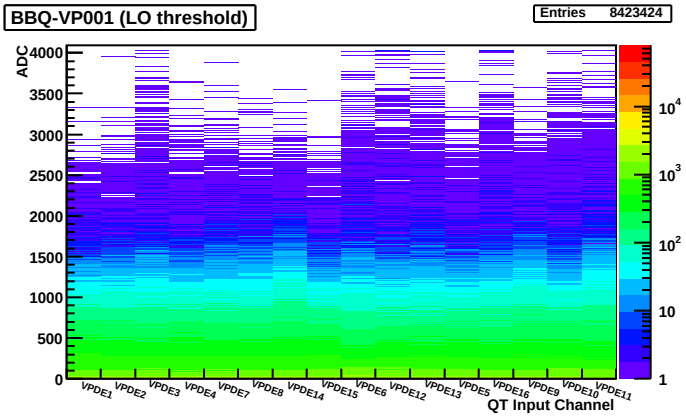

BBQ-BB002 (BBC west small tiles TAC)
Entries 8423424
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

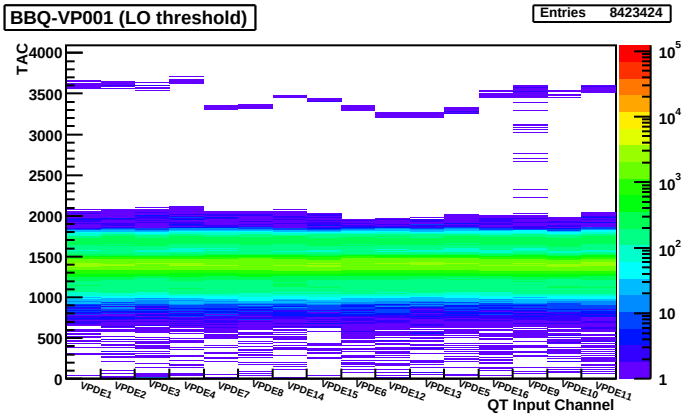

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 8423424
ADC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

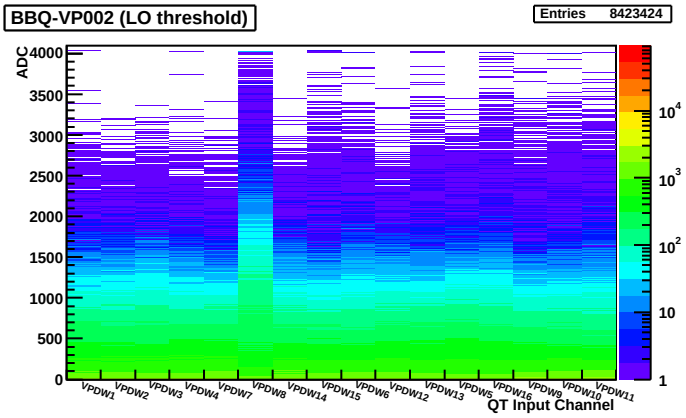

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 8423424
TAC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

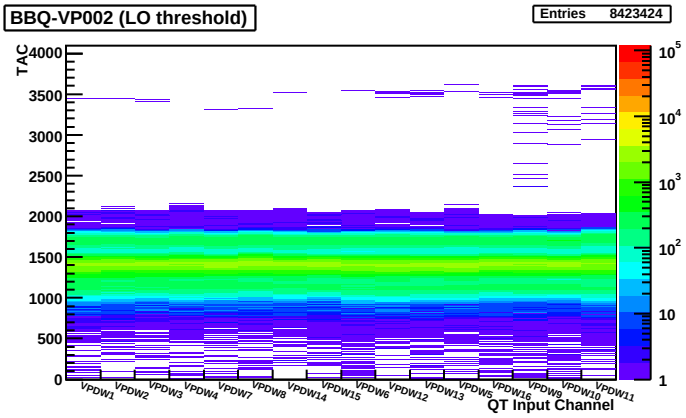

BBQ-ZD001 (ZDC TOWER)
Entries 8423424
ADC
E1 E1A Esum EsumA E2 E3 E2A E+W A W1 W1A Wsum WsumA W2 W3 W2A

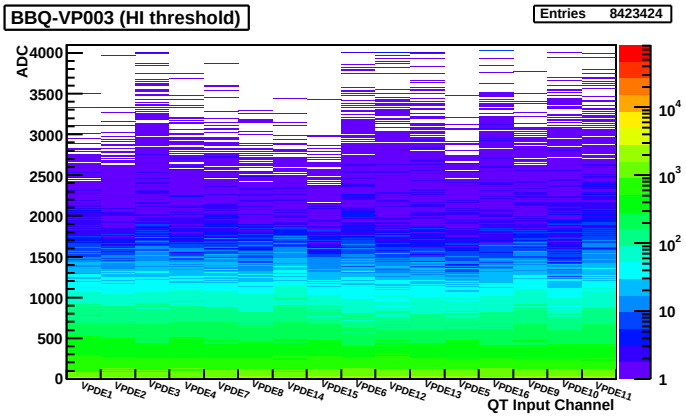

BBQ-ZD001 (ZDC TOWER)
Entries 8423424
TAC
E1TAC E1ATAC EsumTAC EsumATAC E2TAC E3TAC E2ATAC E+W A TAC W1TAC W1ATAC WsumTAC WsumA TAC W2TAC W3TAC W2ATAC

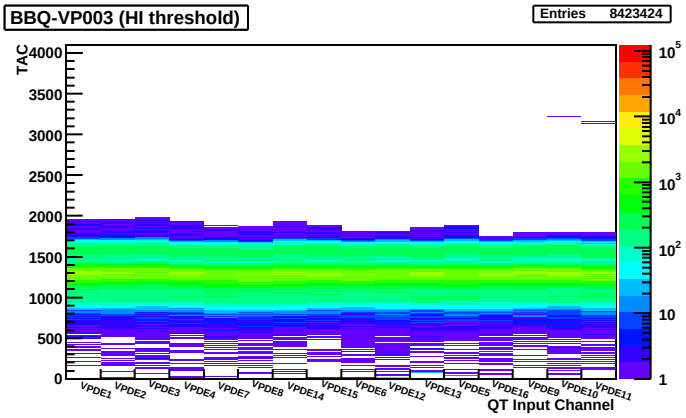

BBQ-VP001 (LO threshold)
Entries 8423424
ADC
QT Input Channel

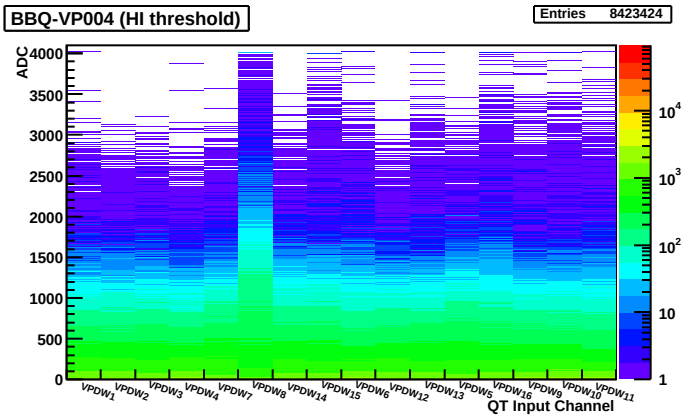

BBQ-VP001 (LO threshold)
Entries 8423424
TAC
QT Input Channel

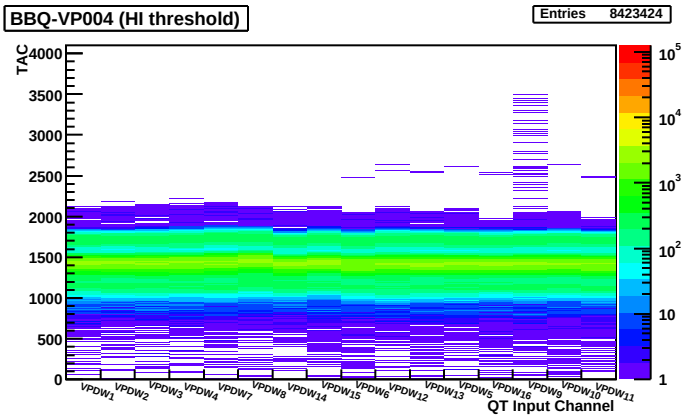

BBQ-VP002 (LO threshold)
Entries 8423424
ADC
QT Input Channel

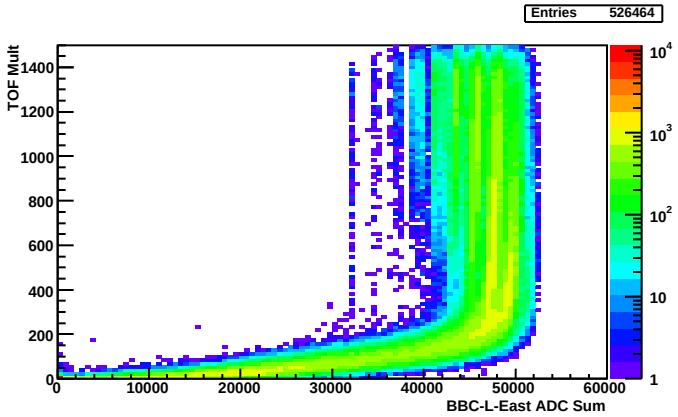

BBQ-VP002 (LO threshold)
Entries 8423424
TAC
QT Input Channel

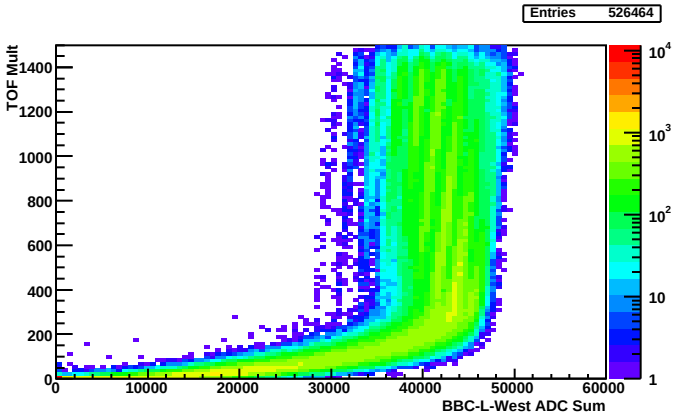

BBQ-VP003 (HI threshold)
Entries 8423424
ADC
QT Input Channel

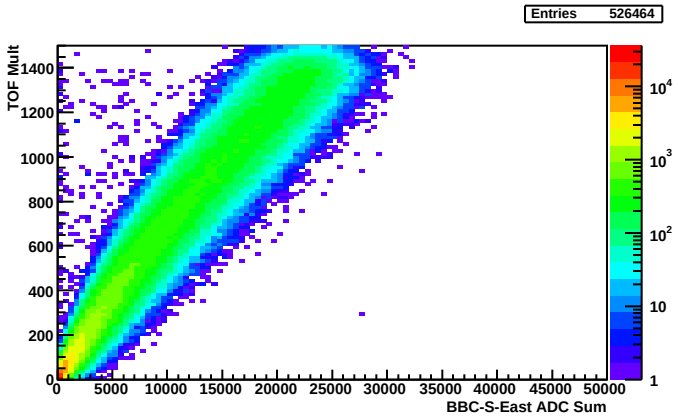

BBQ-VP003 (HI threshold)
Entries 8423424
TAC
QT Input Channel

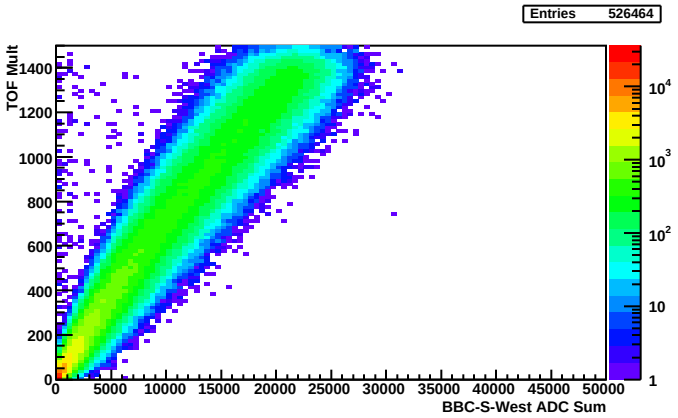

BBQ-VP004 (HI threshold)
Entries 8423424
ADC
QT Input Channel

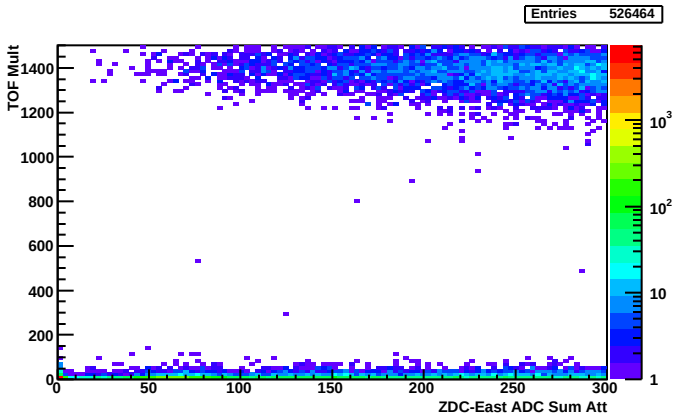

BBQ-VP004 (HI threshold)
Entries 8423424
TAC
QT Input Channel

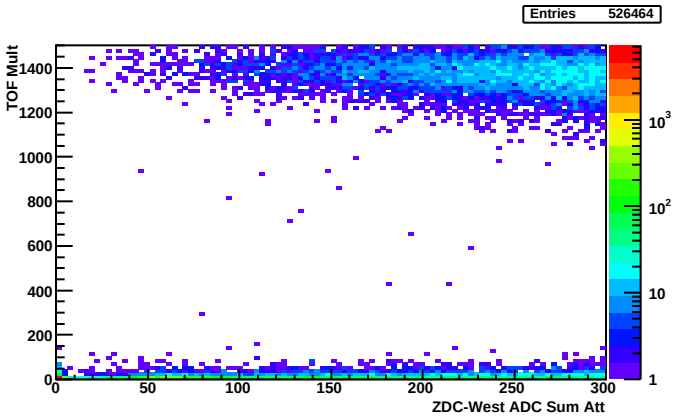

Entries 526464
TOF Mult
BBC-L-East ADC Sum

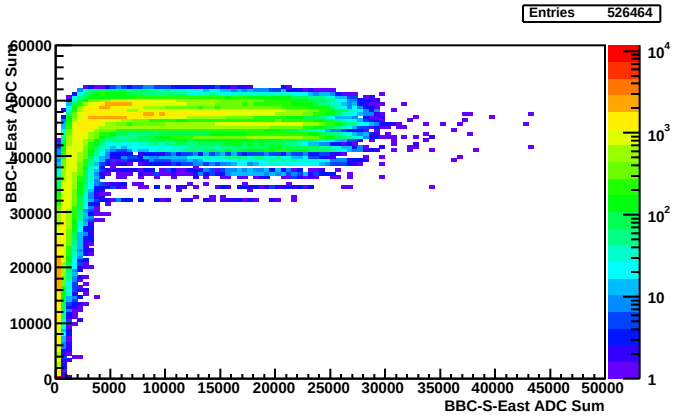

Entries 526464
TOF Mult
BBC-L-West ADC Sum

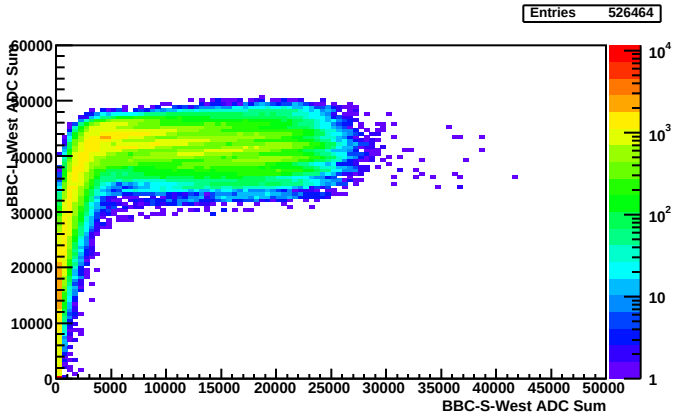

Entries 526464
TOF Mult
BBC-S-East ADC Sum

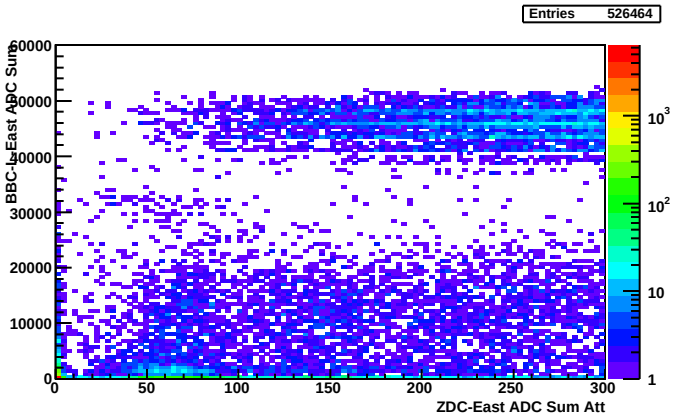

Entries 526464
TOF Mult
BBC-S-West ADC Sum


Entries 526464
TOF Mult
ZDC-East ADC Sum Att


Entries 526464
TOF Mult
ZDC-West ADC Sum Att


Entries 526464
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 526464
BBC-L-West ADC Sum
BBC-S-West ADC Sum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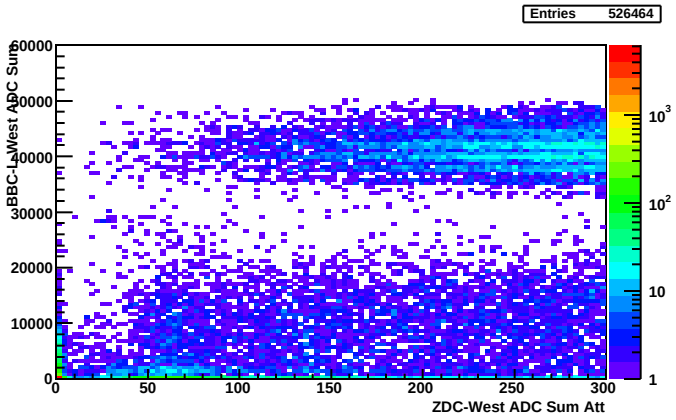
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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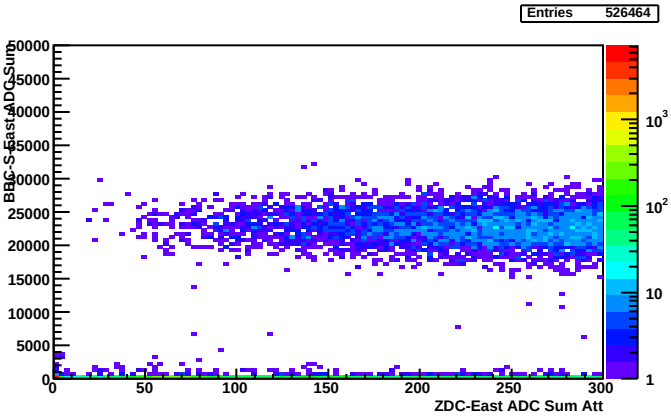
BBC-L-West ADC Sum
ZDC-West ADC Sum Att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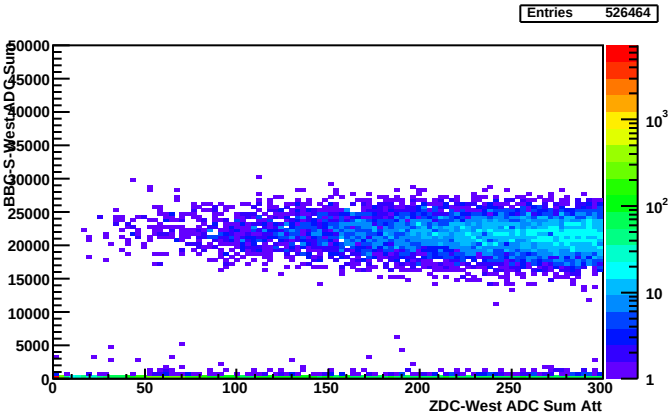
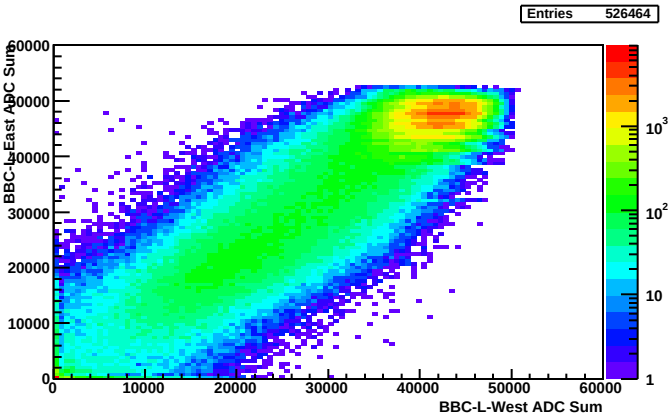
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 526464
BBC-S-West ADC Sum
ZDC-West ADC Sum Att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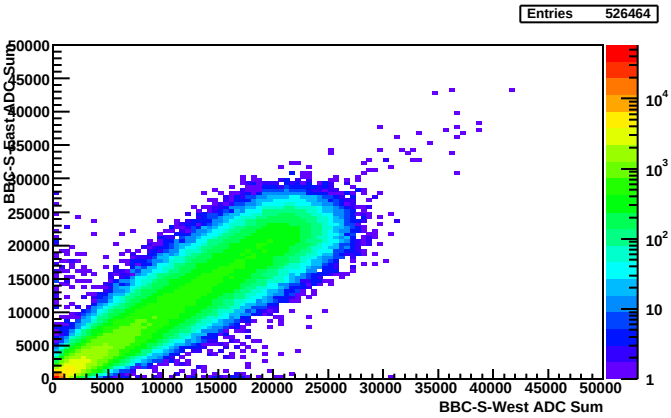
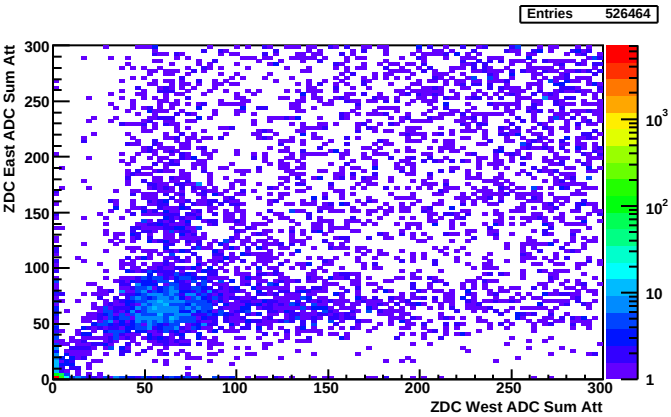
BBC-L-East ADC Sum
BBC-L-West ADC Sum


Entries 526464
BBC-S-East ADC Sum
BBC-S-West ADC Sum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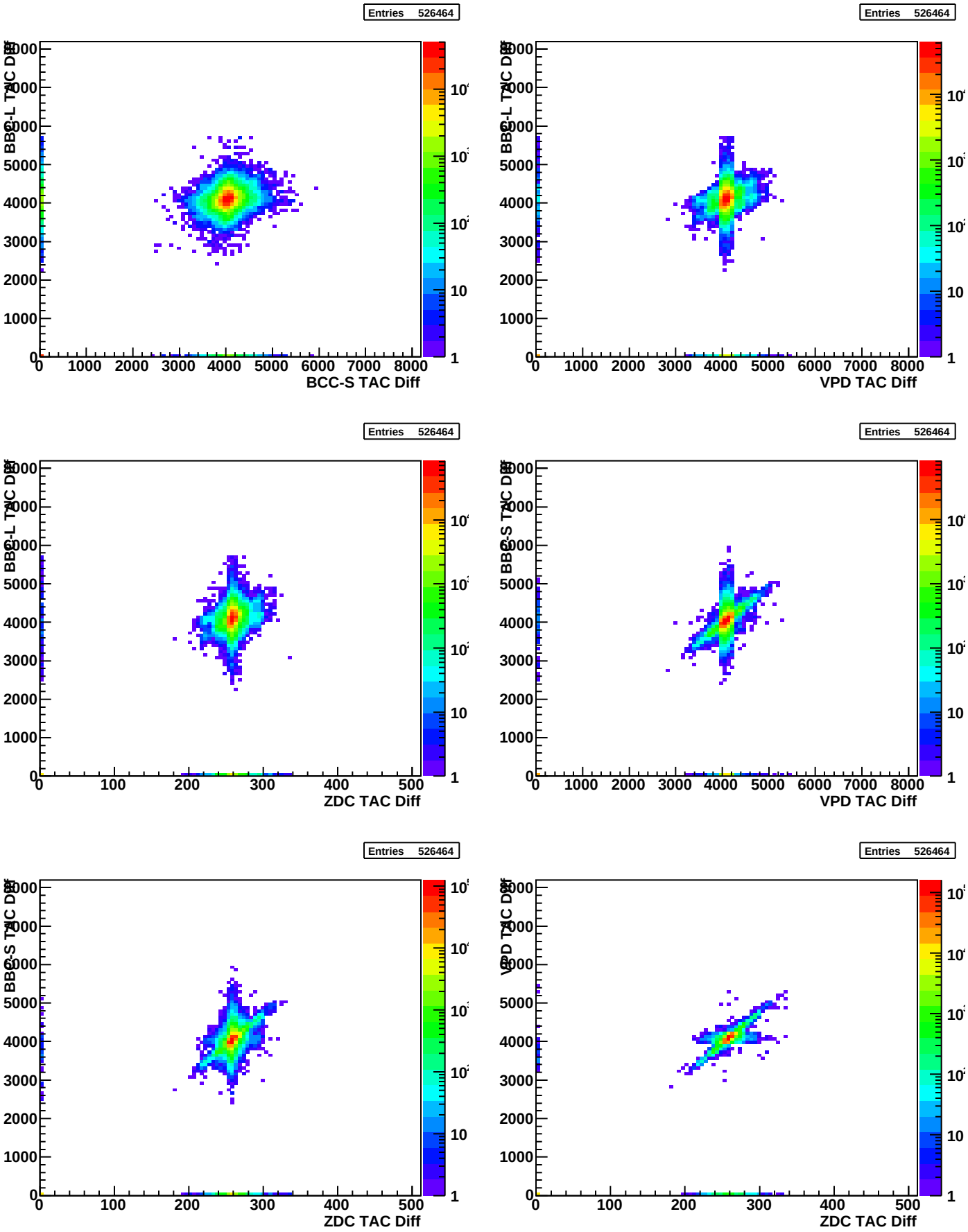
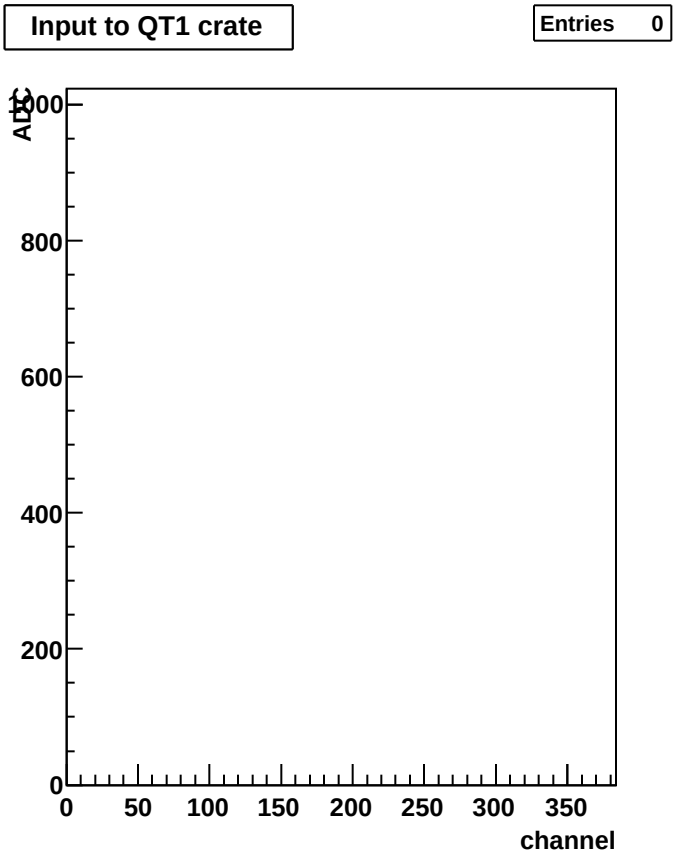
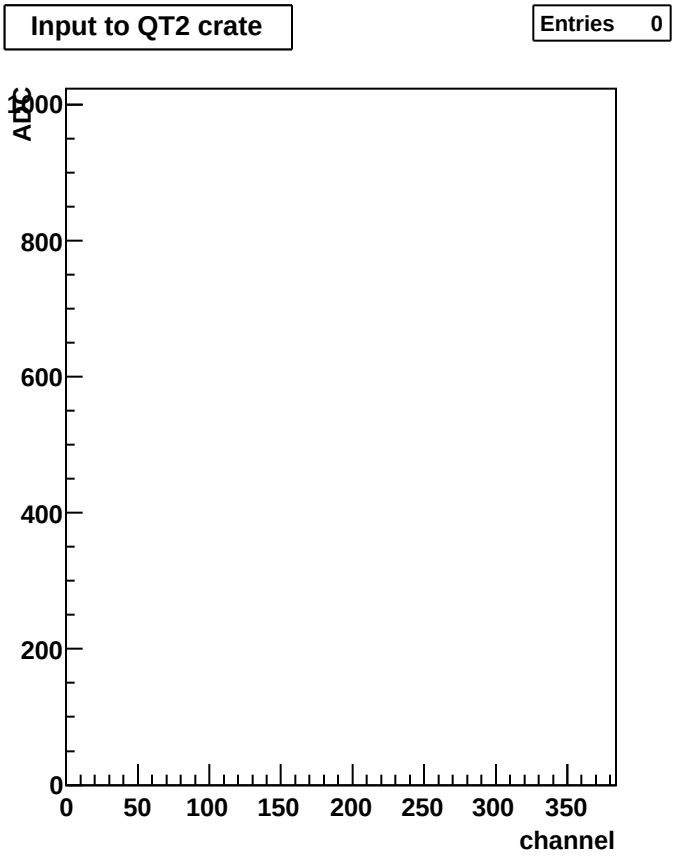
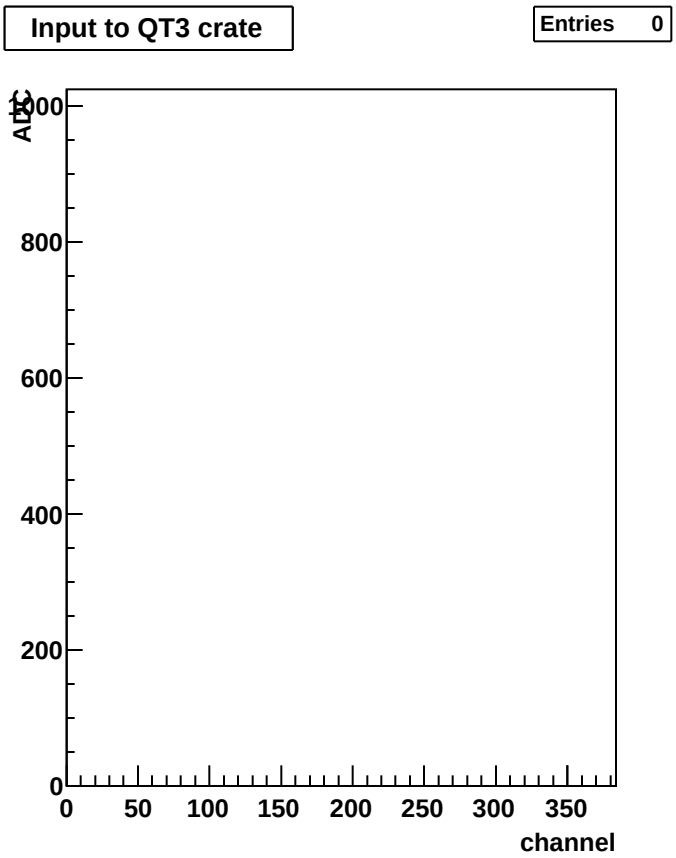
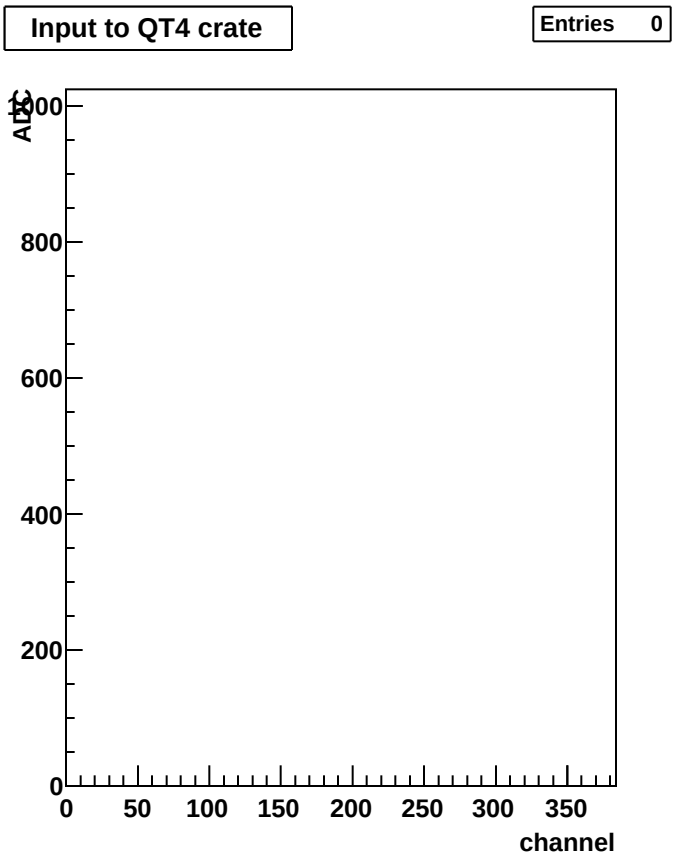
ZDC East ADC Sum Att
ZDC West ADC Sum Att

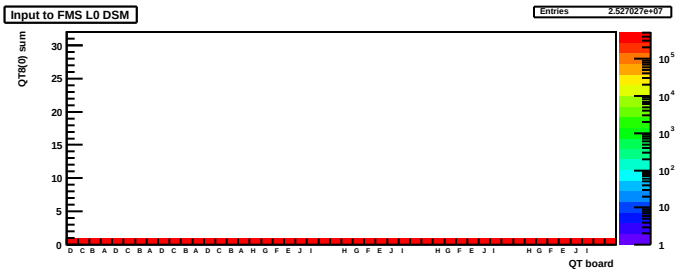

Entries 526464
BBC-L TAC Diff
BCC-S TAC Diff
Entries 526464
BBC-L TAC Diff
VPD TAC Diff
Entries 526464
BBC-L TAC Diff
ZDC TAC Diff
Entries 526464
BBC-S TAC Diff
VPD TAC Diff
Entries 526464
BBC-S TAC Diff
ZDC TAC Diff
Entries 526464
VPD TAC Diff
ZDC TAC Diff

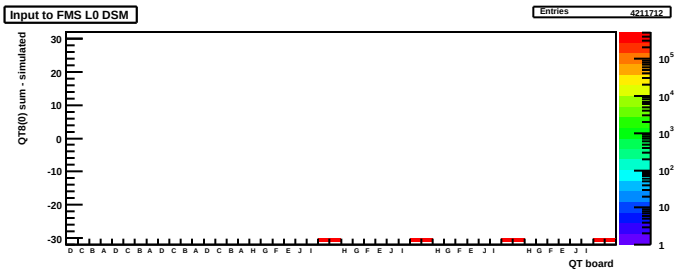

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

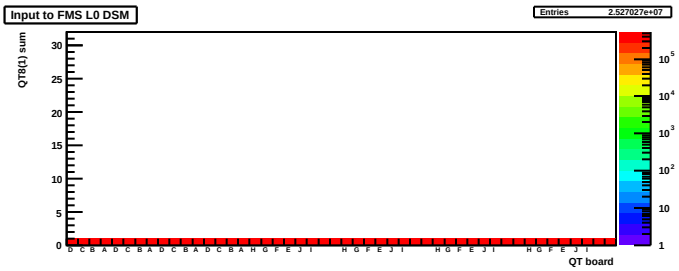

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

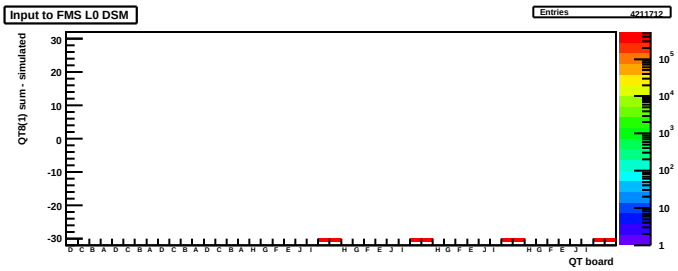

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

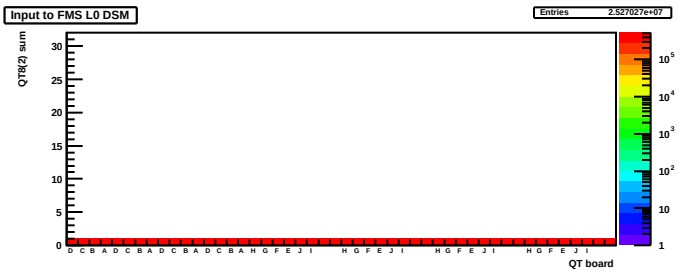

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

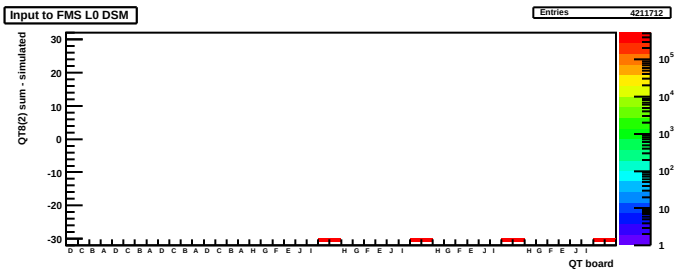

Input to FMS L0 DSM
Entries 2.527027e+07
QT8(0) sum
QT board

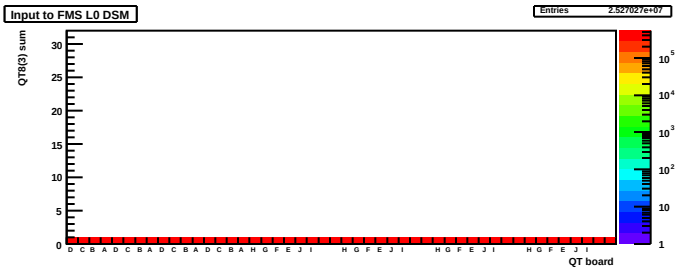

Input to FMS L0 DSM
Entries 4211712
QT8(0) sum - simulated
QT board

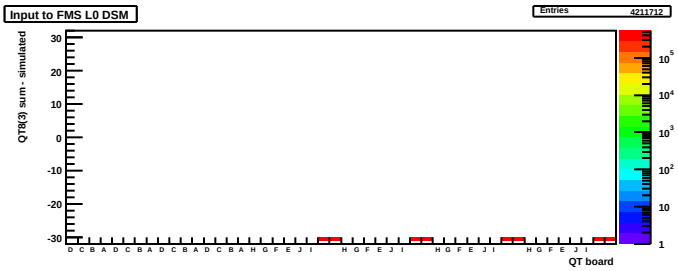

Input to FMS L0 DSM
Entries 2.527027e+07
QT8(1) sum
QT board

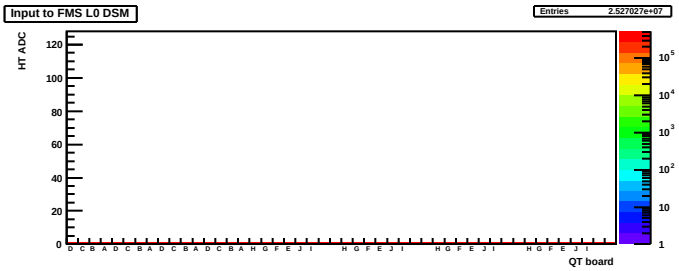

Input to FMS L0 DSM
Entries 4211712
QT8(1) sum - simulated
QT board

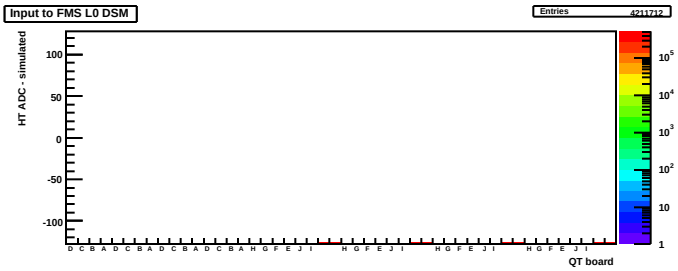

Input to FMS L0 DSM
Entries 2.527027e+07
QT8(2) sum
QT board

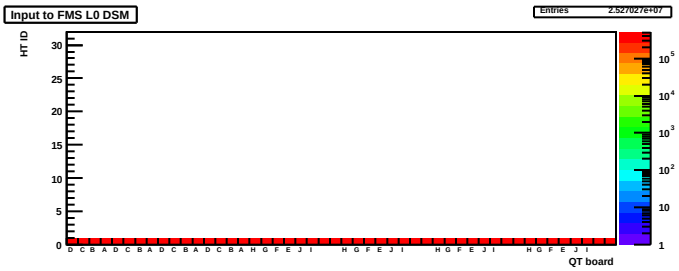

Input to FMS L0 DSM
Entries 4211712
QT8(2) sum - simulated
QT board

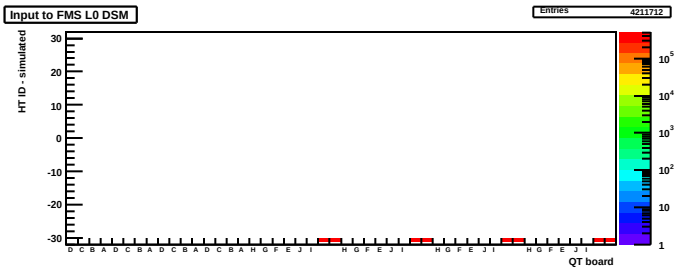

Input to FMS L0 DSM
Entries 2.527027e+07
QT8(3) sum
QT board

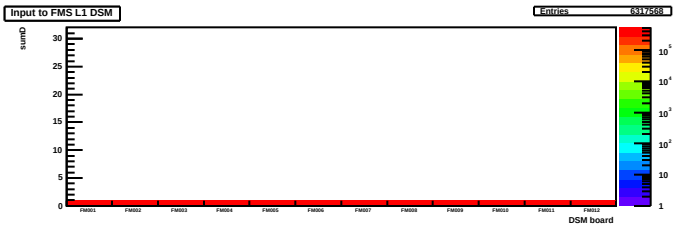

Input to FMS L0 DSM
Entries 4211712
QT8(3) sum - simulated
QT board

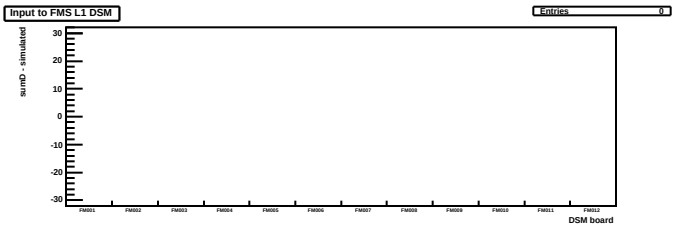

Input to FMS L0 DSM
Entries 2.527027e+07
HT ADC
QT board

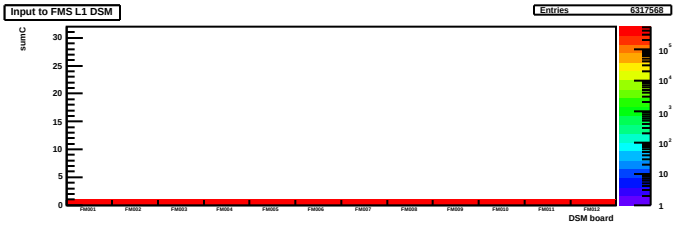

Input to FMS L0 DSM
Entries 4211712
HT ADC - simulated
QT board

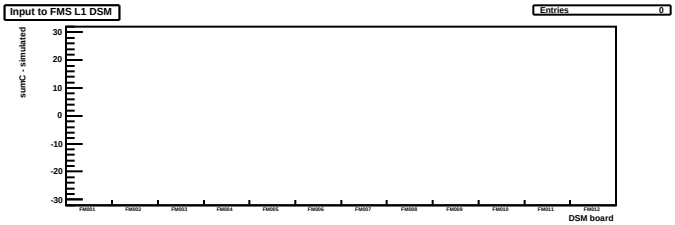

Input to FMS L0 DSM
Entries 2.527027e+07
HT ID
QT board

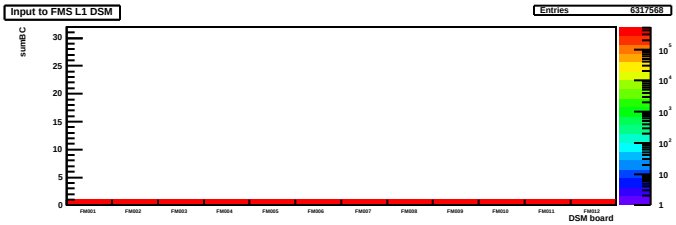

Input to FMS L0 DSM
Entries 4211712
HT ID - simulated
QT board

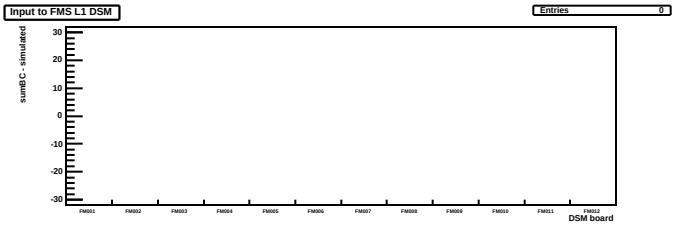

Input to FMS L1 DSM
Entries 6317568
sumD
DSM board

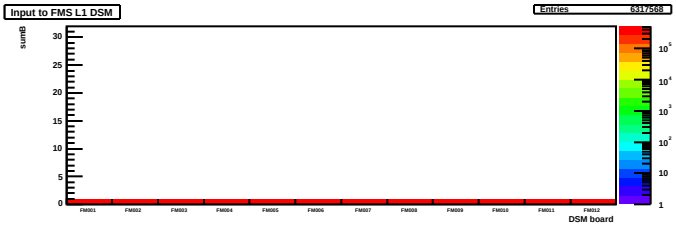

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

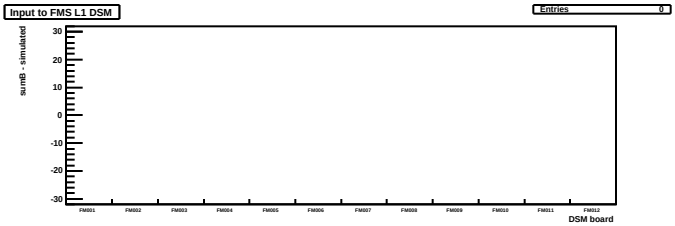

Input to FMS L1 DSM
Entries 6317568
sumC
DSM board

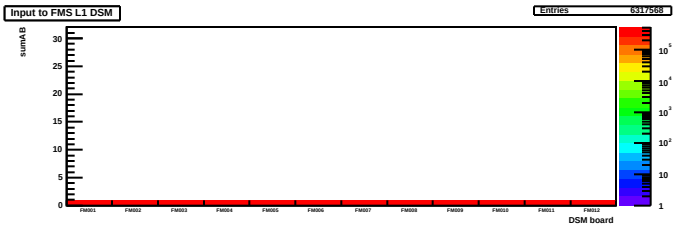

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

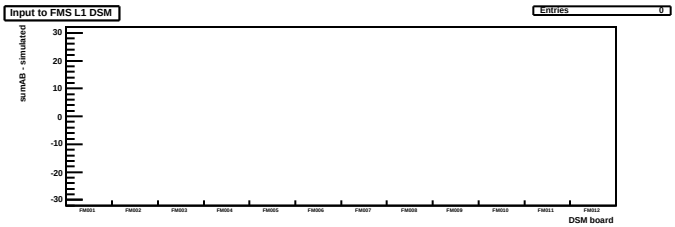

Input to FMS L1 DSM
Entries 6317568
sumBC
DSM board

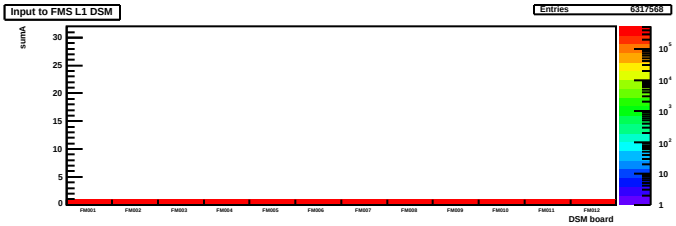

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

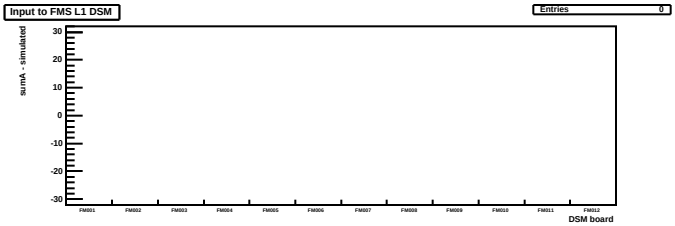

Input to FMS L1 DSM
Entries 6317568
sumB
DSM board

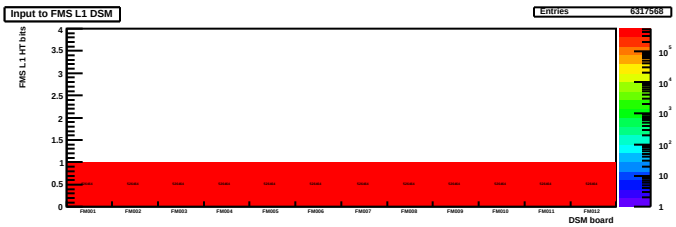

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

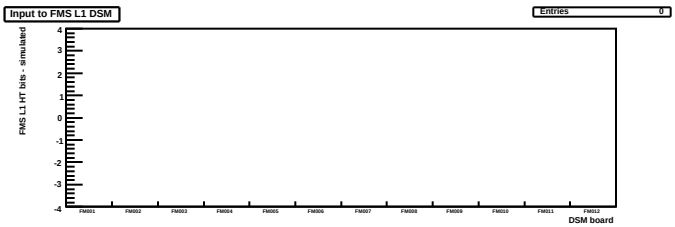

Input to FMS L1 DSM
Entries 6317568
sumAB
DSM board

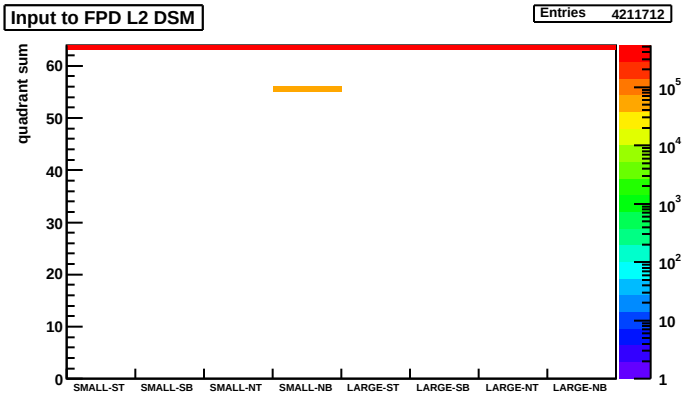

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

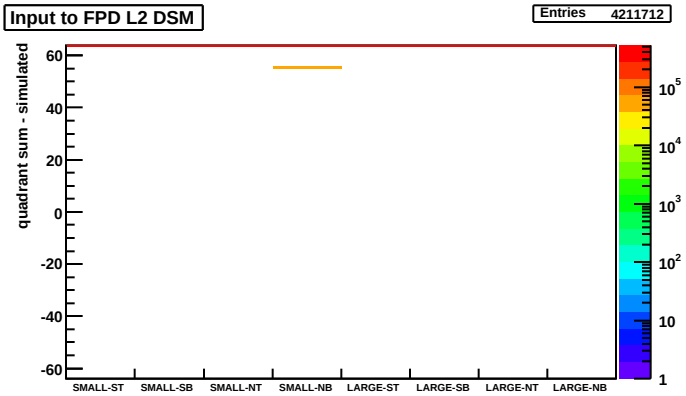

Input to FMS L1 DSM
Entries 6317568
sumA
DSM board

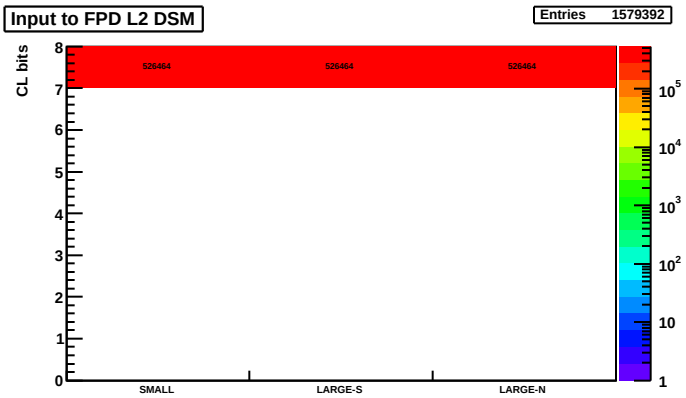

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

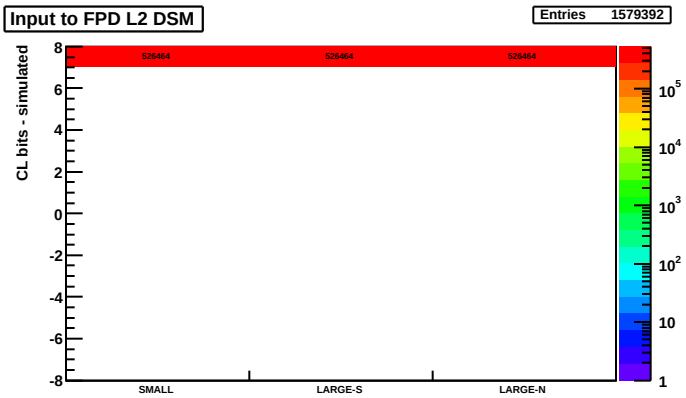

Input to FMS L1 DSM
Entries 6317568
FMS L1 HT bits
DSM board

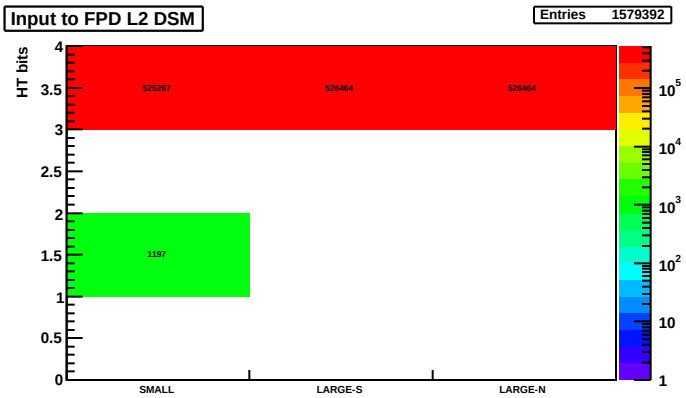

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

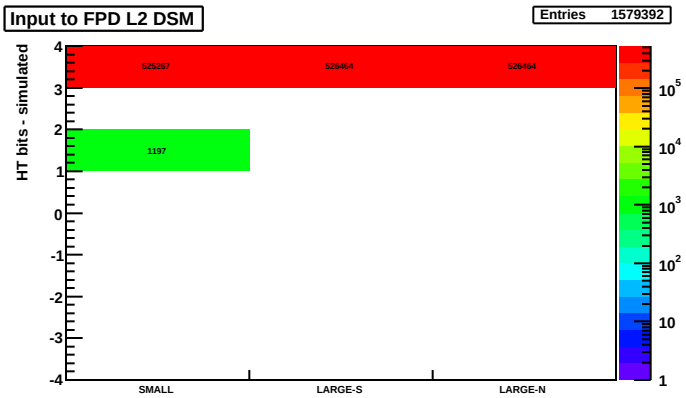

Input to FPD L2 DSM
Entries 4211712
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

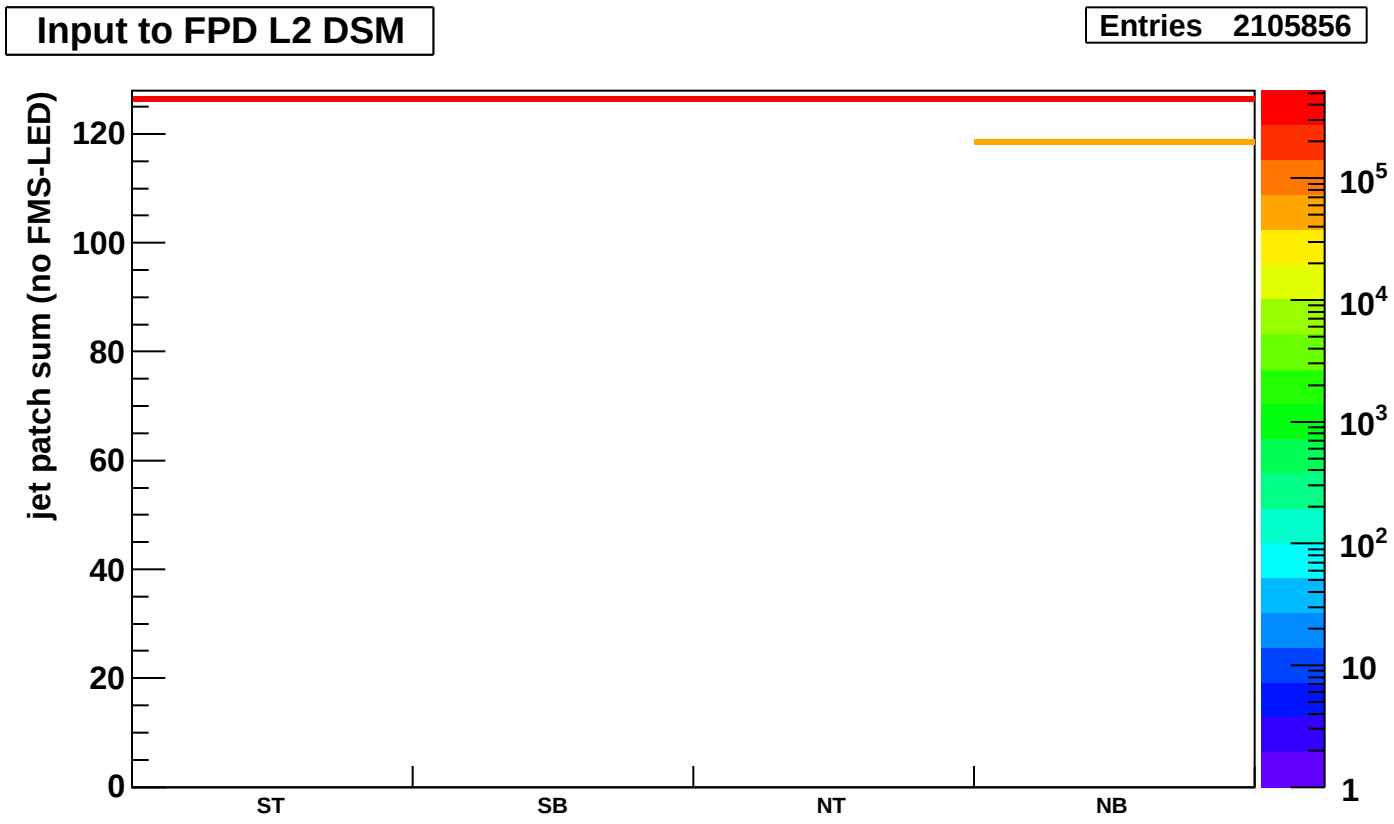

Input to FPD L2 DSM
Entries 4211712
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

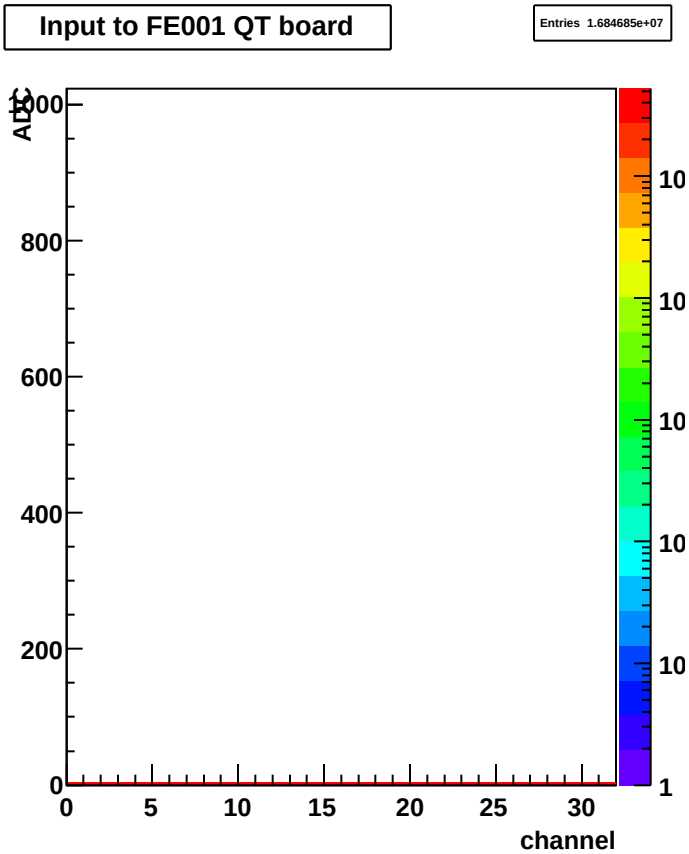

Input to FPD L2 DSM
Entries 1579392
CL bits
526464
526464
526464
SMALL
LARGE-S
LARGE-N

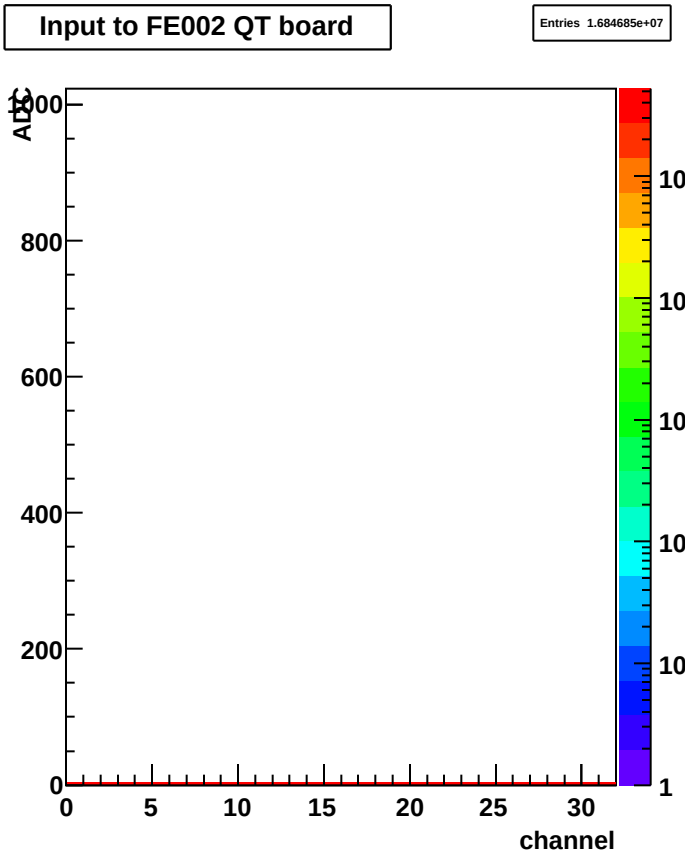

Input to FPD L2 DSM
Entries 1579392
CL bits - simulated
526464
526464
526464
SMALL
LARGE-S
LARGE-N

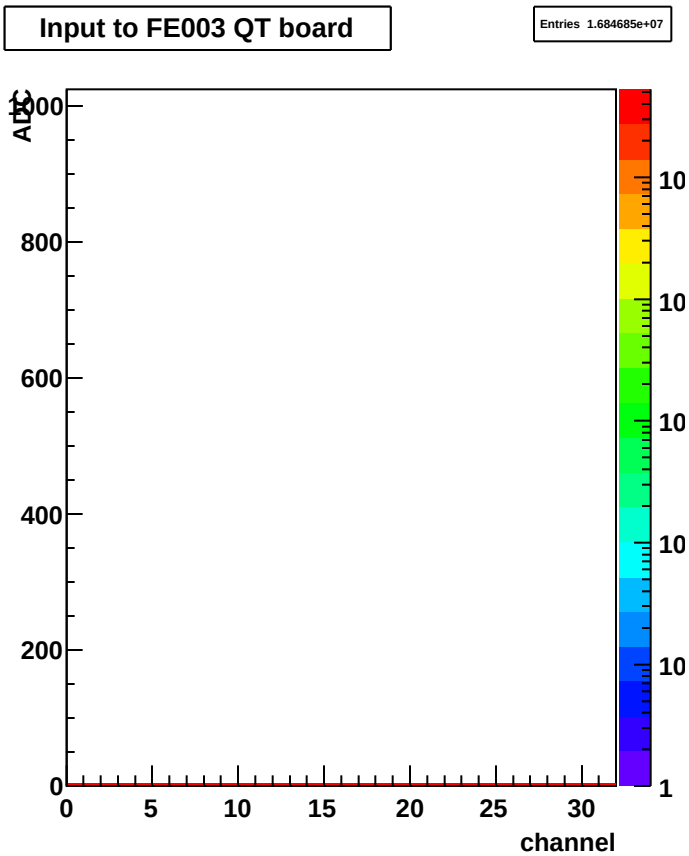

Input to FPD L2 DSM
Entries 1579392
HT bits
525267
526464
526464
1197
SMALL
LARGE-S
LARGE-N

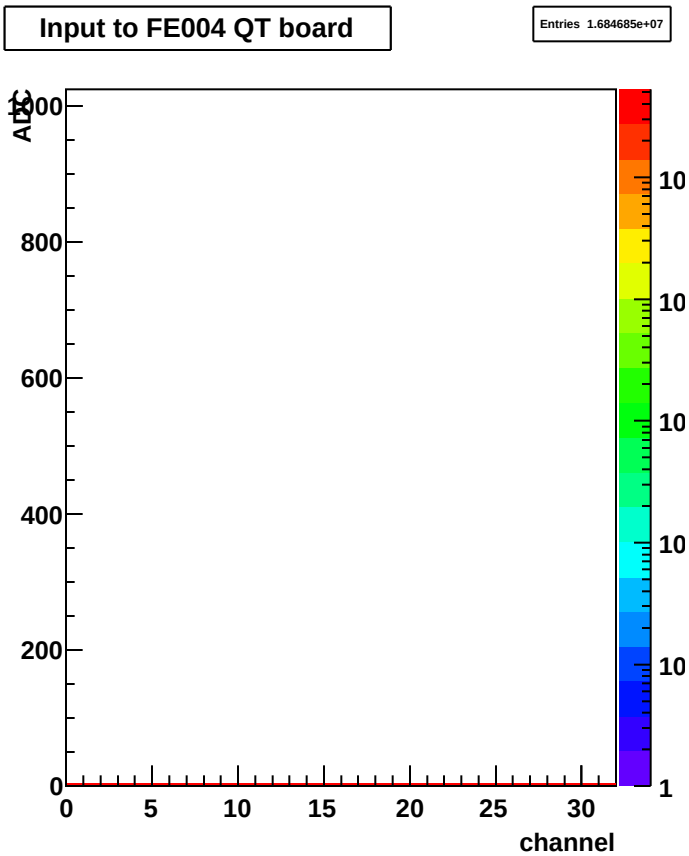

Input to FPD L2 DSM
Entries 1579392
HT bits - simulated
525267
526464
526464
1197
SMALL
LARGE-S
LARGE-N

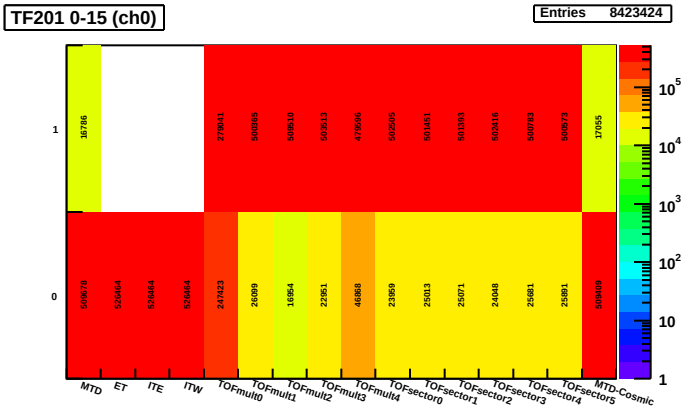

Input to FPD L2 DSM
Entries 2105856
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
$10^2$
$10^3$
$10^4$
$10^5$

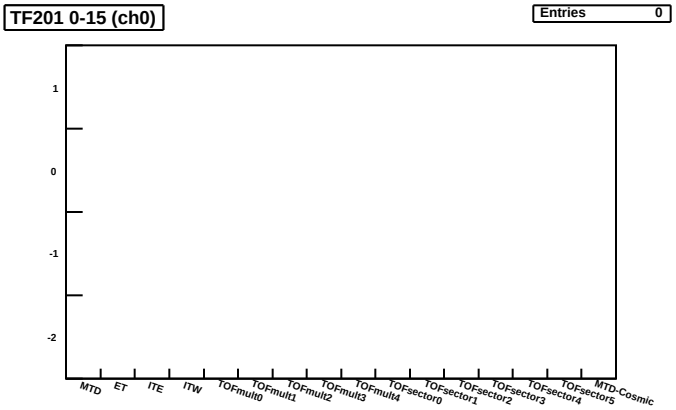

Input to FE001 QT board
Entries 1.684685e+07
ADC
channel

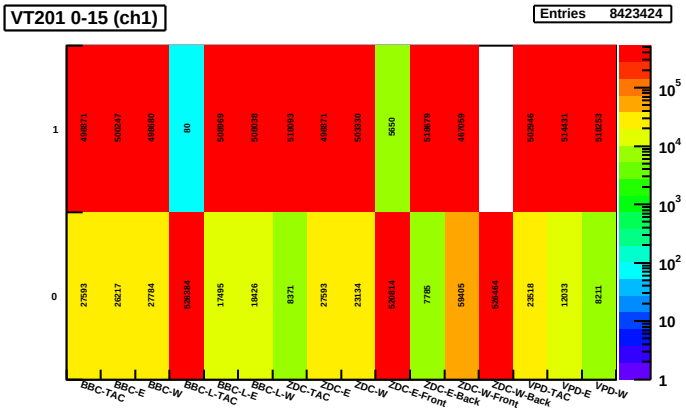

Input to FE002 QT board
Entries 1.684685e+07
ADC
channel

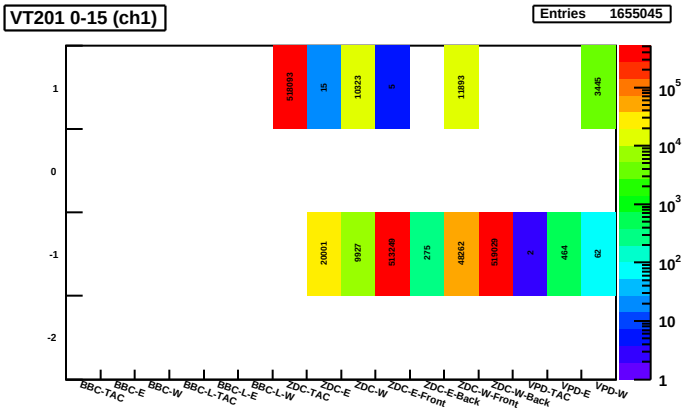

Input to FE003 QT board
Entries 1.684685e+07
ADC
channel

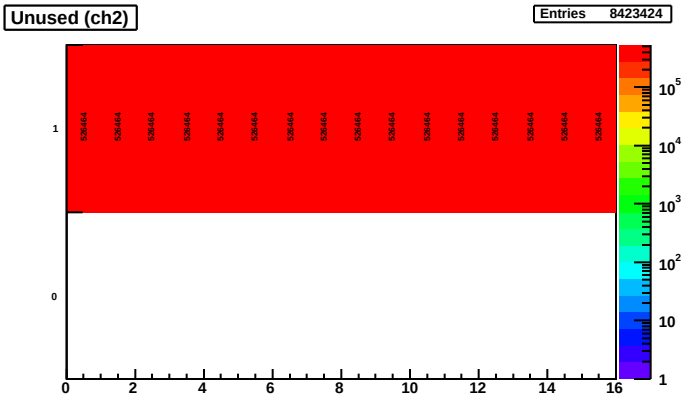

Input to FE004 QT board
Entries 1.684685e+07
ADC
channel

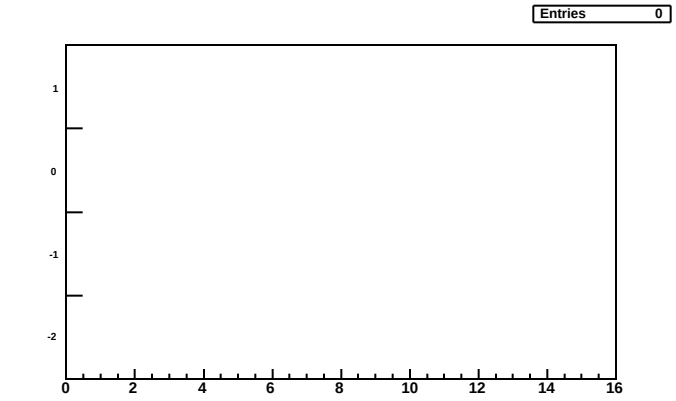

TF201 0-15 (ch0)
Entries 8423424
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

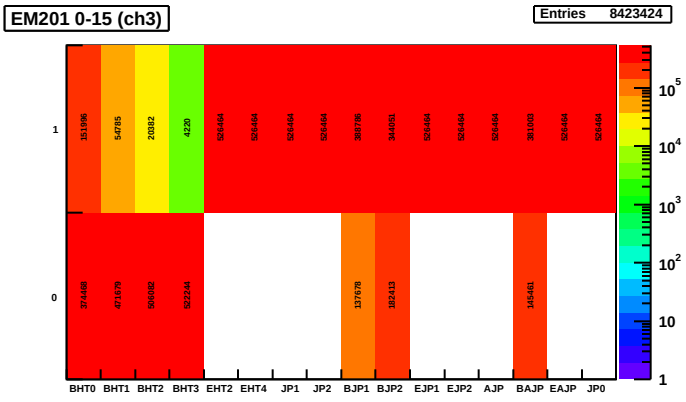

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

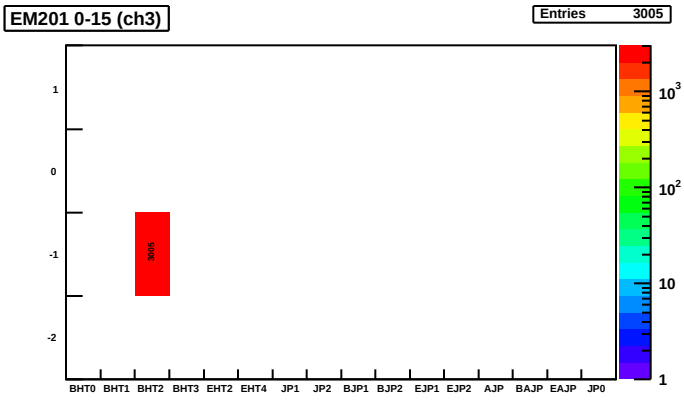

VT201 0-15 (ch1)
Entries 8423424
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

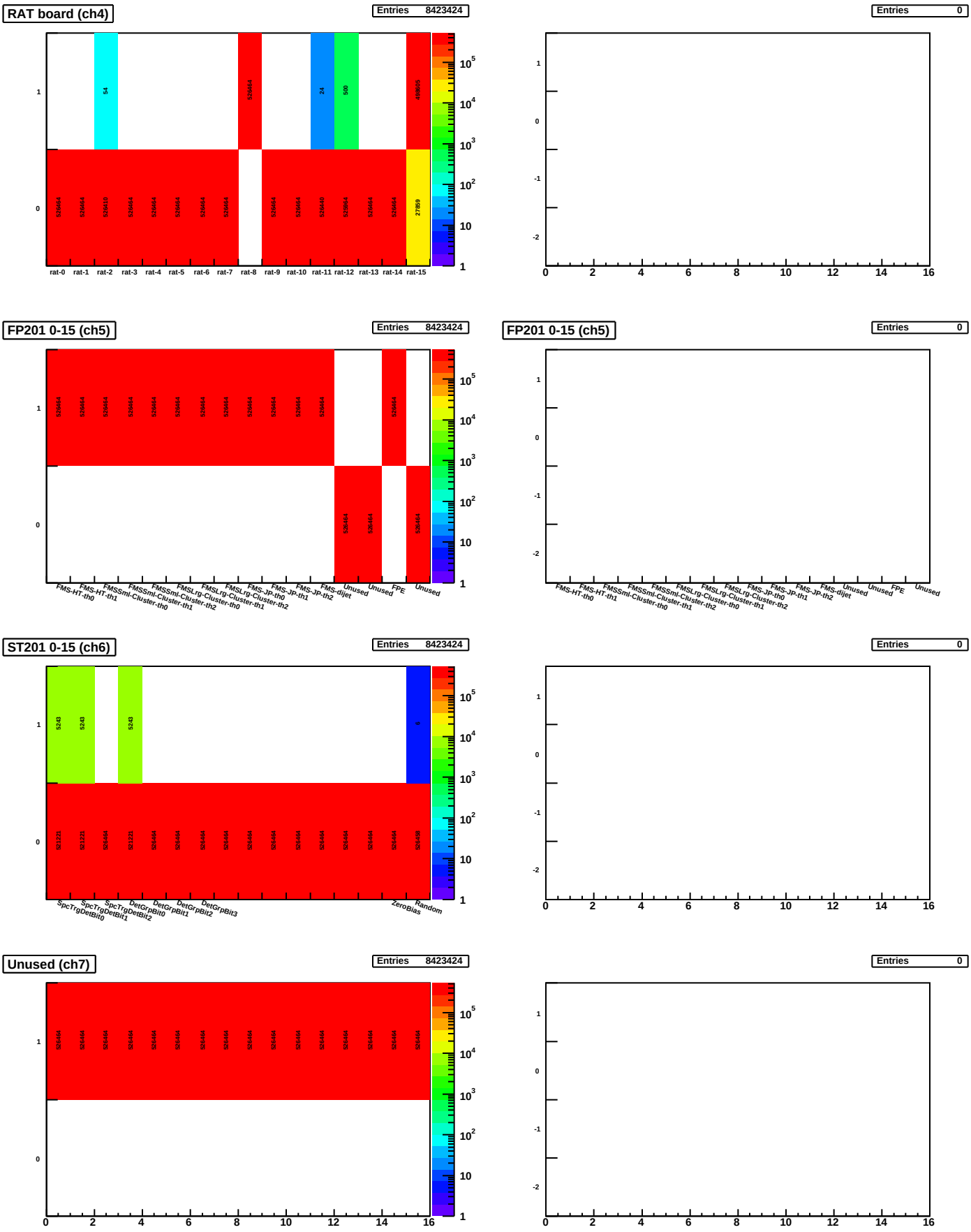

VT201 0-15 (ch1)
Entries 1655045
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


Unused (ch2)
Entries 8423424


Entries 0


EM201 0-15 (ch3)
Entries 8423424
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0


EM201 0-15 (ch3)
Entries 3005
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0


RAT board (ch4)
Entries 8423424
Entries 0
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15
FP201 0-15 (ch5)
Entries 8423424
Entries 0
FMS-HT-th0 FMS-HT-th1 FMSSml-Cluster-th0 FMSSml-Cluster-th1 FMSSml-Cluster-th2 FMSLrg-Cluster-th0 FMSLrg-Cluster-th1 FMSLrg-Cluster-th2 FMS-JP-th0 FMS-JP-th1 FMS-JP-th2 FMS-dijet Unused Unused FPE Unused
ST201 0-15 (ch6)
Entries 8423424
Entries 0
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random
Unused (ch7)
Entries 8423424
Entries 0