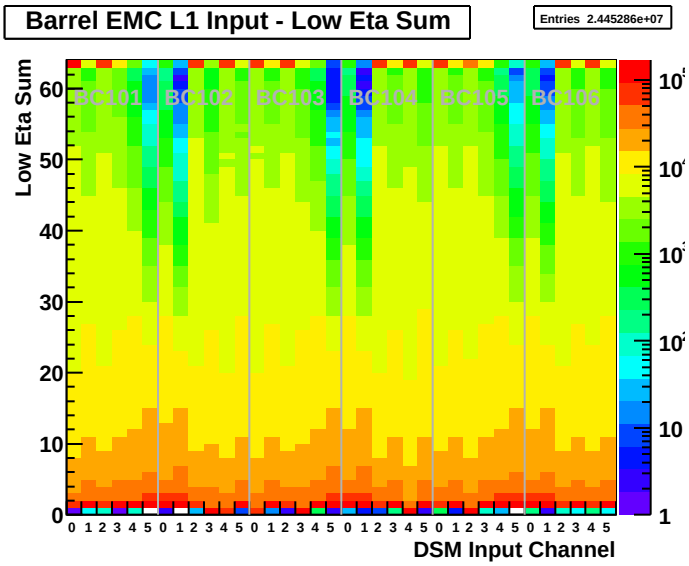
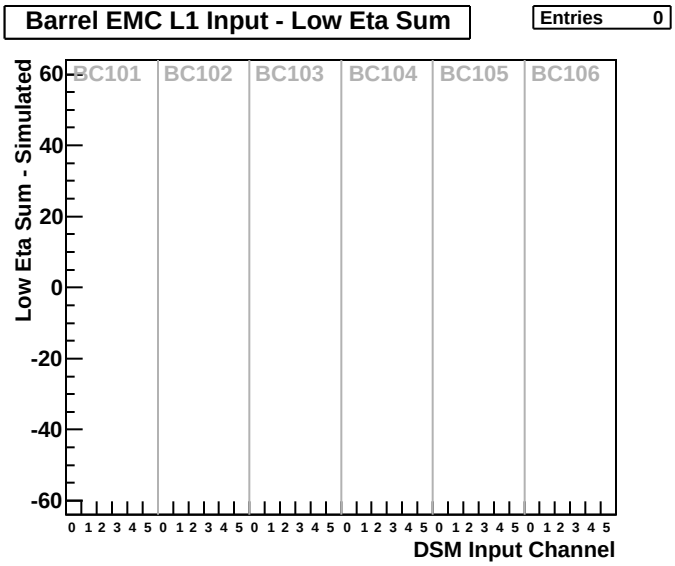
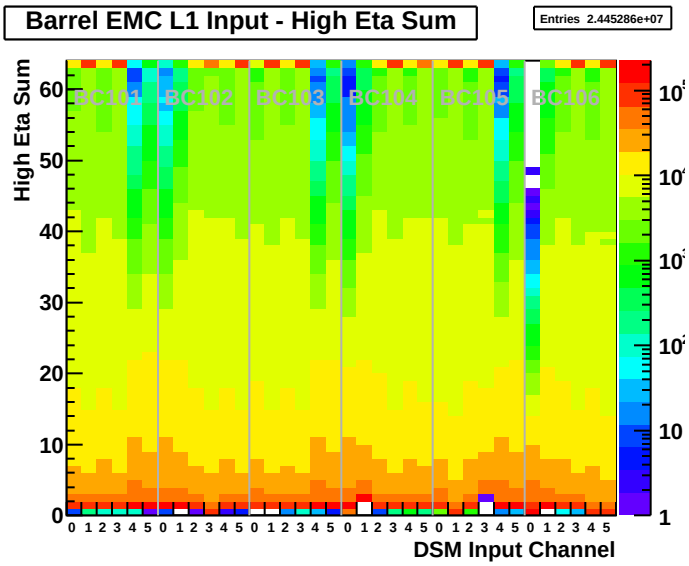
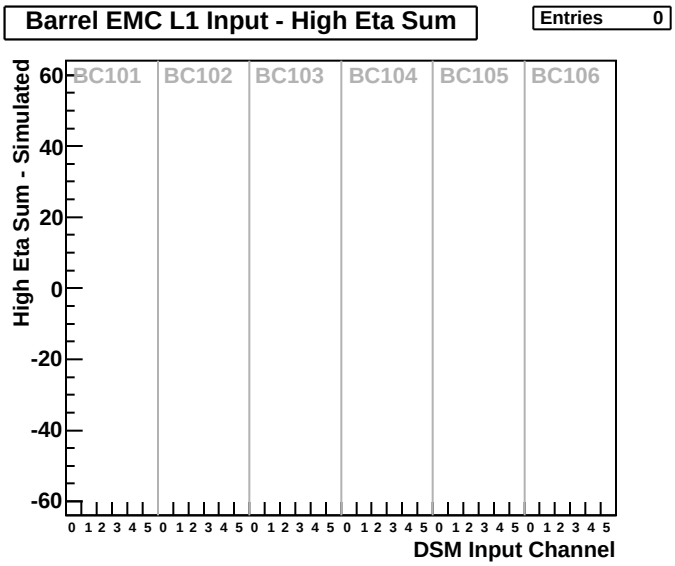
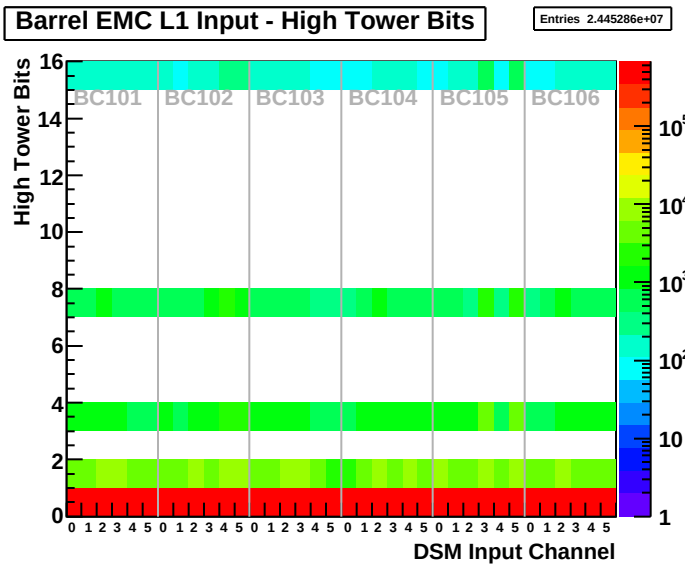
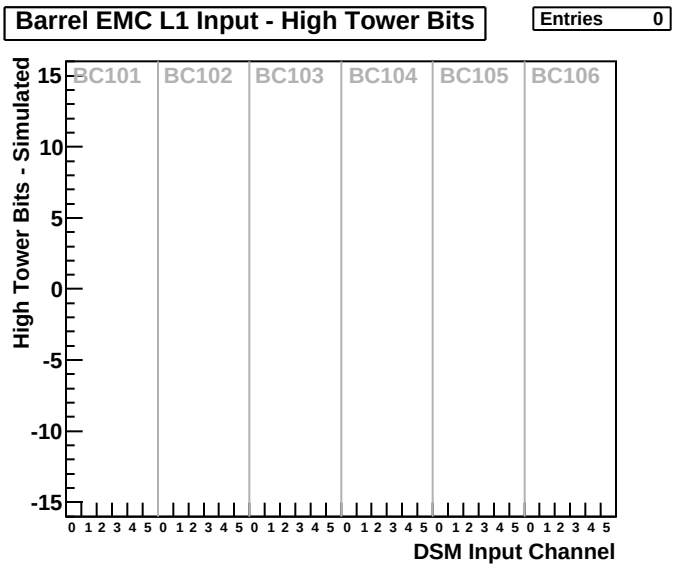

Barrel EMC L1 Input - Low Eta Sum
Entries 2.445286e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

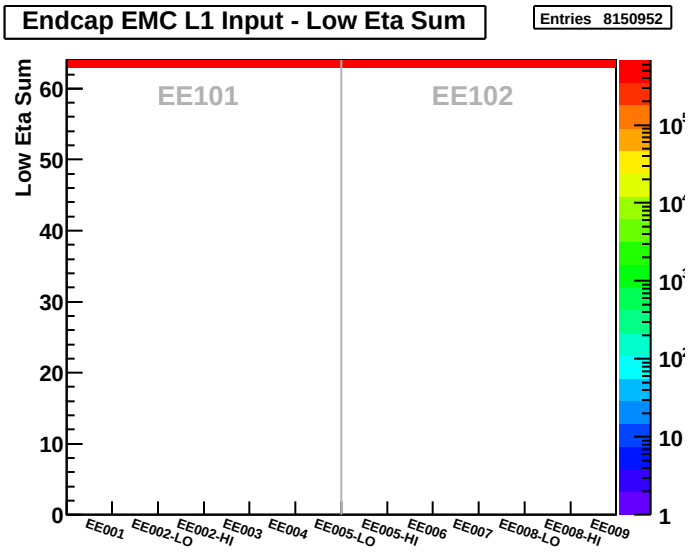

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

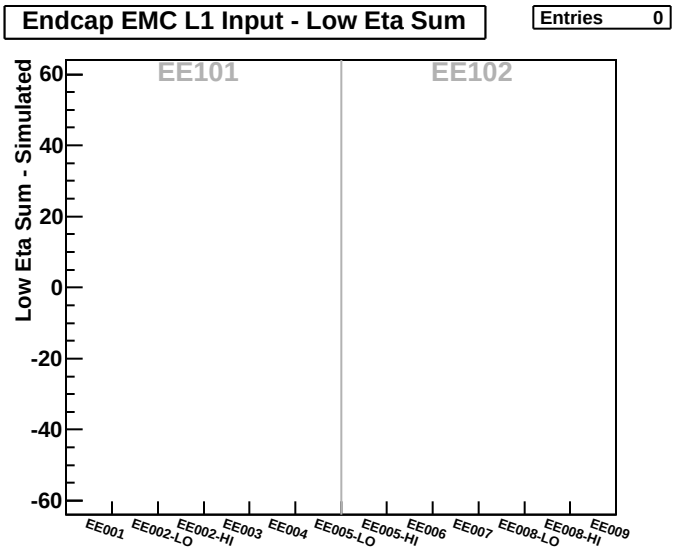

Barrel EMC L1 Input - High Eta Sum
Entries 2.445286e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

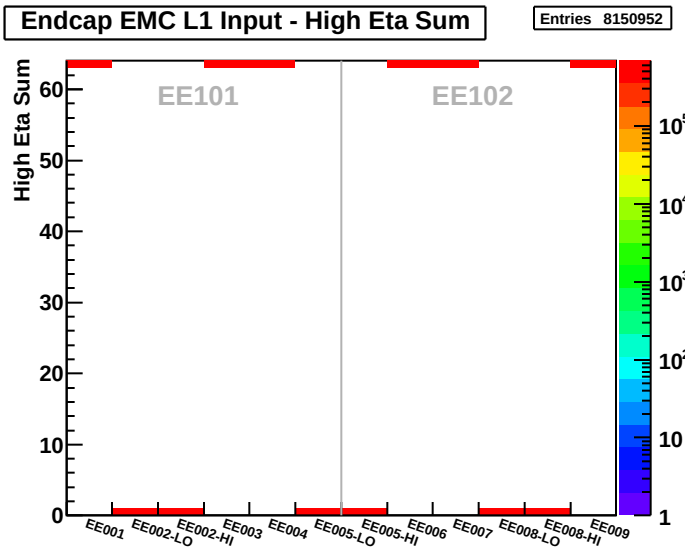

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

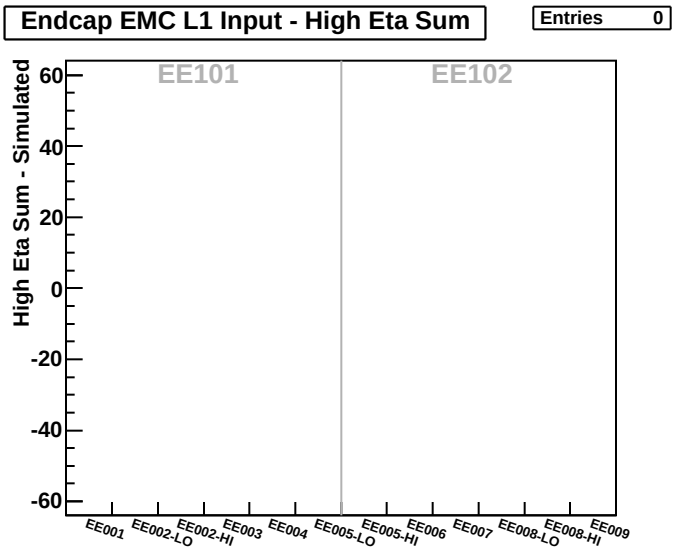

Barrel EMC L1 Input - High Tower Bits
Entries 2.445286e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

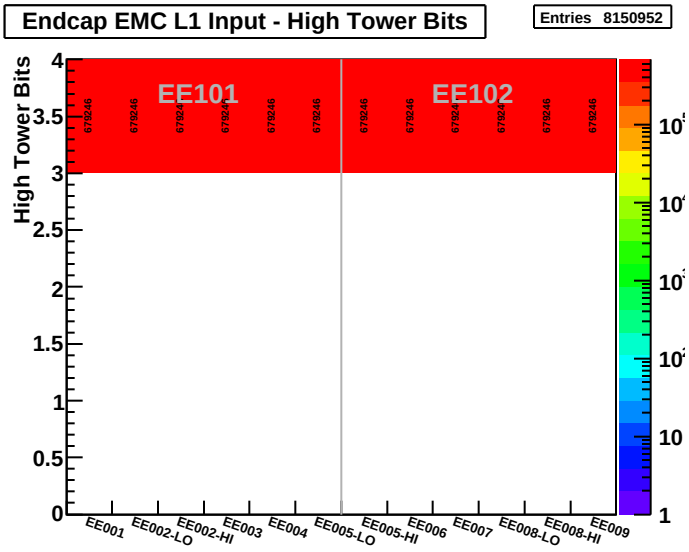

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

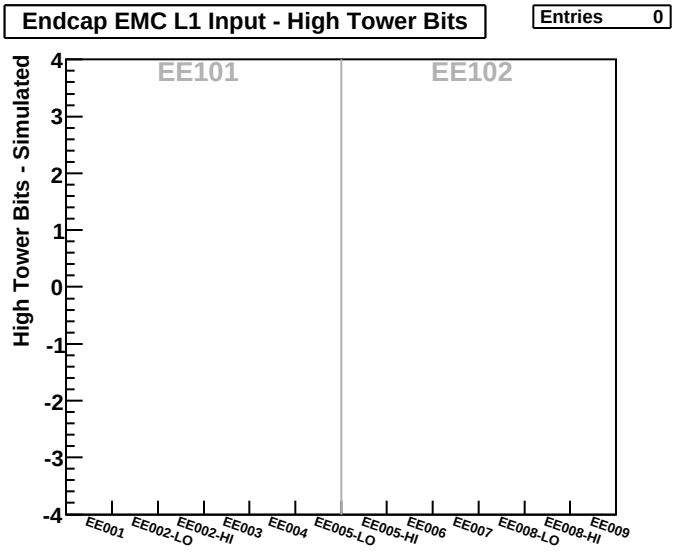

Endcap EMC L1 Input - Low Eta Sum
Entries 8150952
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

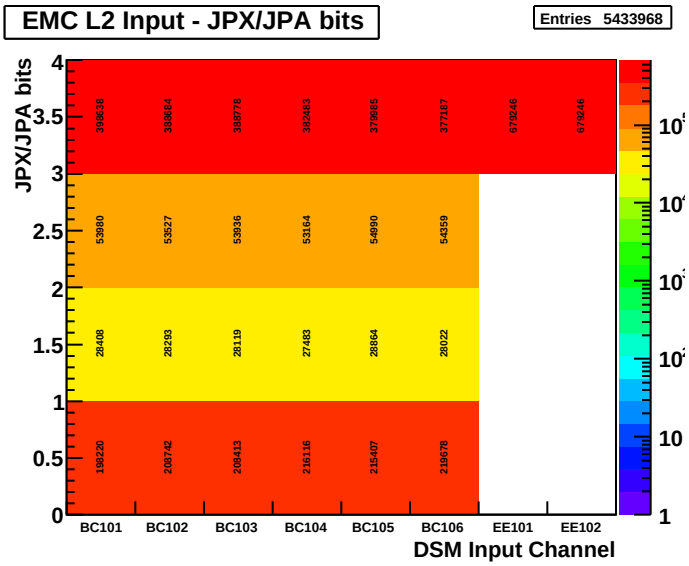

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

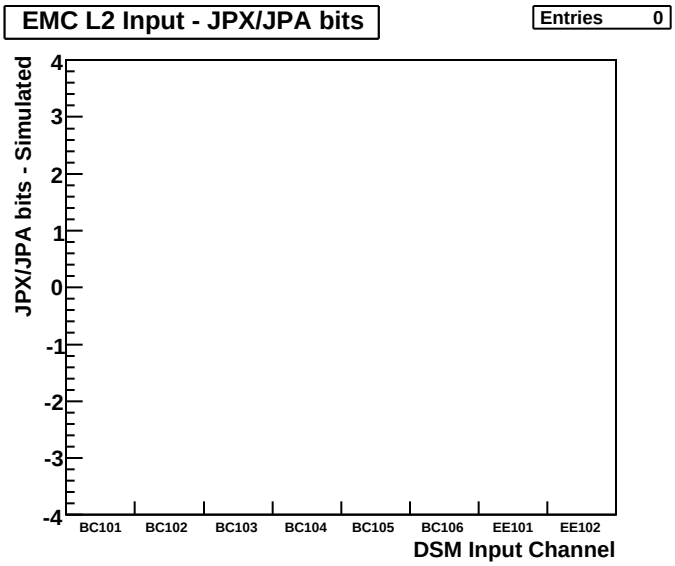

Endcap EMC L1 Input - High Eta Sum
Entries 8150952
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

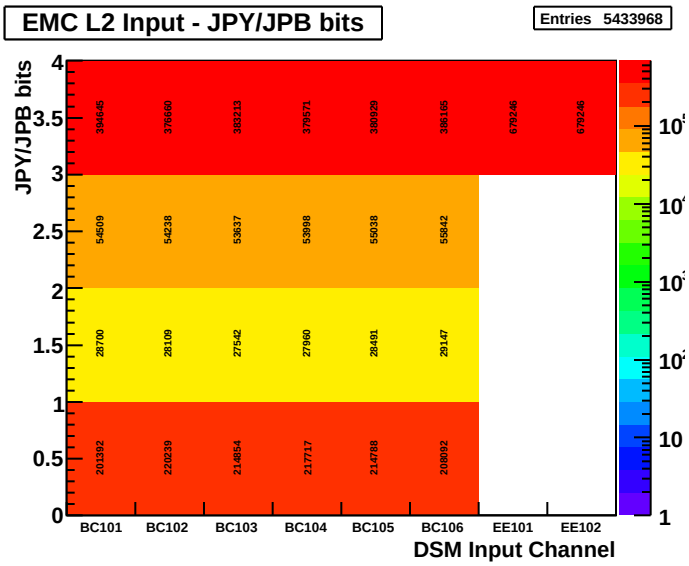

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

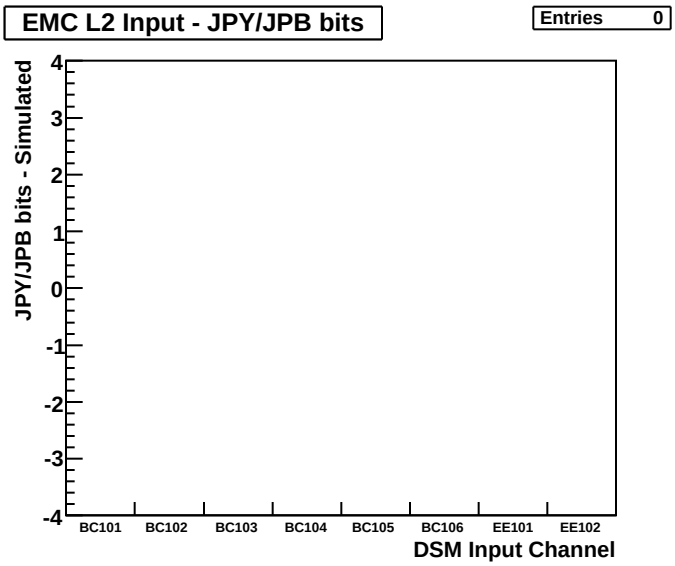

Endcap EMC L1 Input - High Tower Bits
Entries 8150952
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

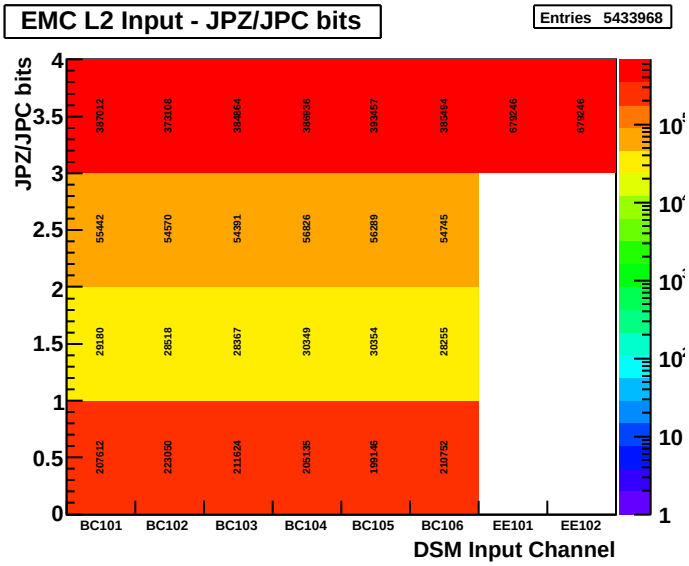

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

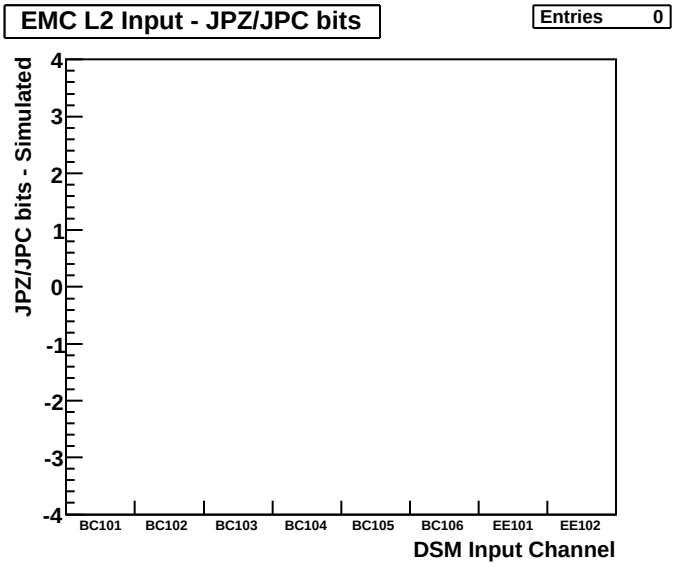

EMC L2 Input - JPX/JPA bits
Entries 5433968
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
198220 208742 208413 216116 215407 219678
28408 28293 28119 27483 28864 28022
53980 53527 53936 53164 54090 54359
398638 398684 398778 382483 379985 377187 679246 679246

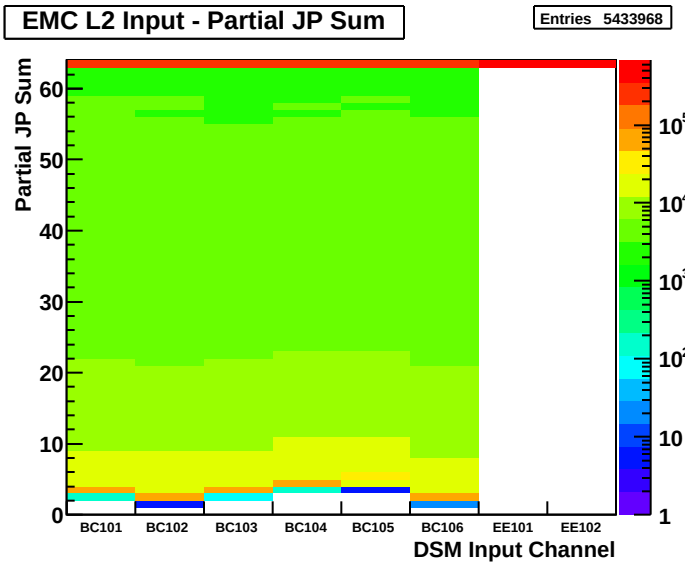

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

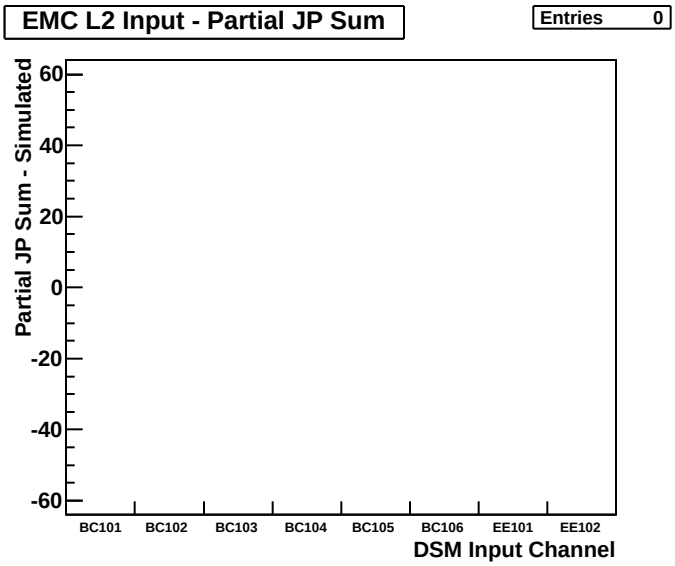

EMC L2 Input - JPY/JPB bits
Entries 5433968
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
201392 220239 214854 217717 214788 208092
28700 28109 27542 27960 28491 29147
54509 54238 53637 53998 55038 55842
394645 376680 383213 379571 380929 386165 679246 679246

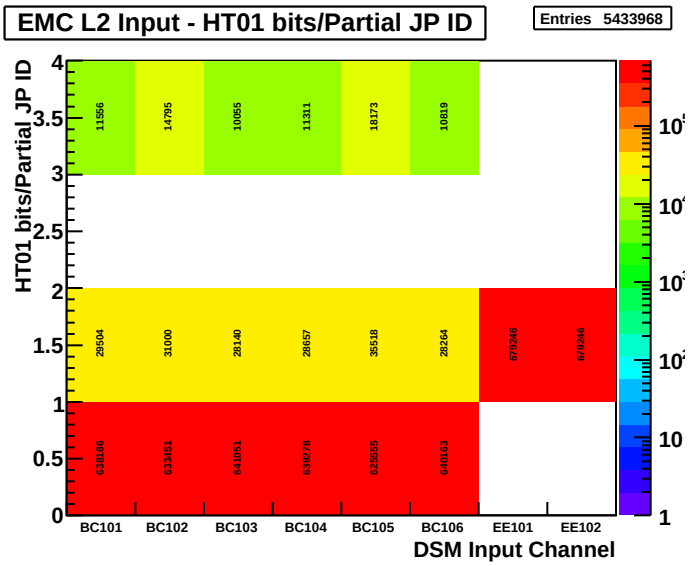

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

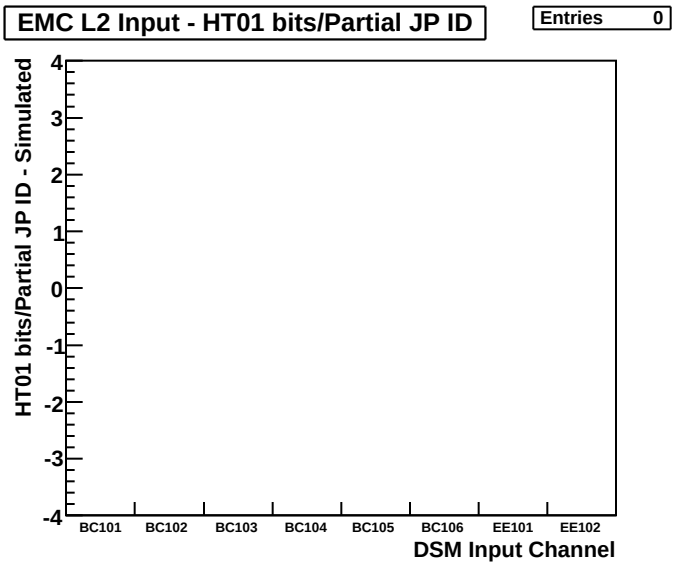

EMC L2 Input - JPZ/JPC bits
Entries 5433968
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
207612 223050 211624 205135 199146 210752
29180 28518 28367 30349 30354 28255
55442 54570 54391 56826 56289 54745
387012 373108 384864 388936 393457 385494 679246 679246

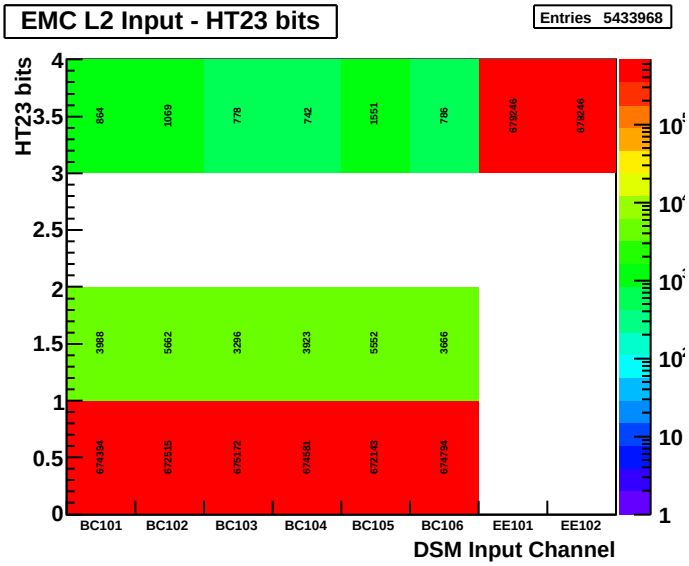

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

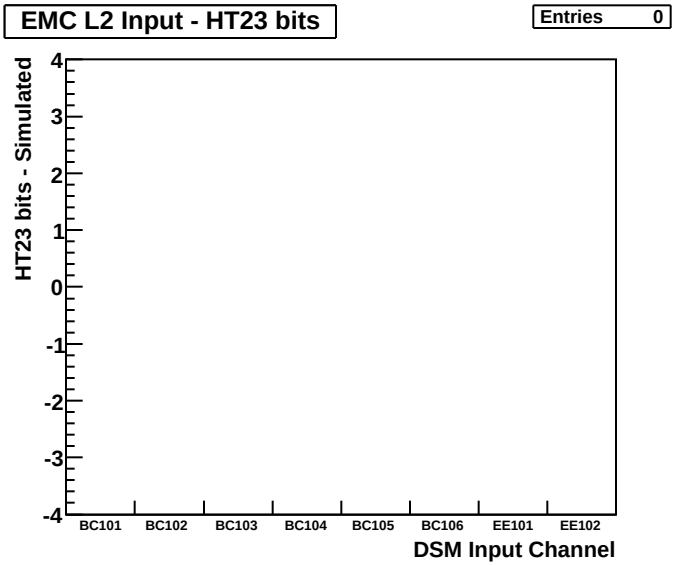

EMC L2 Input - Partial JP Sum
Entries 5433968
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

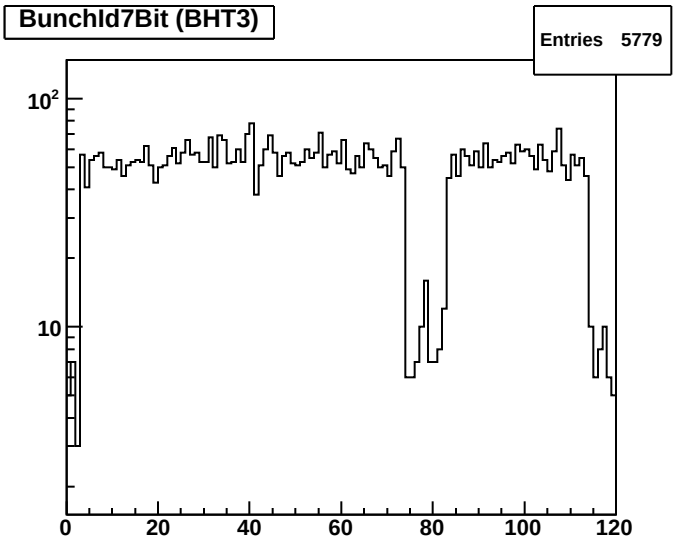

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

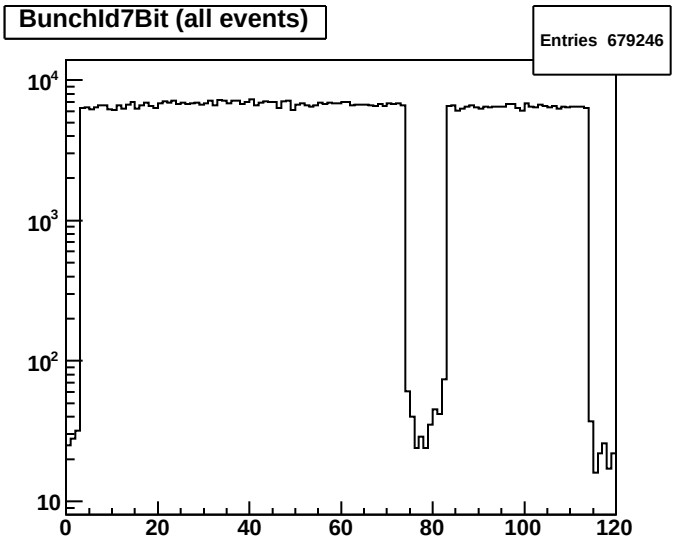

EMC L2 Input - HT01 bits/Partial JP ID
Entries 5433968
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

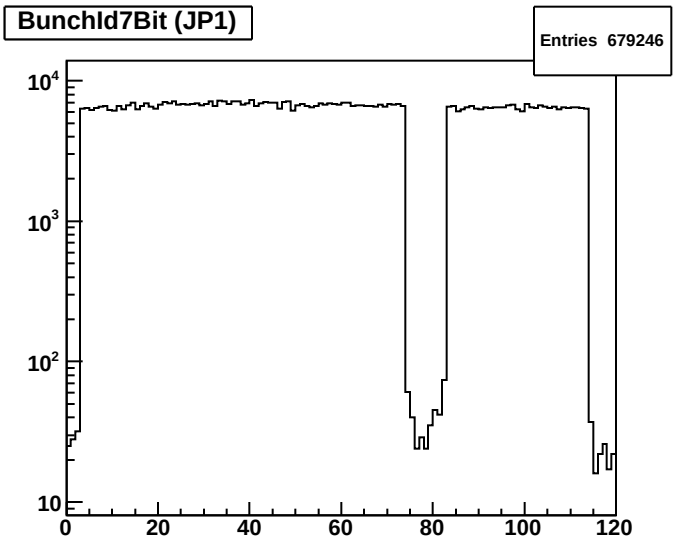

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

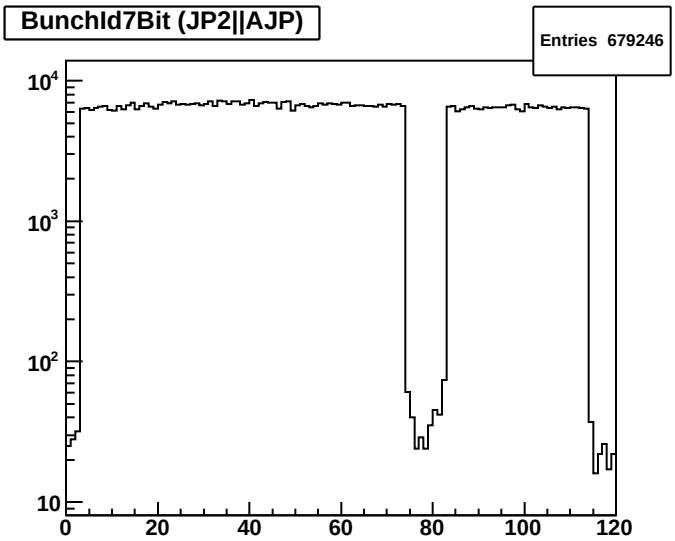

EMC L2 Input - HT23 bits
Entries 5433968
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

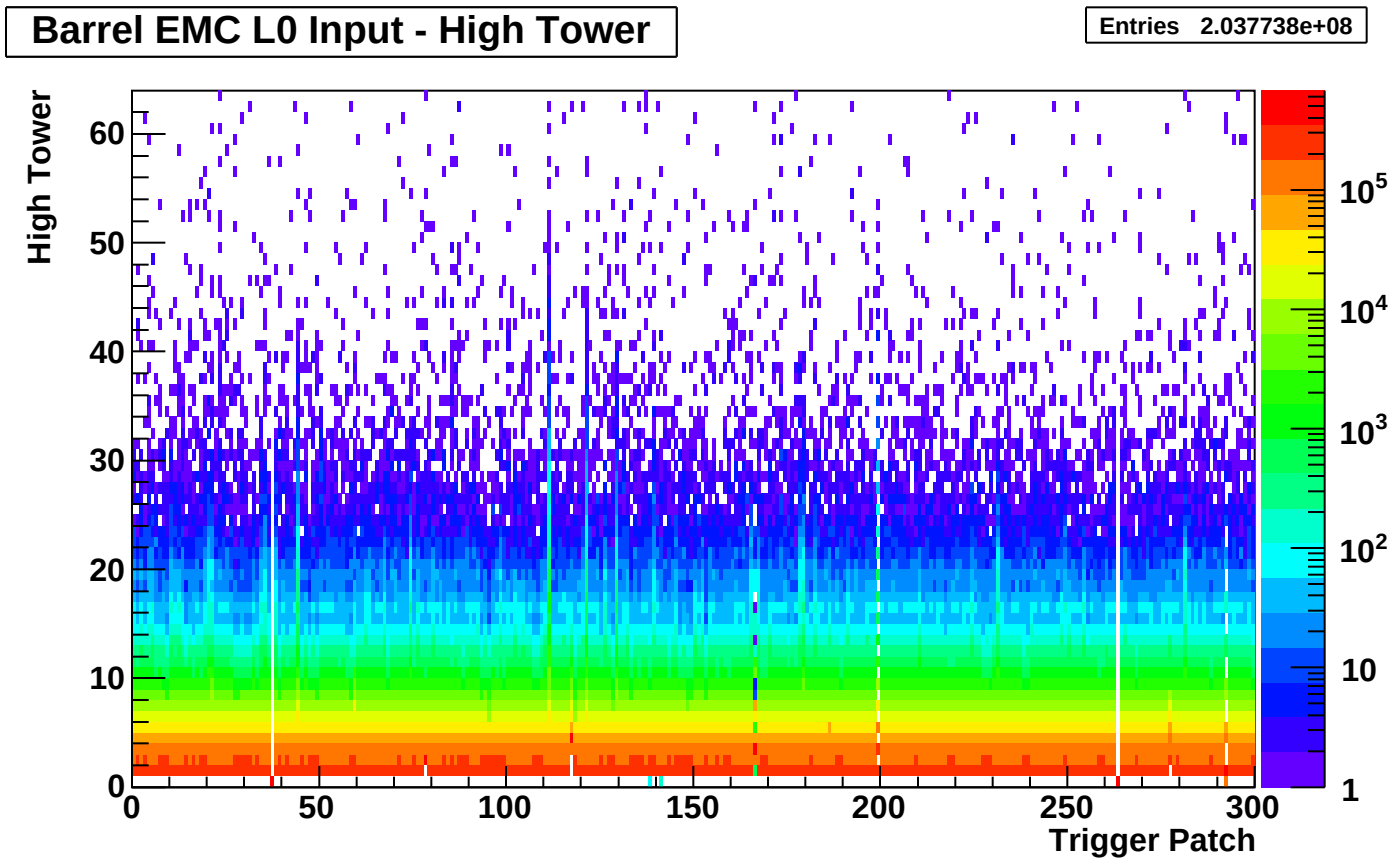

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

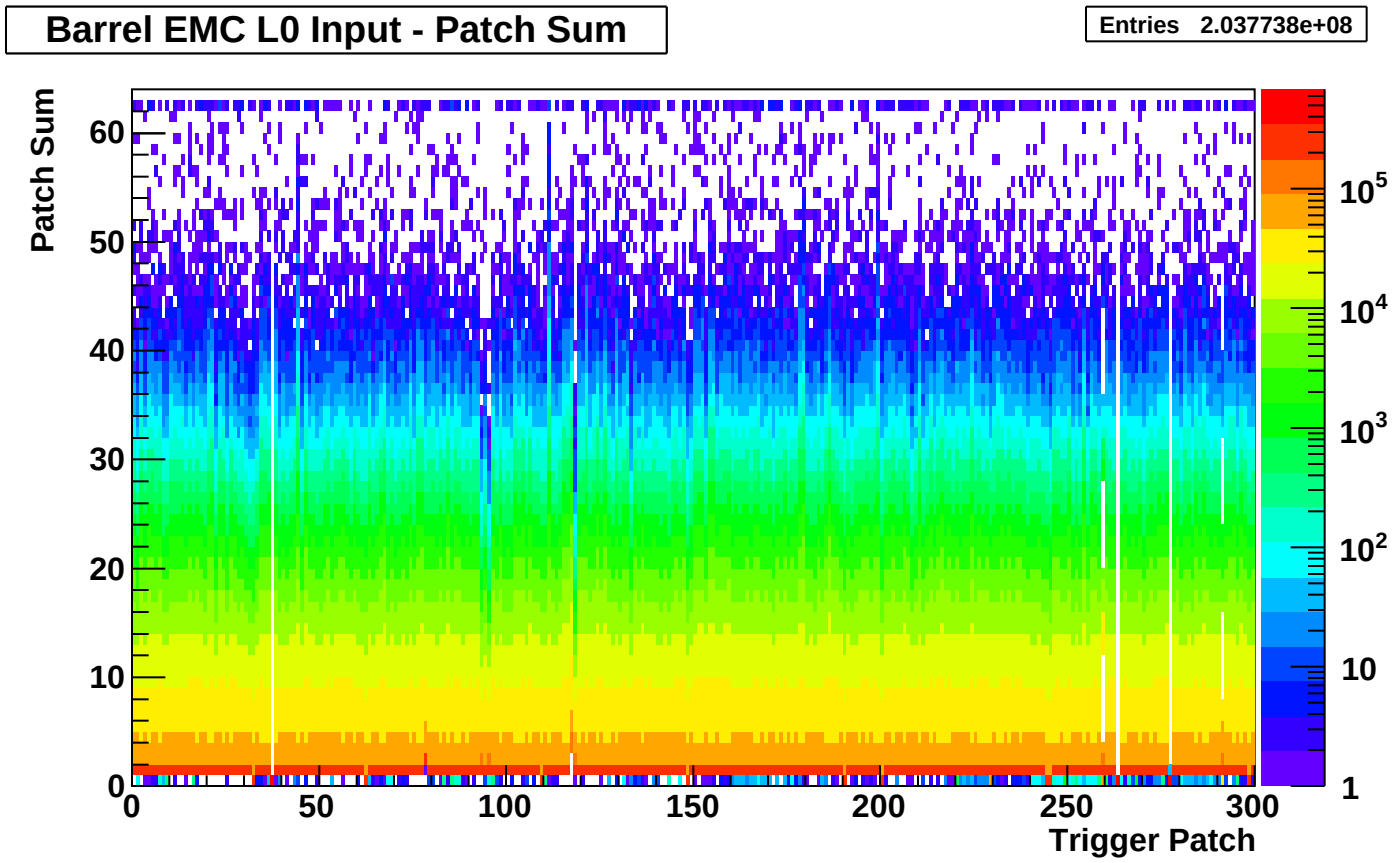

BunchId7Bit (BHT3)
Entries 5779
10^2
10
0
20
40
60
80
100
120

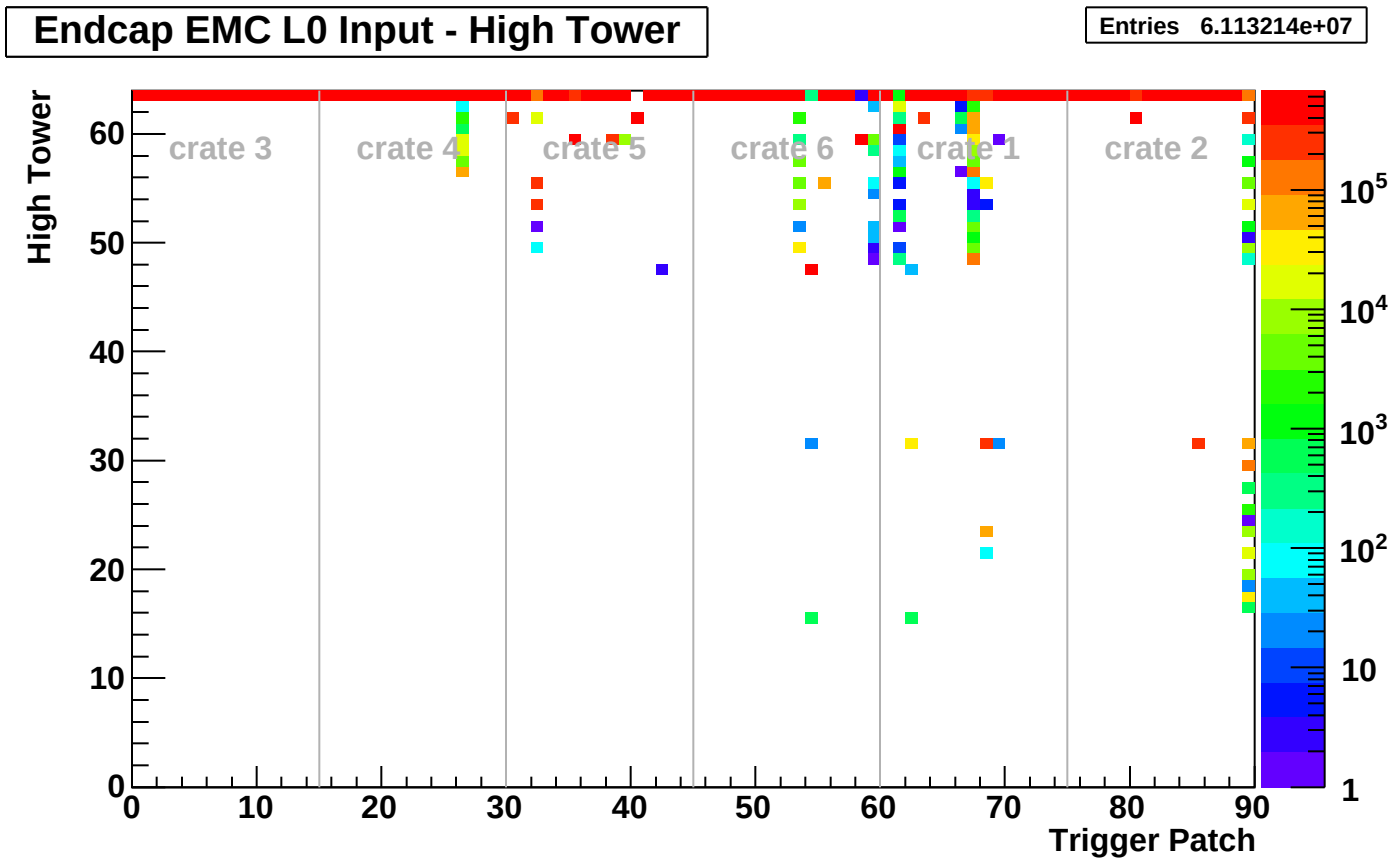

BunchId7Bit (all events)
Entries 679246
10^4
10^3
10^2
10
0
20
40
60
80
100
120

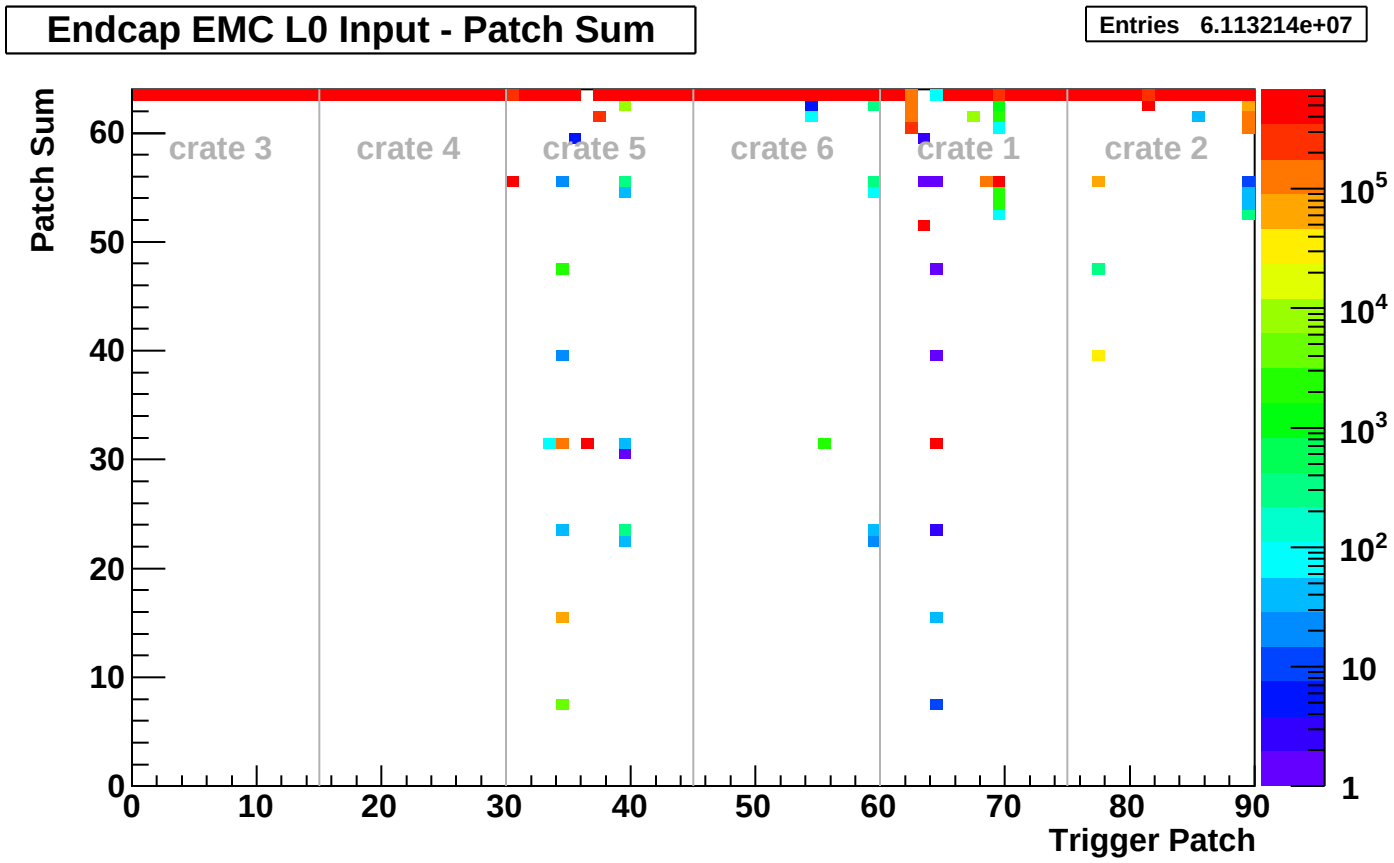

BunchId7Bit (JP1)
Entries 679246
10^4
10^3
10^2
10
0
20
40
60
80
100
120

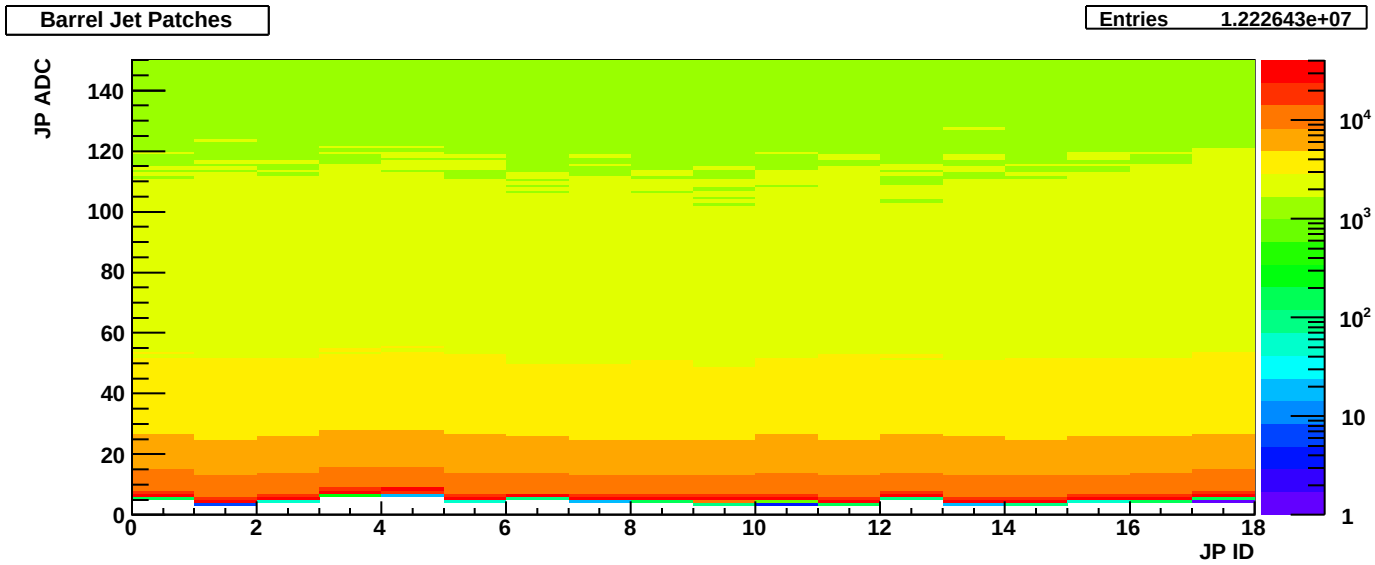

BunchId7Bit (JP2||AJP)
Entries 679246
10^4
10^3
10^2
10
0
20
40
60
80
100
120

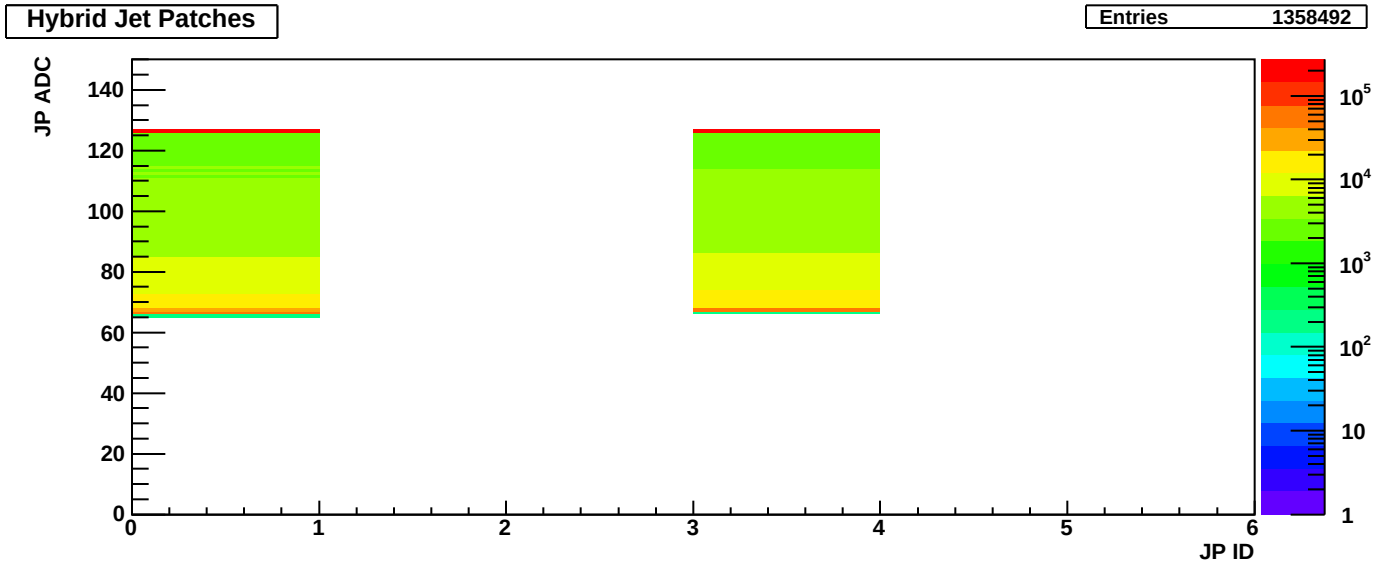

Barrel EMC L0 Input - High Tower
Entries 2.037738e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

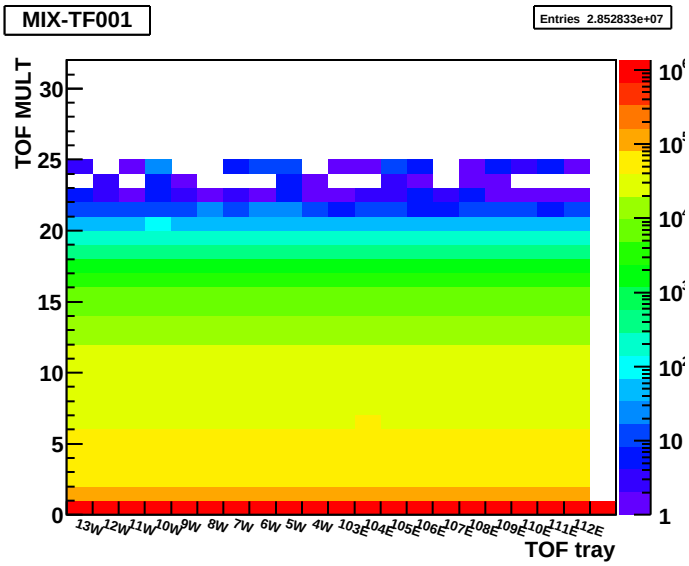

Barrel EMC L0 Input - Patch Sum
Entries 2.037738e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

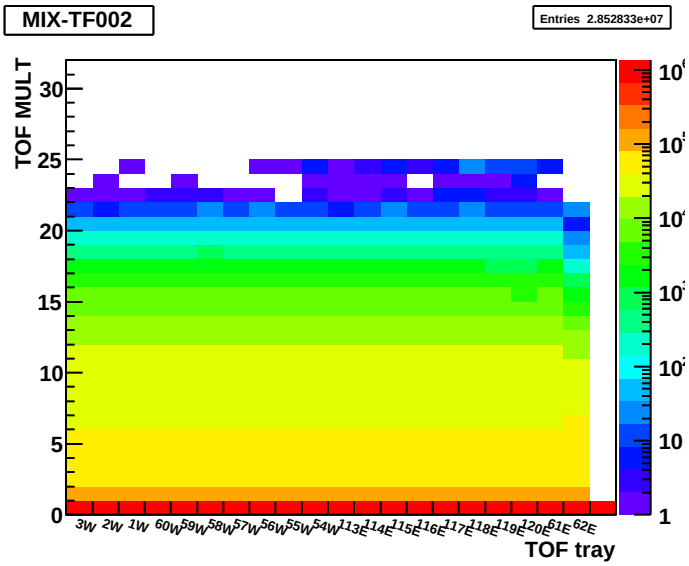

Endcap EMC L0 Input - High Tower
Entries 6.113214e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

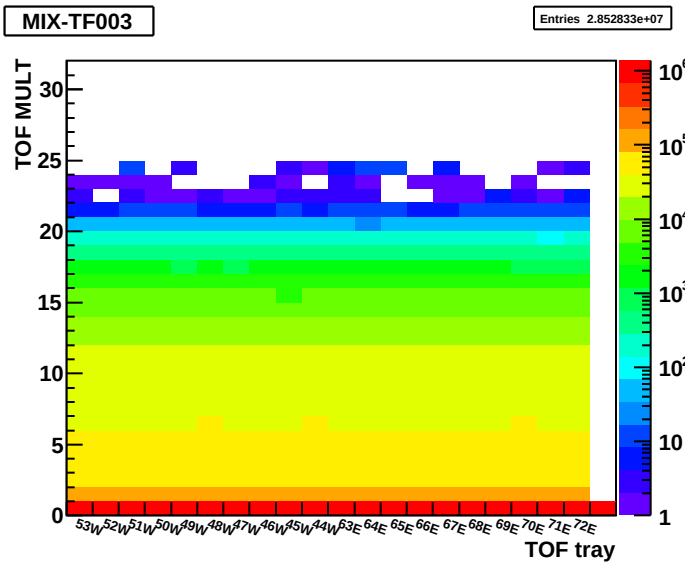

Endcap EMC L0 Input - Patch Sum
Entries 6.113214e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

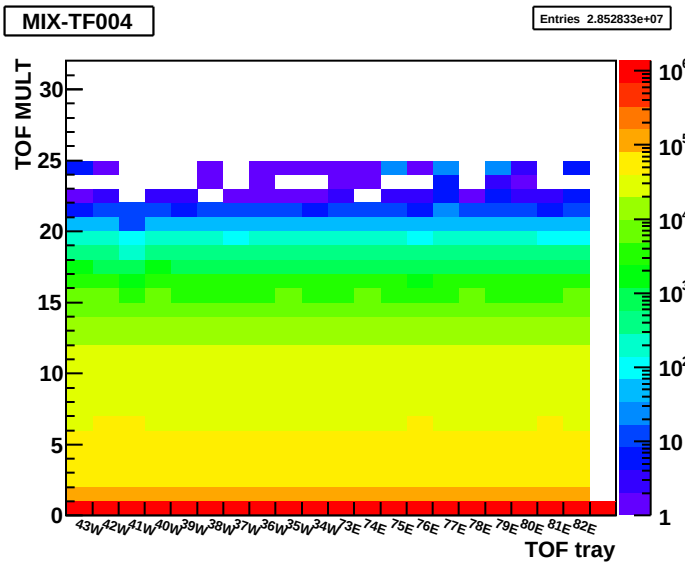

Barrel Jet Patches
Entries 1.222643e+07
JP ADC
JP ID

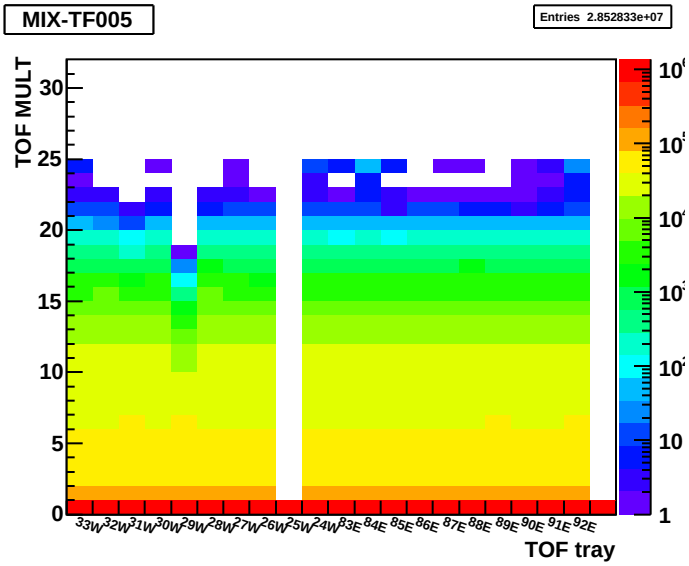

Hybrid Jet Patches
Entries 1358492
JP ADC
JP ID

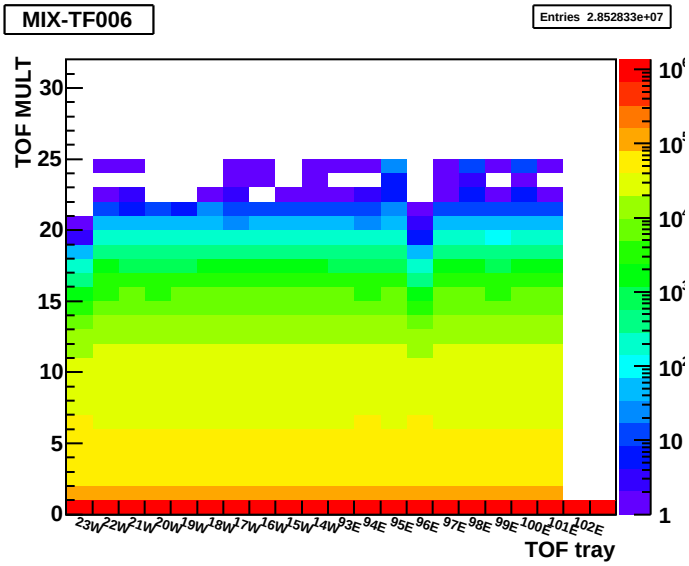

MIX-TF001
Entries 2.852833e+07
TOF MULT
TOF tray

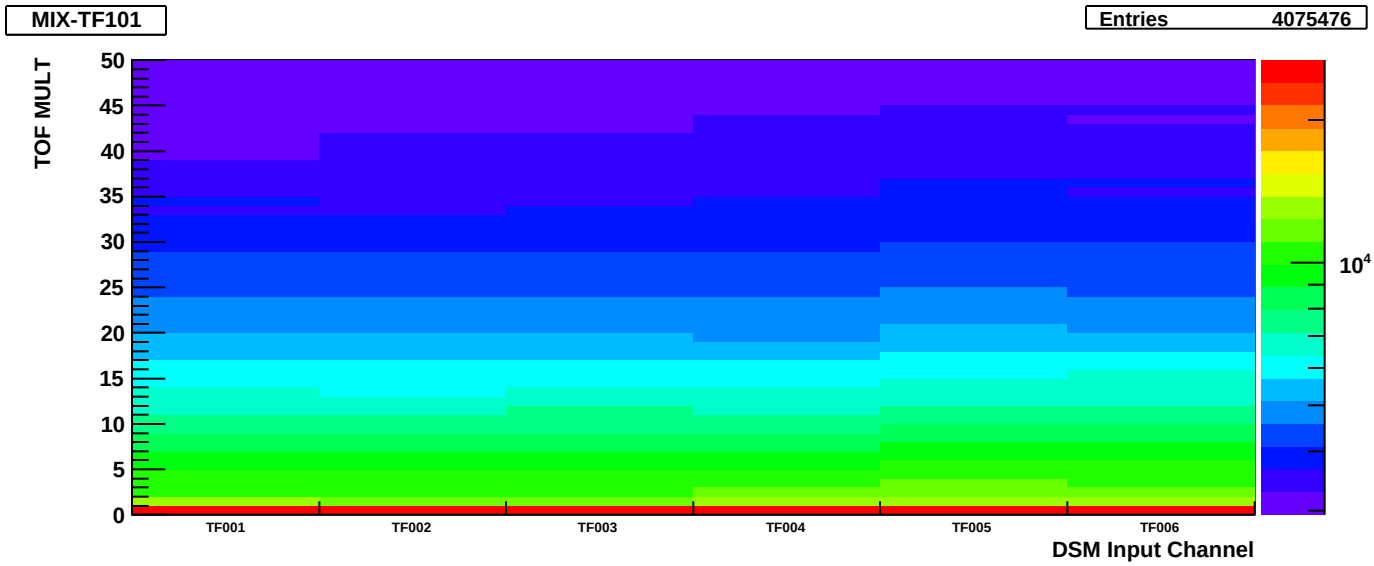

MIX-TF002
Entries 2.852833e+07
TOF MULT
TOF tray

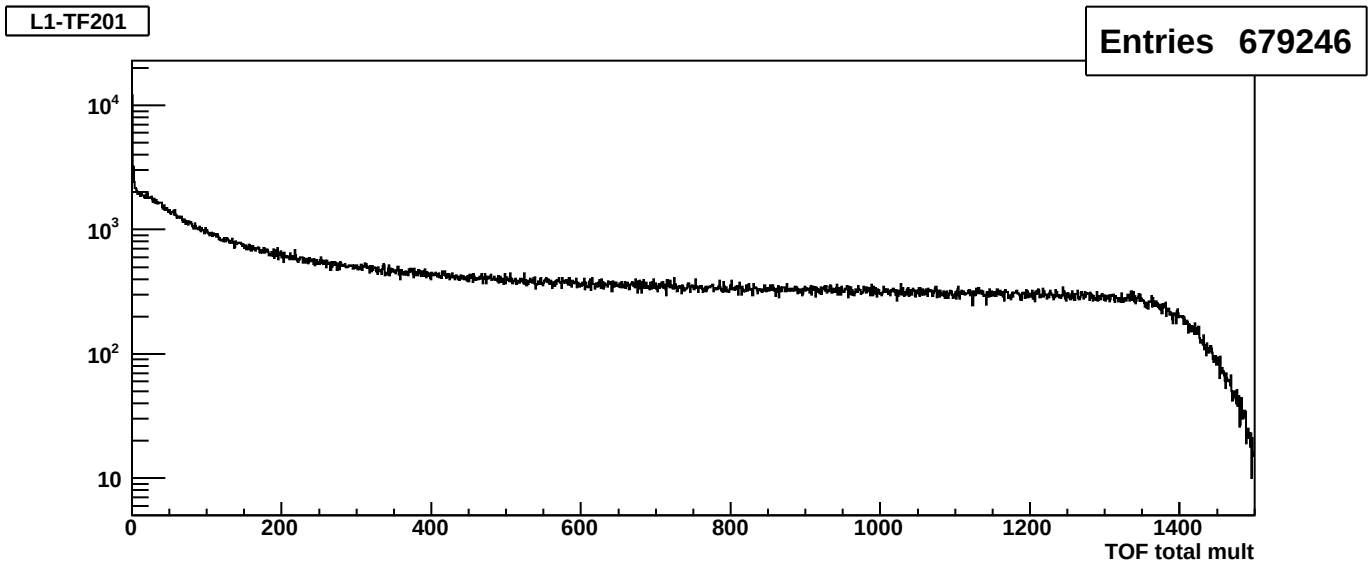

MIX-TF003
Entries 2.852833e+07
TOF MULT
TOF tray

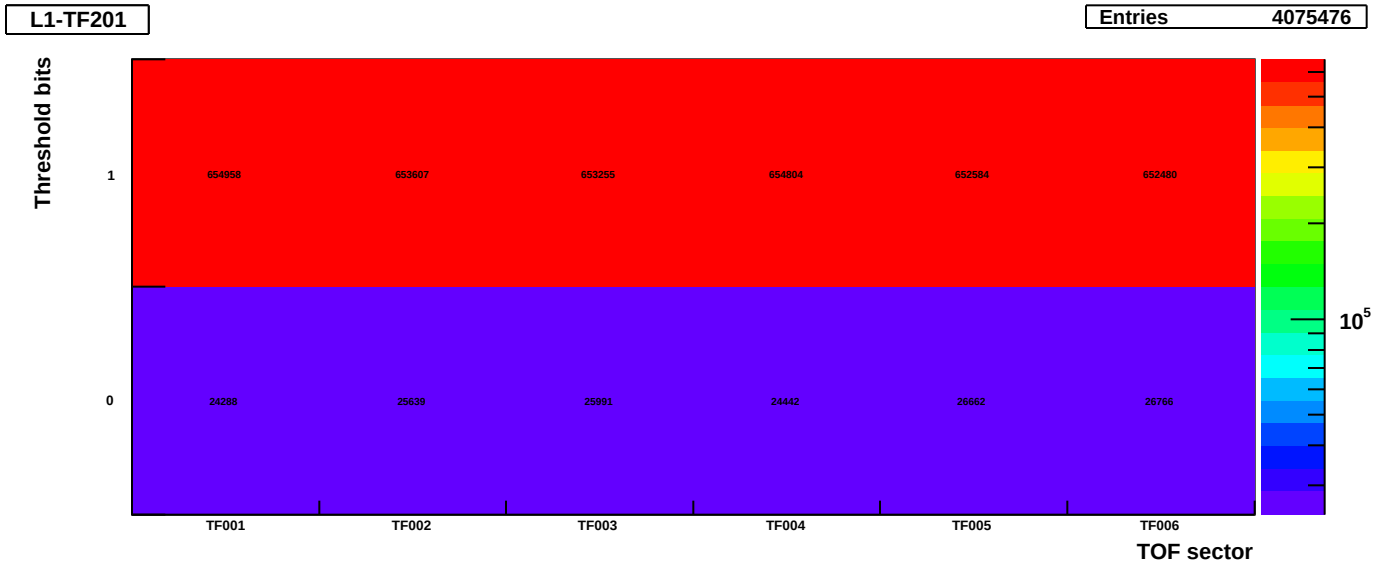

MIX-TF004
Entries 2.852833e+07
TOF MULT
TOF tray

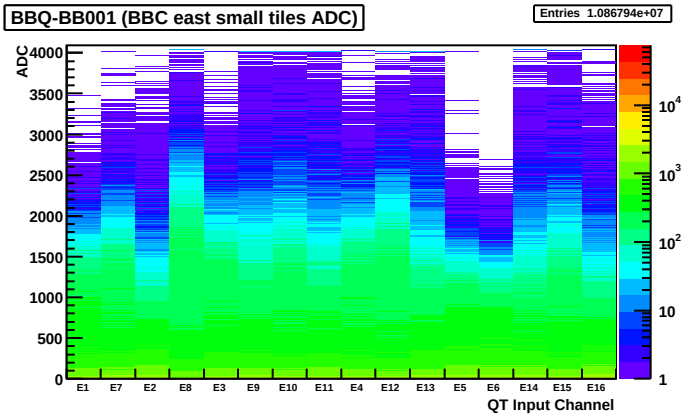

MIX-TF005
Entries 2.852833e+07
TOF MULT
TOF tray

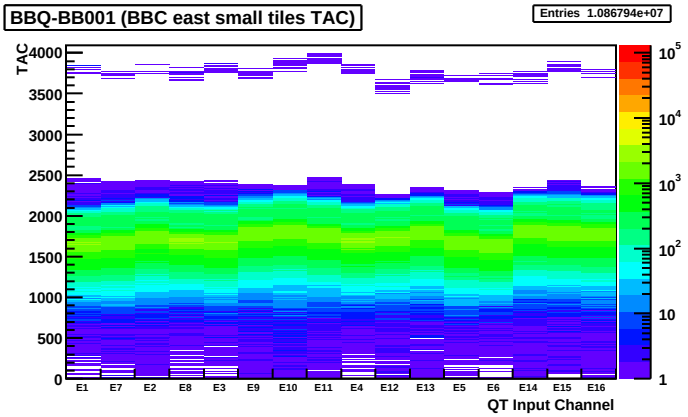

MIX-TF006
Entries 2.852833e+07
TOF MULT
TOF tray

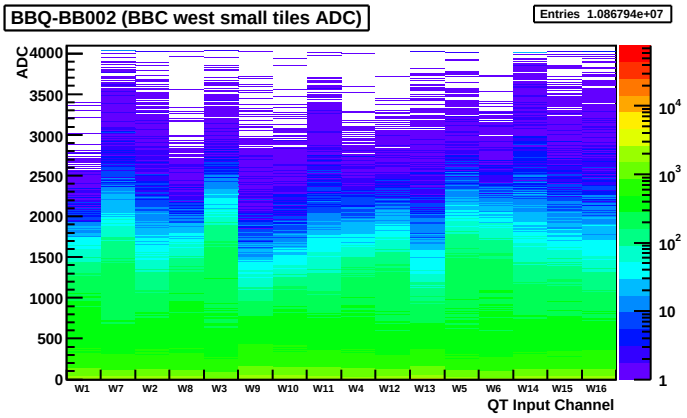

MIX-TF101
Entries 4075476
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

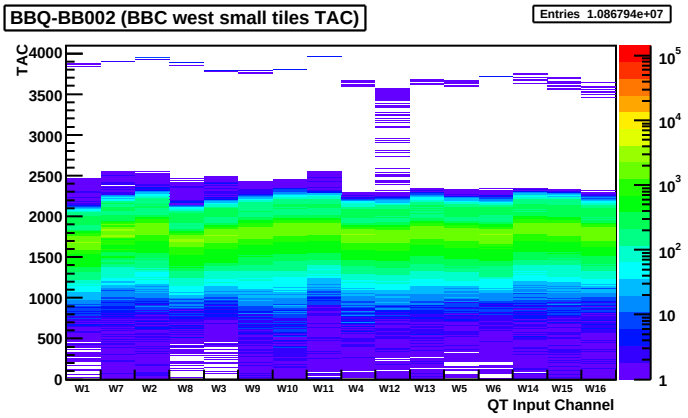

L1-TF201
Entries 679246
10^4
10^3
10^2
10
0
200
400
600
800
1000
1200
1400
TOF total mult

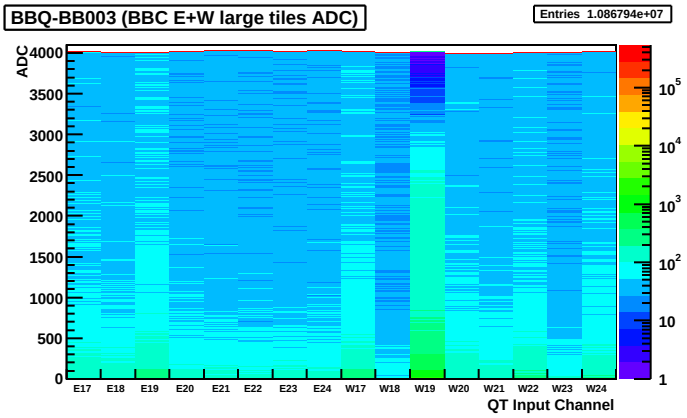

L1-TF201
Entries 4075476
Threshold bits
1
0
654958
653607
653255
654804
652584
652480
24288
25639
25991
24442
26662
26766
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

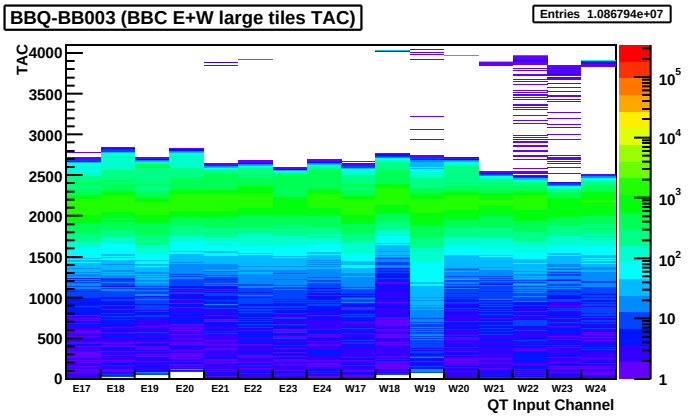

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.086794e+07
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

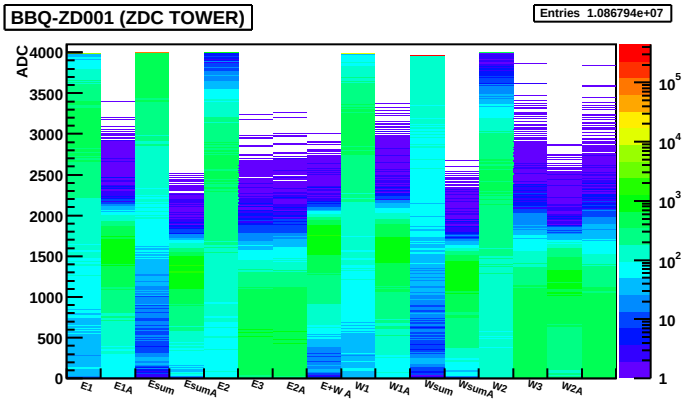

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.086794e+07
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

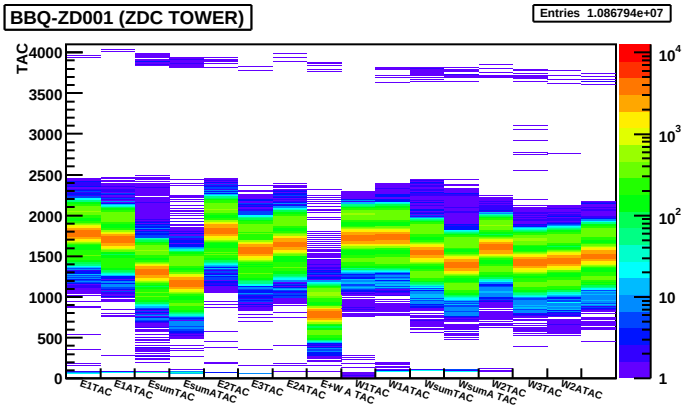

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.086794e+07
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

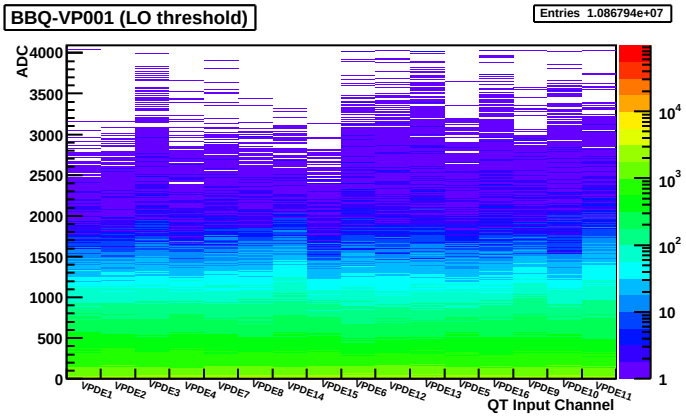

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.086794e+07
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

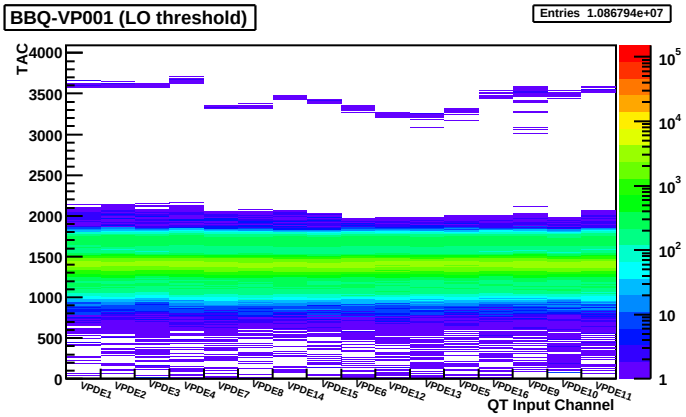

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.086794e+07
ADC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

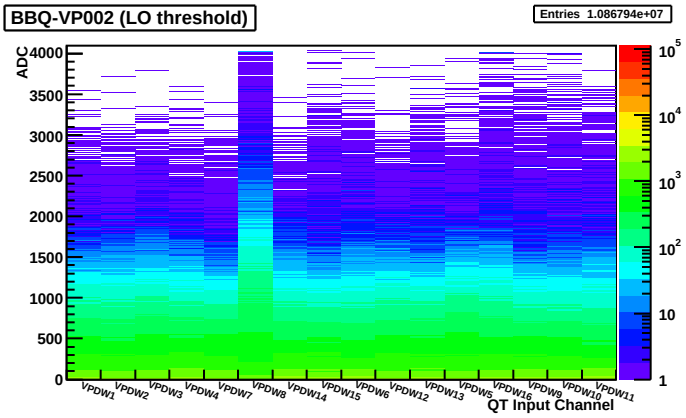

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.086794e+07
TAC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

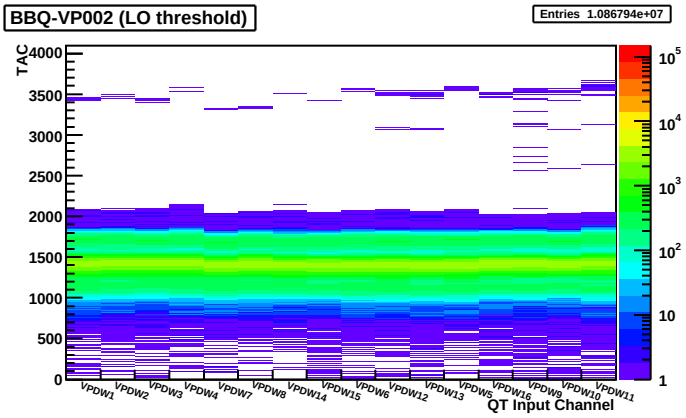

BBQ-ZD001 (ZDC TOWER)
Entries 1.086794e+07
ADC
E1 E1A Esum EsumA E2 E3 E2A E+W A W1 W1A Wsum WsumA W2 W3 W2A

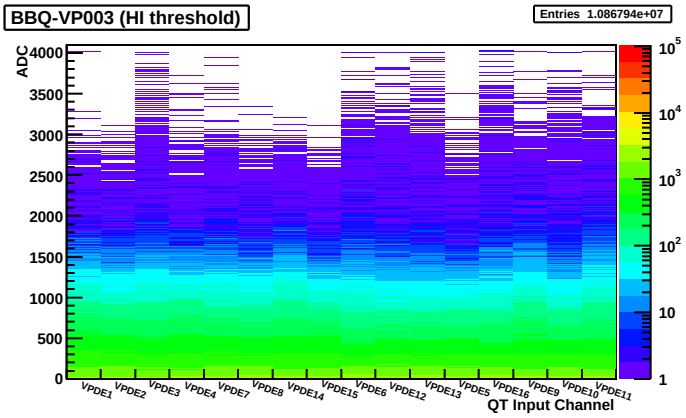

BBQ-ZD001 (ZDC TOWER)
Entries 1.086794e+07
TAC
E1TAC E1ATAC EsumTAC EsumATAC E2TAC E3TAC E2ATAC E+W A TAC W1TAC W1ATAC WsumTAC WsumA TAC W2TAC W3TAC W2ATAC

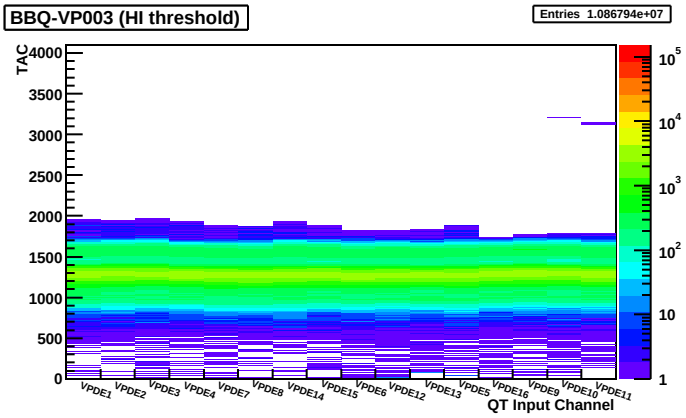

BBQ-VP001 (LO threshold)
Entries 1.086794e+07
ADC
QT Input Channel

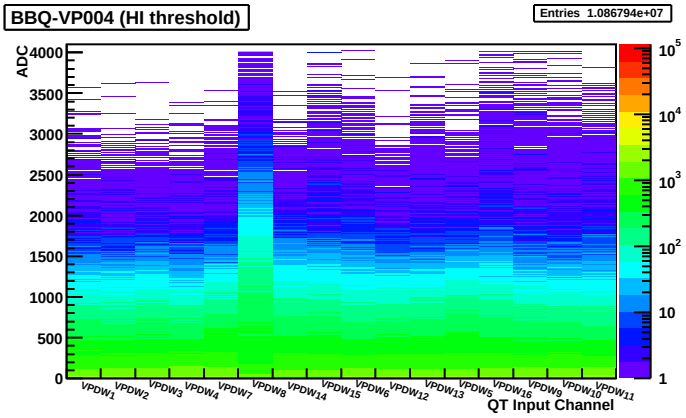

BBQ-VP001 (LO threshold)
Entries 1.086794e+07
TAC
QT Input Channel

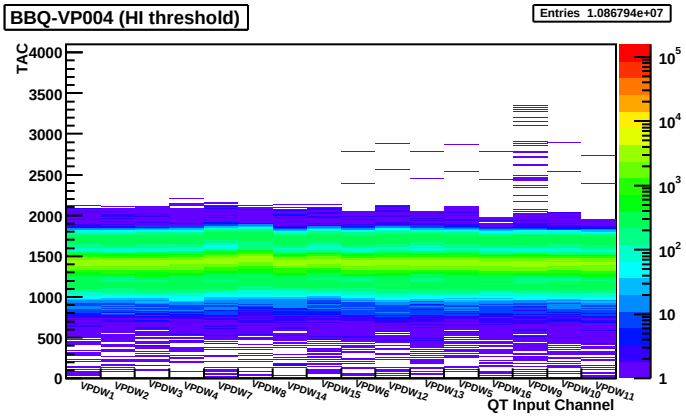

BBQ-VP002 (LO threshold)
Entries 1.086794e+07
ADC
QT Input Channel

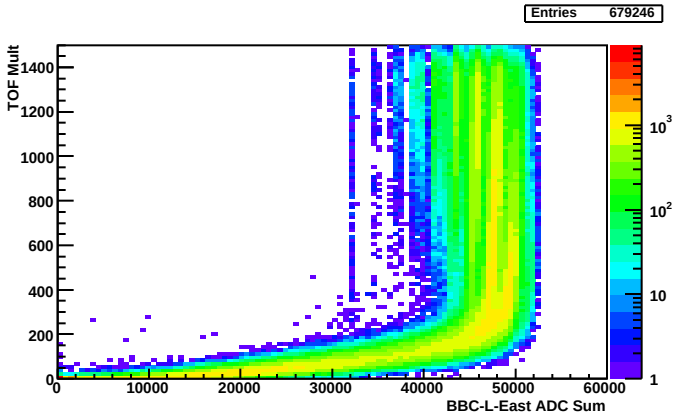

BBQ-VP002 (LO threshold)
Entries 1.086794e+07
TAC
QT Input Channel

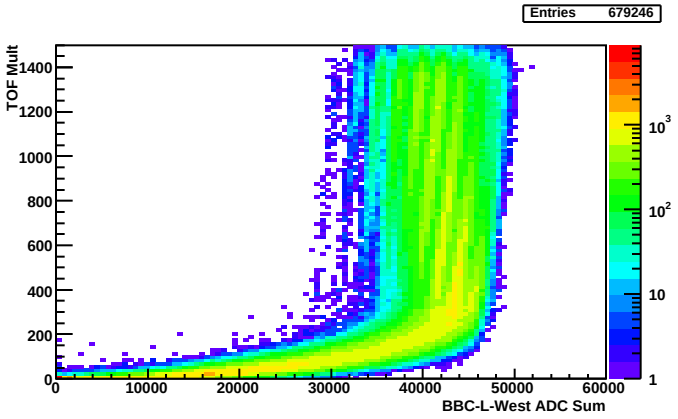

BBQ-VP003 (HI threshold)
Entries 1.086794e+07
ADC
QT Input Channel

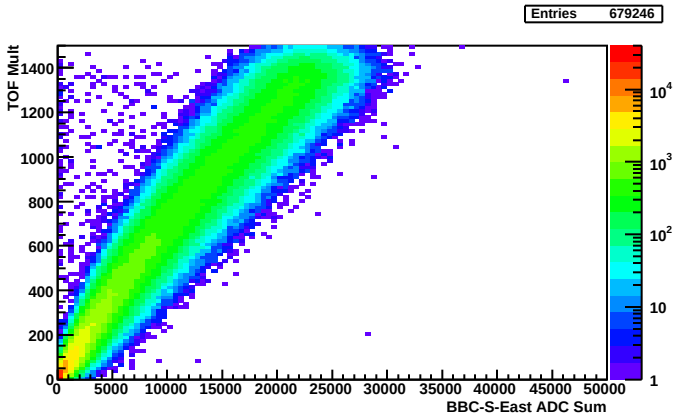

BBQ-VP003 (HI threshold)
Entries 1.086794e+07
TAC
QT Input Channel

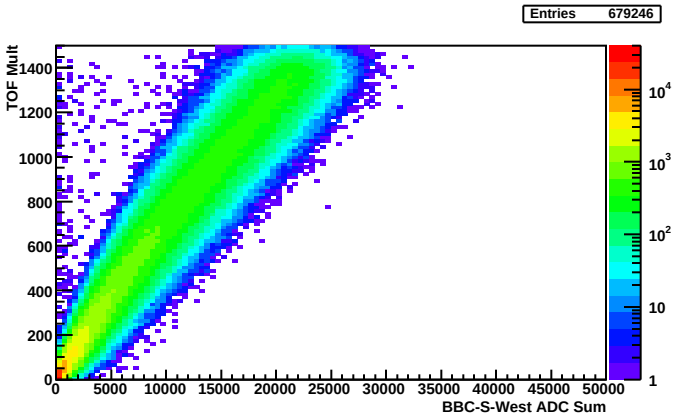

BBQ-VP004 (HI threshold)
Entries 1.086794e+07
ADC
QT Input Channel

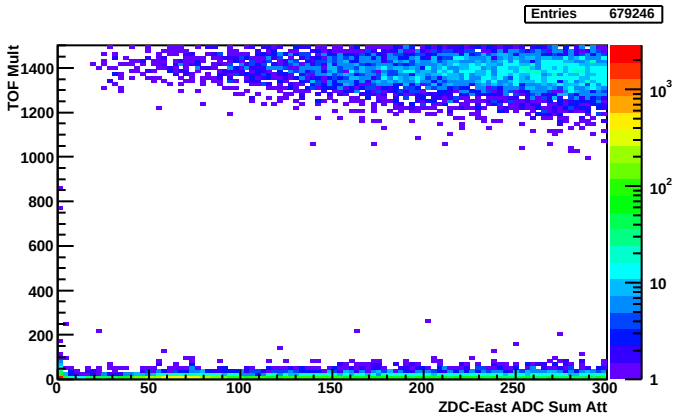

BBQ-VP004 (HI threshold)
Entries 1.086794e+07
TAC
QT Input Channel

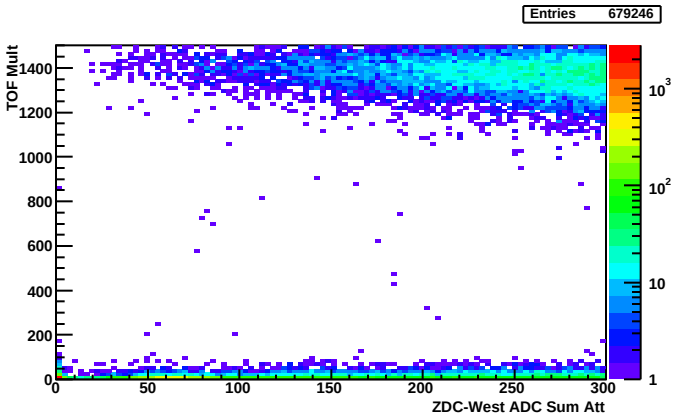

Entries 679246
TOF Mult
BBC-L-East ADC Sum

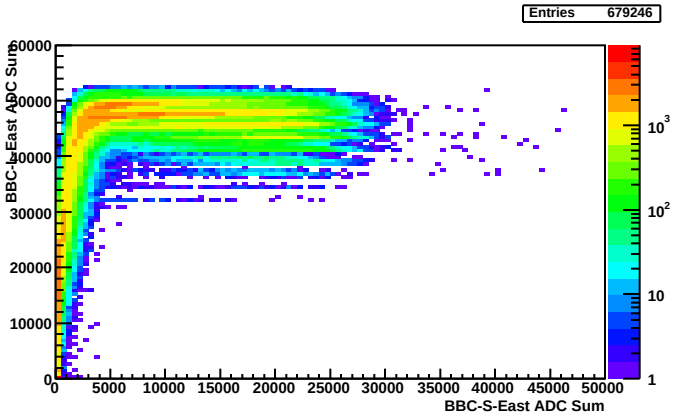

Entries 679246
TOF Mult
BBC-L-West ADC Sum

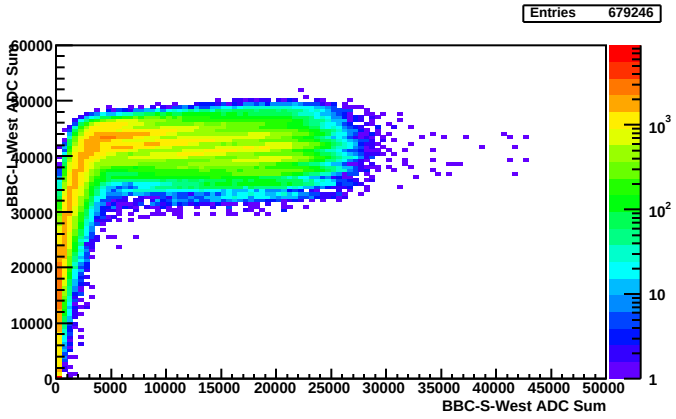

Entries 679246
TOF Mult
BBC-S-East ADC Sum

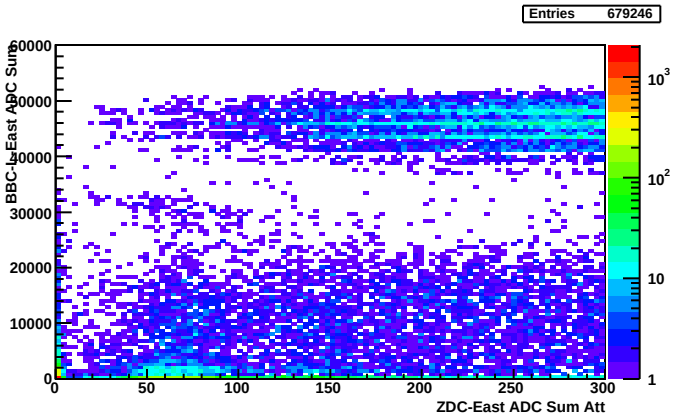

Entries 679246
TOF Mult
BBC-S-West ADC Sum


Entries 679246
TOF Mult
ZDC-East ADC Sum Att


Entries 679246
TOF Mult
ZDC-West ADC Sum Att


Entries 679246
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 679246
BBC-L-West ADC Sum
BBC-S-West ADC Sum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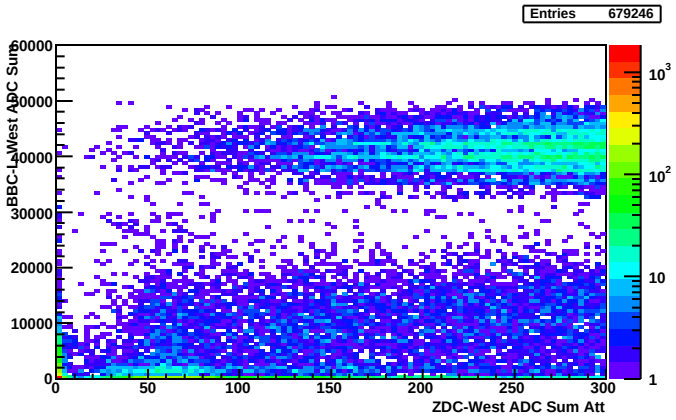
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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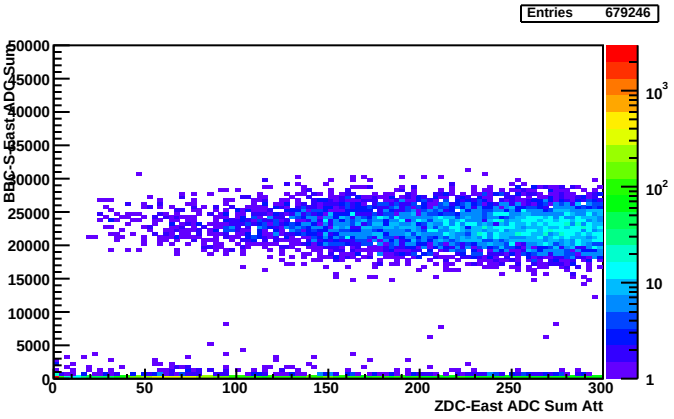
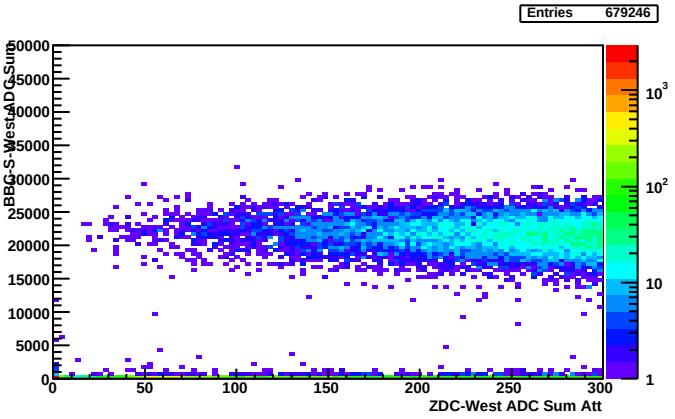
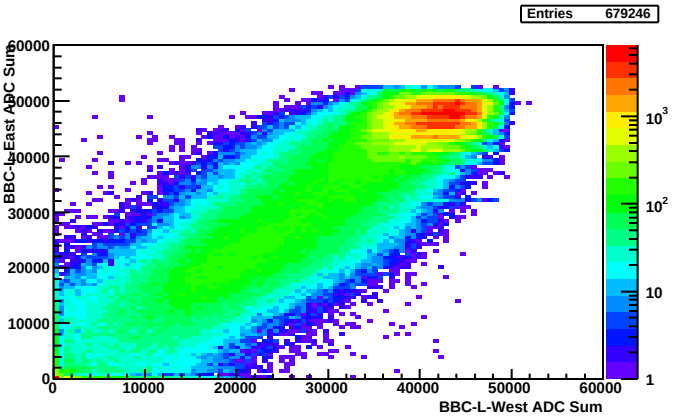
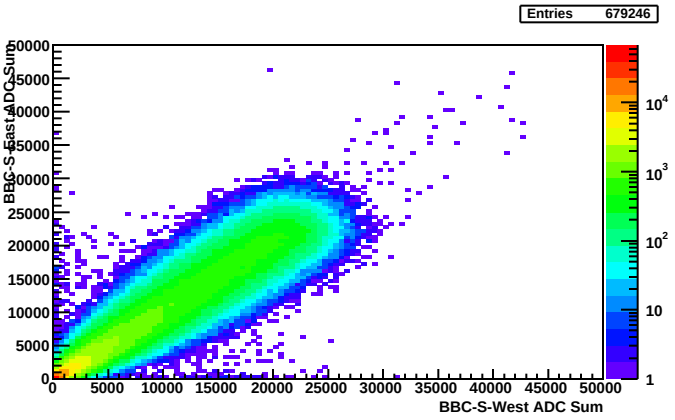
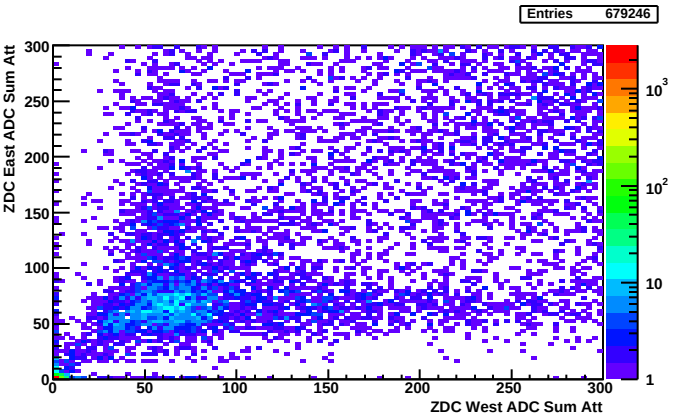
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

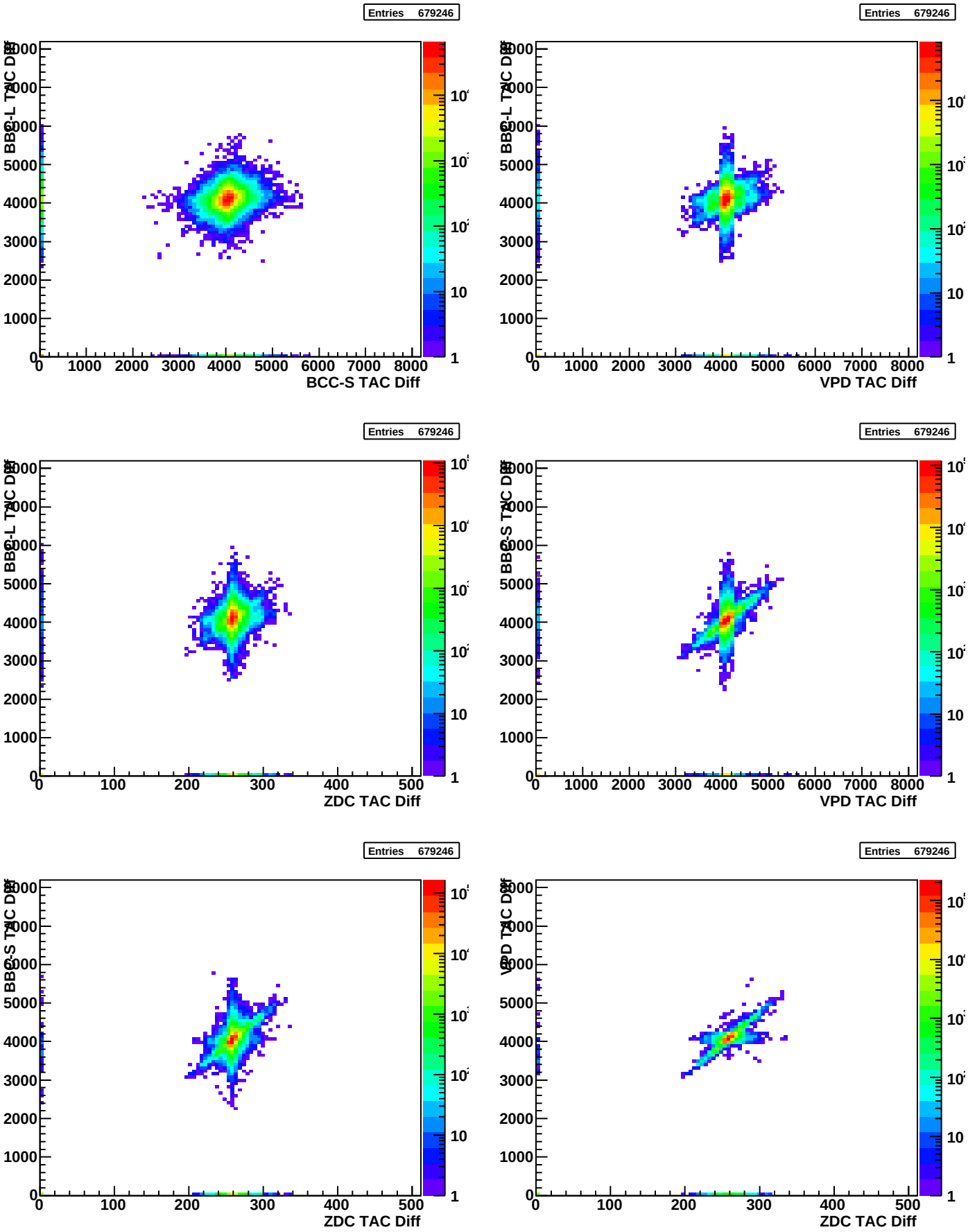

Entries 679246
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

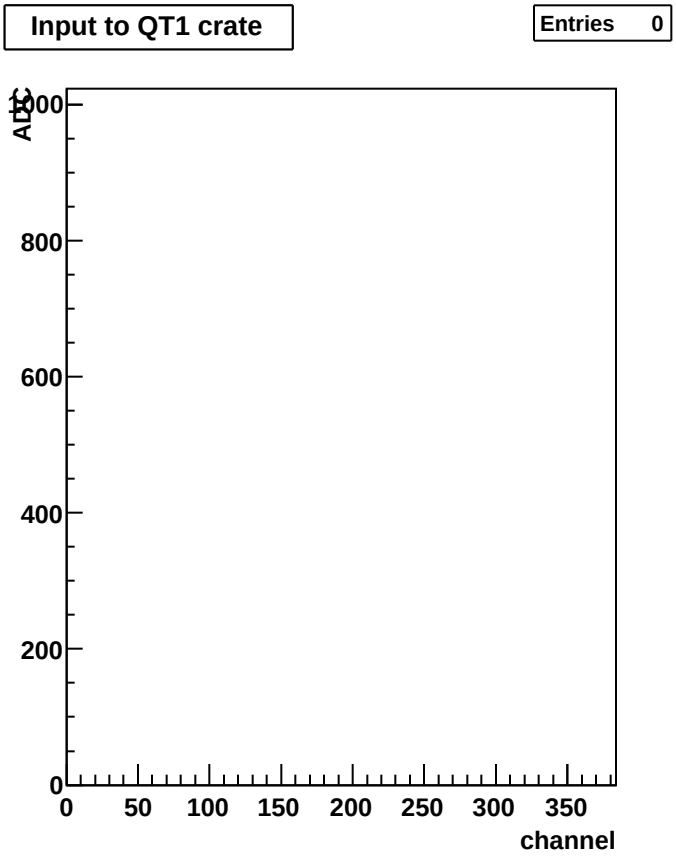

Entries 679246
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

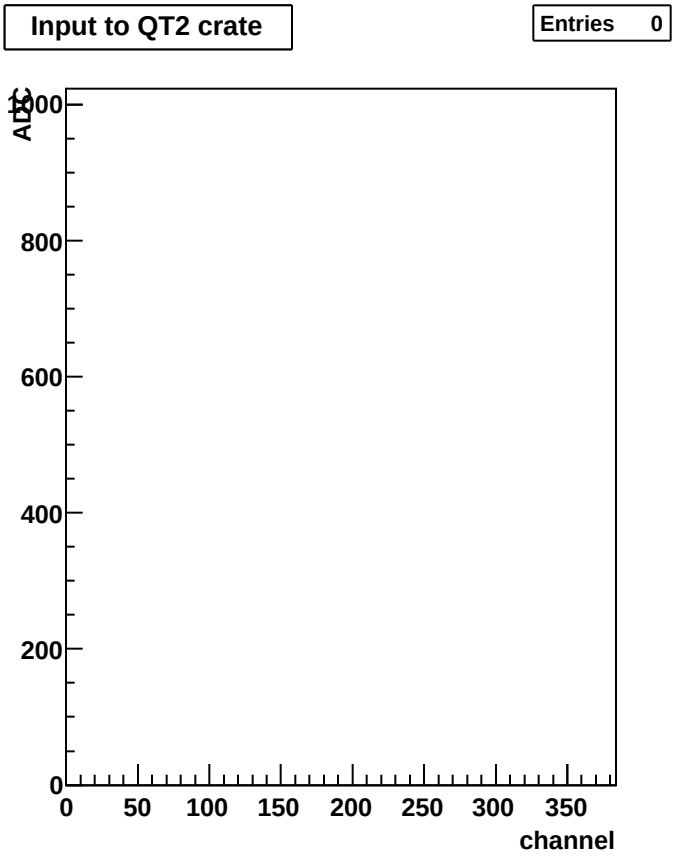

Entries 679246
BBC-L-East ADC Sum
BBC-L-West ADC Sum

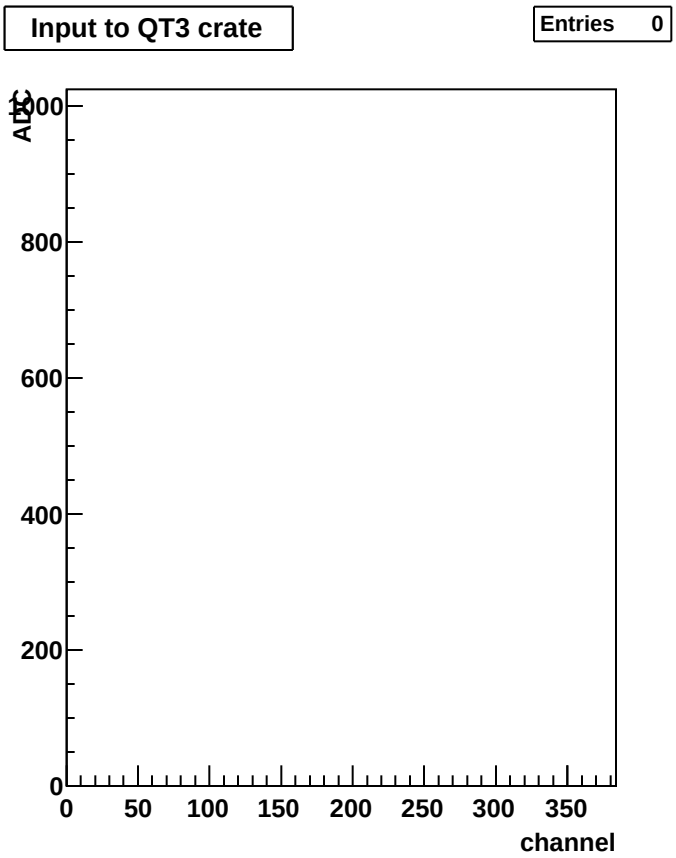

Entries 679246
BBC-S-East ADC Sum
BBC-S-West ADC Sum

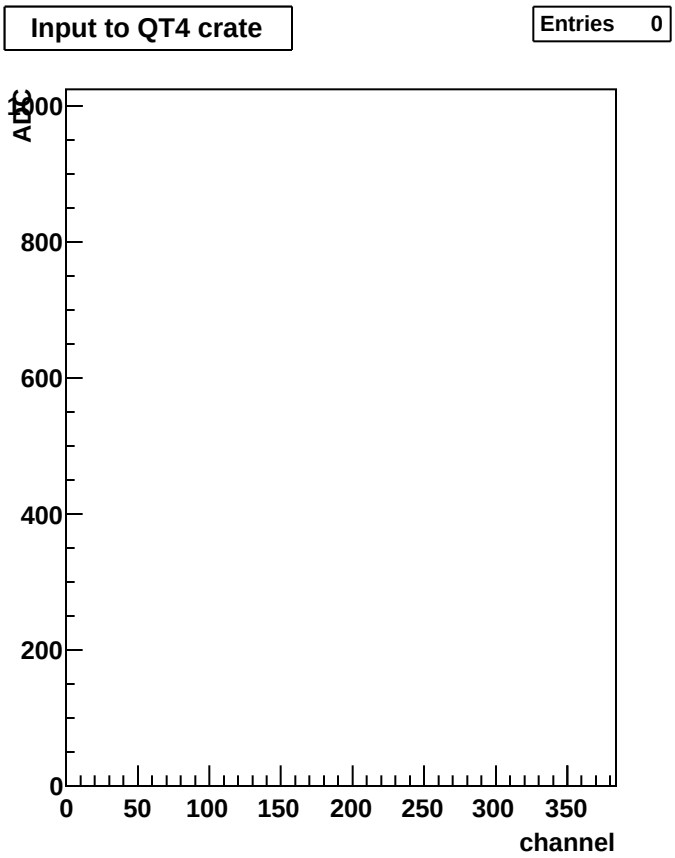

Entries 679246
ZDC East ADC Sum Att
ZDC West ADC Sum Att

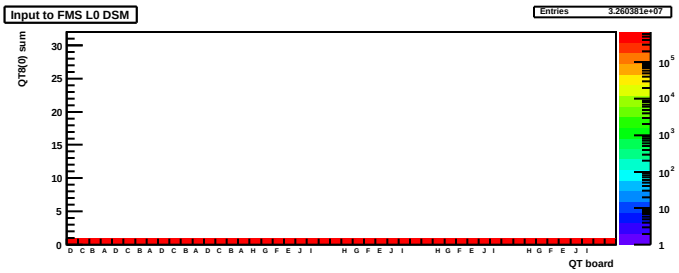

Entries 679246
BBC-L TAC Diff
BCC-S TAC Diff
Entries 679246
BBC-L TAC Diff
VPD TAC Diff
Entries 679246
BBC-L TAC Diff
ZDC TAC Diff
Entries 679246
BBC-S TAC Diff
VPD TAC Diff
Entries 679246
BBC-S TAC Diff
ZDC TAC Diff
Entries 679246
VPD TAC Diff
ZDC TAC Diff

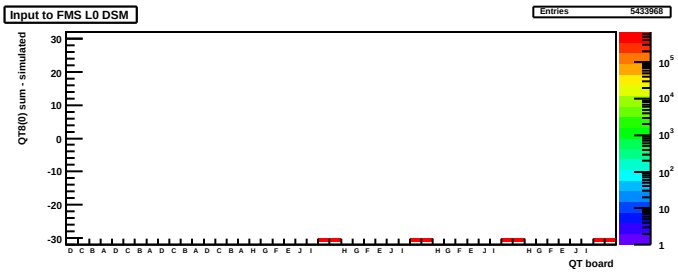

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

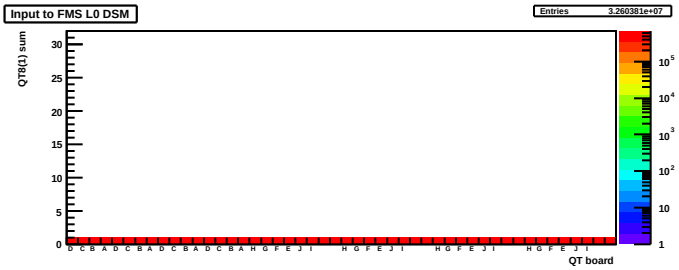

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

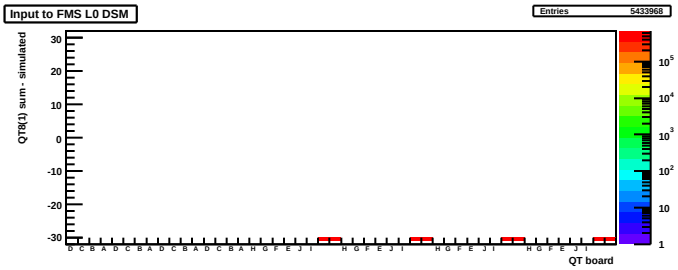

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

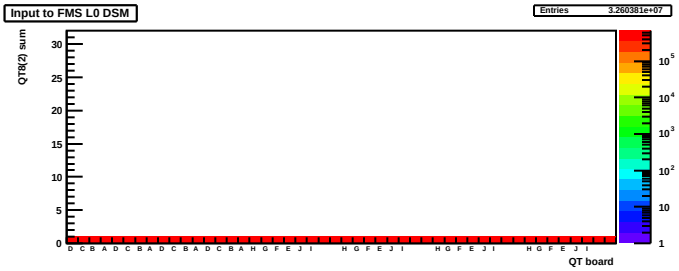

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

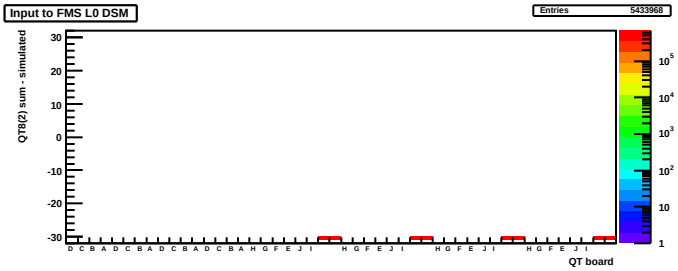

Input to FMS L0 DSM
Entries 3.260381e+07
QT8(0) sum
QT board

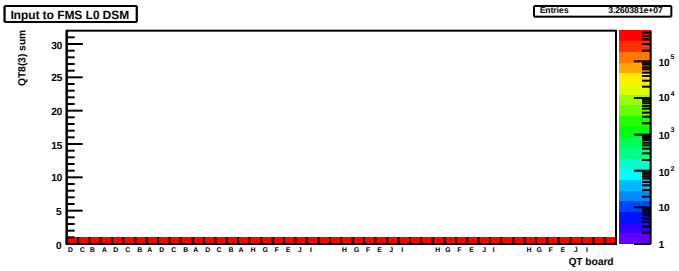

Input to FMS L0 DSM
Entries 5433968
QT8(0) sum - simulated
QT board

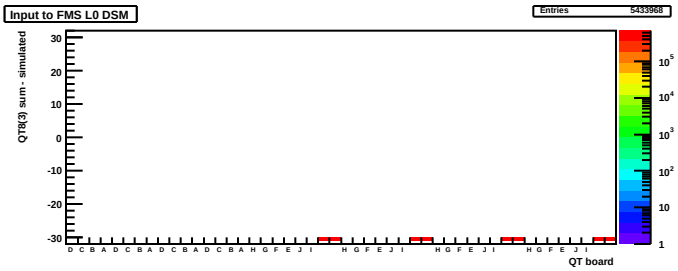

Input to FMS L0 DSM
Entries 3.260381e+07
QT8(1) sum
QT board

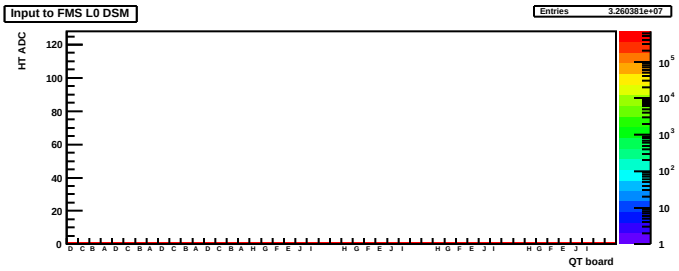

Input to FMS L0 DSM
Entries 5433968
QT8(1) sum - simulated
QT board

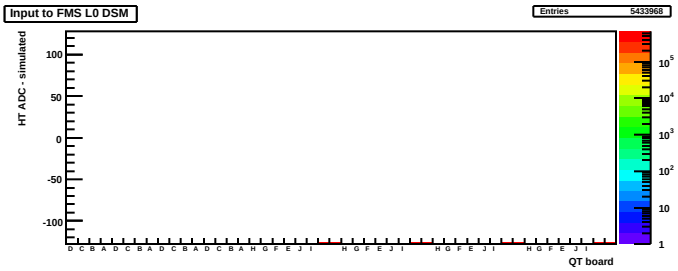

Input to FMS L0 DSM
Entries 3.260381e+07
QT8(2) sum
QT board

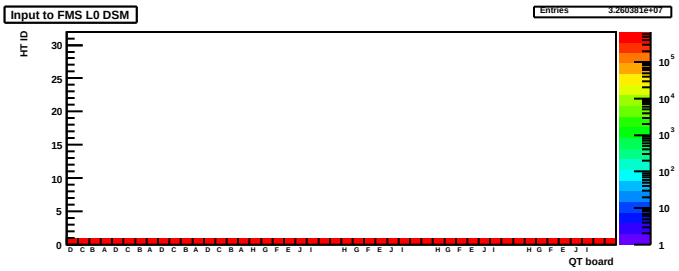

Input to FMS L0 DSM
Entries 5433968
QT8(2) sum - simulated
QT board

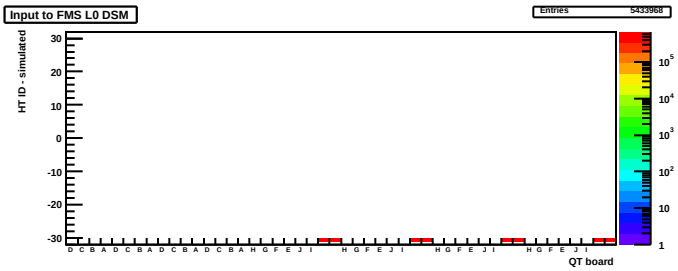

Input to FMS L0 DSM
Entries 3.260381e+07
QT8(3) sum
QT board

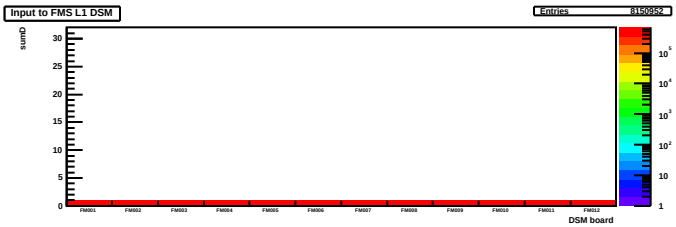

Input to FMS L0 DSM
Entries 5433968
QT8(3) sum - simulated
QT board

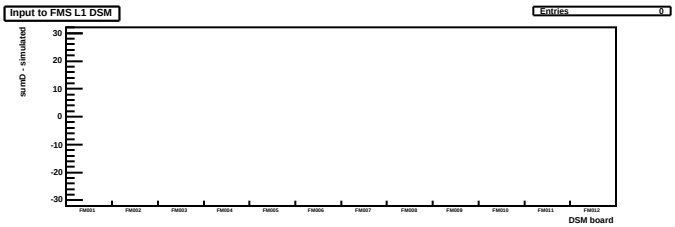

Input to FMS L0 DSM
Entries 3.260381e+07
HT ADC
QT board

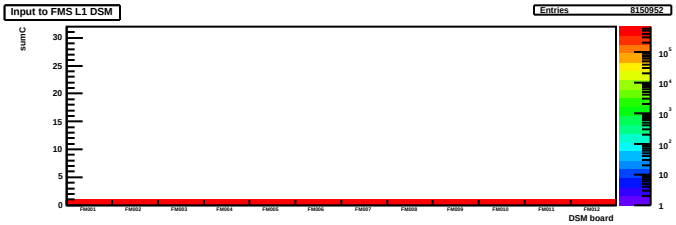

Input to FMS L0 DSM
Entries 5433968
HT ADC - simulated
QT board

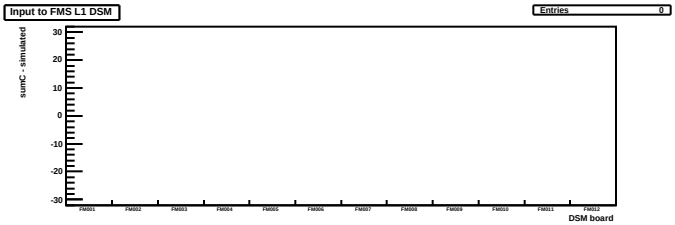

Input to FMS L0 DSM
Entries 3.260381e+07
HT ID
QT board

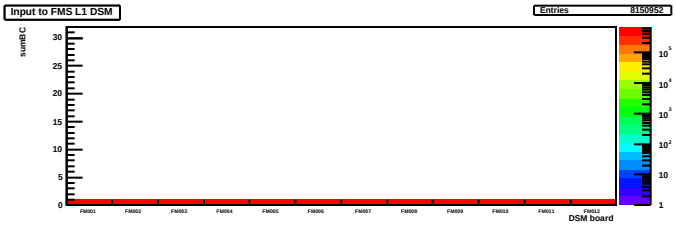

Input to FMS L0 DSM
Entries 5433968
HT ID - simulated
QT board

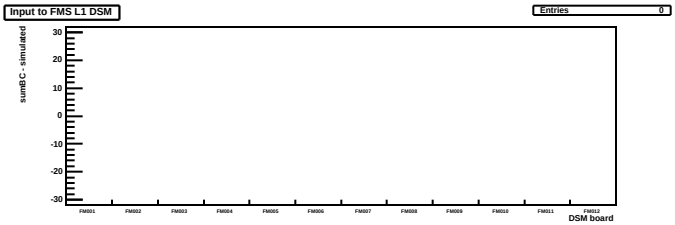

Input to FMS L1 DSM
Entries 8150952
sumD
DSM board

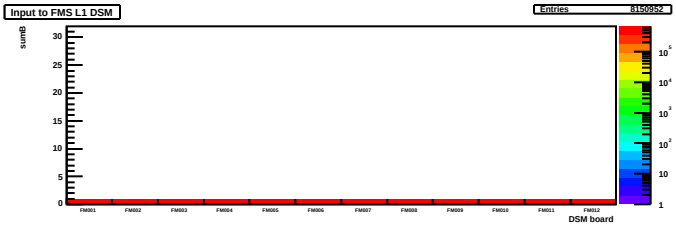

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

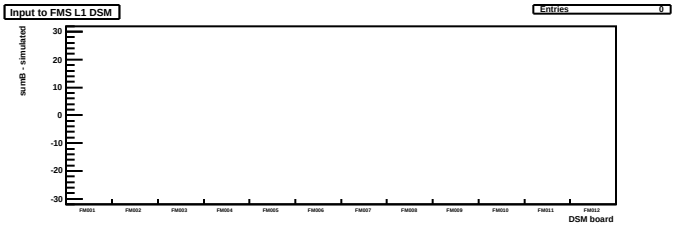

Input to FMS L1 DSM
Entries 8150952
sumC
DSM board

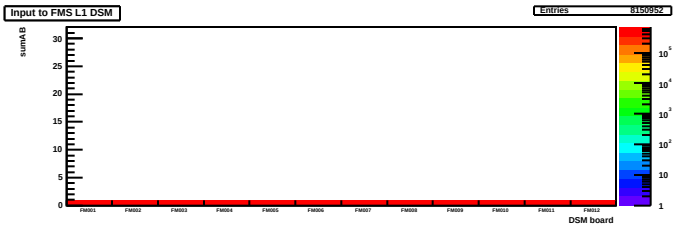

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

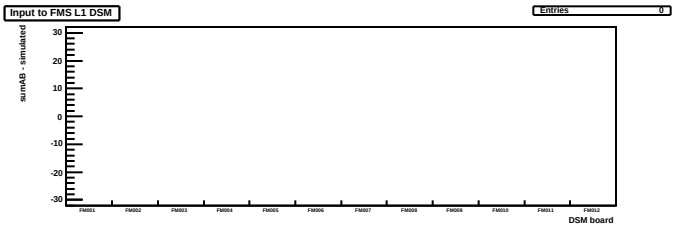

Input to FMS L1 DSM
Entries 8150952
sumBC
DSM board

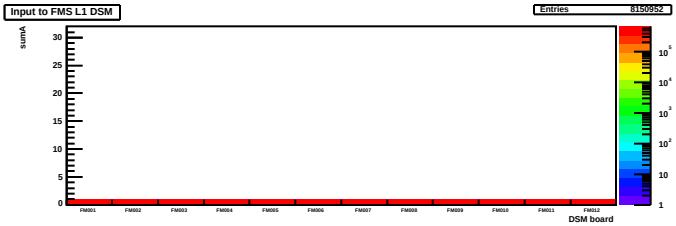

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

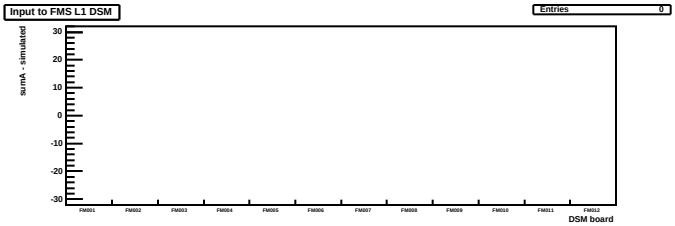

Input to FMS L1 DSM
Entries 8150952
sumB
DSM board

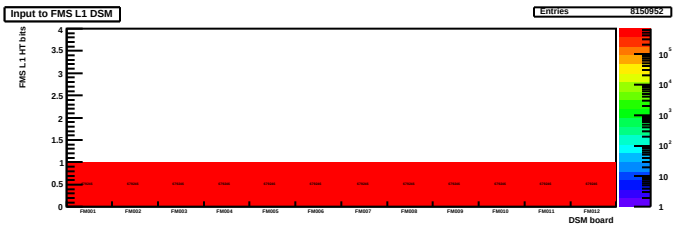

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

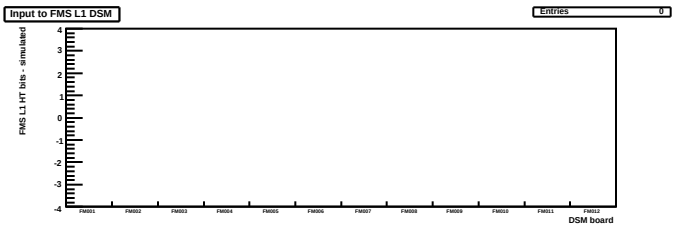

Input to FMS L1 DSM
Entries 8150952
sumAB
DSM board

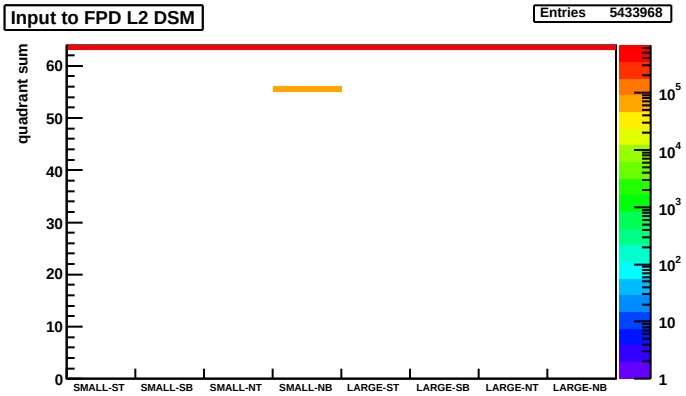

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

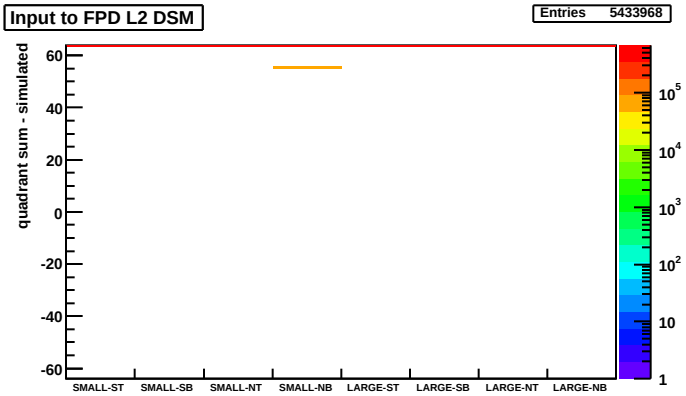

Input to FMS L1 DSM
Entries 8150952
sumA
DSM board

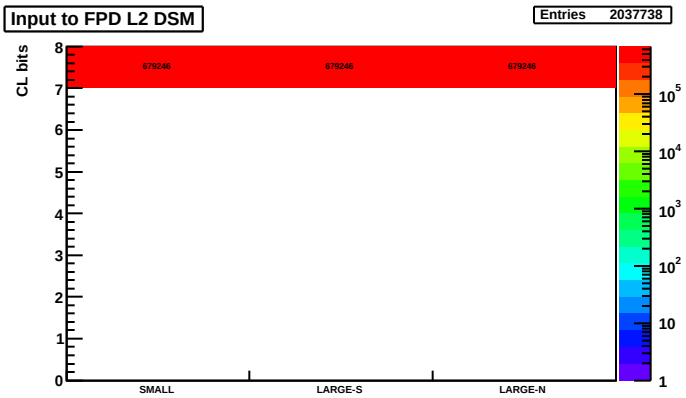

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

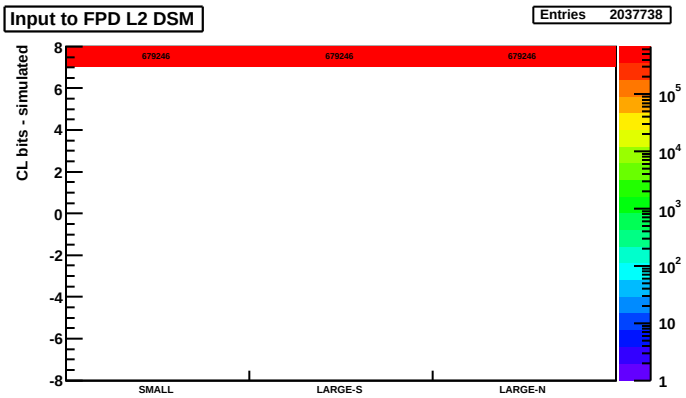

Input to FMS L1 DSM
Entries 8150952
FMS L1 HT bits
DSM board

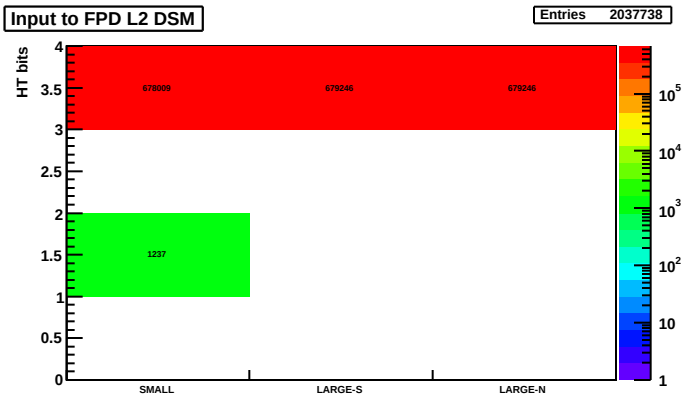

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

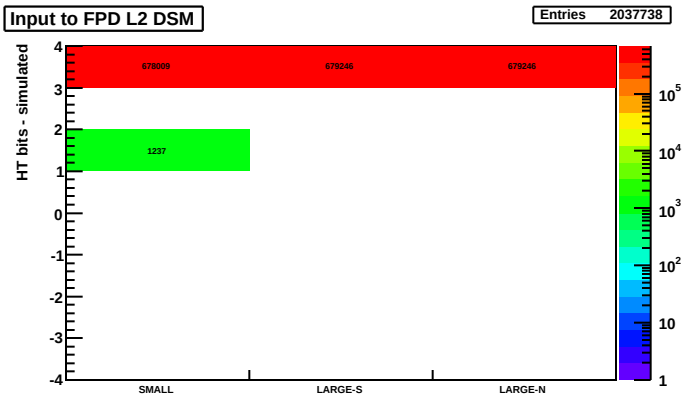

Input to FPD L2 DSM
Entries 5433968
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

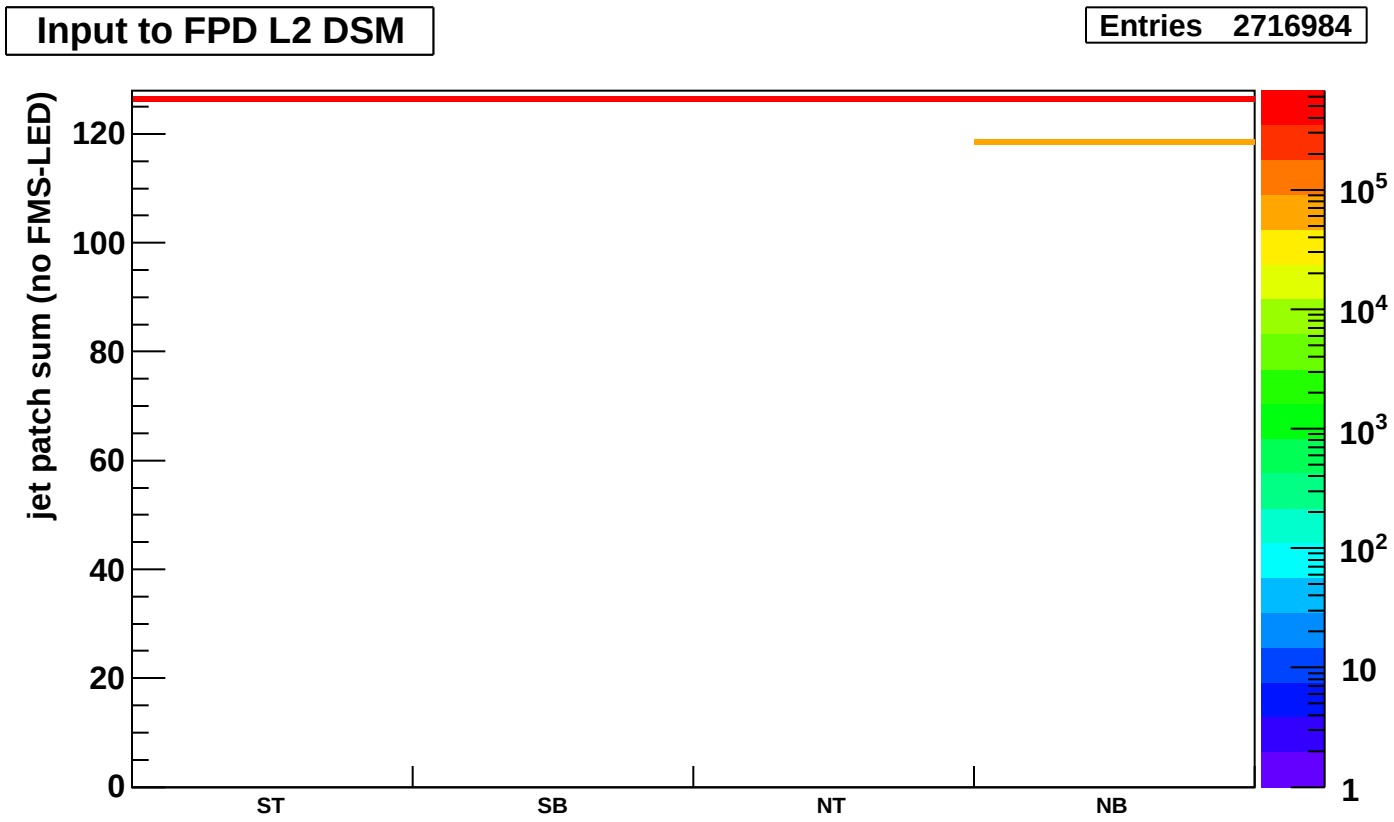

Input to FPD L2 DSM
Entries 5433968
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

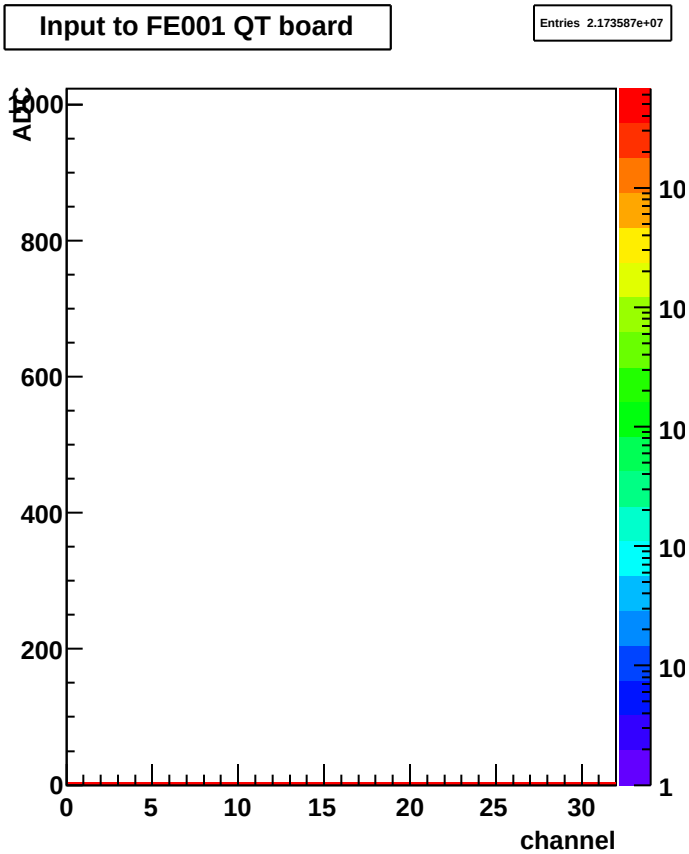

Input to FPD L2 DSM
Entries 2037738
CL bits
679246
679246
679246
SMALL
LARGE-S
LARGE-N

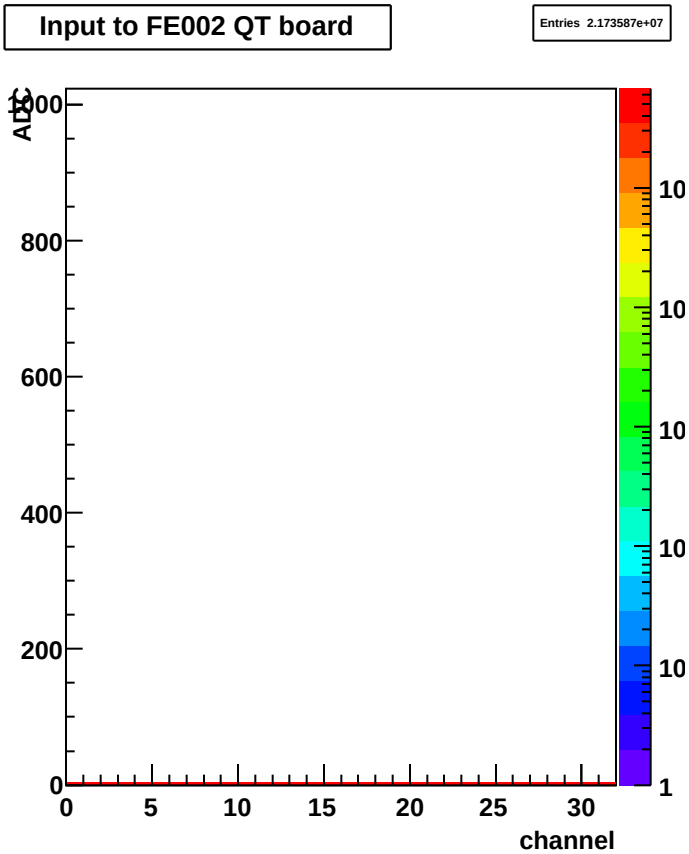

Input to FPD L2 DSM
Entries 2037738
CL bits - simulated
679246
679246
679246
SMALL
LARGE-S
LARGE-N

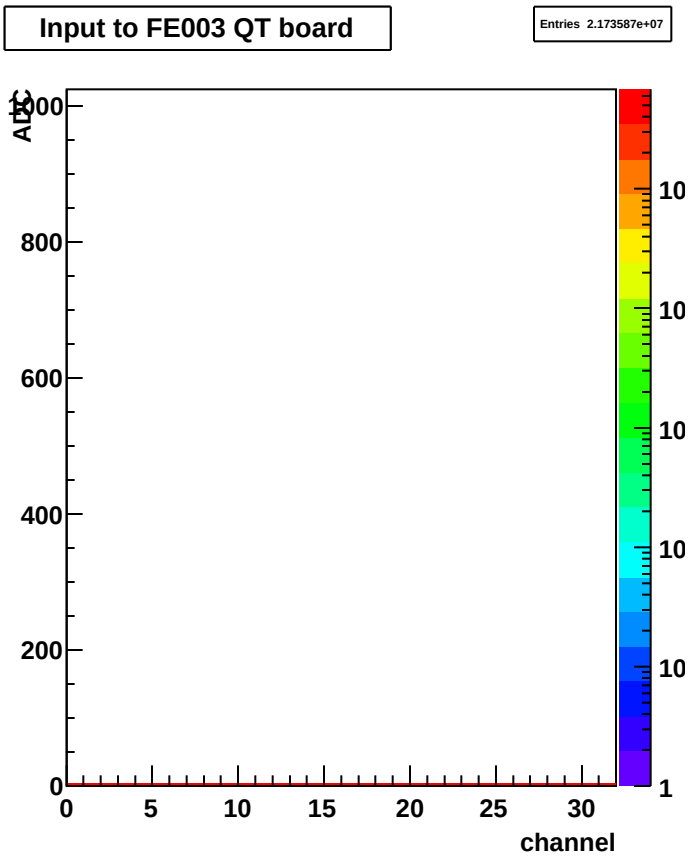

Input to FPD L2 DSM
Entries 2037738
HT bits
678009
679246
679246
1237
SMALL
LARGE-S
LARGE-N

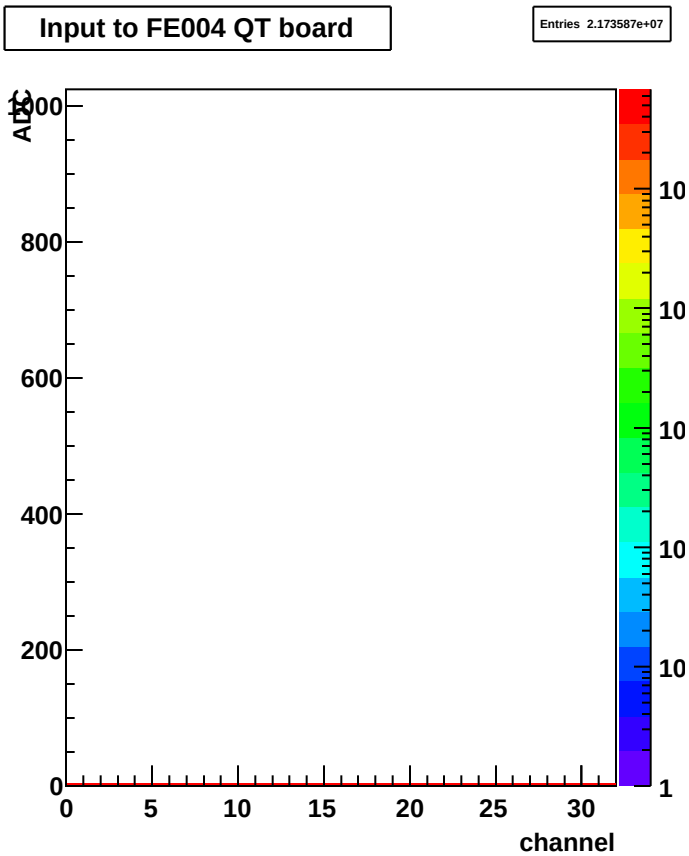

Input to FPD L2 DSM
Entries 2037738
HT bits - simulated
678009
679246
679246
1237
SMALL
LARGE-S
LARGE-N

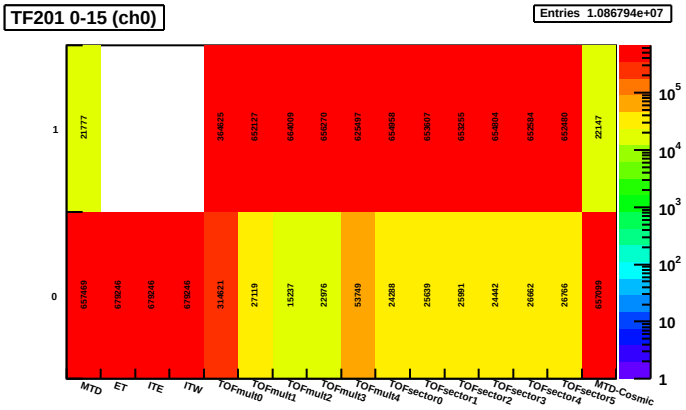

Input to FPD L2 DSM
Entries 2716984
jet patch sum (no FMS-LED)
120
100
80
60
40
20
0
ST
SB
NT
NB
10^5
10^4
10^3
10^2
10
1

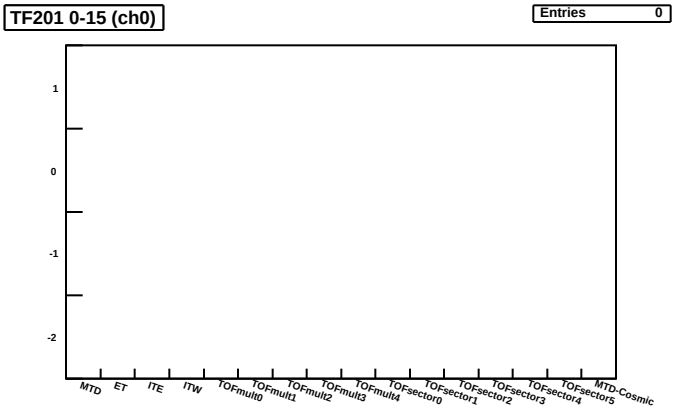

Input to FE001 QT board
Entries 2.173587e+07
ADC
channel

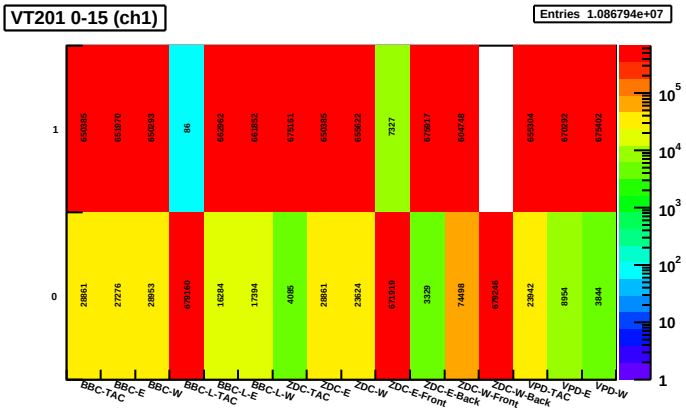

Input to FE002 QT board
Entries 2.173587e+07
ADC
channel

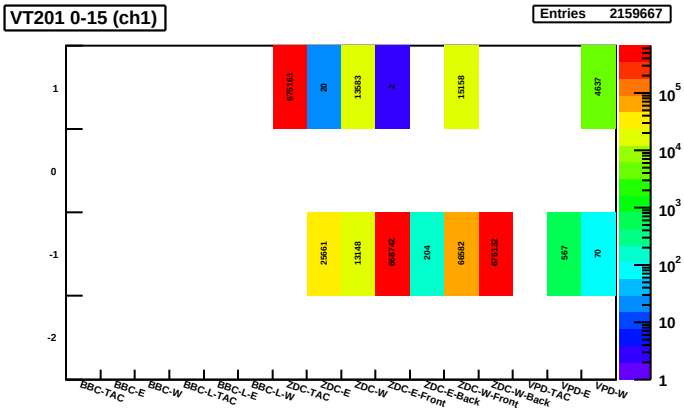

Input to FE003 QT board
Entries 2.173587e+07
ADC
channel

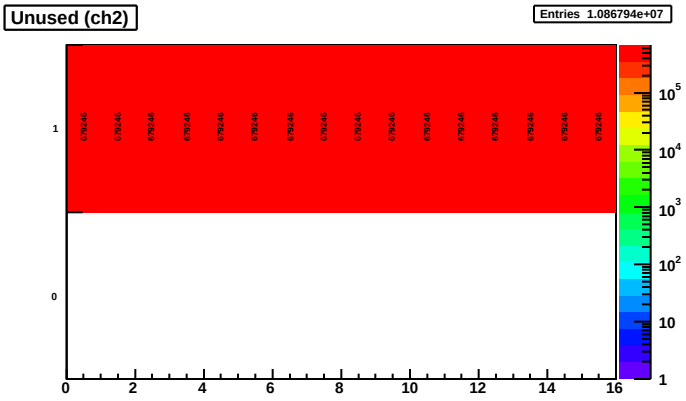

Input to FE004 QT board
Entries 2.173587e+07
ADC
channel

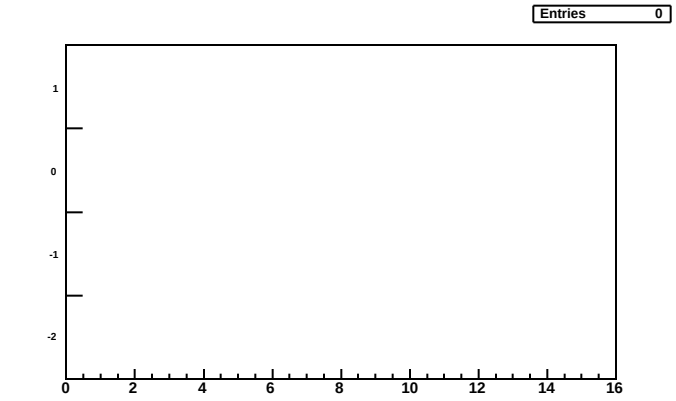

TF201 0-15 (ch0)
Entries 1.086794e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

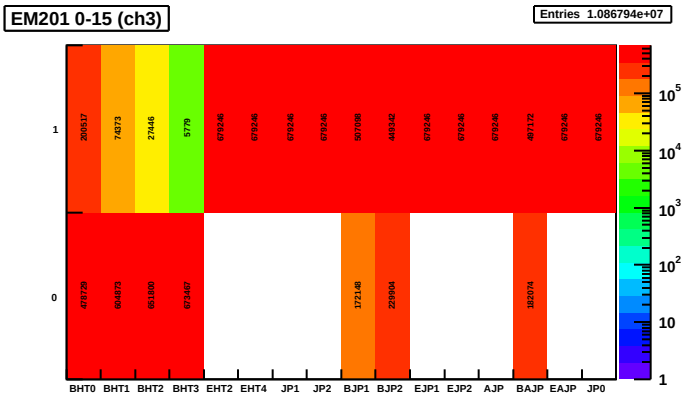

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

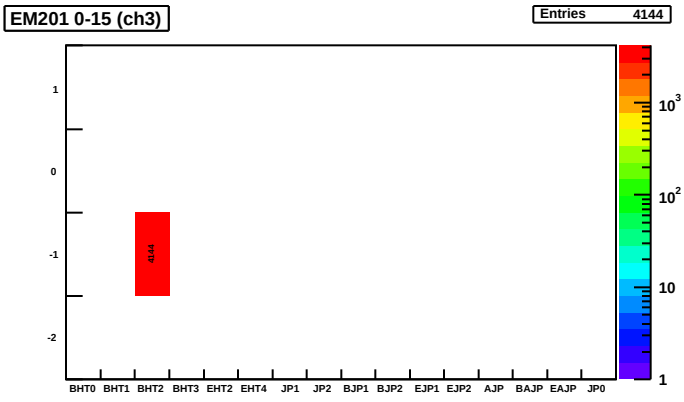

VT201 0-15 (ch1)
Entries 1.086794e+07
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 2159667
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

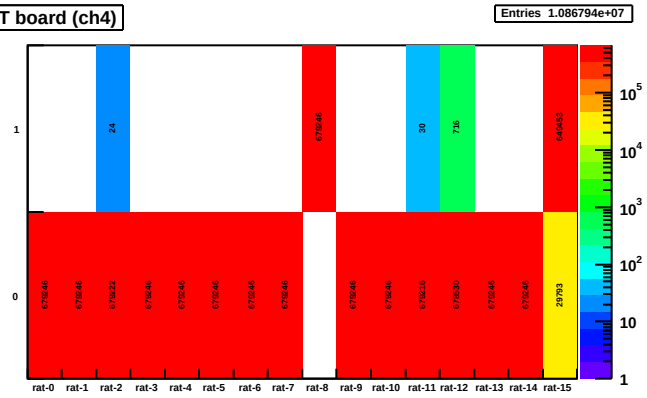

Unused (ch2)
Entries 1.086794e+07

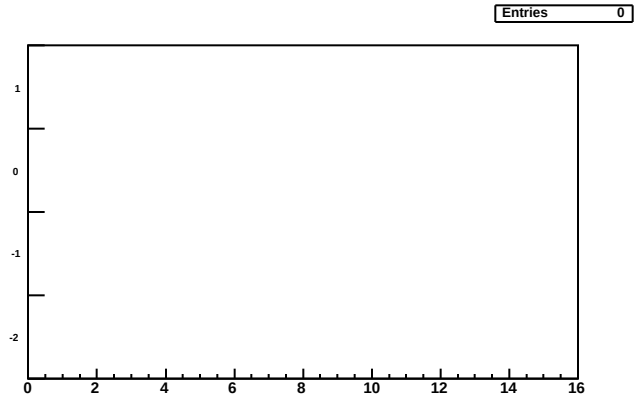

Entries 0


EM201 0-15 (ch3)
Entries 1.086794e+07
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

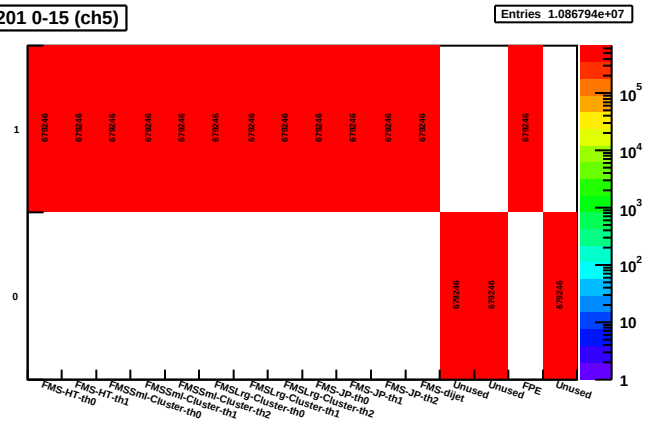

EM201 0-15 (ch3)
Entries 4144
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

RAT board (ch4)

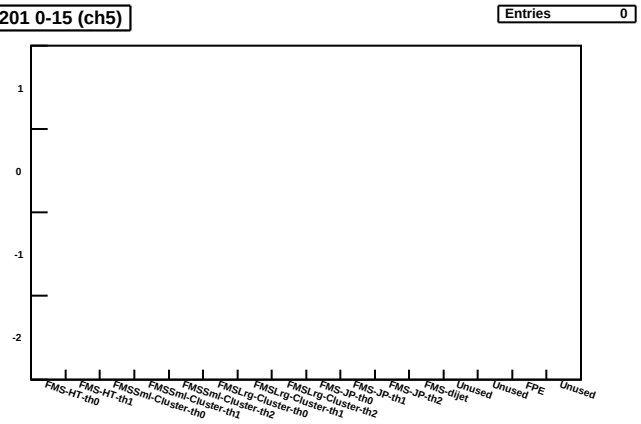




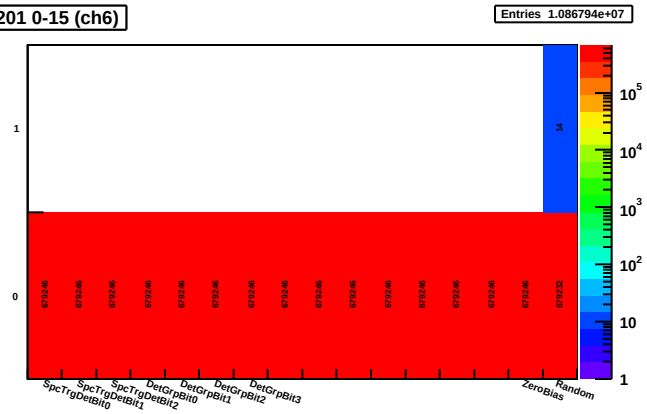
FP201 0-15 (ch5)

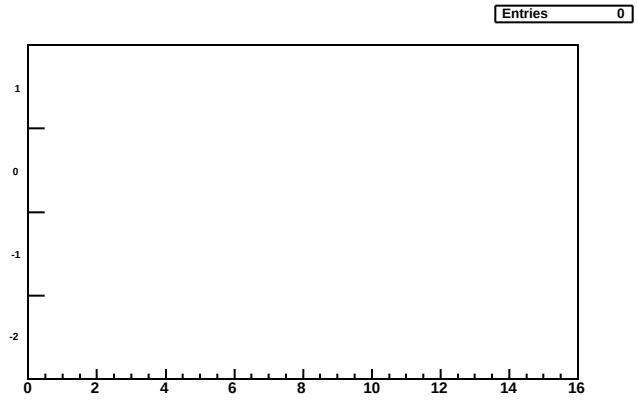
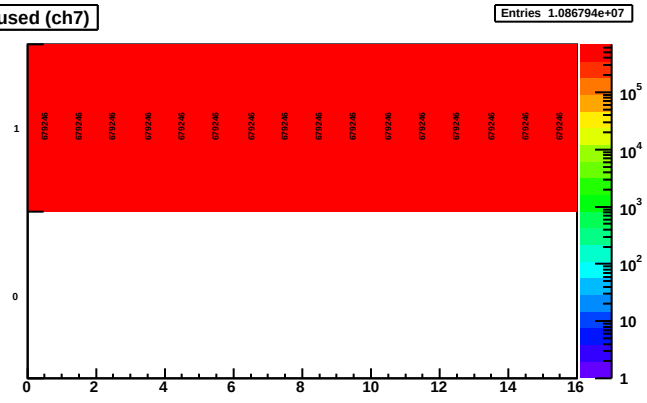
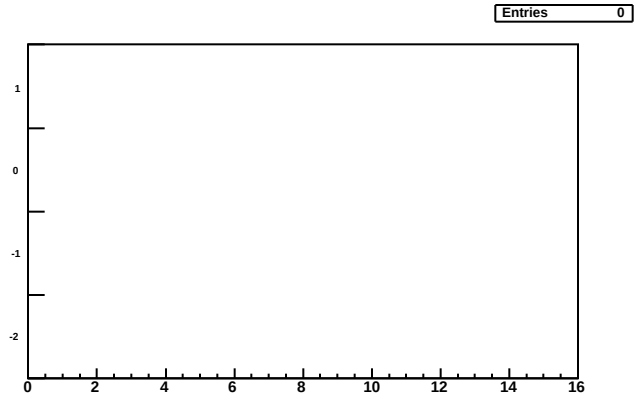


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)