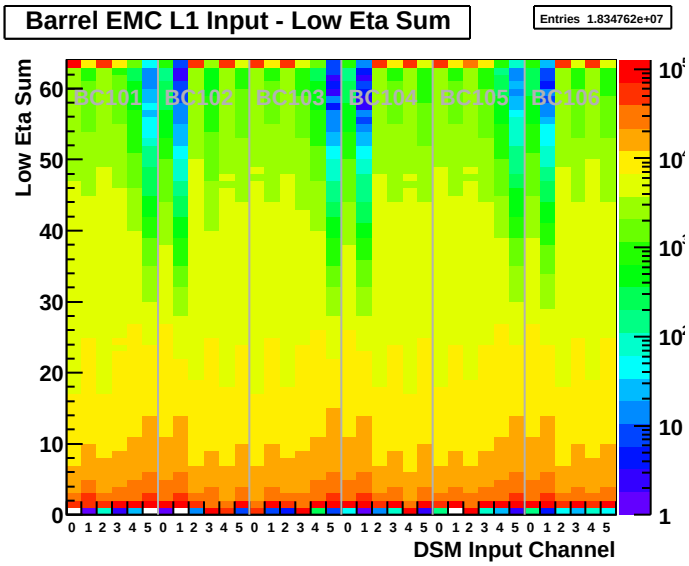
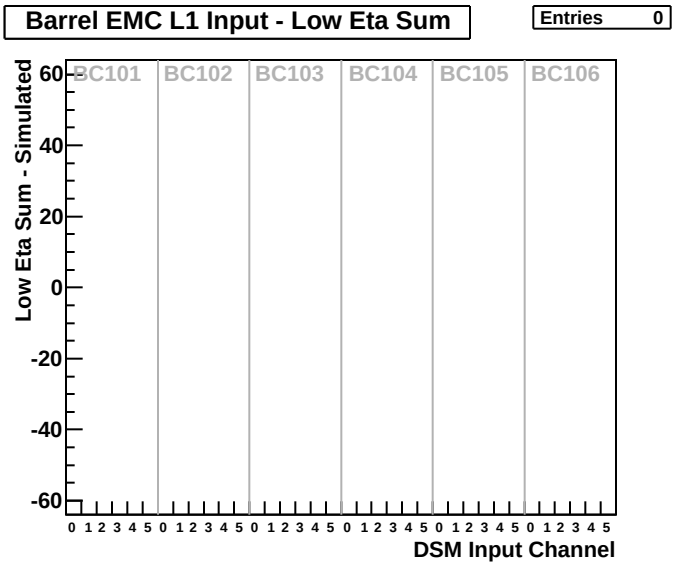
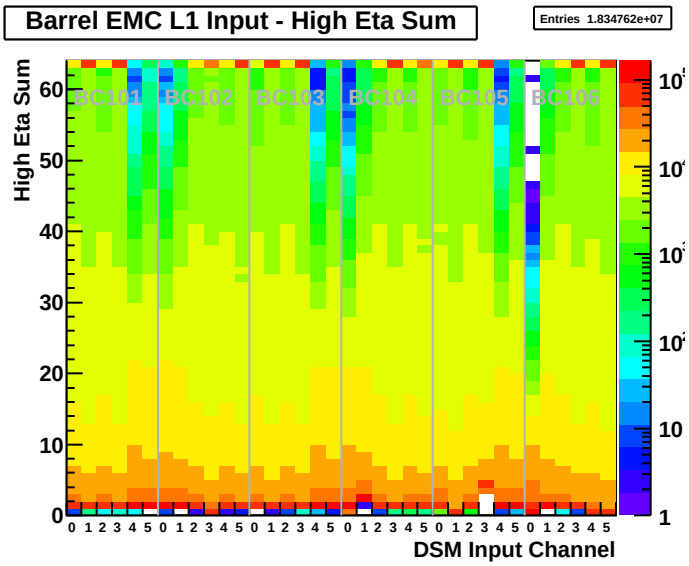
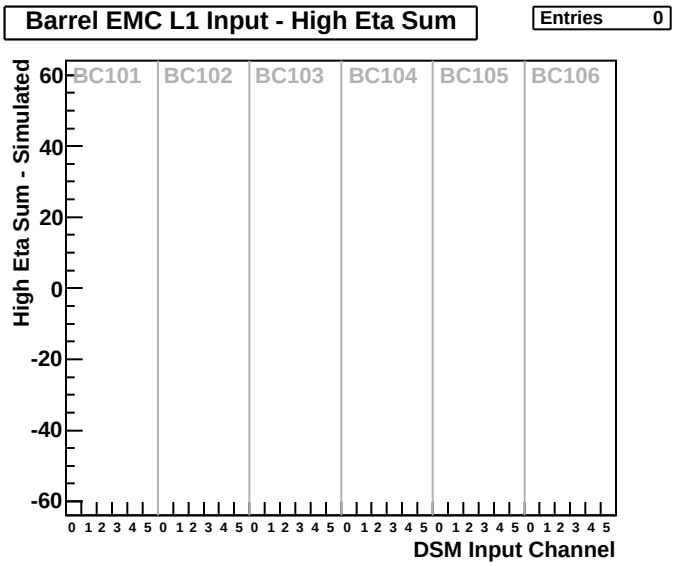
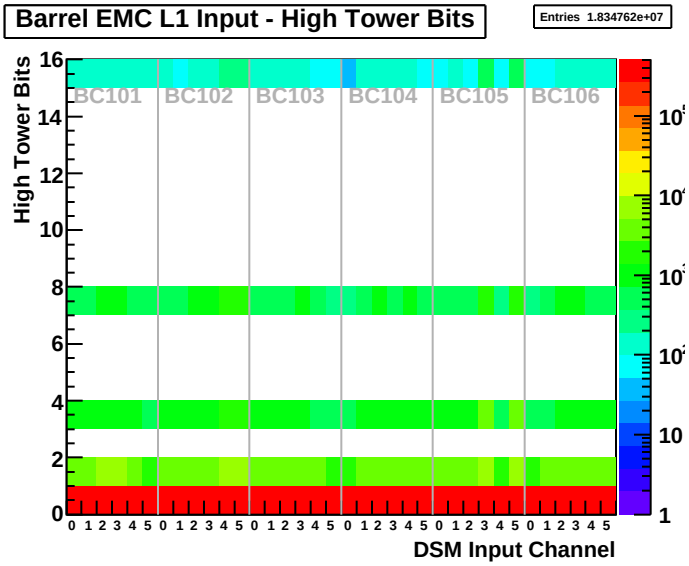
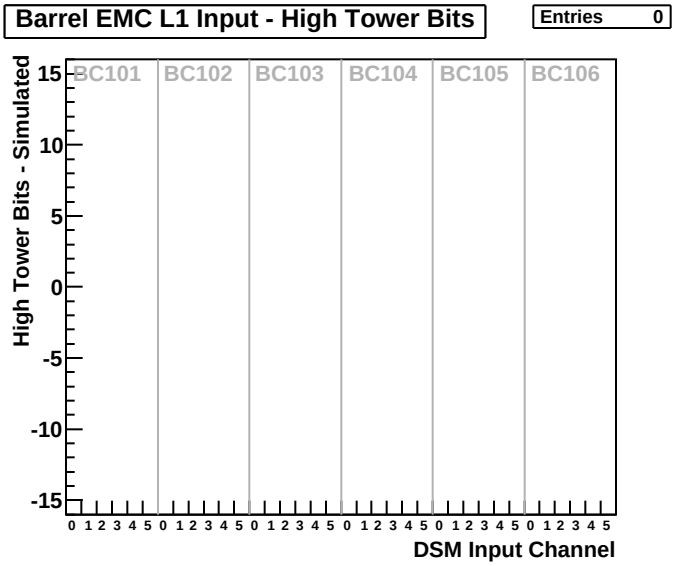

Barrel EMC L1 Input - Low Eta Sum
Entries 1.834762e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

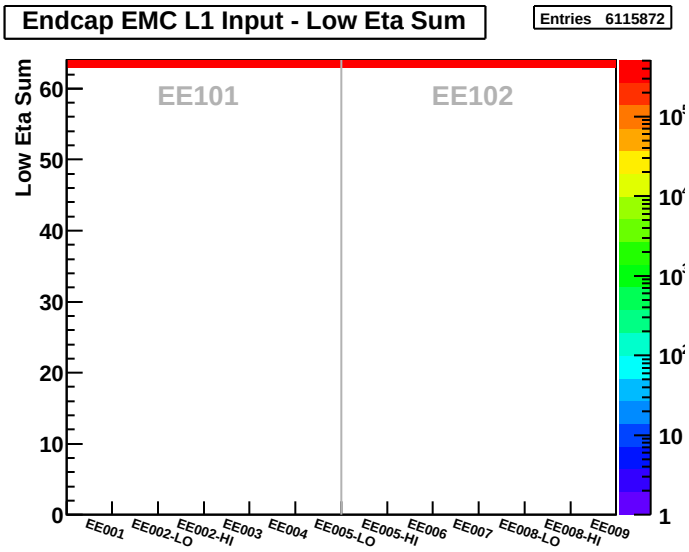

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

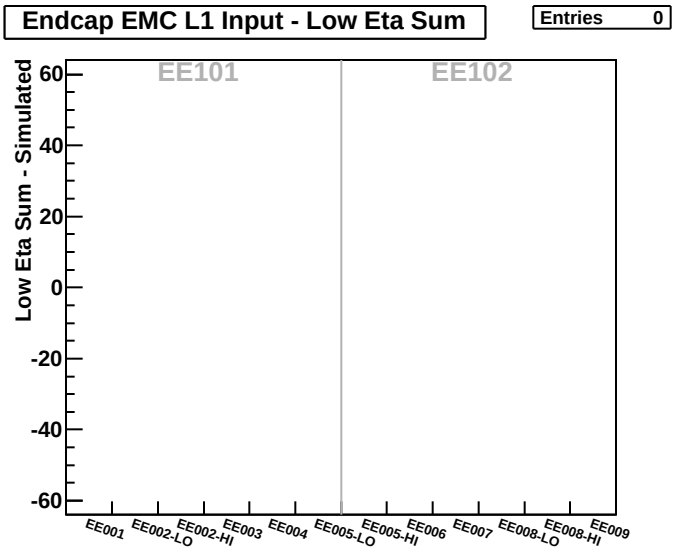

Barrel EMC L1 Input - High Eta Sum
Entries 1.834762e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

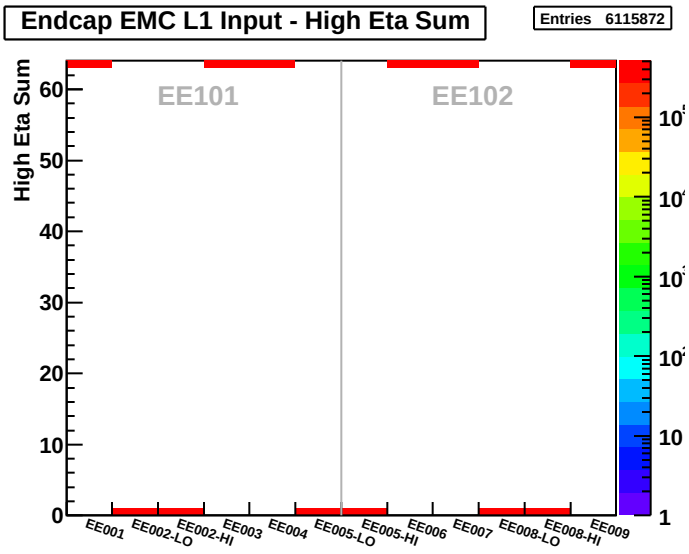

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

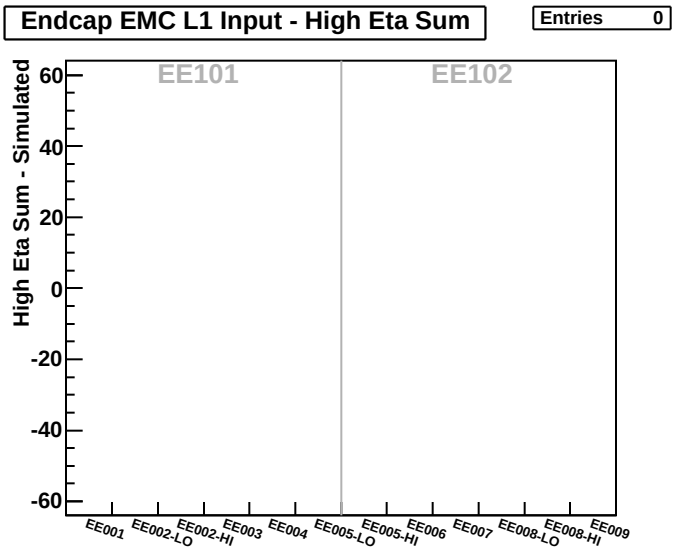

Barrel EMC L1 Input - High Tower Bits
Entries 1.834762e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

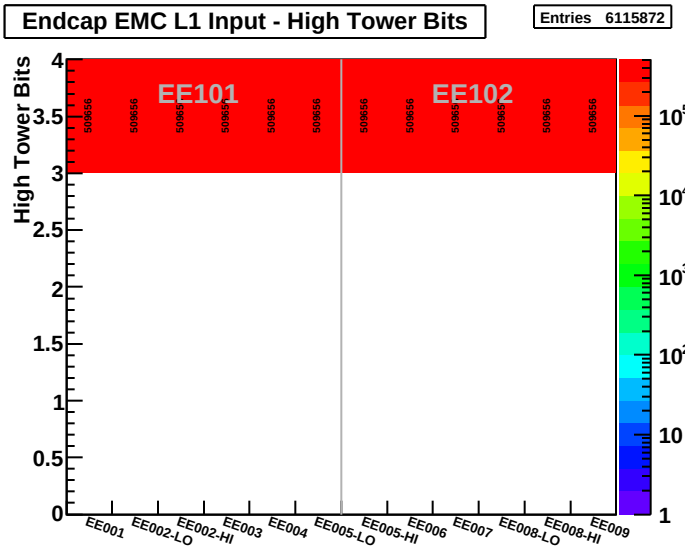

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

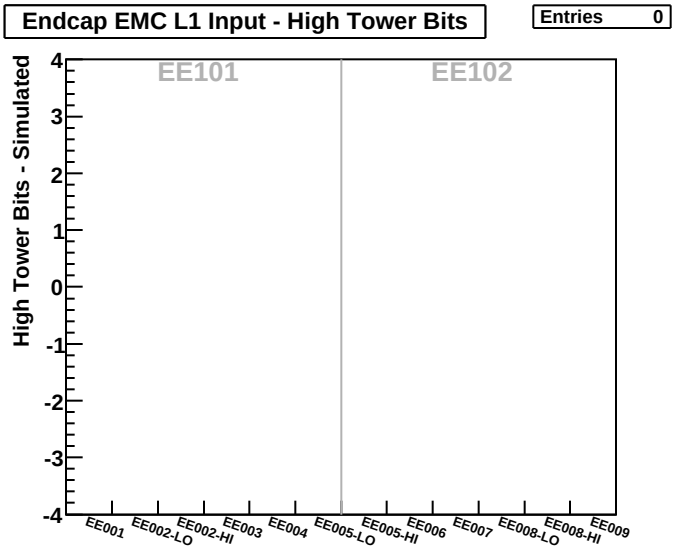

Endcap EMC L1 Input - Low Eta Sum
Entries 6115872
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

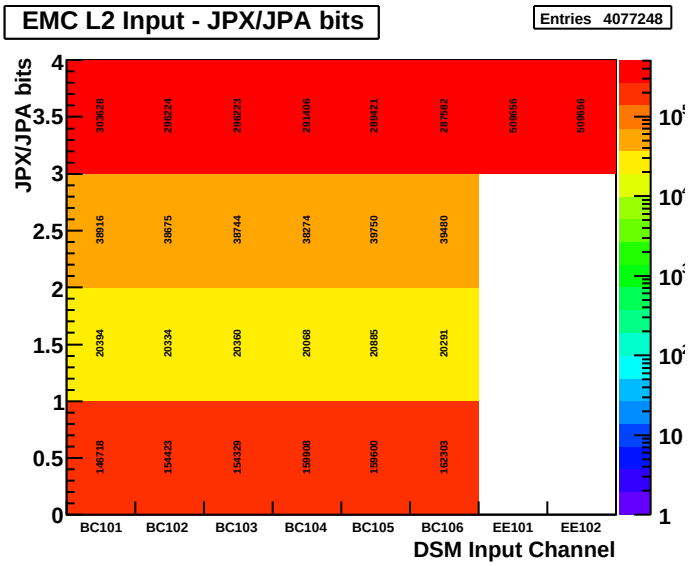

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

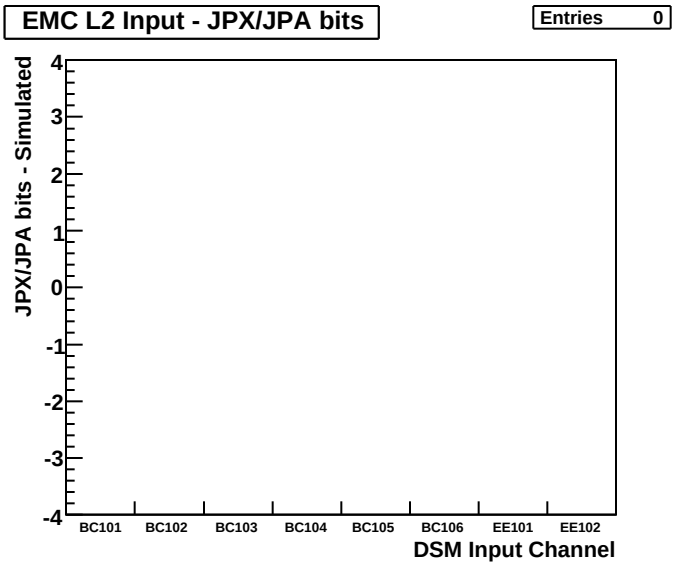

Endcap EMC L1 Input - High Eta Sum
Entries 6115872
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

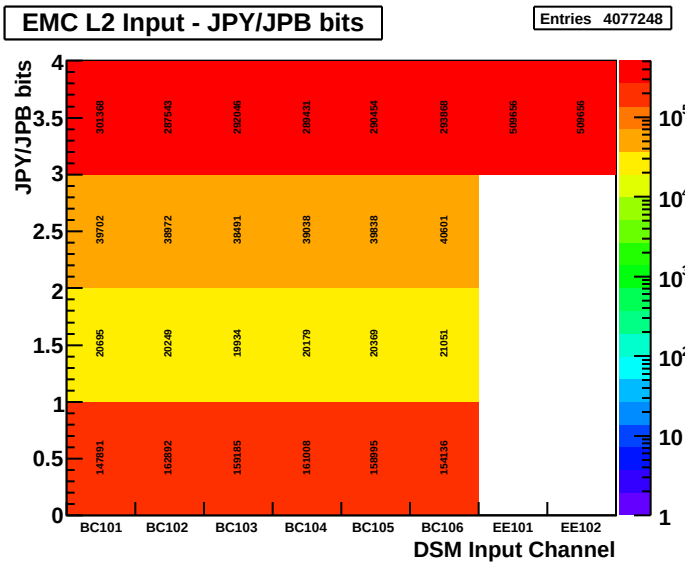

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

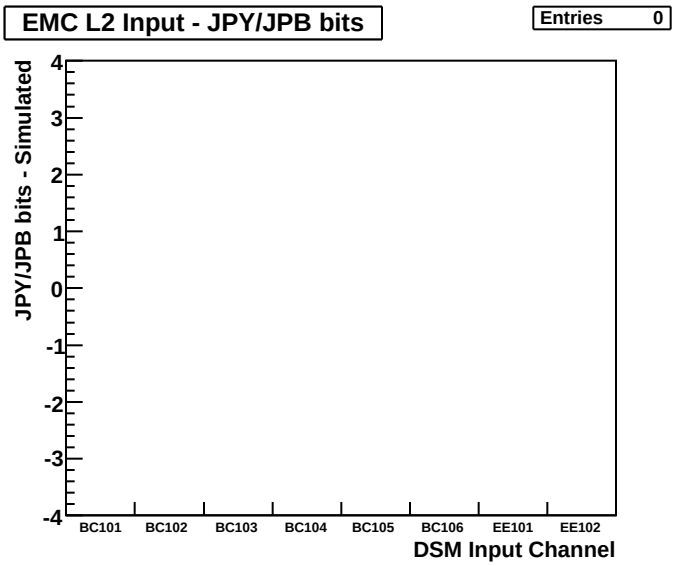

Endcap EMC L1 Input - High Tower Bits
Entries 6115872
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

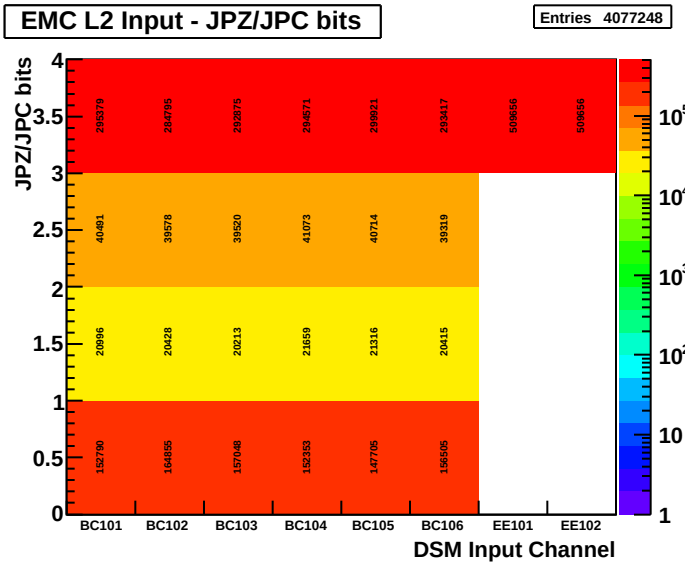

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

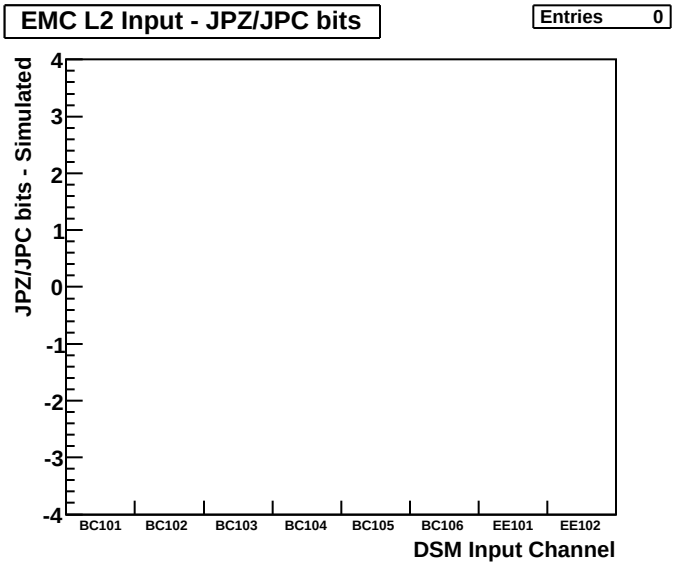

EMC L2 Input - JPX/JPA bits
Entries 4077248
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
303628 296224 296223 291406 289421 287582 509656 509656
38916 38675 38744 38274 39750 39480
20394 20334 20360 20068 20885 20291
146718 154423 154329 159908 159600 162303

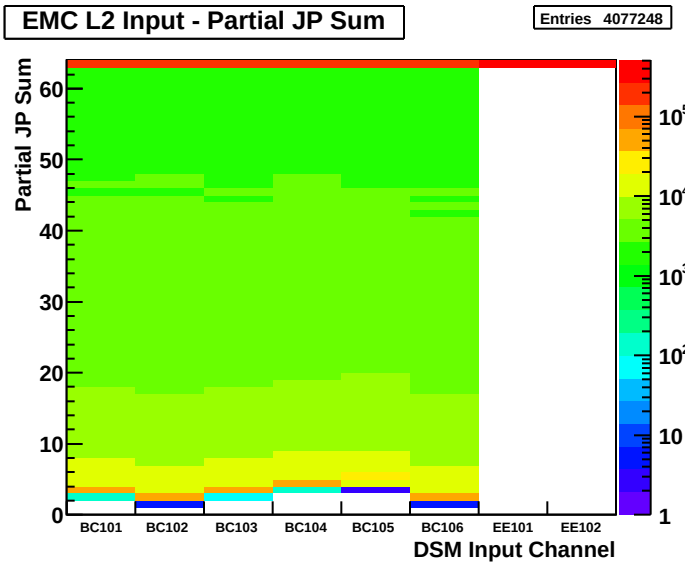

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

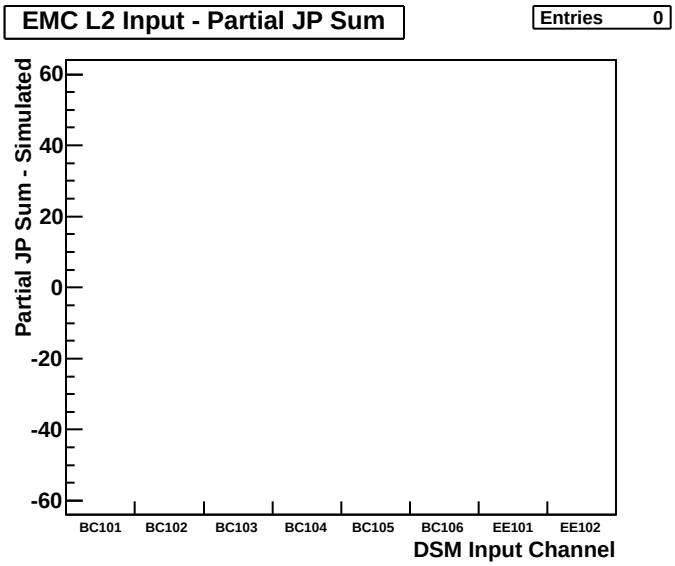

EMC L2 Input - JPY/JPB bits
Entries 4077248
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
301368 287543 292046 289431 290454 293868 509656 509656
39702 38972 38491 39038 39838 40601
20695 20249 19934 20179 20369 21051
147691 162892 159185 161008 158995 154136

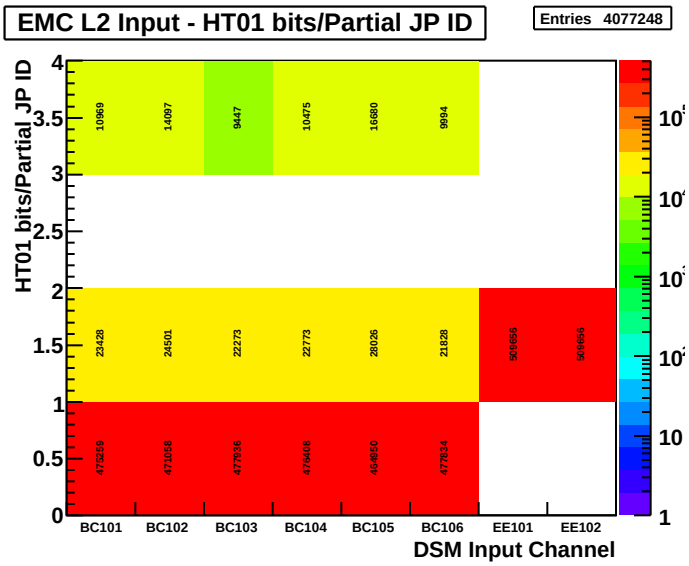

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

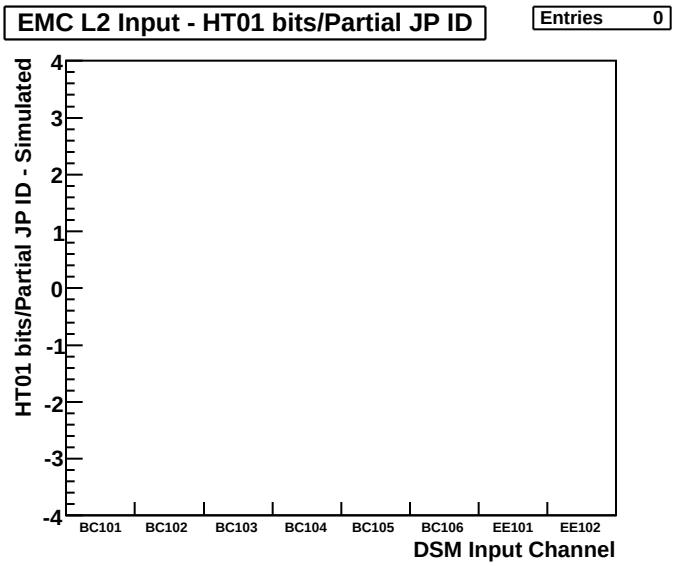

EMC L2 Input - JPZ/JPC bits
Entries 4077248
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
295379 284795 292375 296571 299921 293417 509656 509656
40491 39578 39520 41073 40714 39319
20996 20428 20213 21659 21316 20415
152790 164855 157048 152353 147705 156506

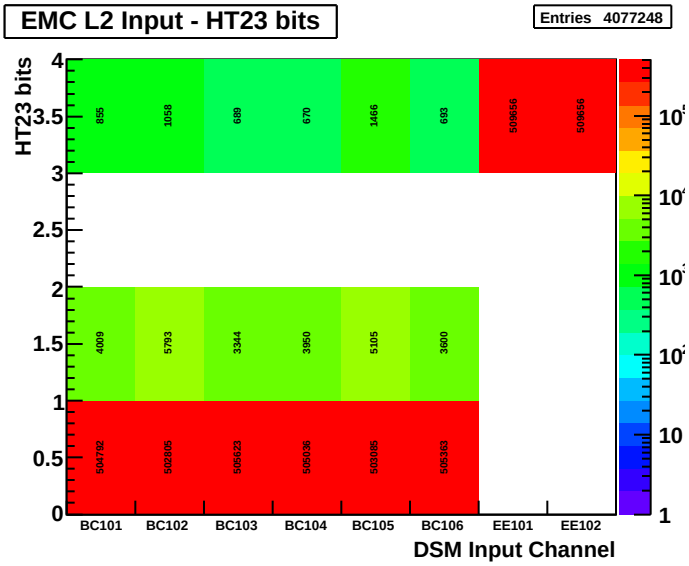

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

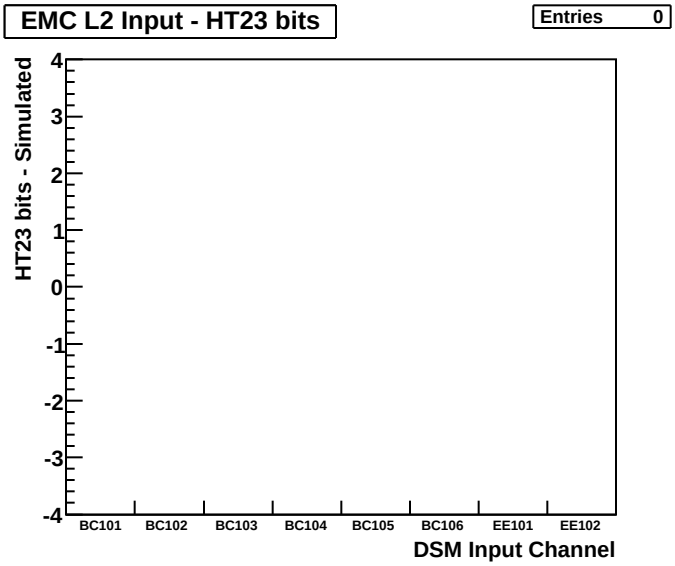

EMC L2 Input - Partial JP Sum
Entries 4077248
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

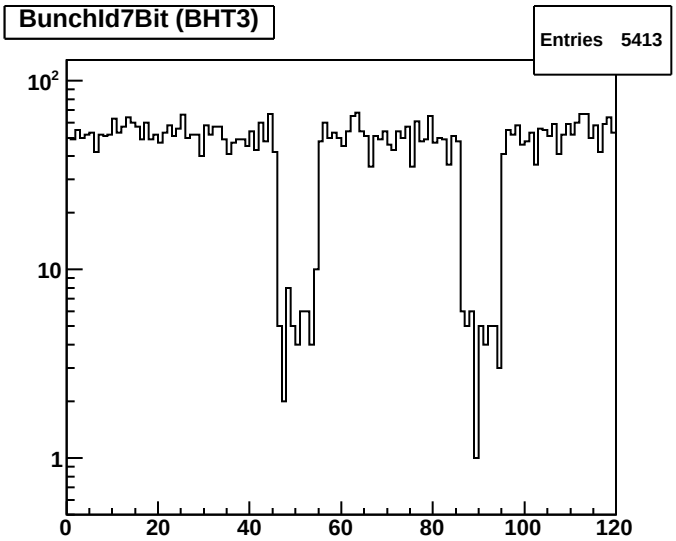

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

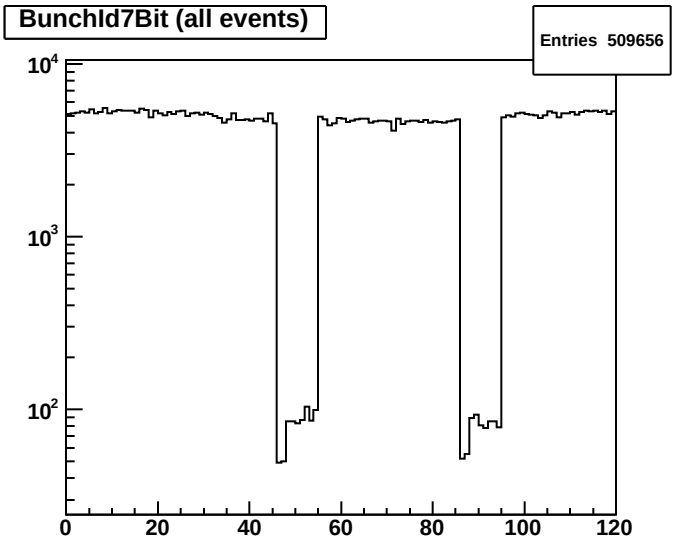

EMC L2 Input - HT01 bits/Partial JP ID
Entries 4077248
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

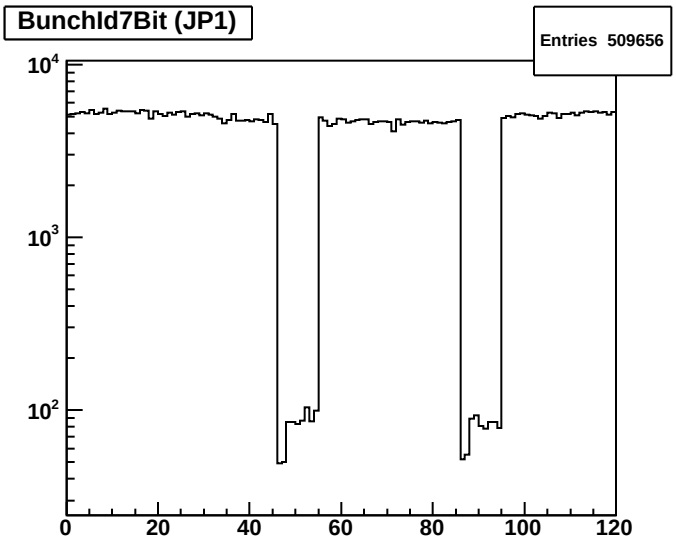

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

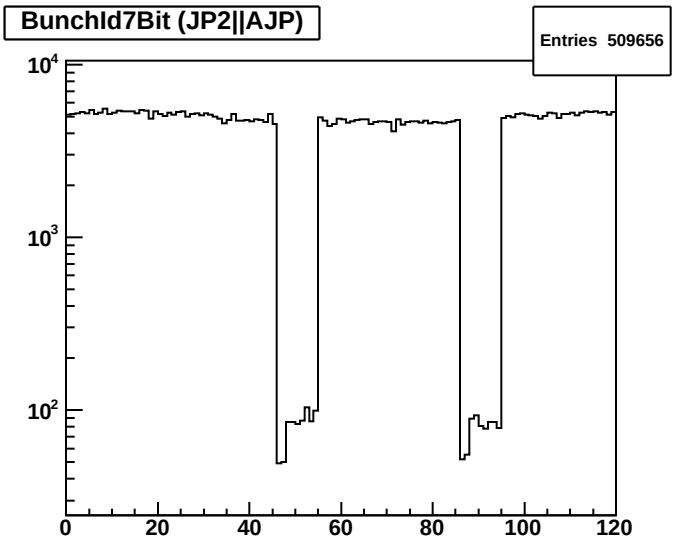

EMC L2 Input - HT23 bits
Entries 4077248
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

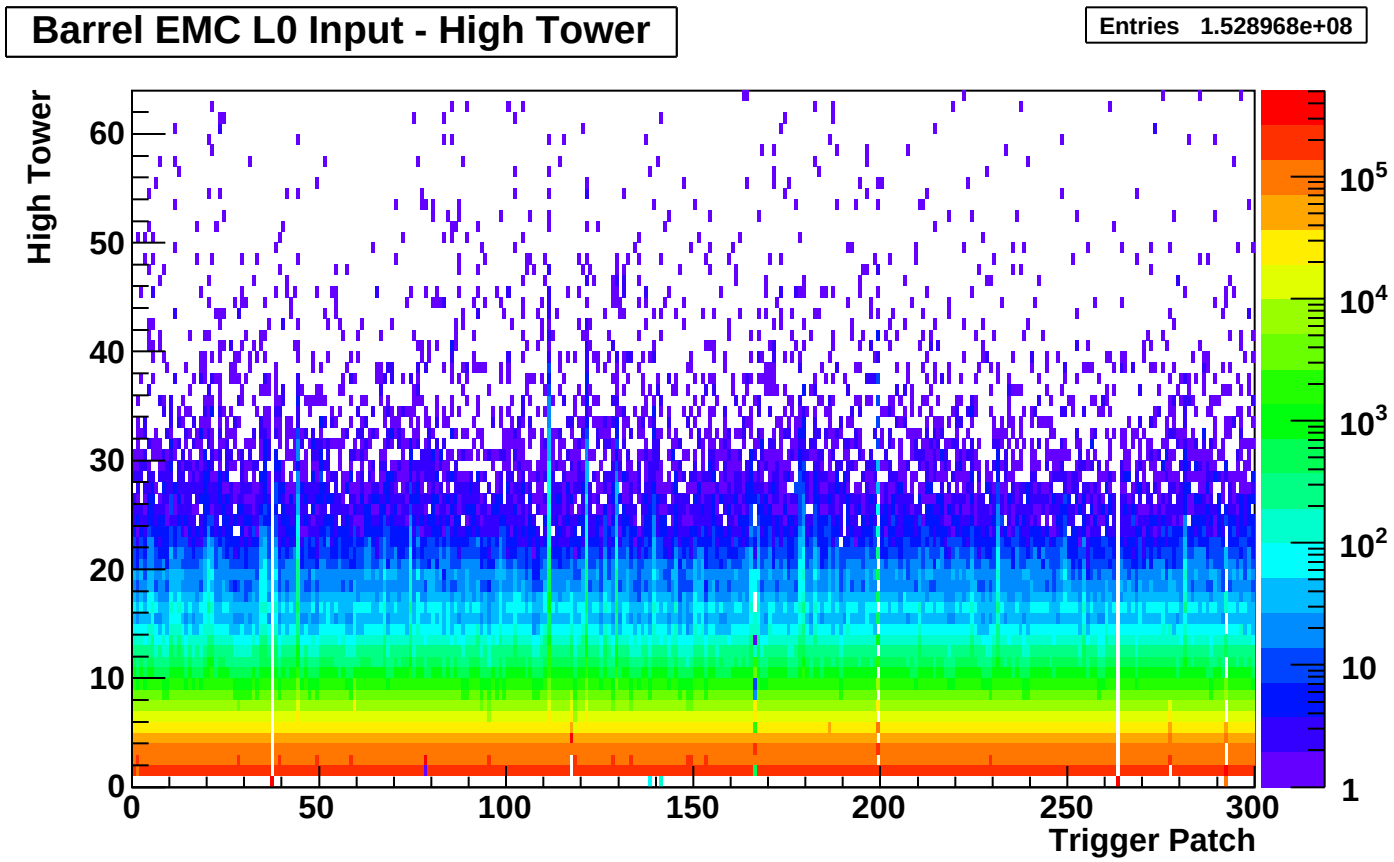

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

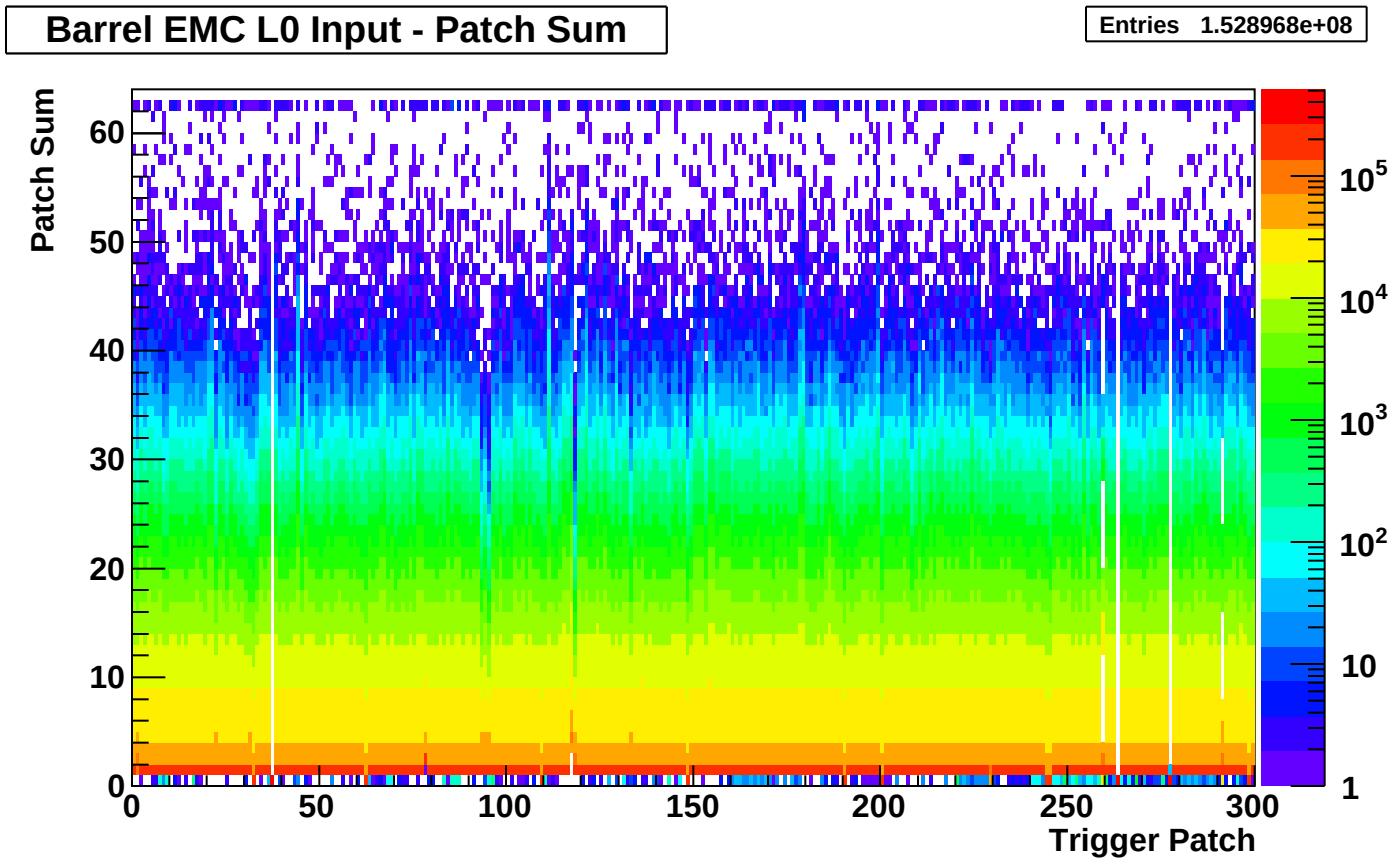

BunchId7Bit (BHT3)
Entries 5413
10^2
10
1
0
20
40
60
80
100
120

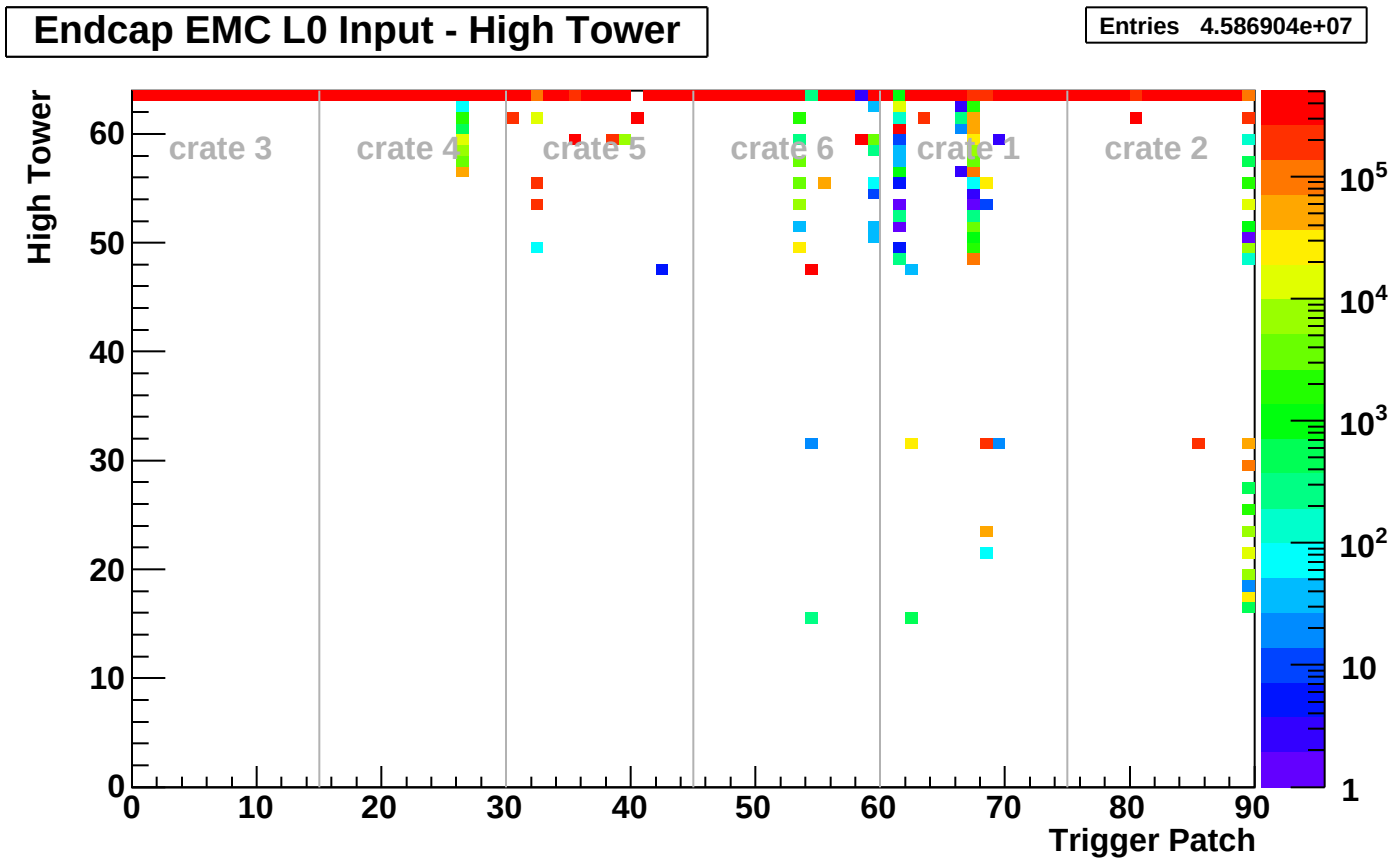

BunchId7Bit (all events)
Entries 509656
10^4
10^3
10^2
0
20
40
60
80
100
120

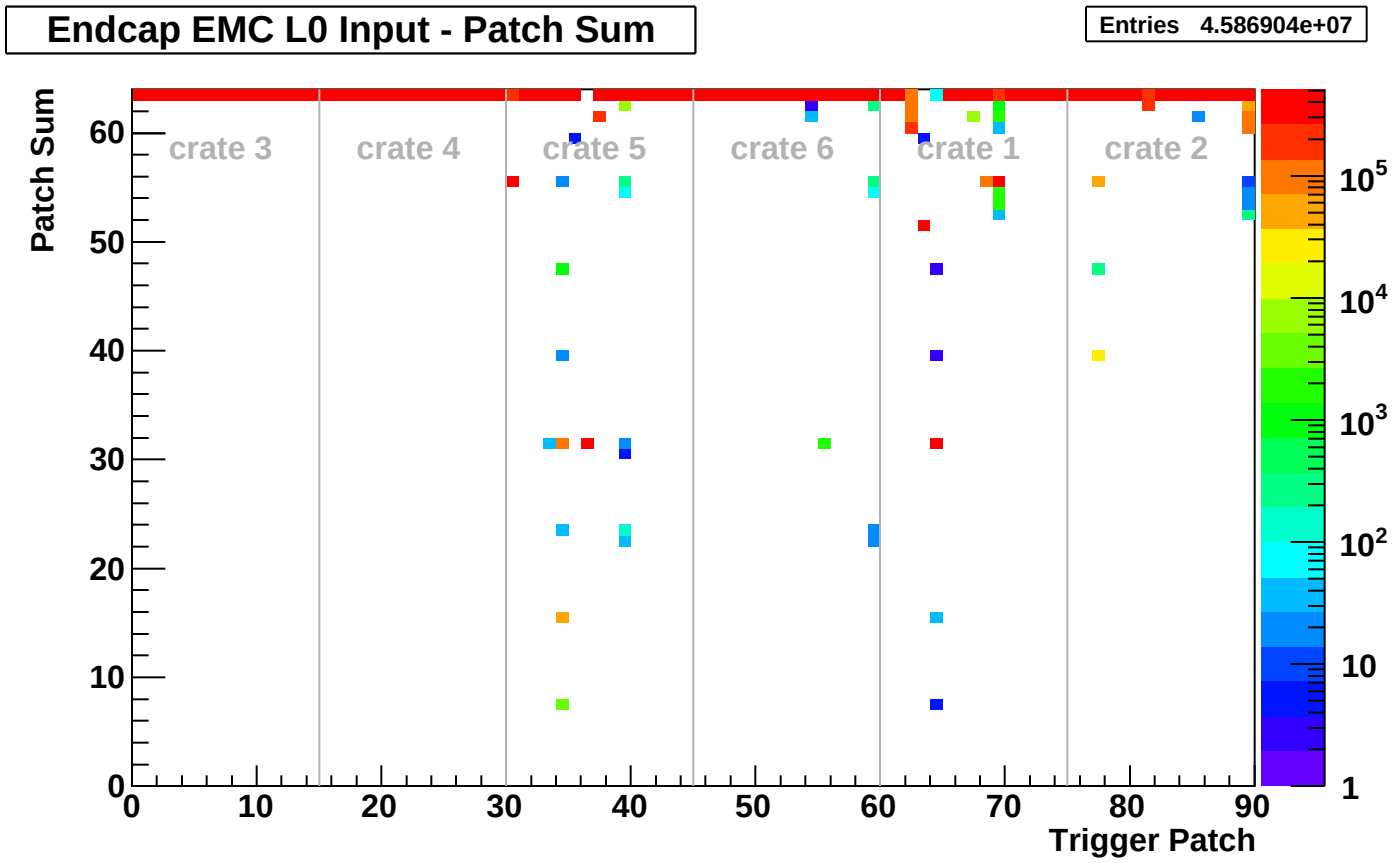

BunchId7Bit (JP1)
Entries 509656
10^4
10^3
10^2
0
20
40
60
80
100
120

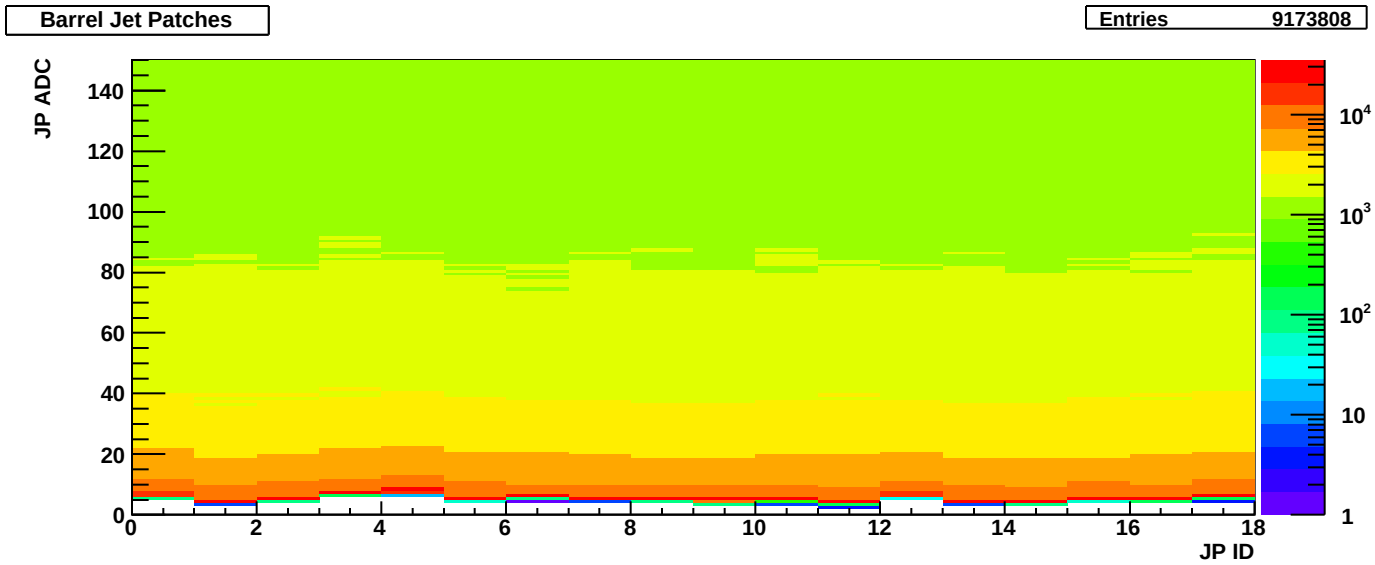

BunchId7Bit (JP2||AJP)
Entries 509656
10^4
10^3
10^2
0
20
40
60
80
100
120

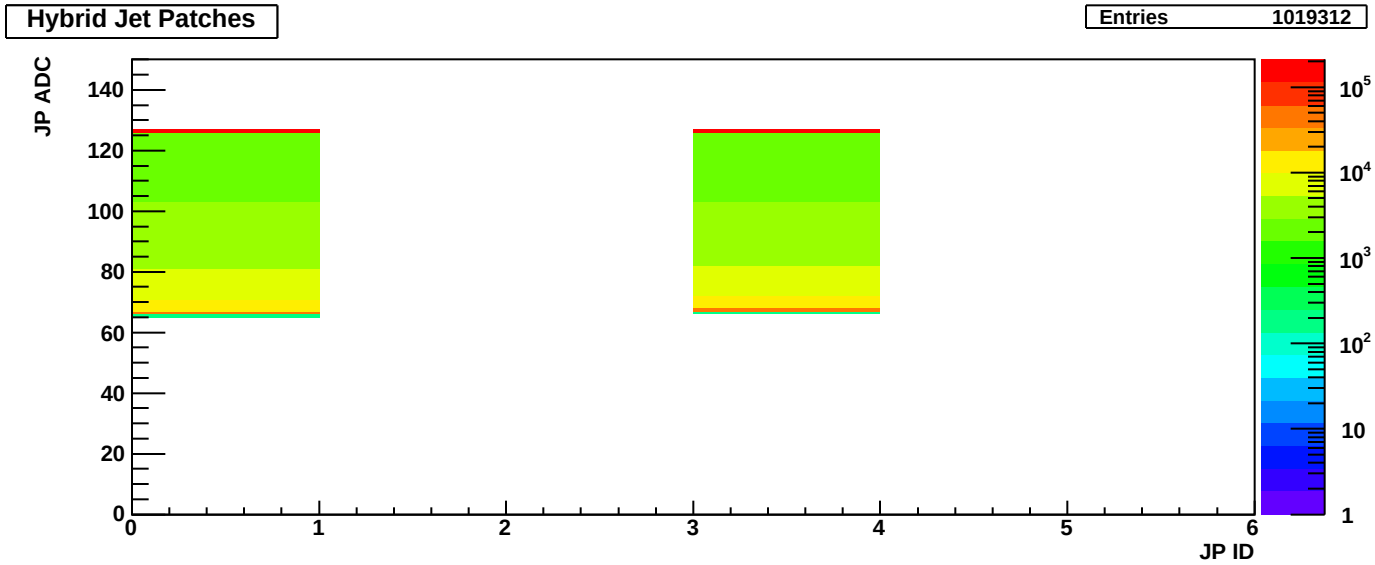

Barrel EMC L0 Input - High Tower
Entries 1.528968e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

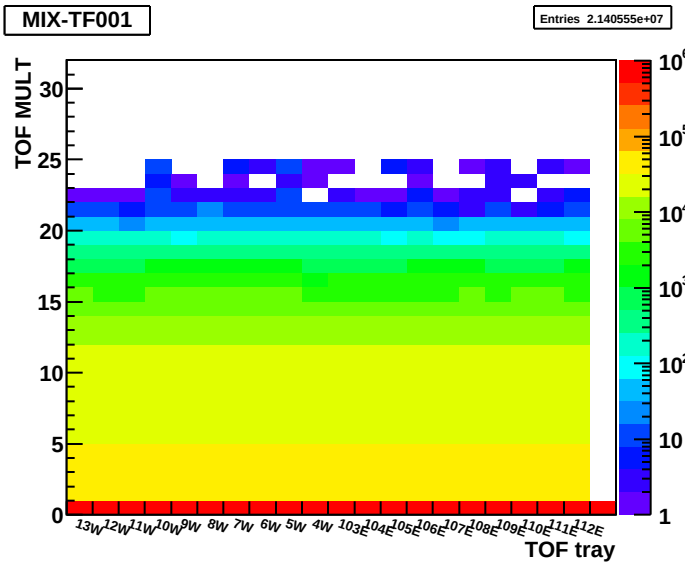

Barrel EMC L0 Input - Patch Sum
Entries 1.528968e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

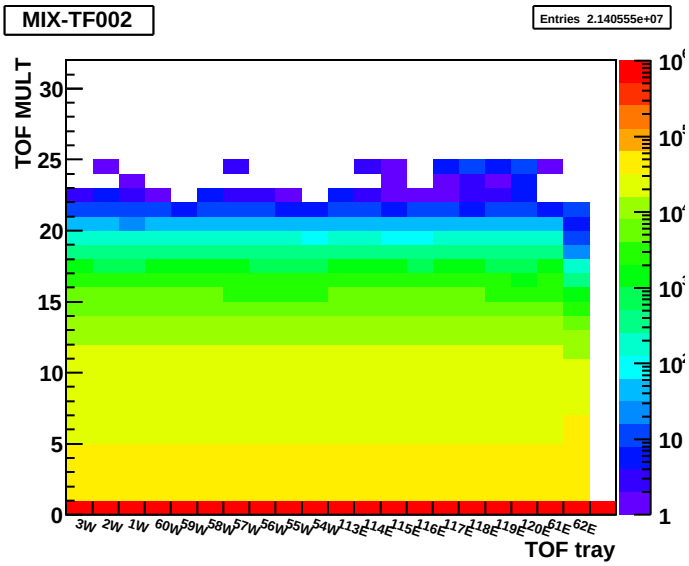

Endcap EMC L0 Input - High Tower
Entries 4.586904e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

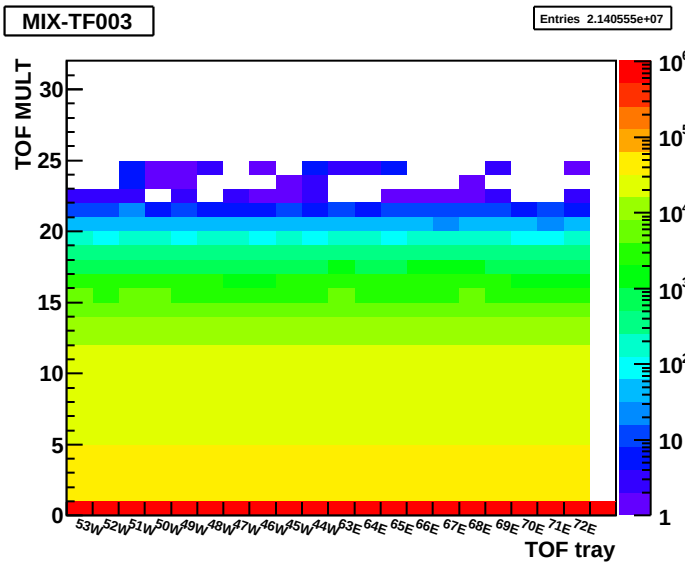

Endcap EMC L0 Input - Patch Sum
Entries 4.586904e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

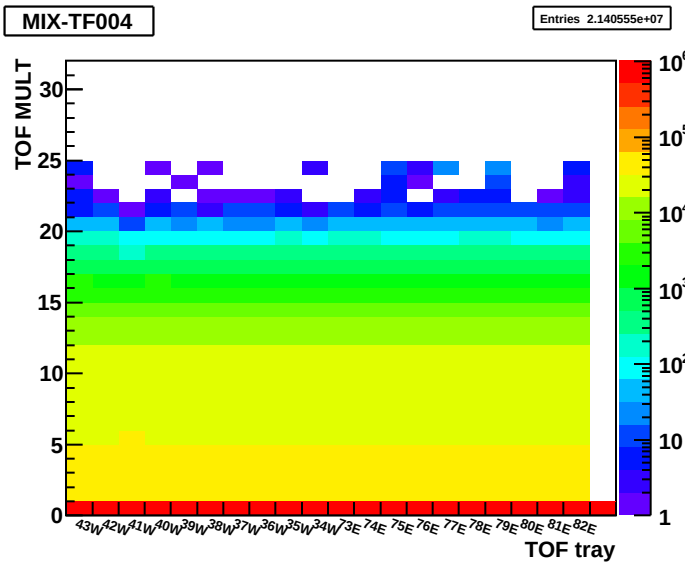

Barrel Jet Patches
Entries 9173808
JP ADC
JP ID

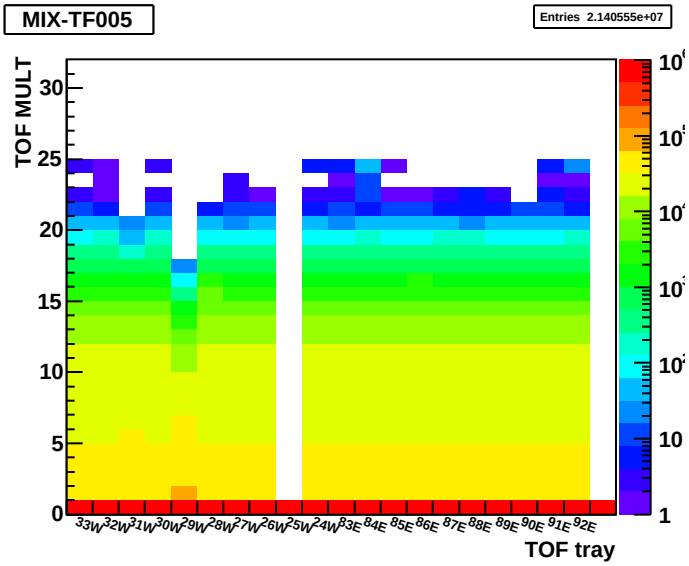

Hybrid Jet Patches
Entries 1019312
JP ADC
JP ID

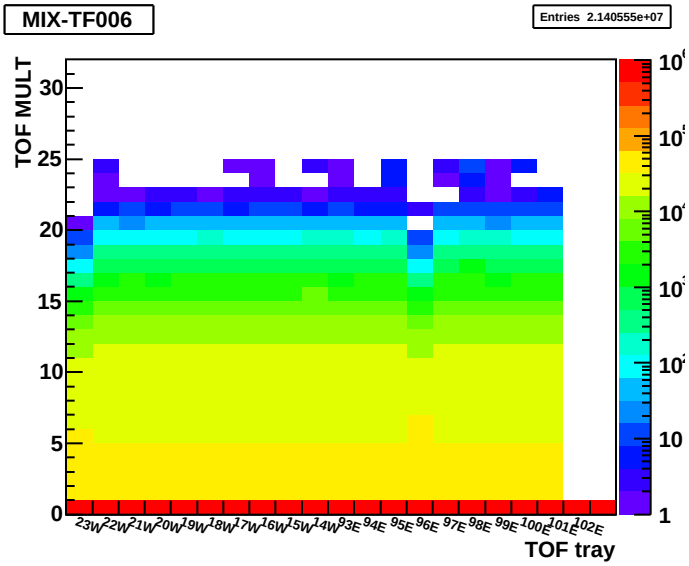

MIX-TF001
Entries 2.140555e+07
TOF MULT
TOF tray

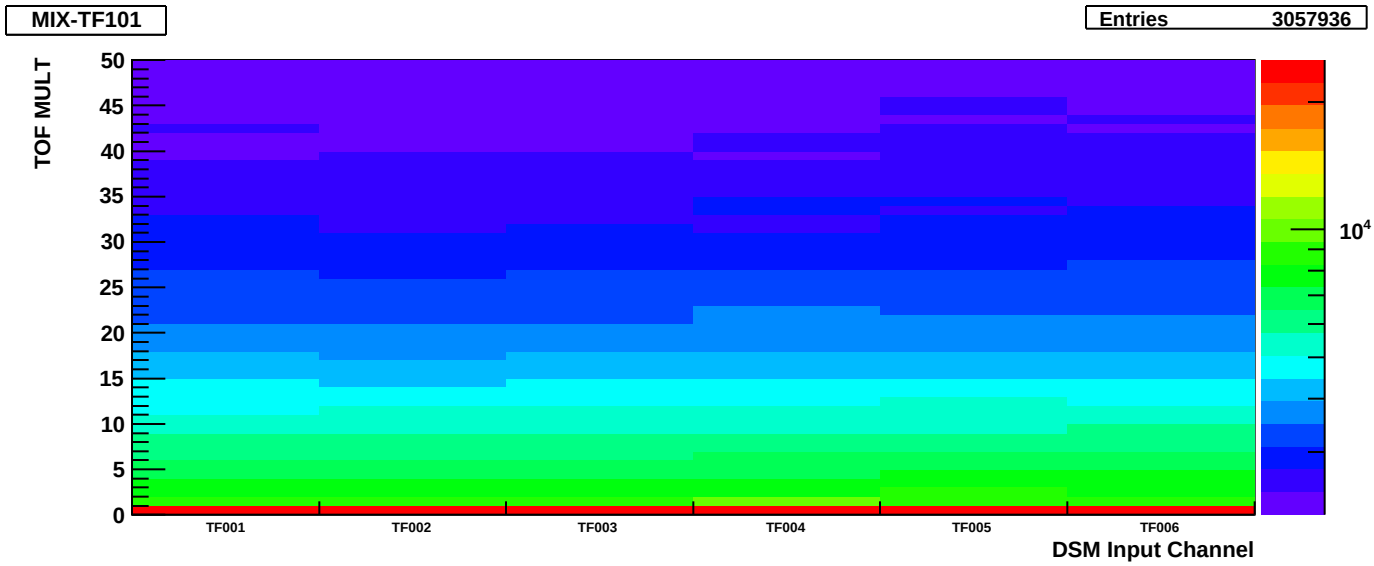

MIX-TF002
Entries 2.140555e+07
TOF MULT
TOF tray

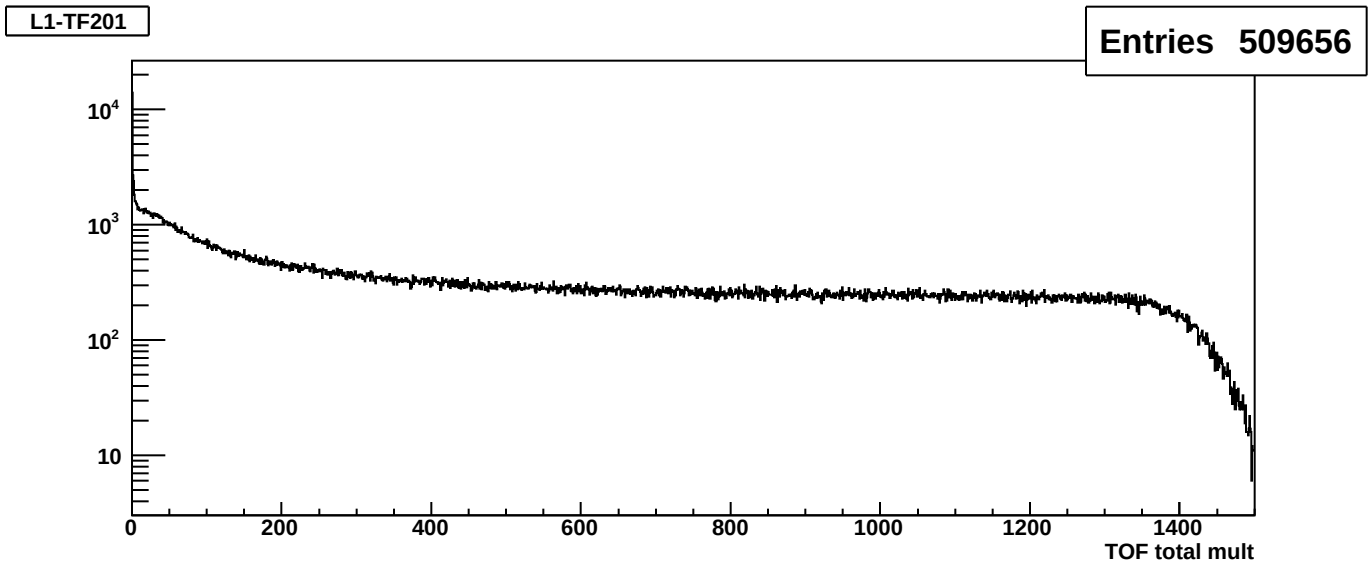

MIX-TF003
Entries 2.140555e+07
TOF MULT
TOF tray

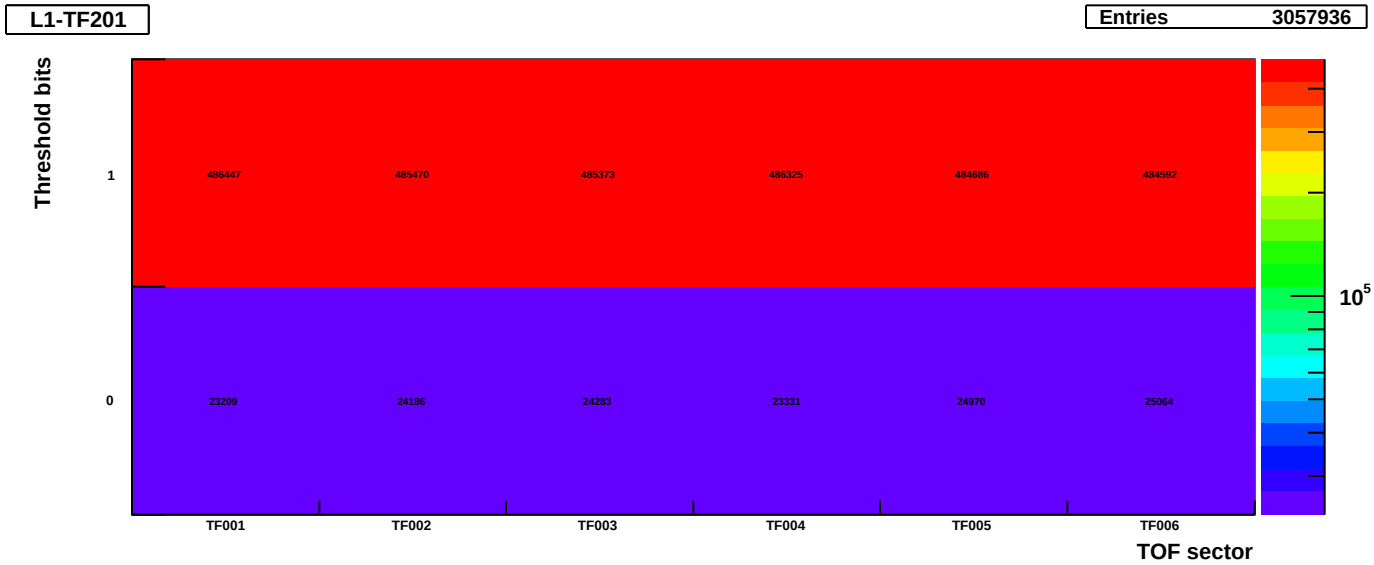

MIX-TF004
Entries 2.140555e+07
TOF MULT
TOF tray

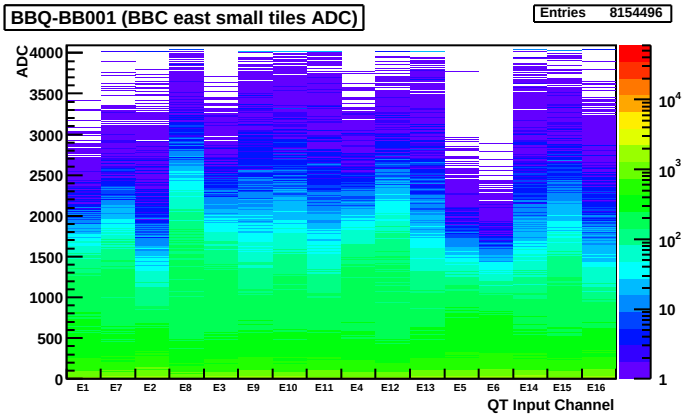

MIX-TF005
Entries 2.140555e+07
TOF MULT
TOF tray

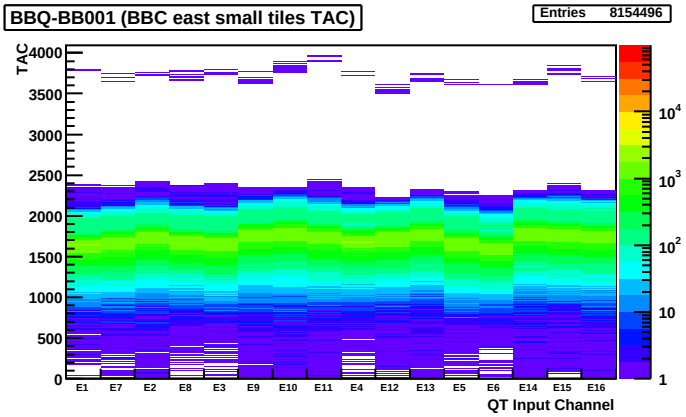

MIX-TF006
Entries 2.140555e+07
TOF MULT
TOF tray

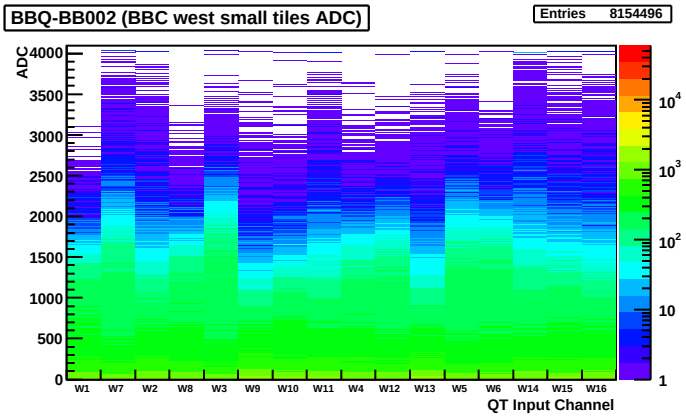

MIX-TF101
Entries 3057936
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

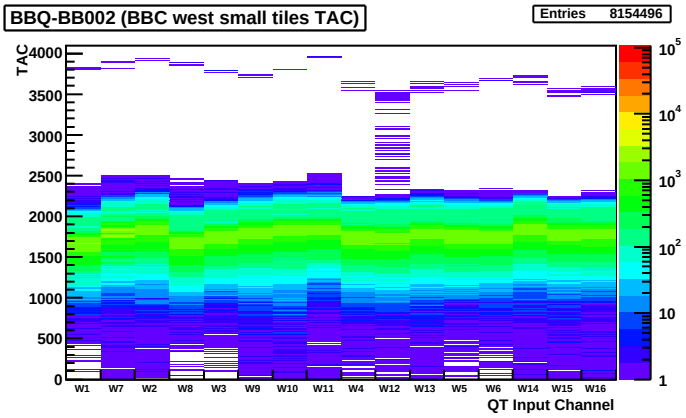

L1-TF201
Entries 509656
10^4
10^3
10^2
10
0
200
400
600
800
1000
1200
1400
TOF total mult

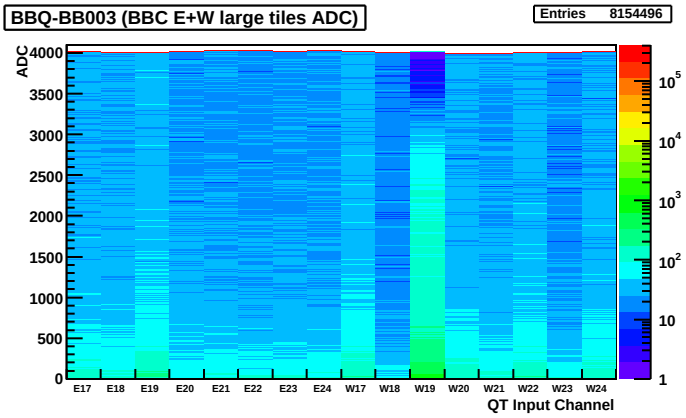

L1-TF201
Entries 3057936
Threshold bits
1
0
486447
485470
485373
486325
484686
484592
23209
24186
24283
23331
24970
25064
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

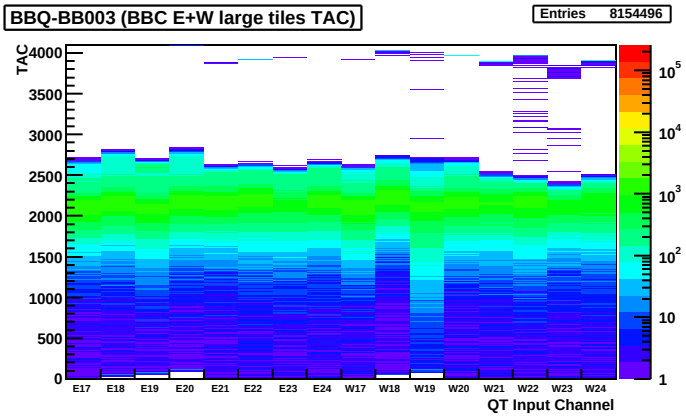

BBQ-BB001 (BBC east small tiles ADC)
Entries 8154496
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

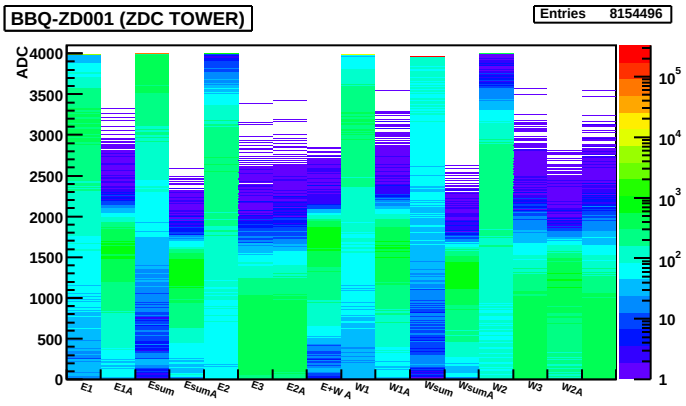

BBQ-BB001 (BBC east small tiles TAC)
Entries 8154496
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

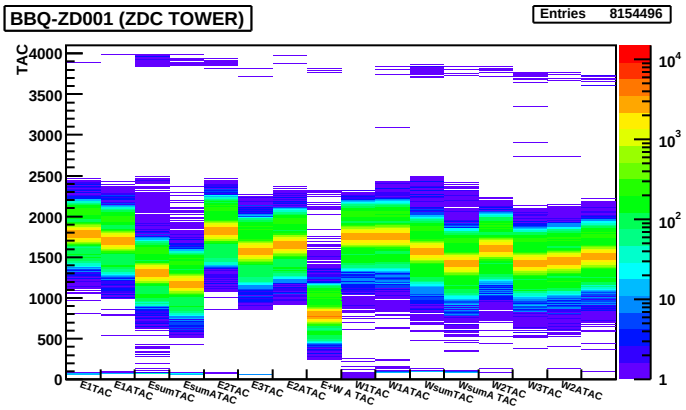

BBQ-BB002 (BBC west small tiles ADC)
Entries 8154496
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

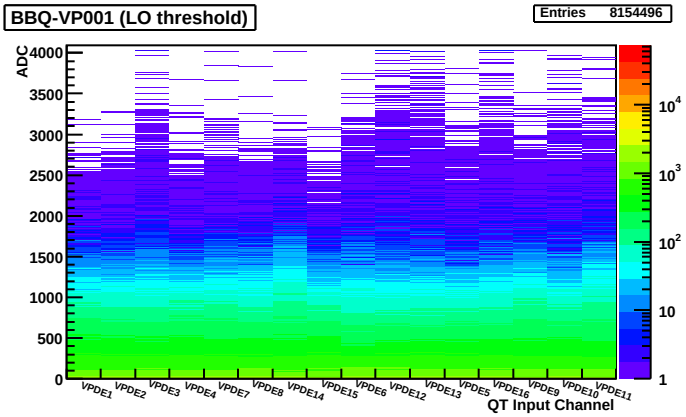

BBQ-BB002 (BBC west small tiles TAC)
Entries 8154496
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

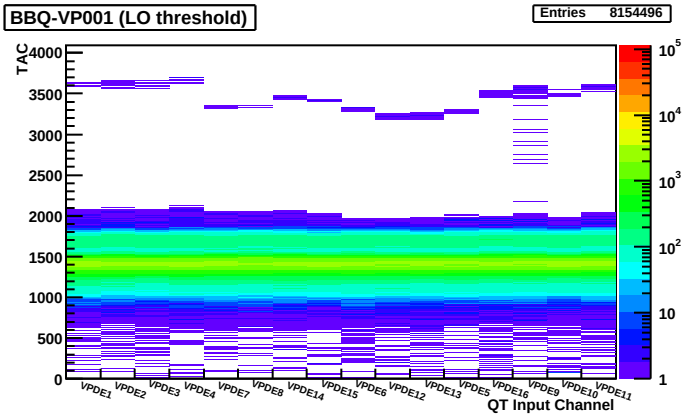

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 8154496
ADC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

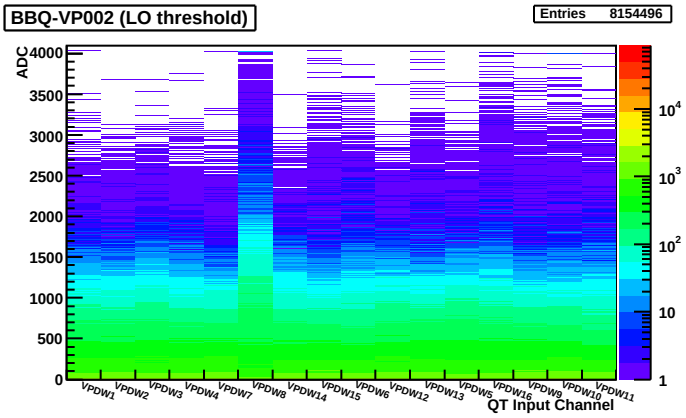

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 8154496
TAC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

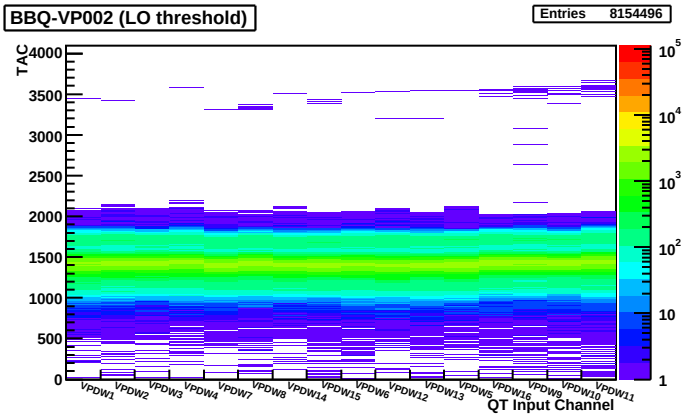

BBQ-ZD001 (ZDC TOWER)
Entries 8154496
ADC
E1 E1A Esum EsumA E2 E3 E2A E+W A W1 W1A Wsum WsumA W2 W3 W2A

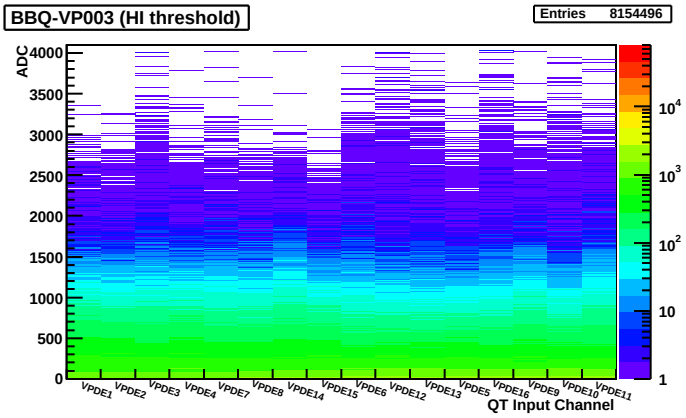

BBQ-ZD001 (ZDC TOWER)
Entries 8154496
TAC
E1TAC E1ATAC EsumTAC EsumATAC E2TAC E3TAC E2ATAC E+W A TAC W1TAC W1ATAC WsumTAC WsumA TAC W2TAC W3TAC W2ATAC

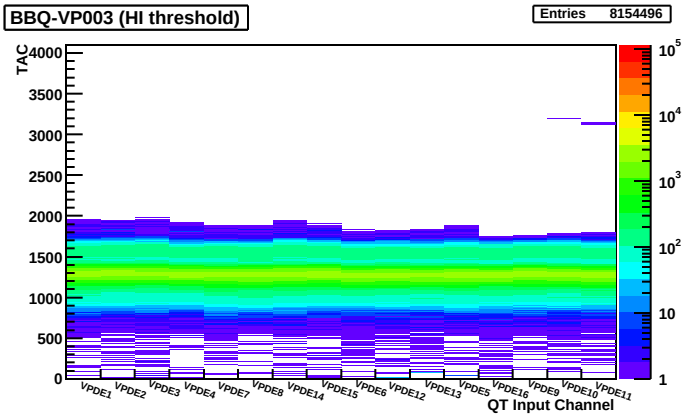

BBQ-VP001 (LO threshold)
Entries 8154496
ADC
QT Input Channel

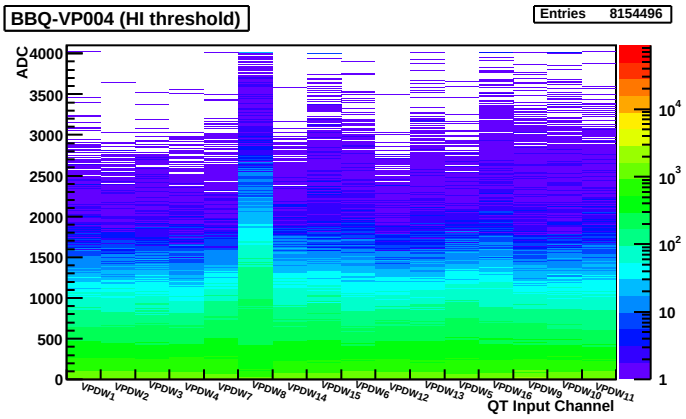

BBQ-VP001 (LO threshold)
Entries 8154496
TAC
QT Input Channel

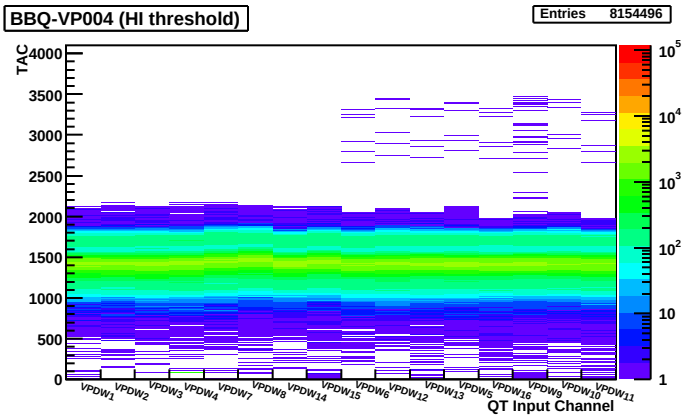

BBQ-VP002 (LO threshold)
Entries 8154496
ADC
QT Input Channel

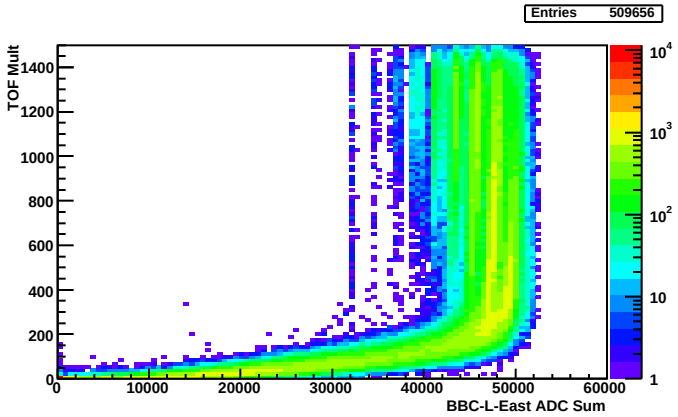

BBQ-VP002 (LO threshold)
Entries 8154496
TAC
QT Input Channel

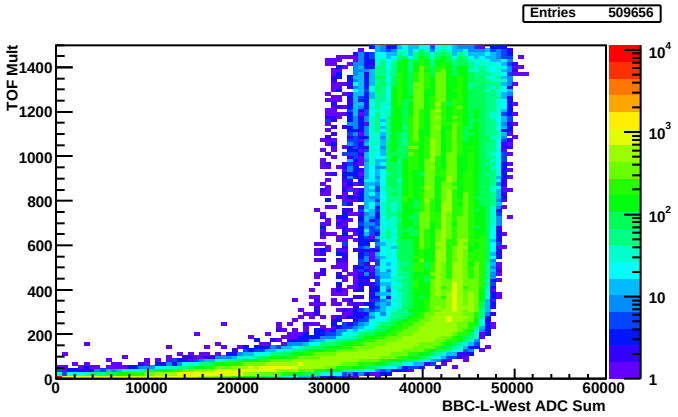

BBQ-VP003 (HI threshold)
Entries 8154496
ADC
QT Input Channel

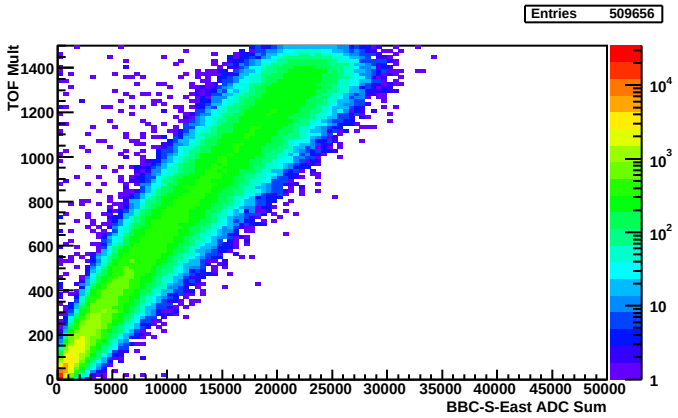

BBQ-VP003 (HI threshold)
Entries 8154496
TAC
QT Input Channel

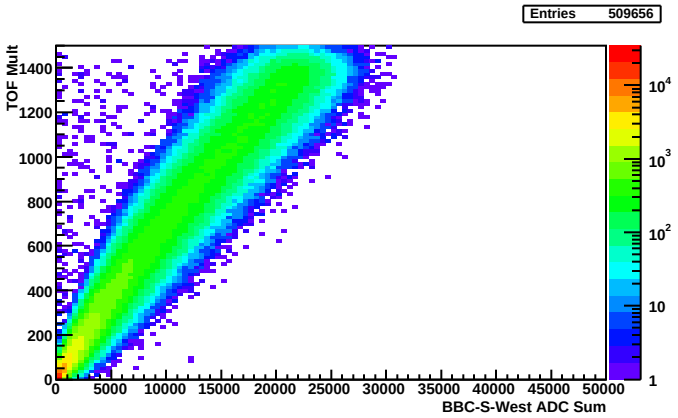

BBQ-VP004 (HI threshold)
Entries 8154496
ADC
QT Input Channel

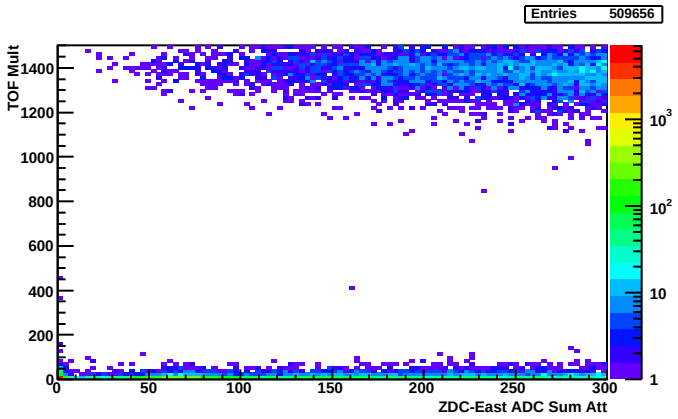

BBQ-VP004 (HI threshold)
Entries 8154496
TAC
QT Input Channel

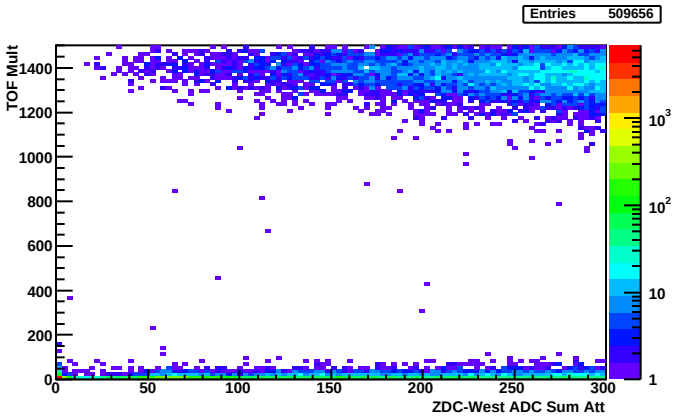

Entries 509656
TOF Mult
BBC-L-East ADC Sum

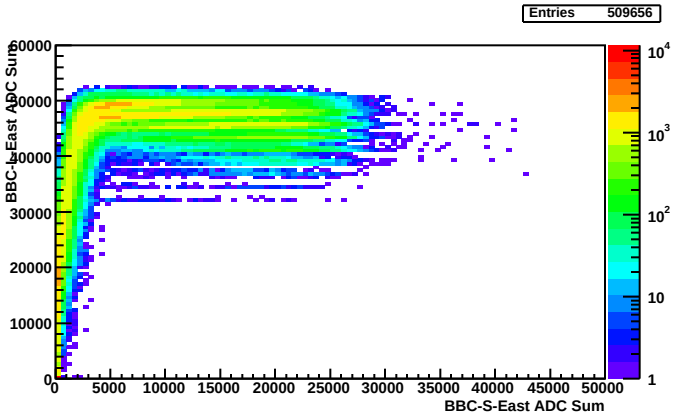

Entries 509656
TOF Mult
BBC-L-West ADC Sum

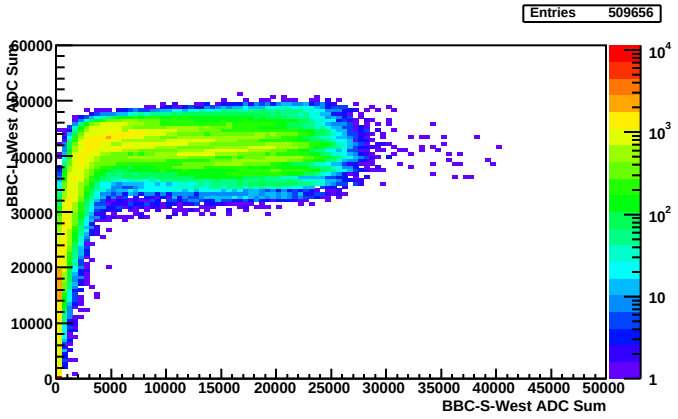

Entries 509656
TOF Mult
BBC-S-East ADC Sum

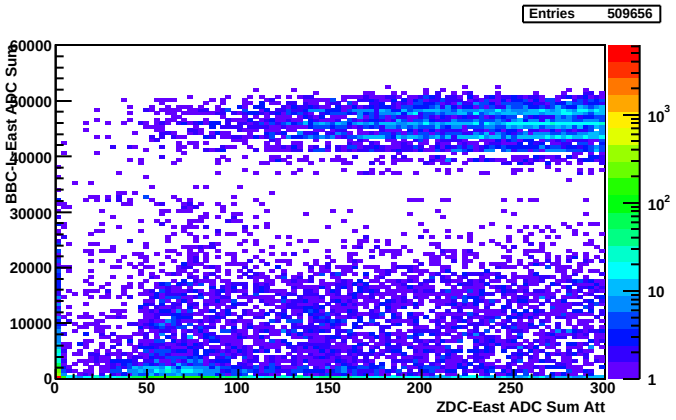

Entries 509656
TOF Mult
BBC-S-West ADC Sum

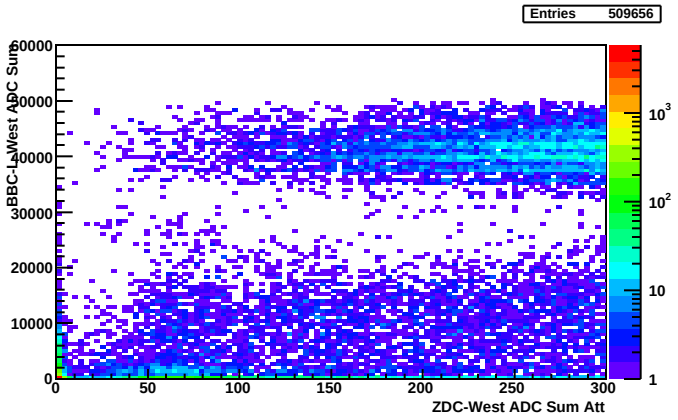

Entries 509656
TOF Mult
ZDC-East ADC Sum Att

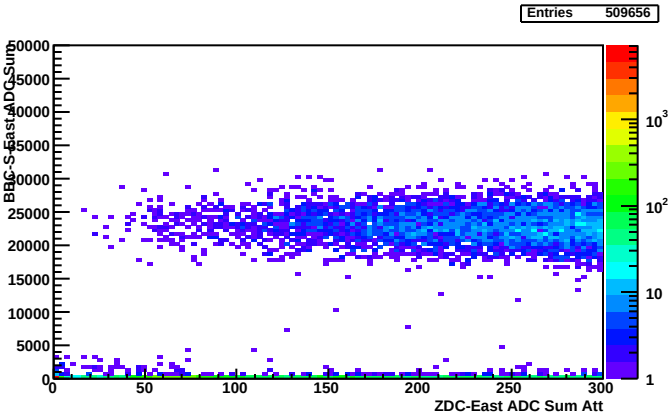

Entries 509656
TOF Mult
ZDC-West ADC Sum Att

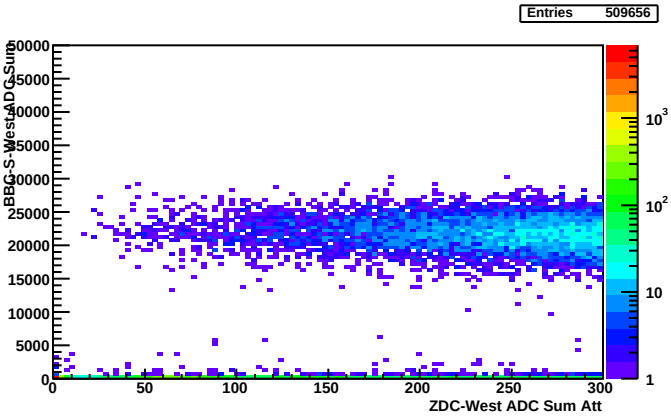

Entries 509656
BBC-L-East ADC Sum
BBC-S-East ADC Sum

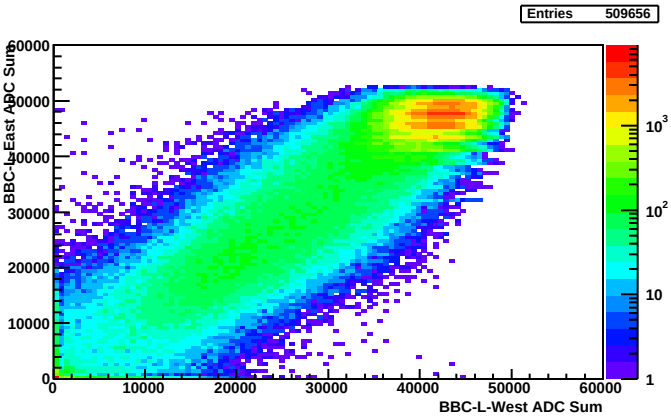

Entries 509656
BBC-L-West ADC Sum
BBC-S-West ADC Sum

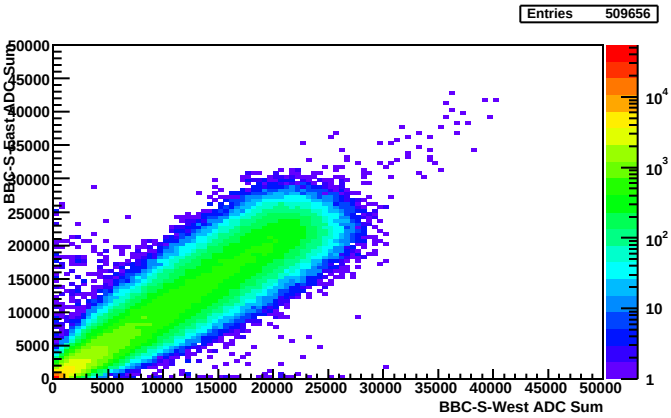

Entries 509656
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

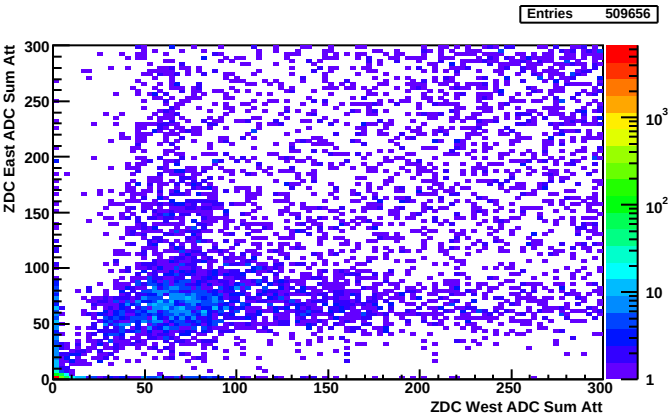

Entries 509656
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

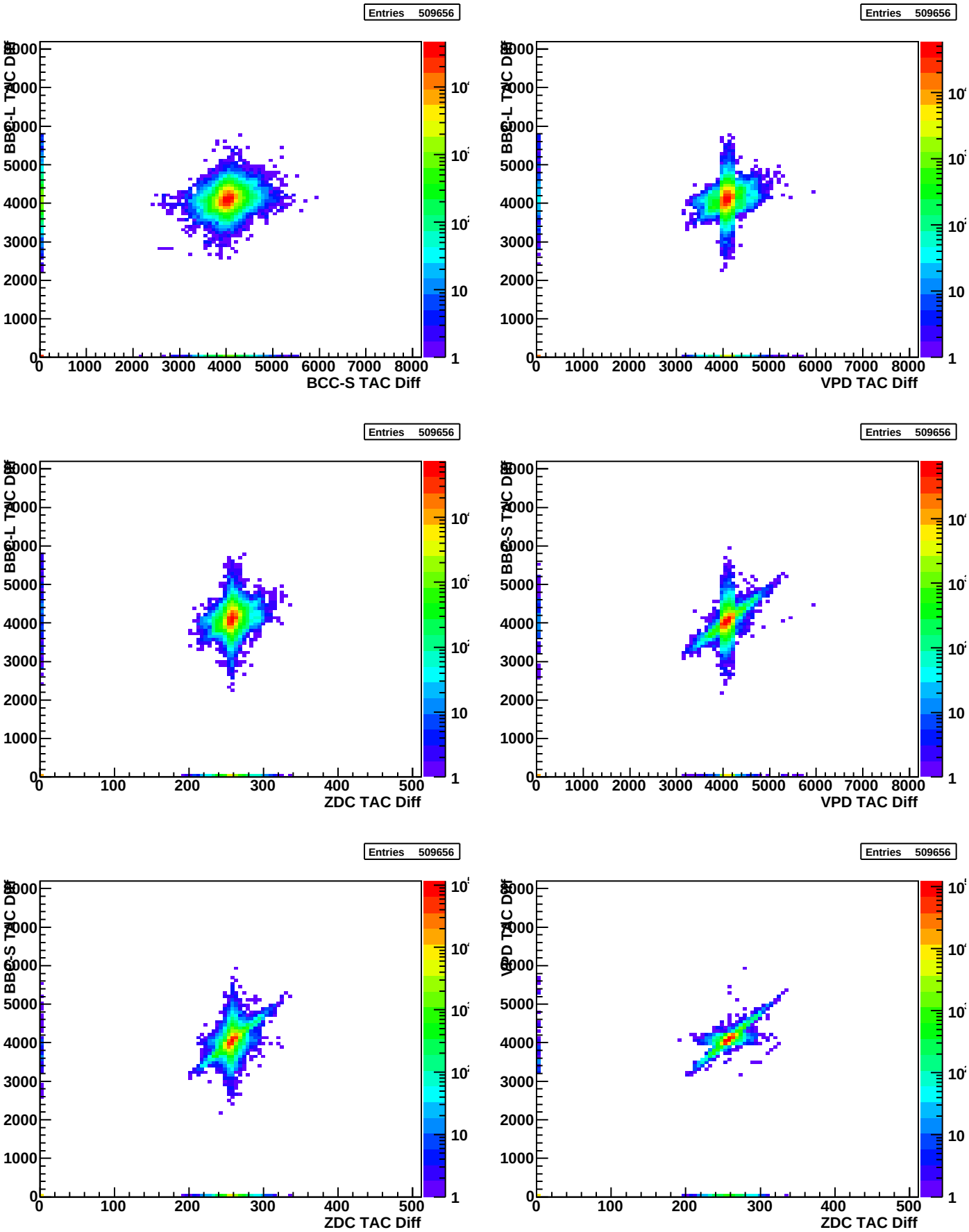

Entries 509656
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

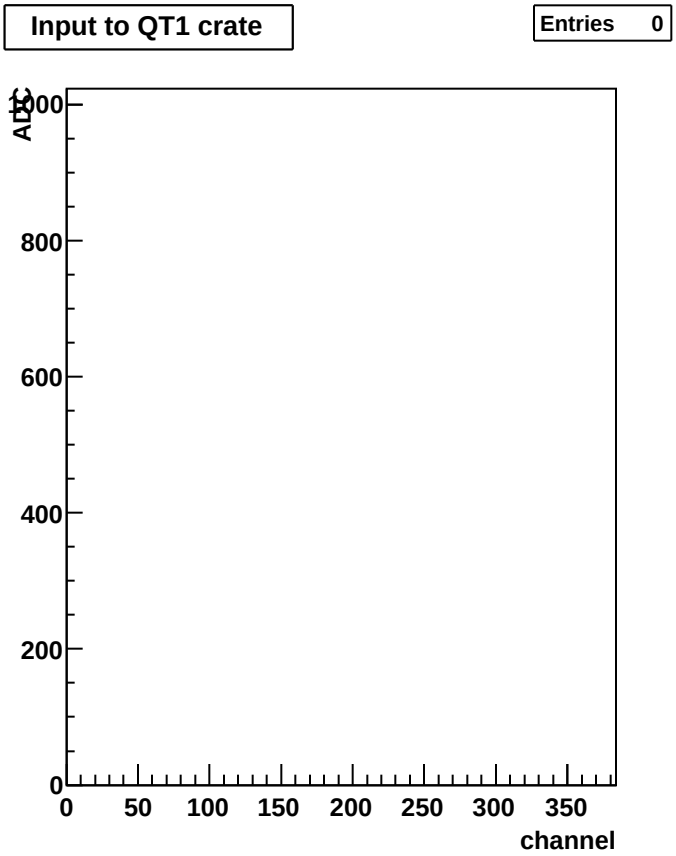

Entries 509656
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

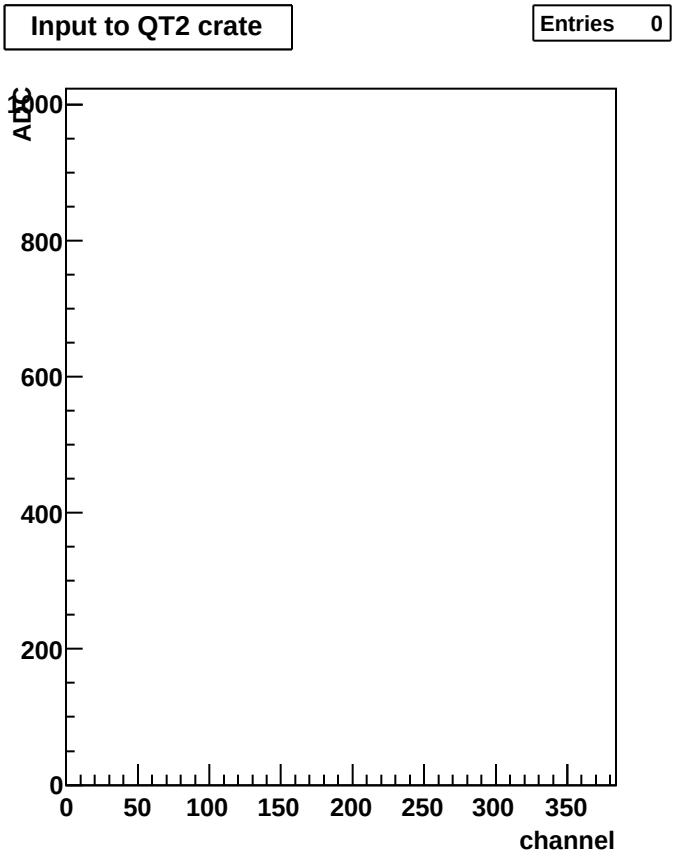

Entries 509656
BBC-L-East ADC Sum
BBC-L-West ADC Sum

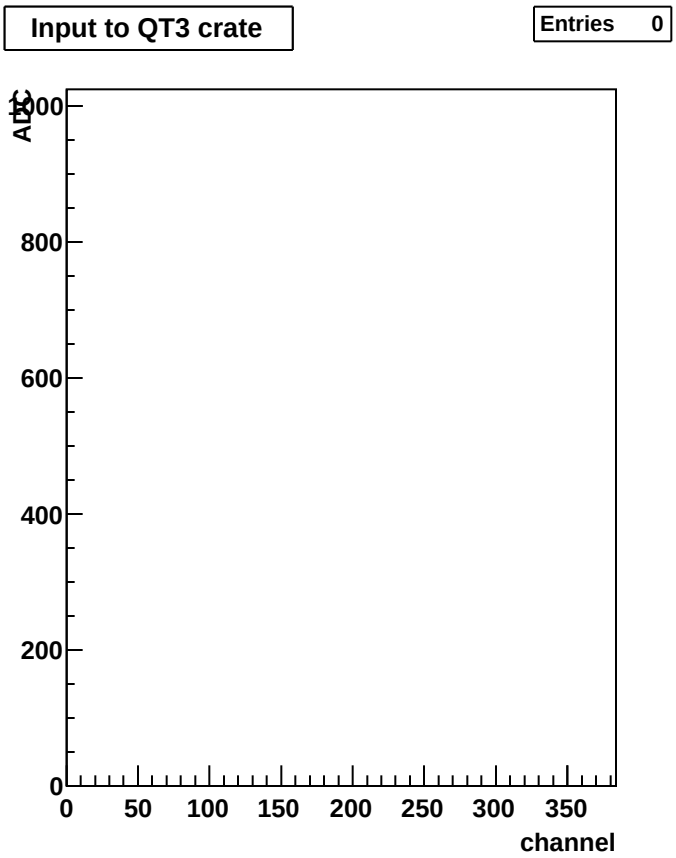

Entries 509656
BBC-S-East ADC Sum
BBC-S-West ADC Sum

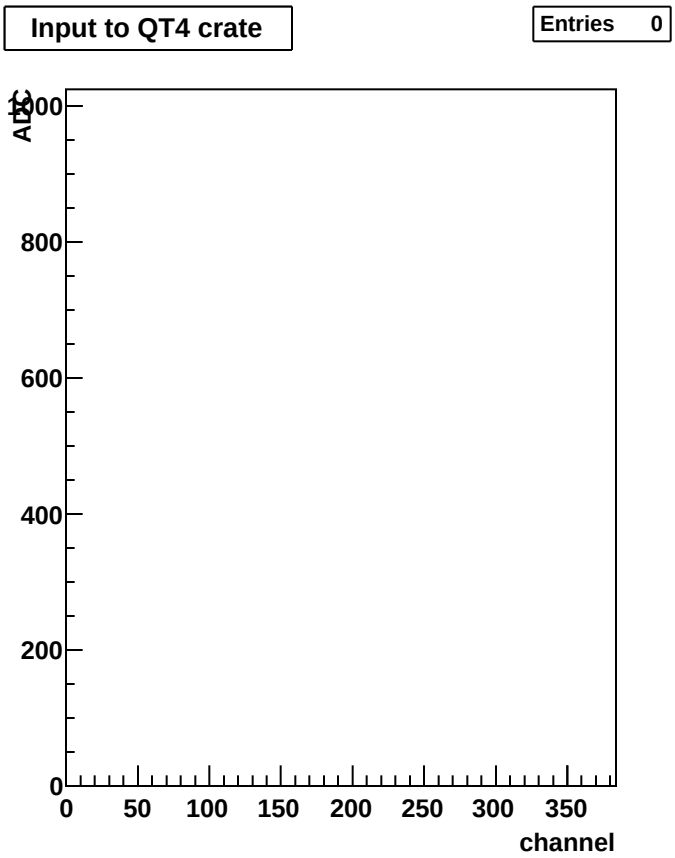

Entries 509656
ZDC East ADC Sum Att
ZDC West ADC Sum Att

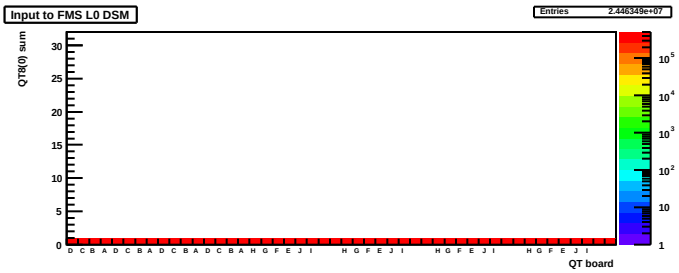

Entries 509656
BBC-L TAC Diff
BCC-S TAC Diff
Entries 509656
BBC-L TAC Diff
VPD TAC Diff
Entries 509656
BBC-L TAC Diff
ZDC TAC Diff
Entries 509656
BBC-S TAC Diff
VPD TAC Diff
Entries 509656
BBC-S TAC Diff
ZDC TAC Diff
Entries 509656
VPD TAC Diff
ZDC TAC Diff

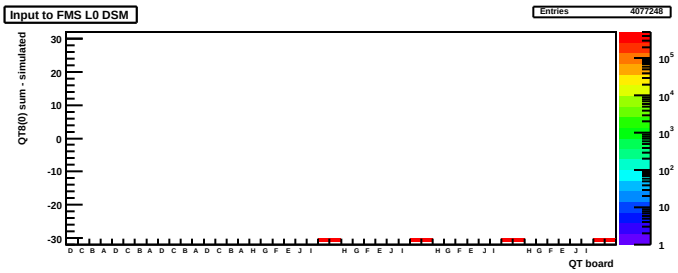

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

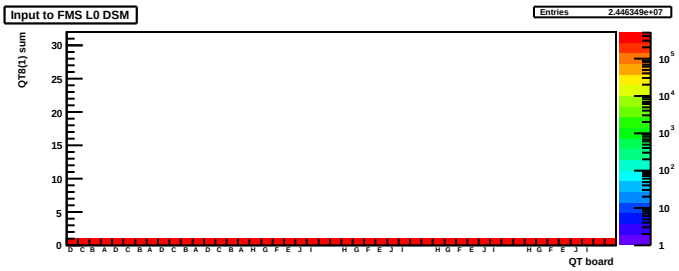

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

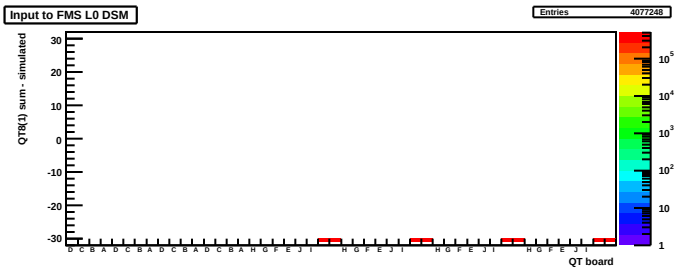

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

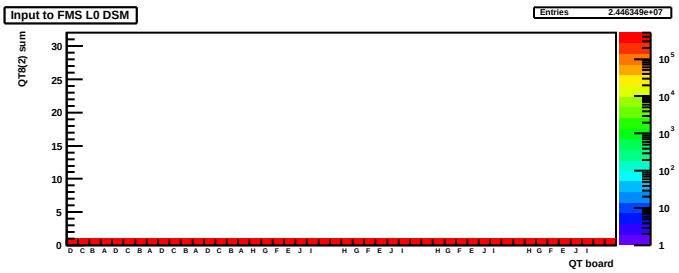

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

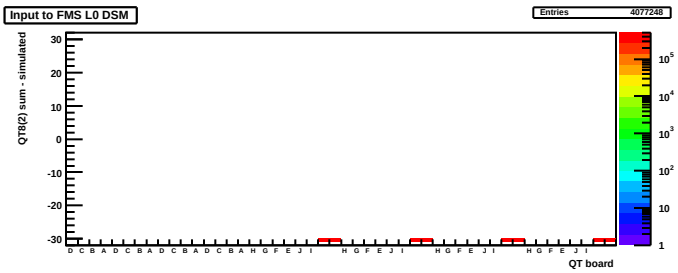

Input to FMS L0 DSM
Entries 2.446349e+07
QT8(0) sum
QT board

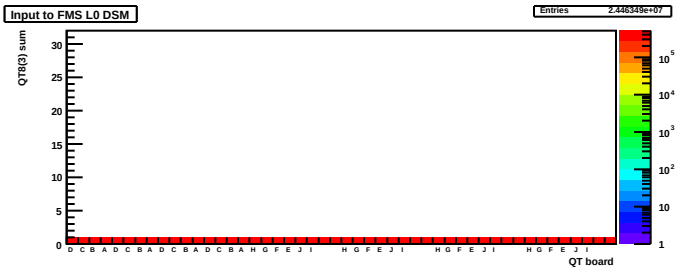

Input to FMS L0 DSM
Entries 4077248
QT8(0) sum - simulated
QT board

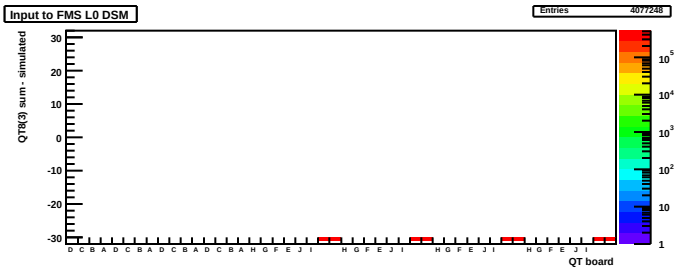

Input to FMS L0 DSM
Entries 2.446349e+07
QT8(1) sum
QT board

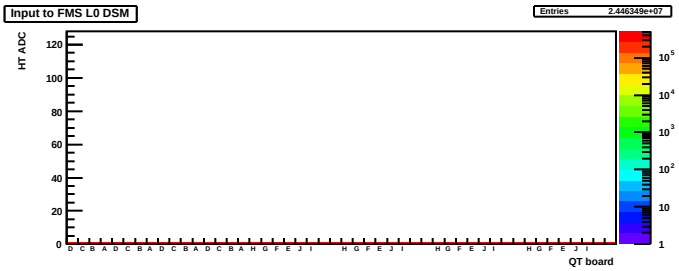

Input to FMS L0 DSM
Entries 4077248
QT8(1) sum - simulated
QT board

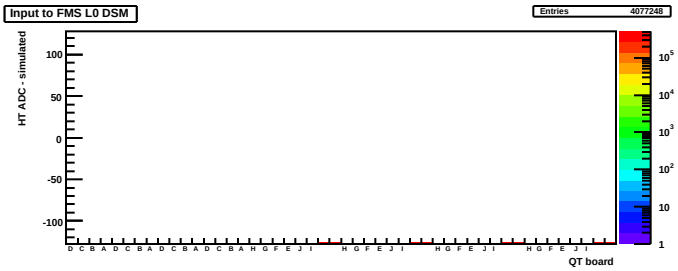

Input to FMS L0 DSM
Entries 2.446349e+07
QT8(2) sum
QT board

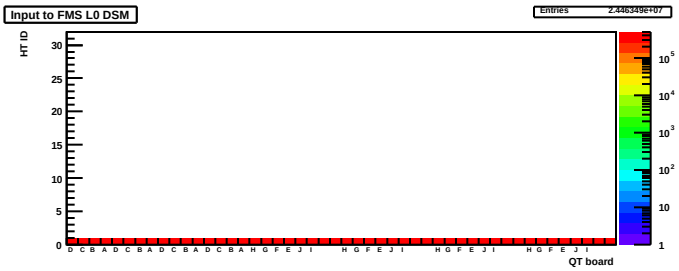

Input to FMS L0 DSM
Entries 4077248
QT8(2) sum - simulated
QT board

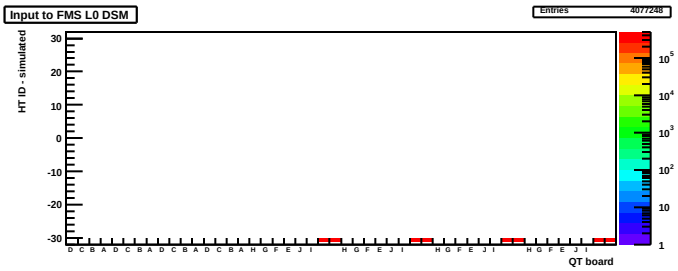

Input to FMS L0 DSM
Entries 2.446349e+07
QT8(3) sum
QT board

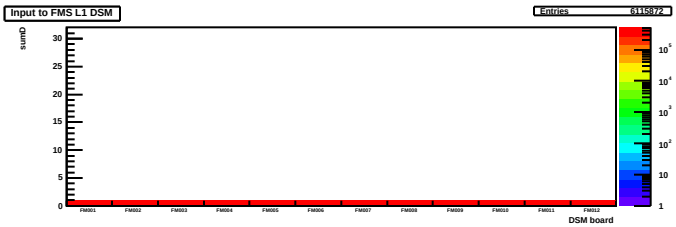

Input to FMS L0 DSM
Entries 4077248
QT8(3) sum - simulated
QT board

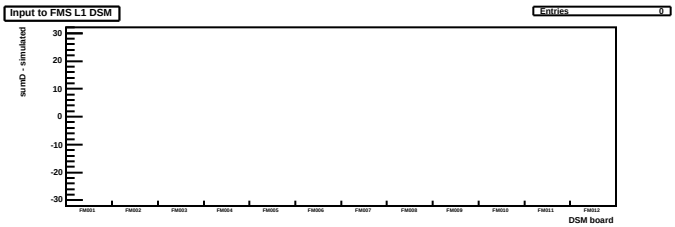

Input to FMS L0 DSM
Entries 2.446349e+07
HT ADC
QT board

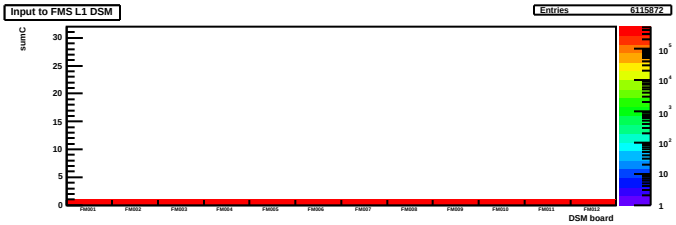

Input to FMS L0 DSM
Entries 4077248
HT ADC - simulated
QT board

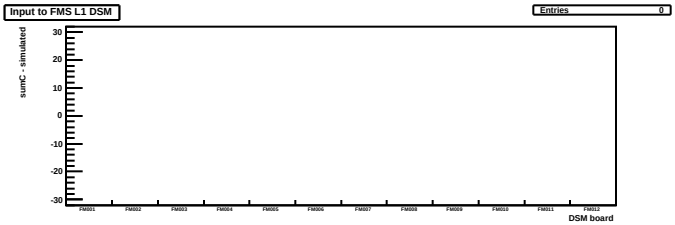

Input to FMS L0 DSM
Entries 2.446349e+07
HT ID
QT board

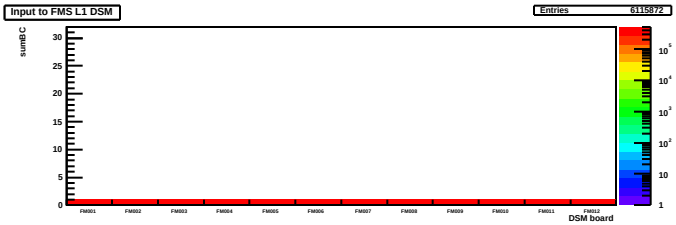

Input to FMS L0 DSM
Entries 4077248
HT ID - simulated
QT board

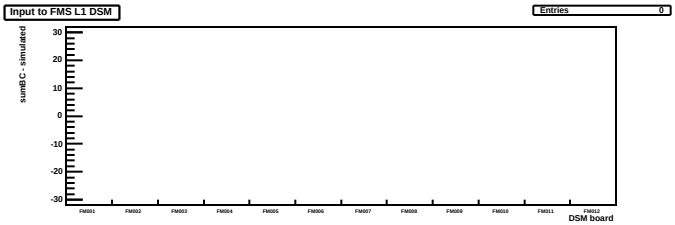

Input to FMS L1 DSM
Entries 6115872
sumD
DSM board

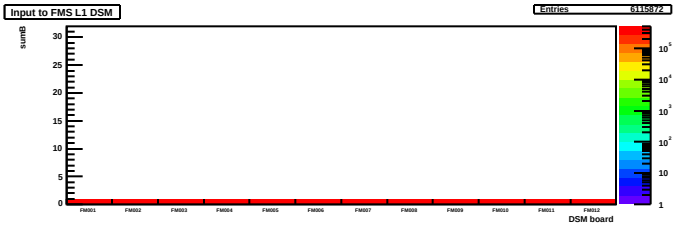

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

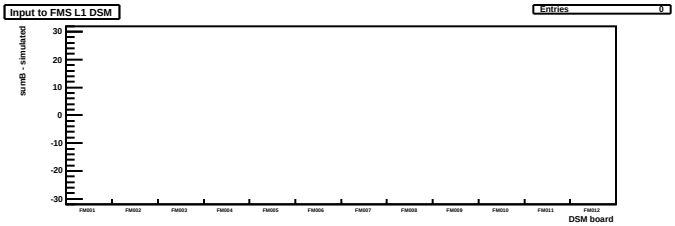

Input to FMS L1 DSM
Entries 6115872
sumC
DSM board

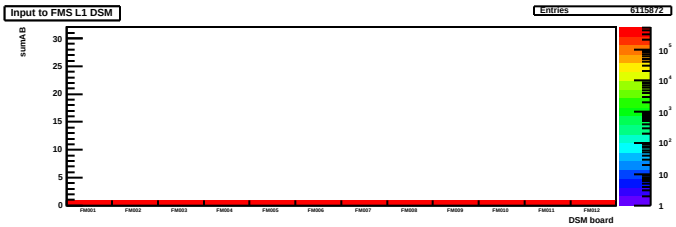

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

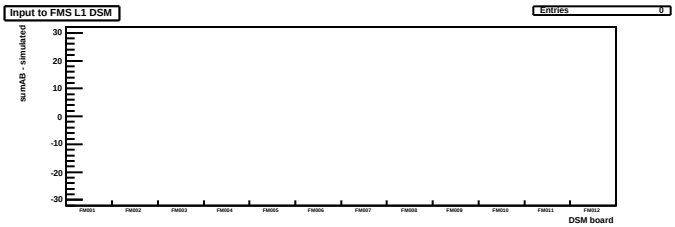

Input to FMS L1 DSM
Entries 6115872
sumBC
DSM board

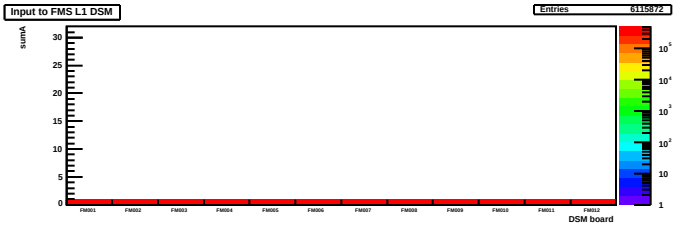

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

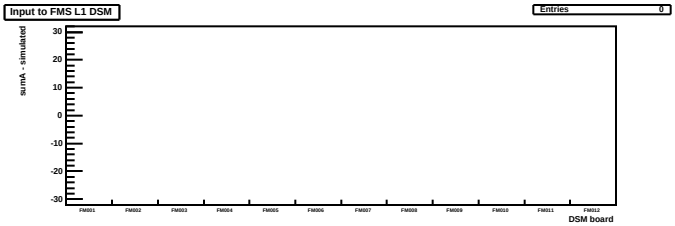

Input to FMS L1 DSM
Entries 6115872
sumB
DSM board

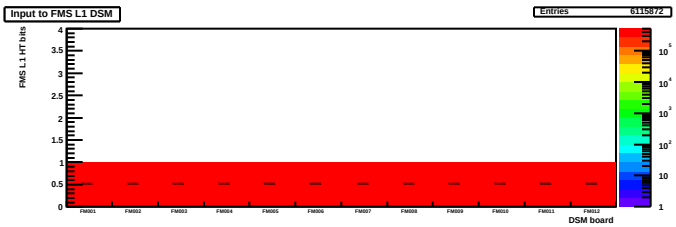

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

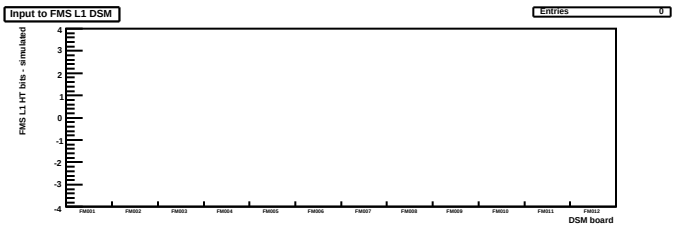

Input to FMS L1 DSM
Entries 6115872
sumAB
DSM board

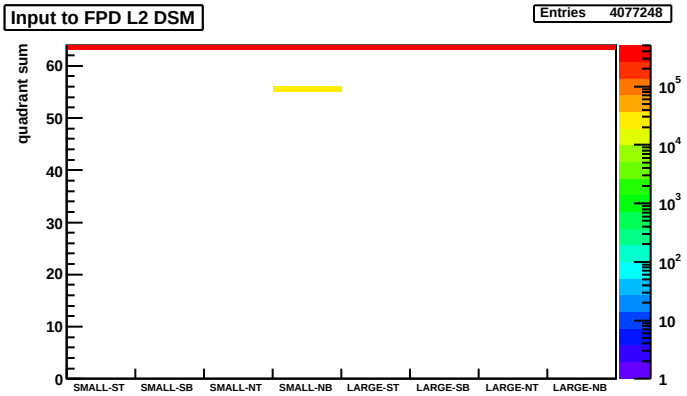

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

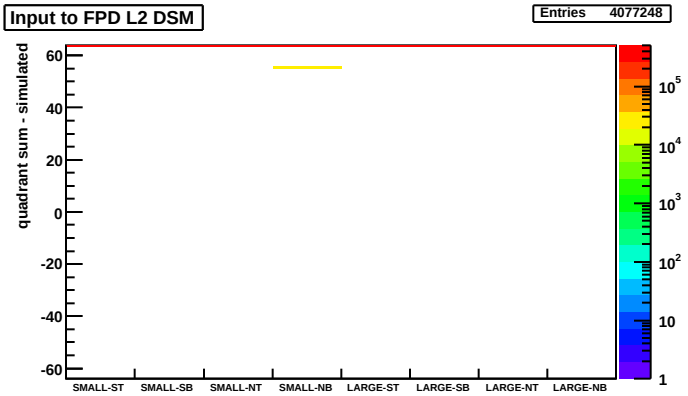

Input to FMS L1 DSM
Entries 6115872
sumA
DSM board

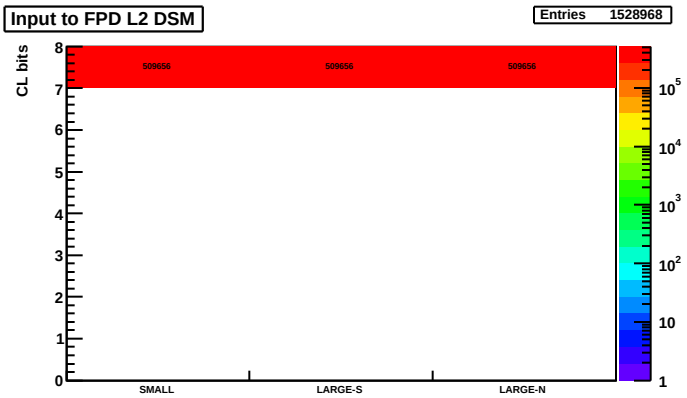

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

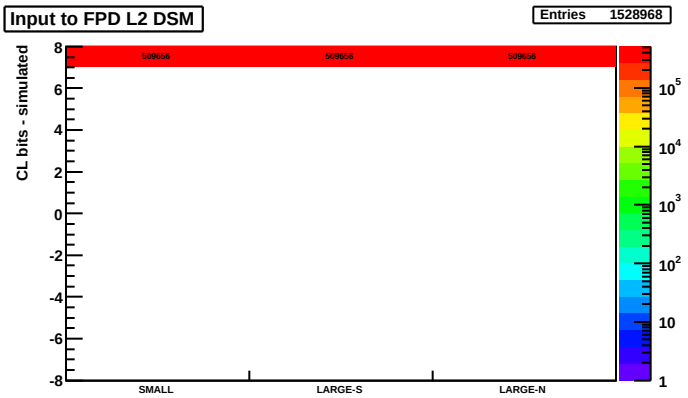

Input to FMS L1 DSM
Entries 6115872
FMS L1 HT bits
DSM board

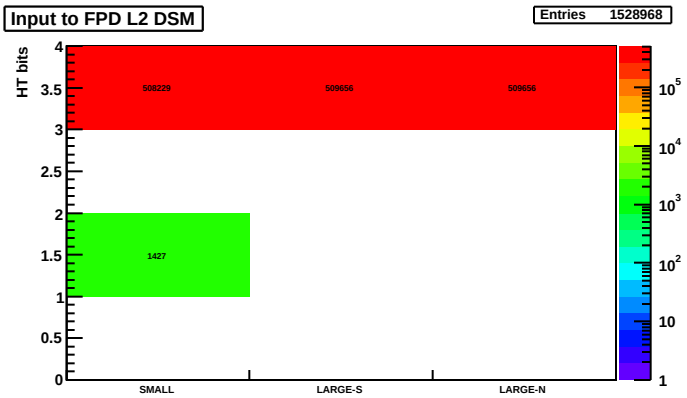

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

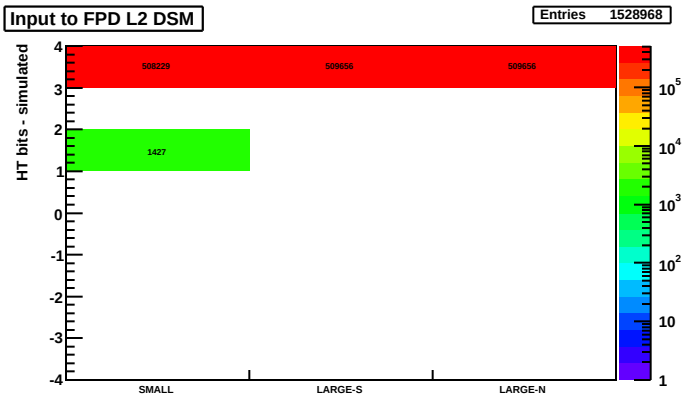

Input to FPD L2 DSM
Entries 4077248
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

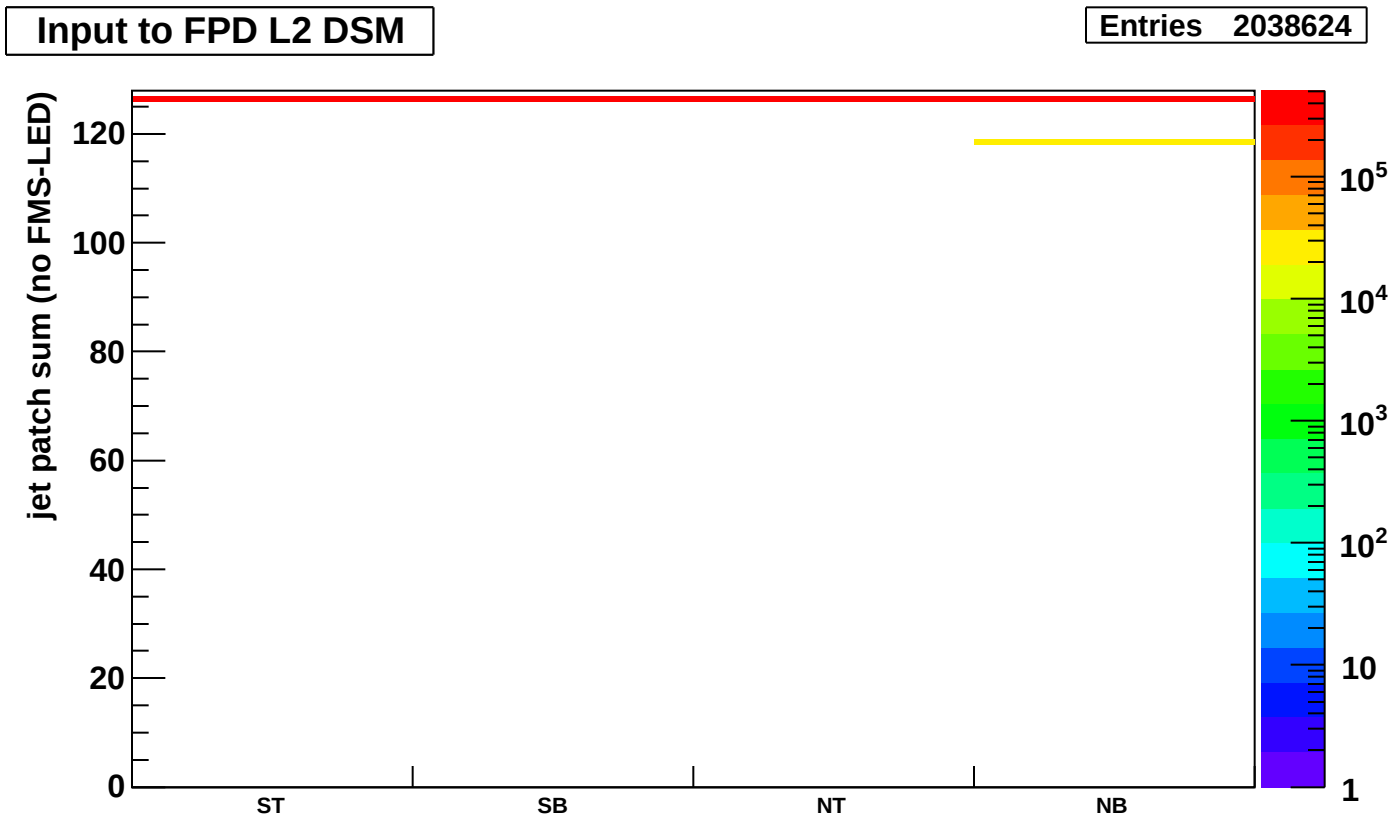

Input to FPD L2 DSM
Entries 4077248
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

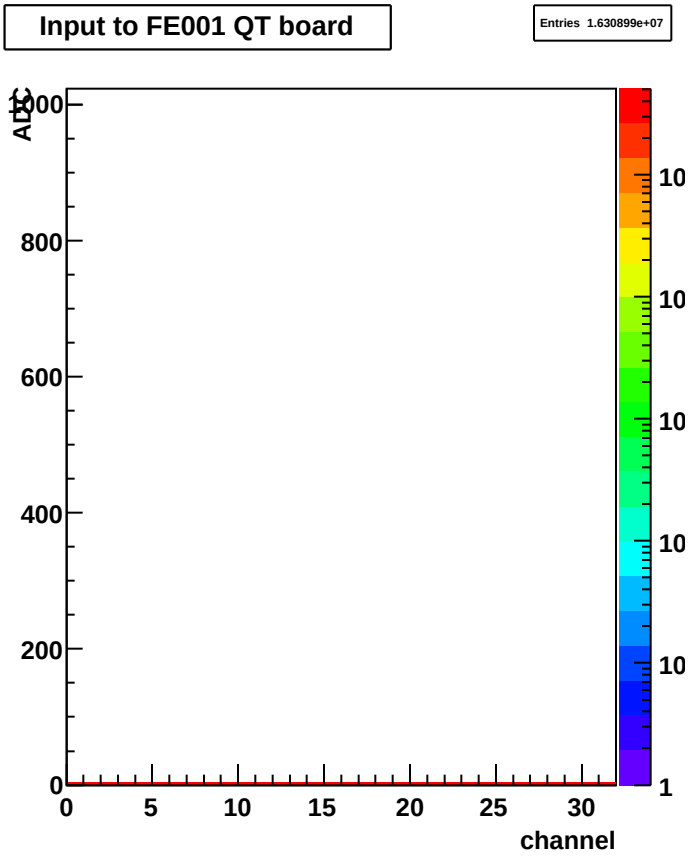

Input to FPD L2 DSM
Entries 1528968
CL bits
509656
509656
509656
SMALL
LARGE-S
LARGE-N

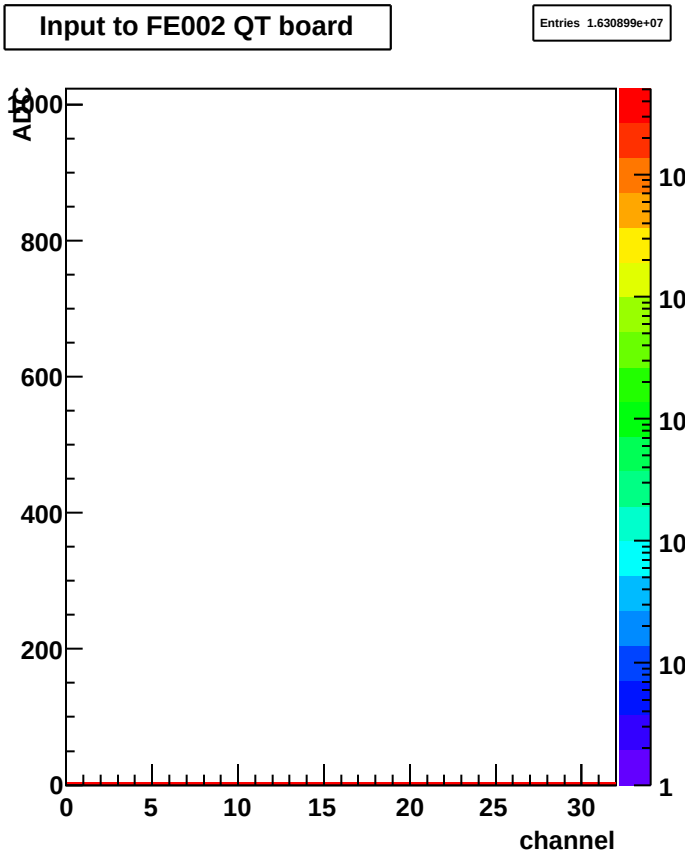

Input to FPD L2 DSM
Entries 1528968
CL bits - simulated
509656
509656
509656
SMALL
LARGE-S
LARGE-N

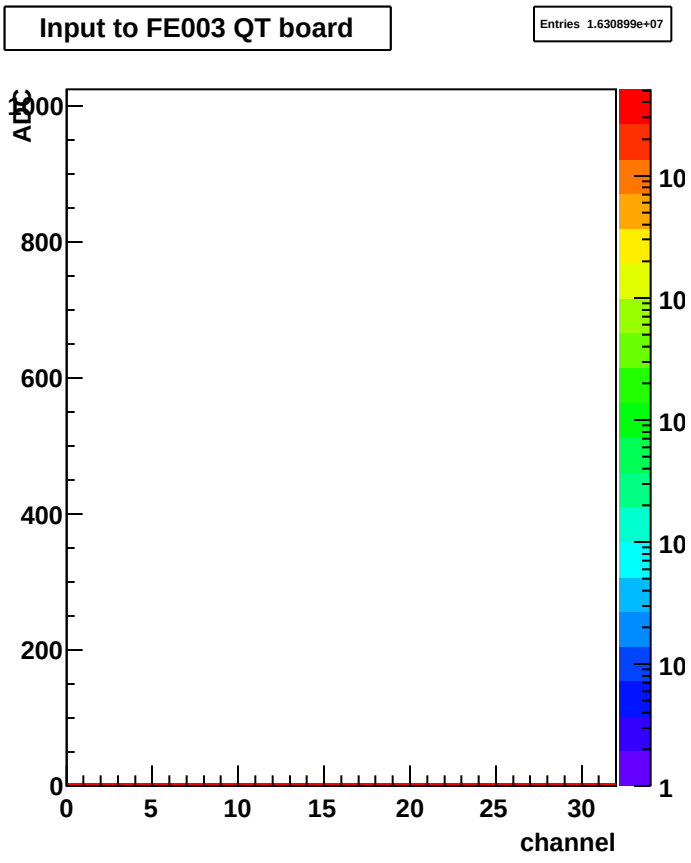

Input to FPD L2 DSM
Entries 1528968
HT bits
508229
509656
509656
1427
SMALL
LARGE-S
LARGE-N

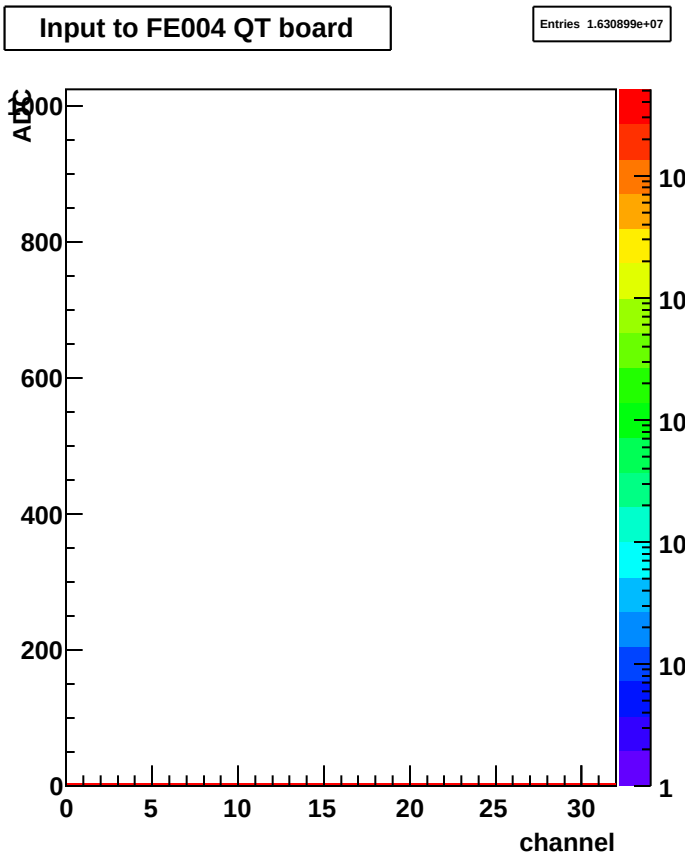

Input to FPD L2 DSM
Entries 1528968
HT bits - simulated
508229
509656
509656
1427
SMALL
LARGE-S
LARGE-N

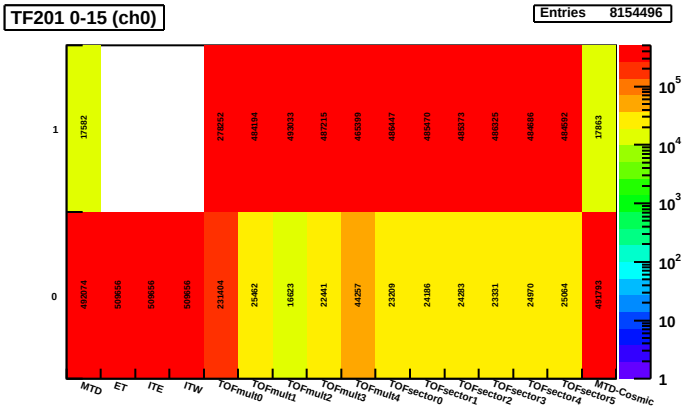

Input to FPD L2 DSM
Entries 2038624
jet patch sum (no FMS-LED)
120
100
80
60
40
20
0
ST
SB
NT
NB
10^5
10^4
10^3
10^2
10
1

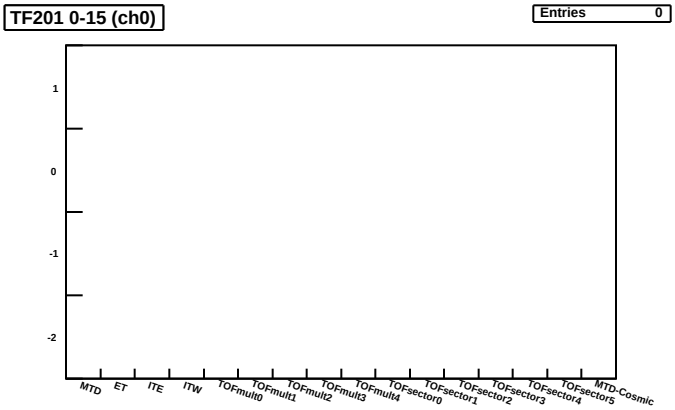

Input to FE001 QT board
Entries 1.630899e+07
ADC
channel

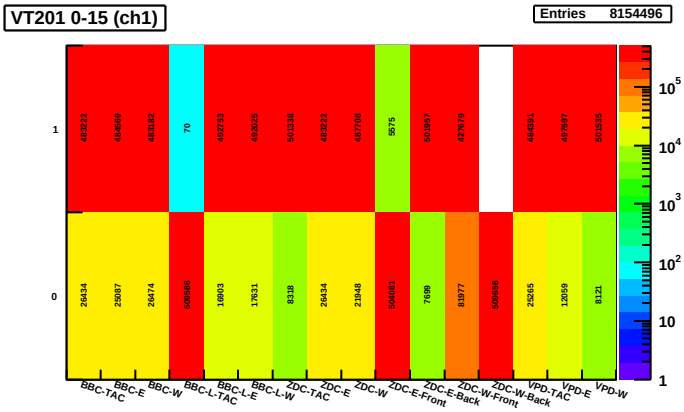

Input to FE002 QT board
Entries 1.630899e+07
ADC
channel

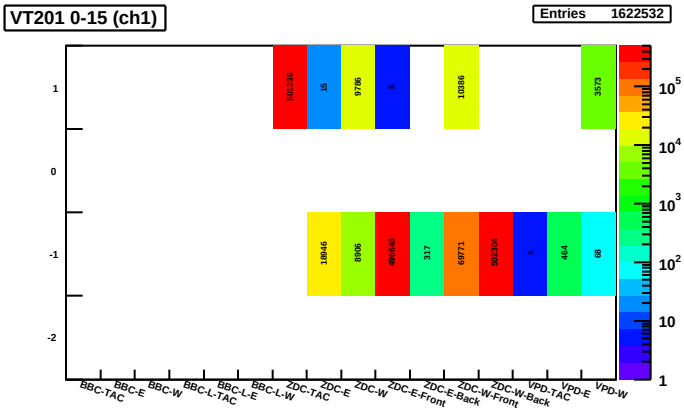

Input to FE003 QT board
Entries 1.630899e+07
ADC
channel

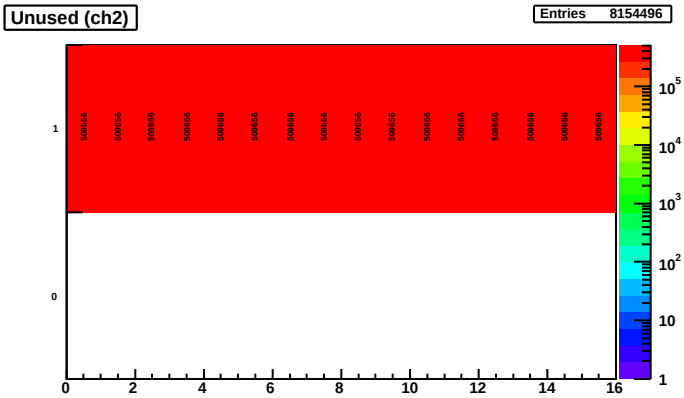

Input to FE004 QT board
Entries 1.630899e+07
ADC
channel

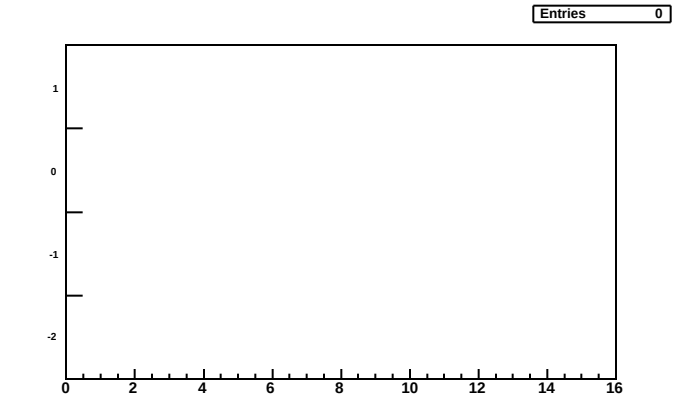

TF201 0-15 (ch0)
Entries 8154496
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

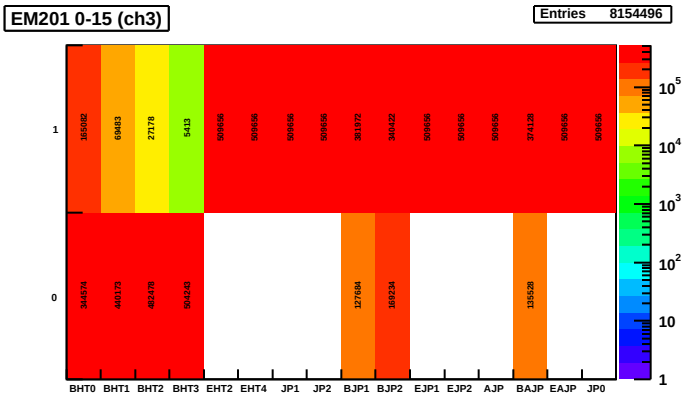

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

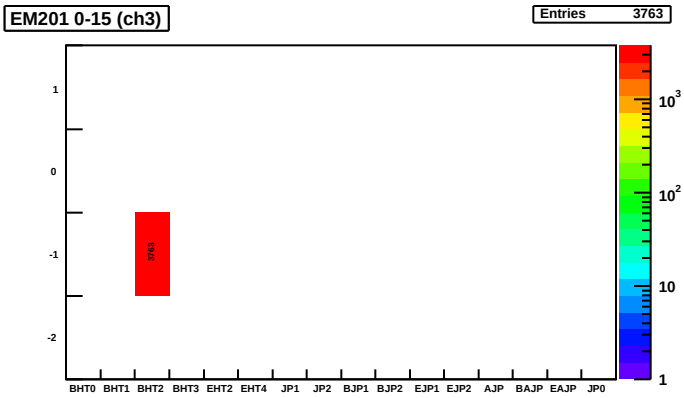

VT201 0-15 (ch1)
Entries 8154496
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 1622532
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

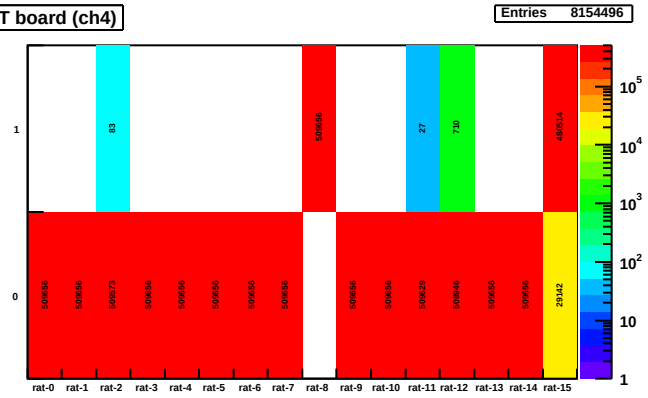

Unused (ch2)
Entries 8154496

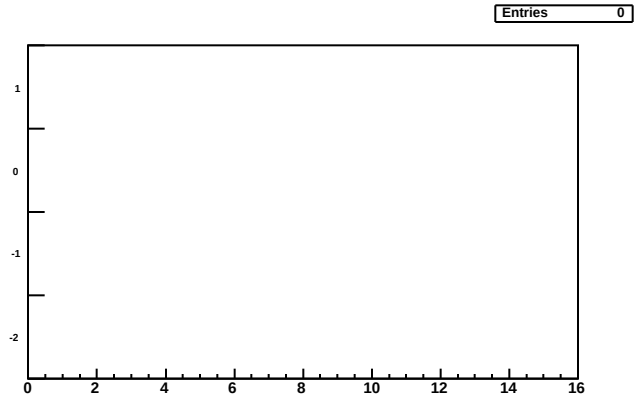

Entries 0


EM201 0-15 (ch3)
Entries 8154496
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

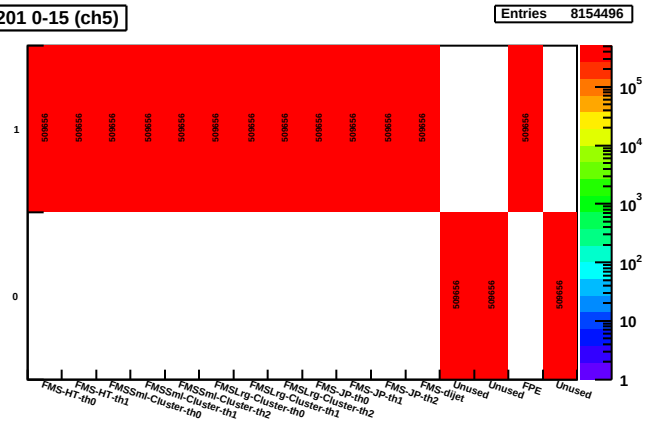

EM201 0-15 (ch3)
Entries 3763
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

RAT board (ch4)

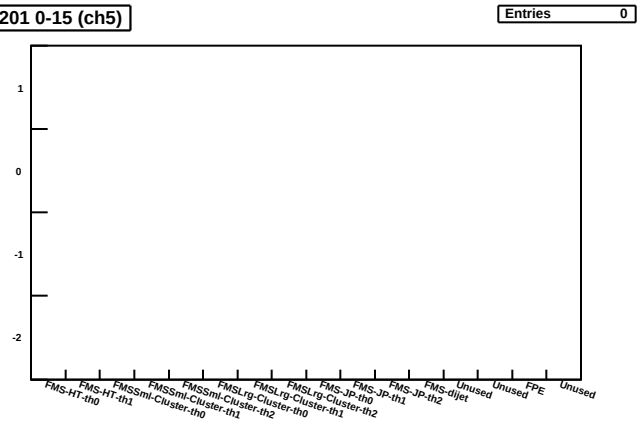




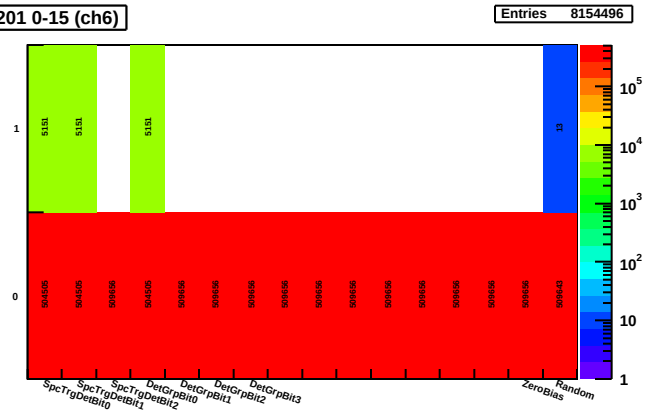
FP201 0-15 (ch5)

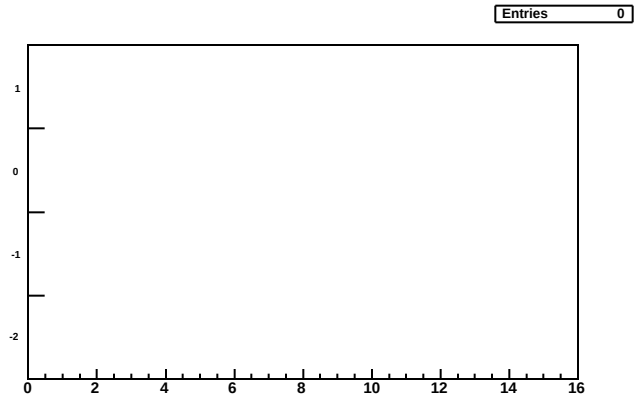
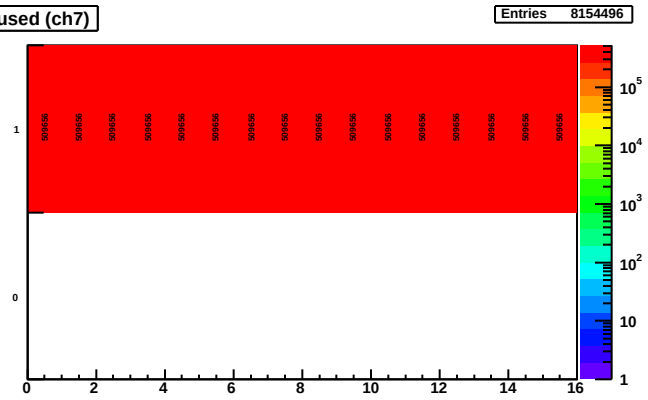
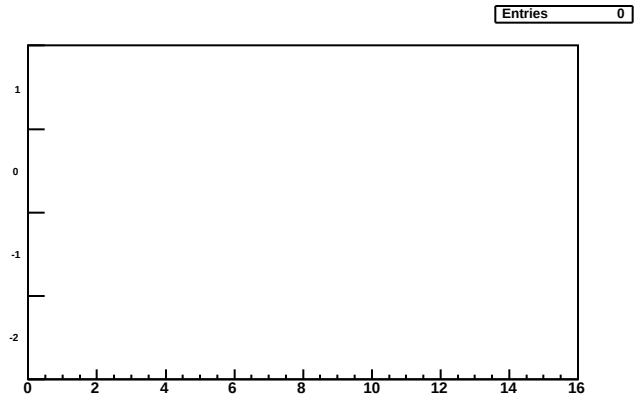


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)