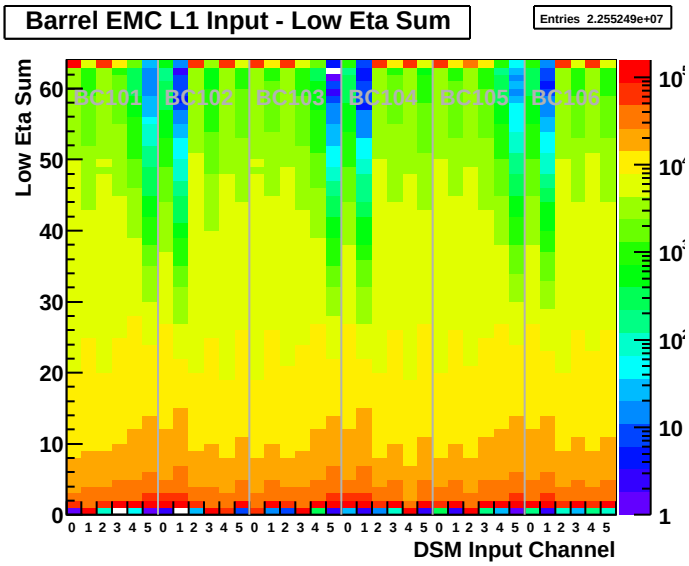
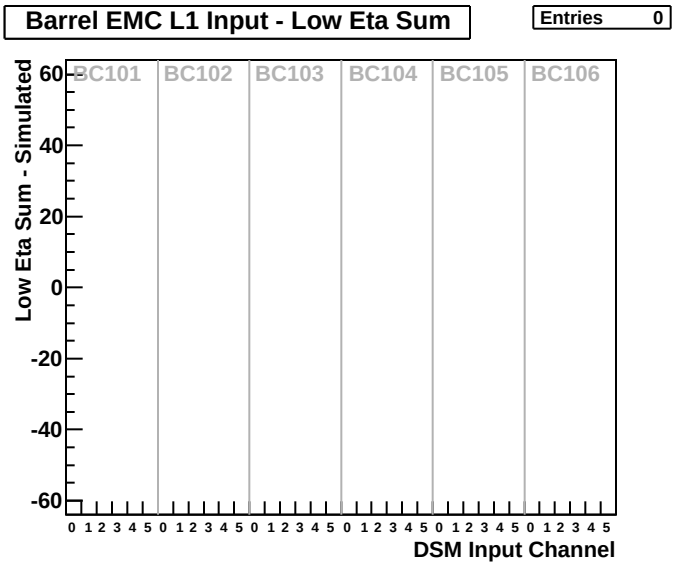
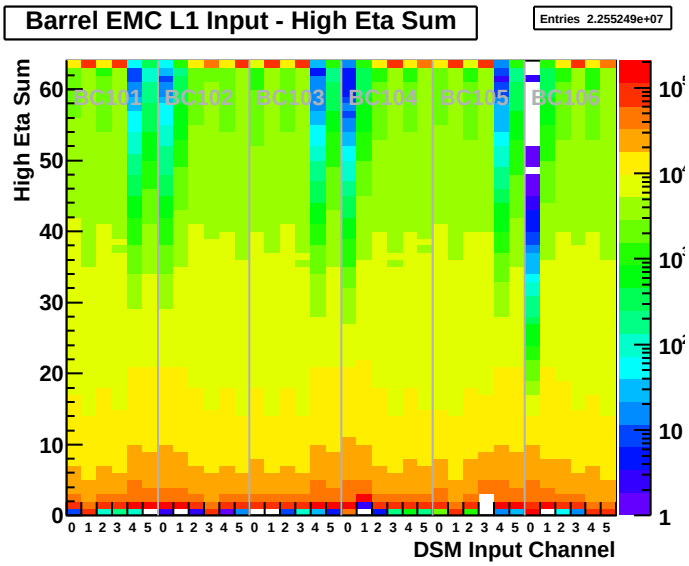
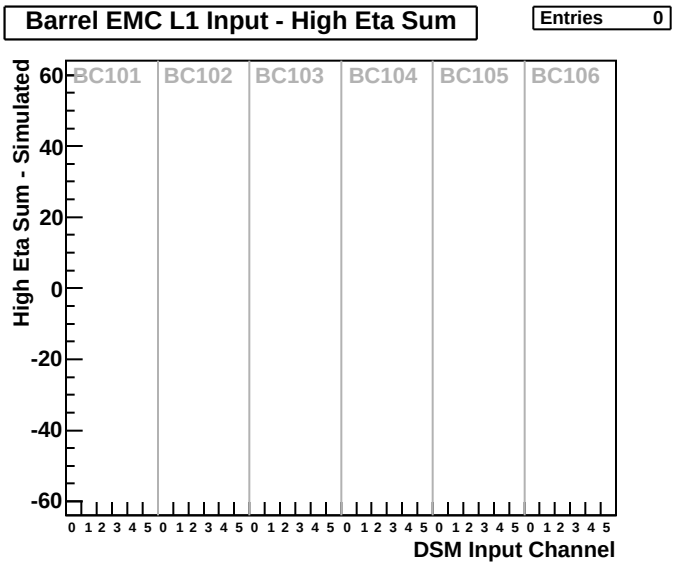
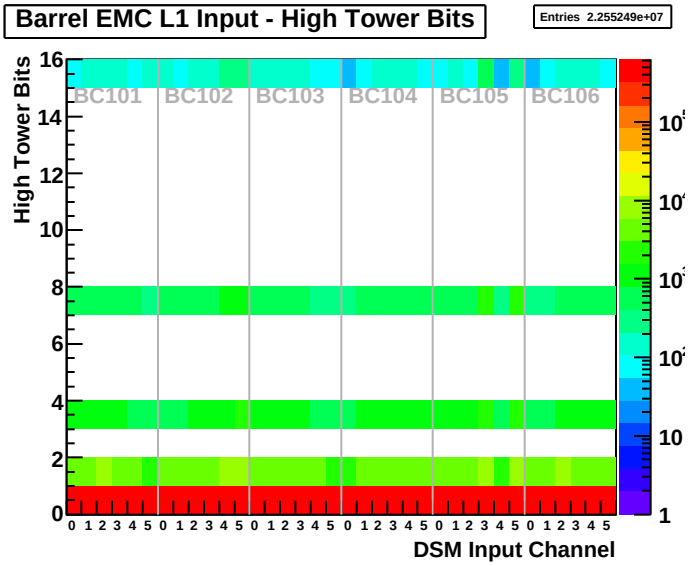
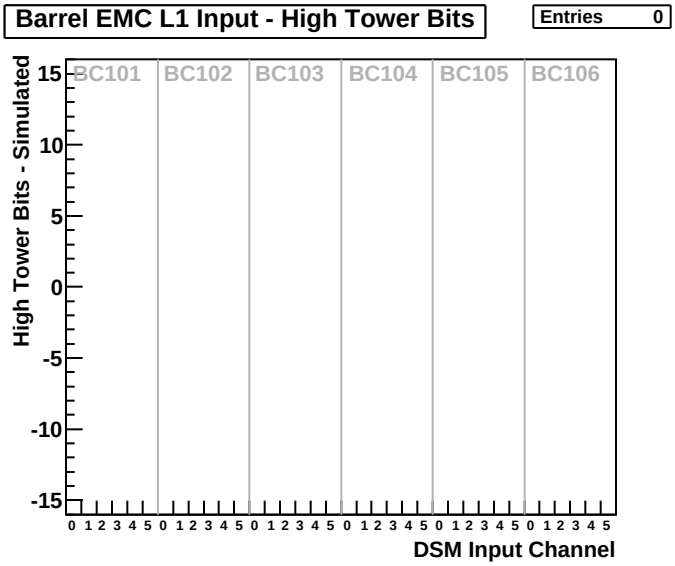

Barrel EMC L1 Input - Low Eta Sum
Entries 2.255249e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

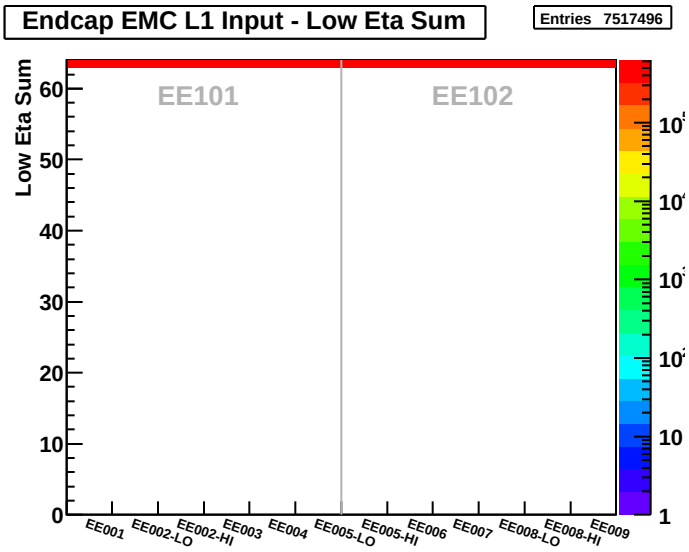

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

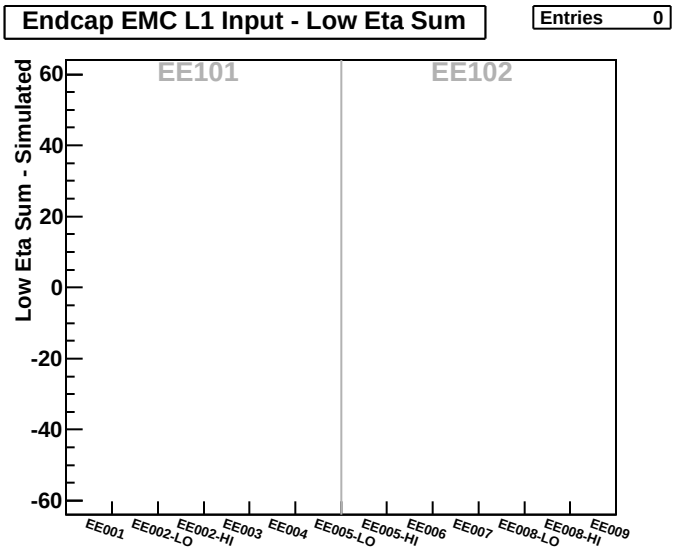

Barrel EMC L1 Input - High Eta Sum
Entries 2.255249e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

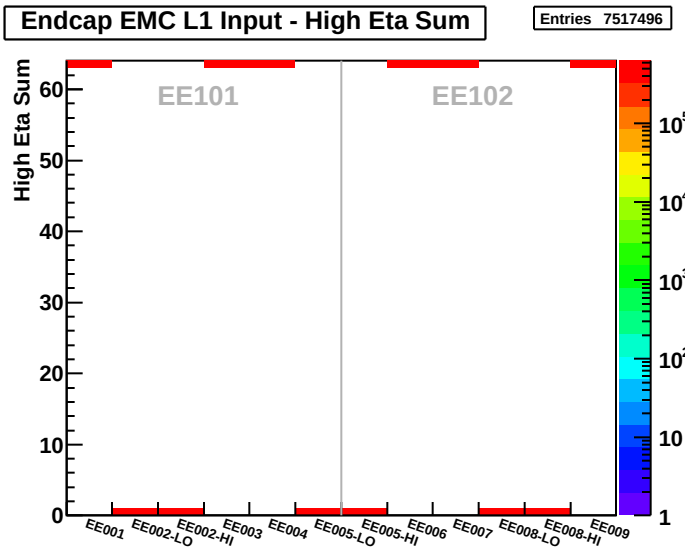

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

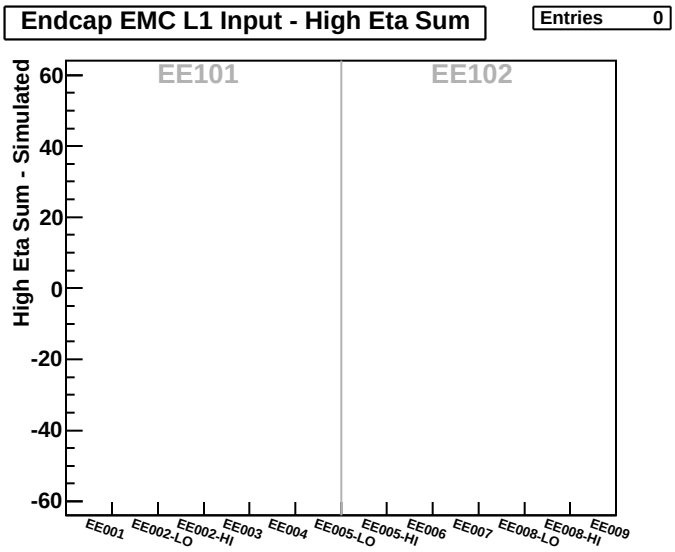

Barrel EMC L1 Input - High Tower Bits
Entries 2.255249e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

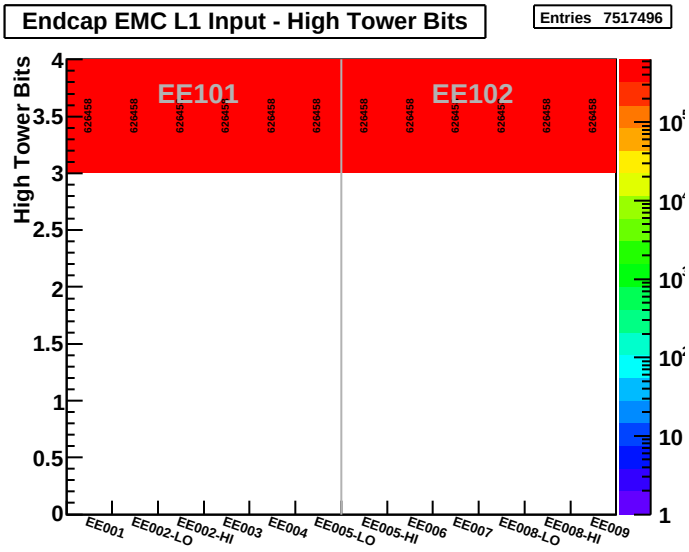

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

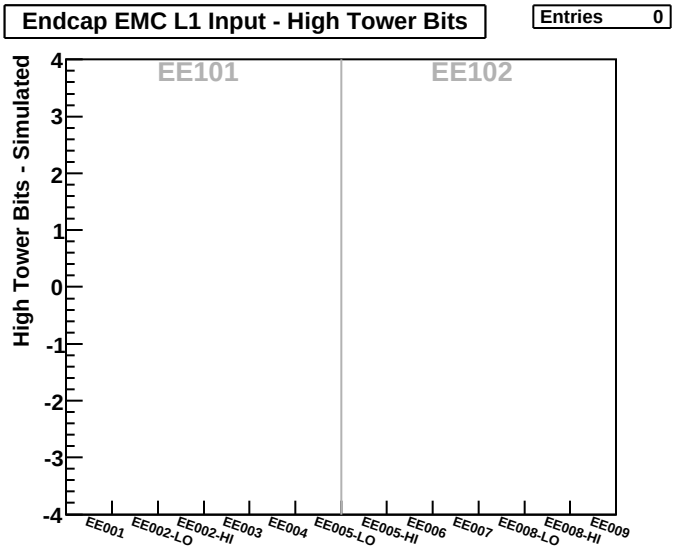

Endcap EMC L1 Input - Low Eta Sum
Entries 7517496
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

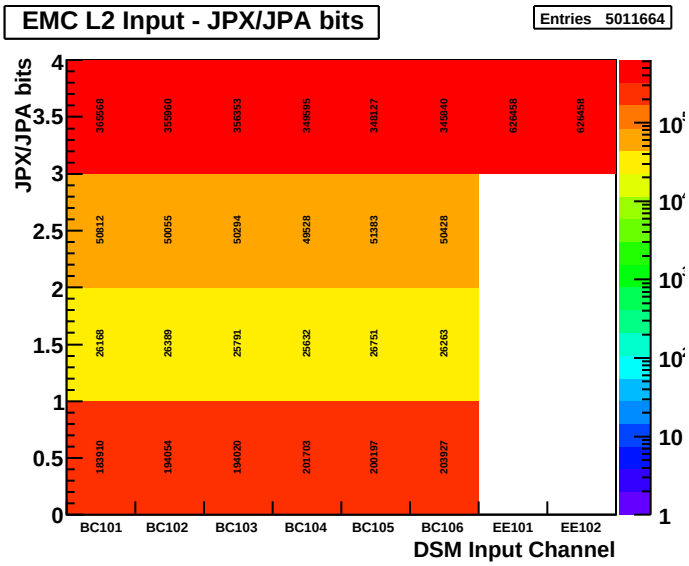

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

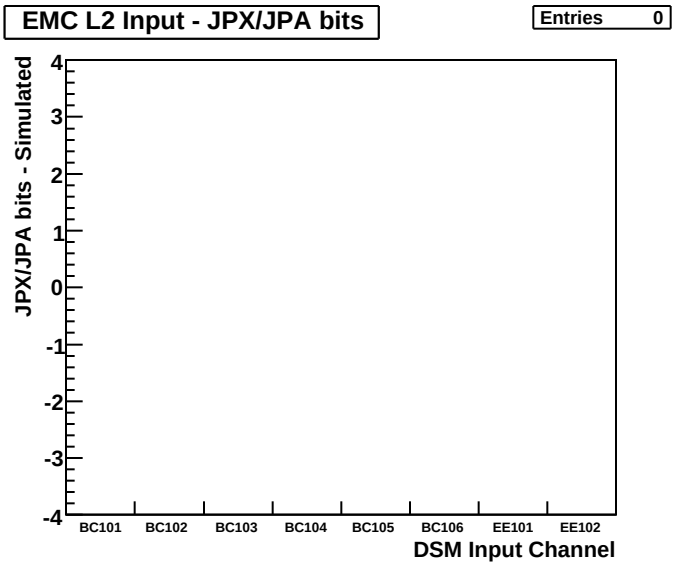

Endcap EMC L1 Input - High Eta Sum
Entries 7517496
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

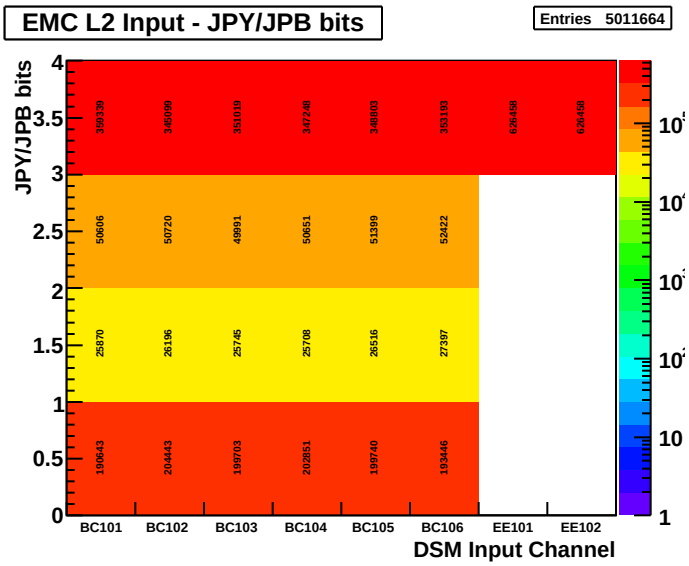

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

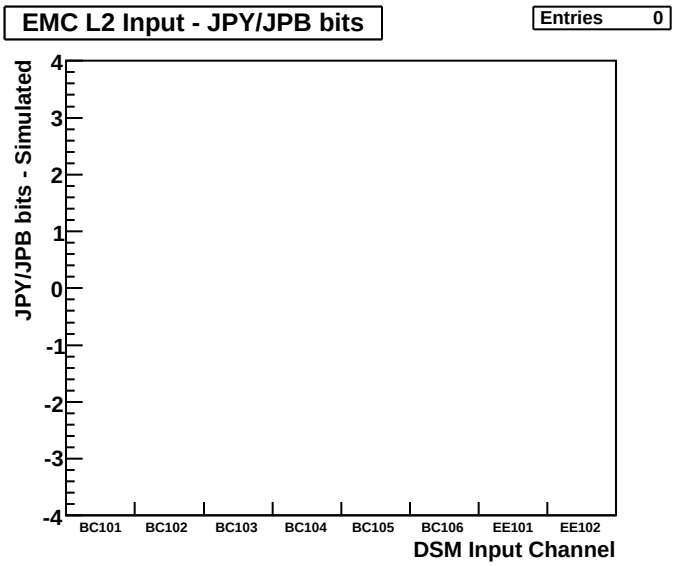

Endcap EMC L1 Input - High Tower Bits
Entries 7517496
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

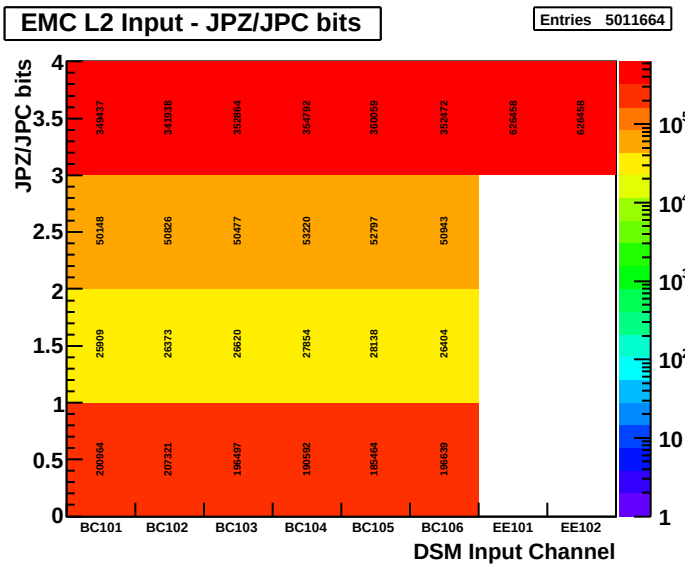

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

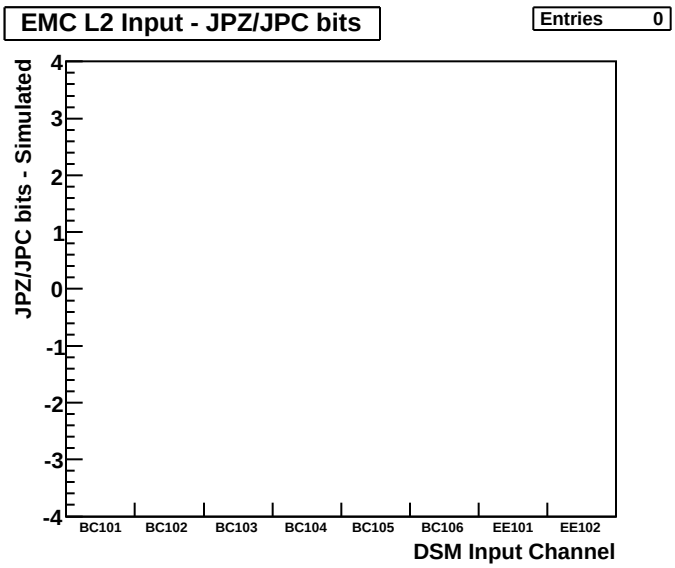

EMC L2 Input - JPX/JPA bits
Entries 5011664
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
365568 359860 356353 349595 348127 345840 626458 626458
50612 50055 50294 49528 51383 50428
26168 26389 25791 25632 26751 26263
183910 194054 194020 201703 200197 203927

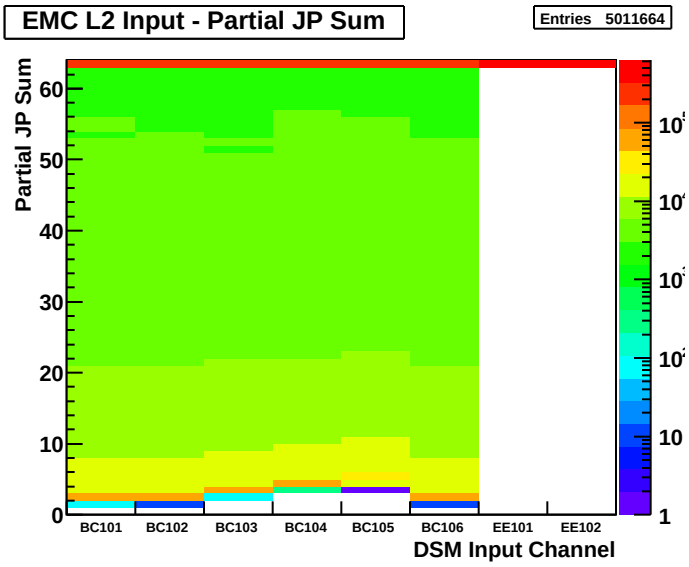

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

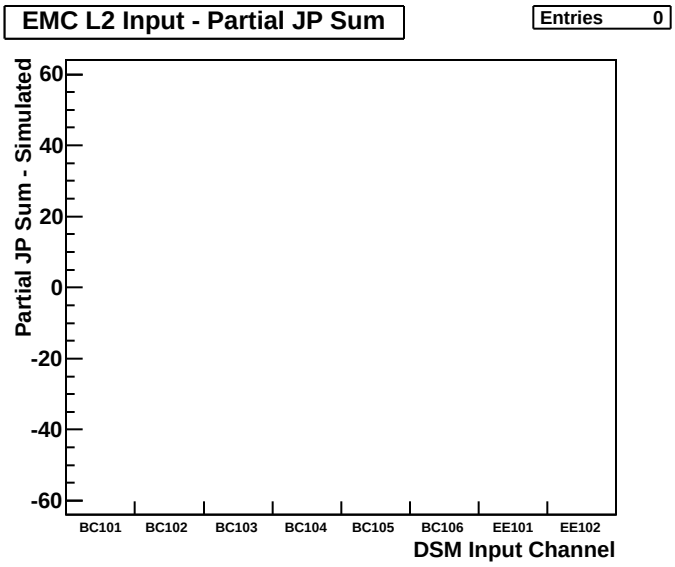

EMC L2 Input - JPY/JPB bits
Entries 5011664
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
359339 345099 351019 347248 348803 353193 626458 626458
50606 50720 49991 50651 51399 52422
25870 26196 25745 25708 26516 27397
190643 204443 199703 202851 199740 193446

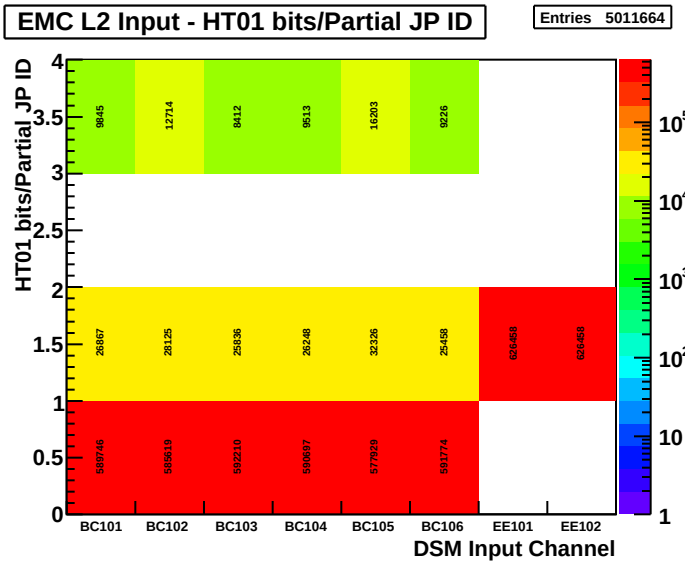

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

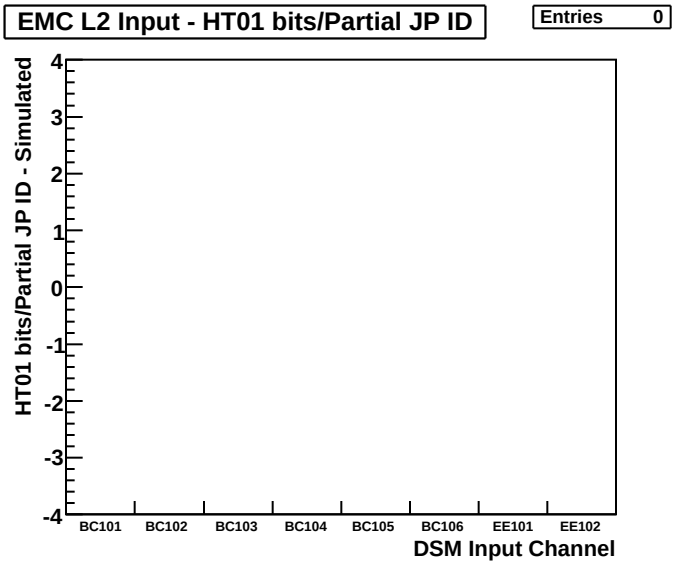

EMC L2 Input - JPZ/JPC bits
Entries 5011664
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
349437 349938 352864 354792 360059 352472 626458 626458
50148 50826 50477 53220 52797 50943
25909 26373 26620 27854 28138 26404
200964 207321 196497 190592 185464 196639

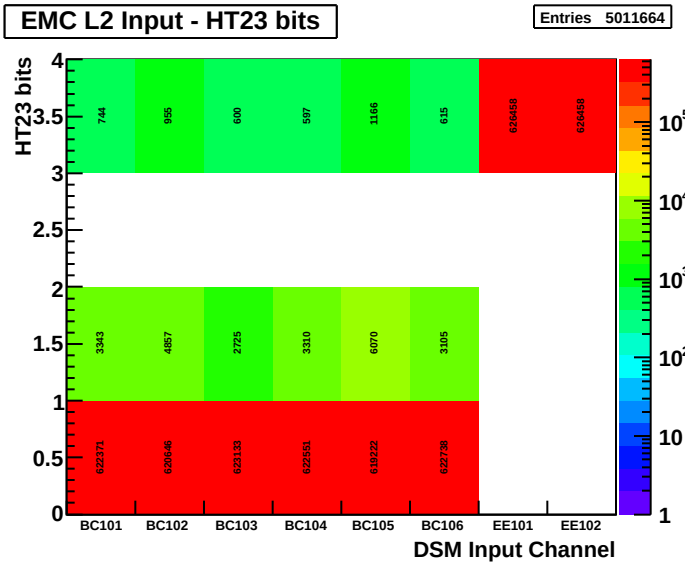

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

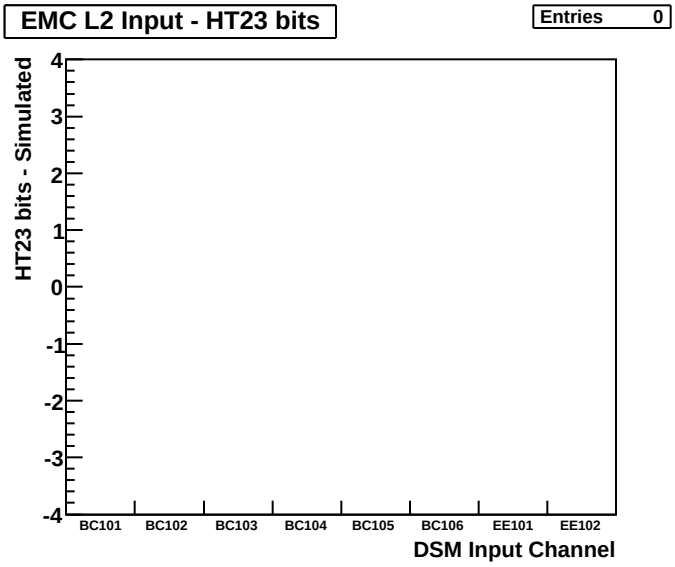

EMC L2 Input - Partial JP Sum
Entries 5011664
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

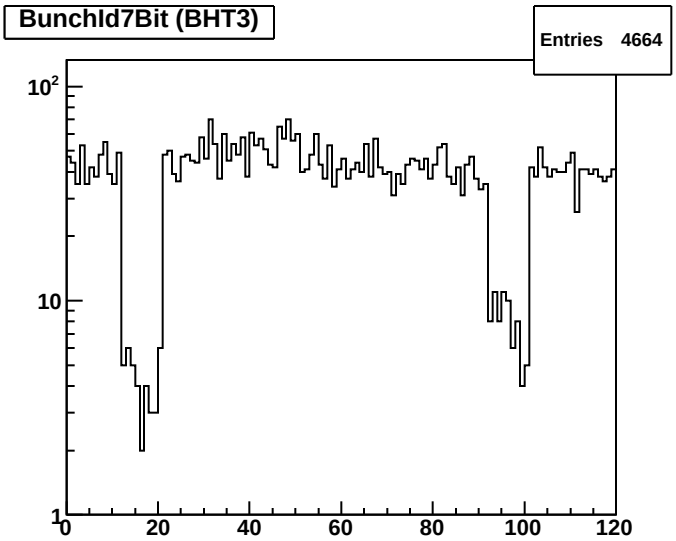

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

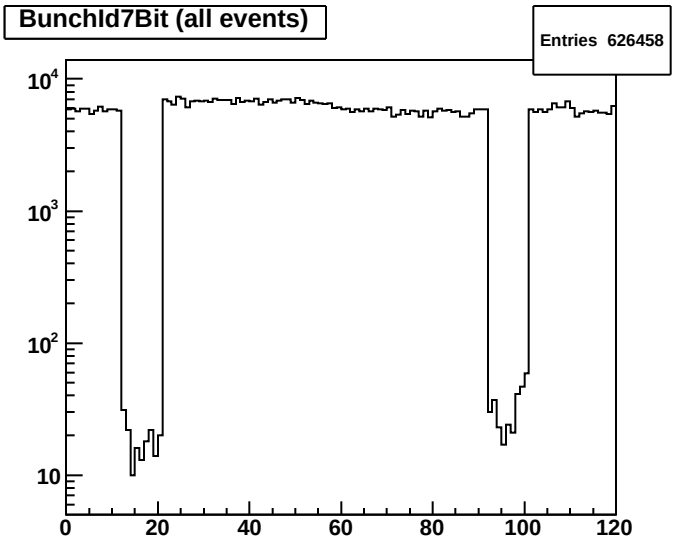

EMC L2 Input - HT01 bits/Partial JP ID
Entries 5011664
HT01 bits/Partial JP ID
9845 12714 8412 9513 16203 9226
26867 28125 25836 26248 32326 25458 626458 626458
589746 588619 592210 590697 577929 591774
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

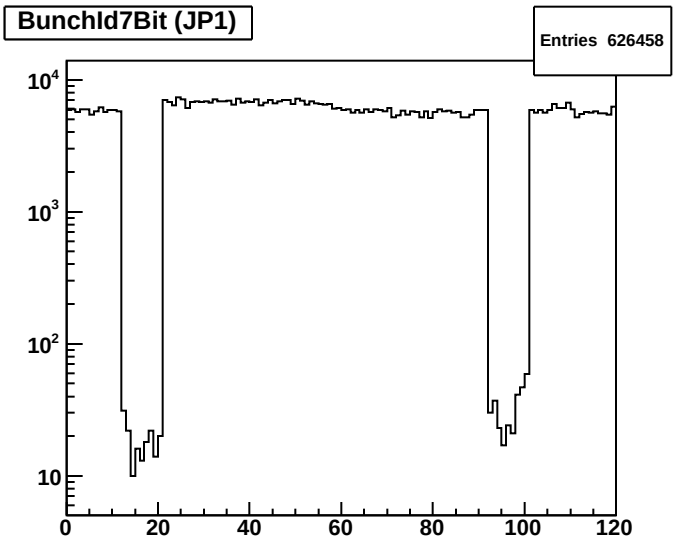

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

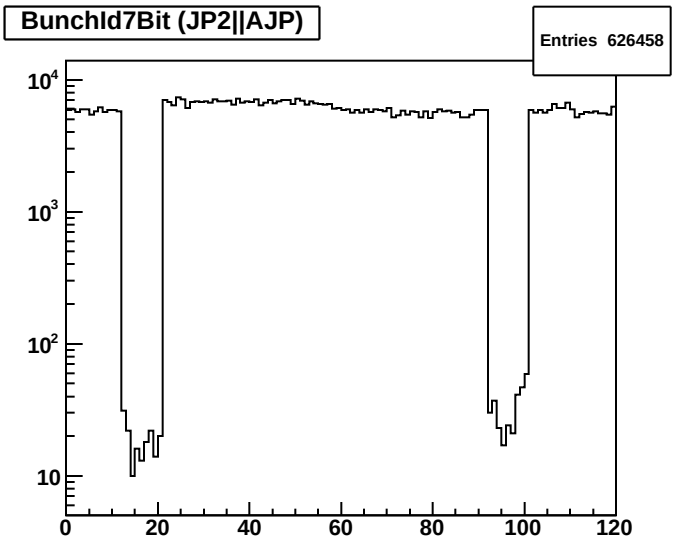

EMC L2 Input - HT23 bits
Entries 5011664
HT23 bits
744 955 600 597 1166 615 626458 626458
3343 4857 2725 3310 6070 3105
622371 620646 623133 622551 619222 622738
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

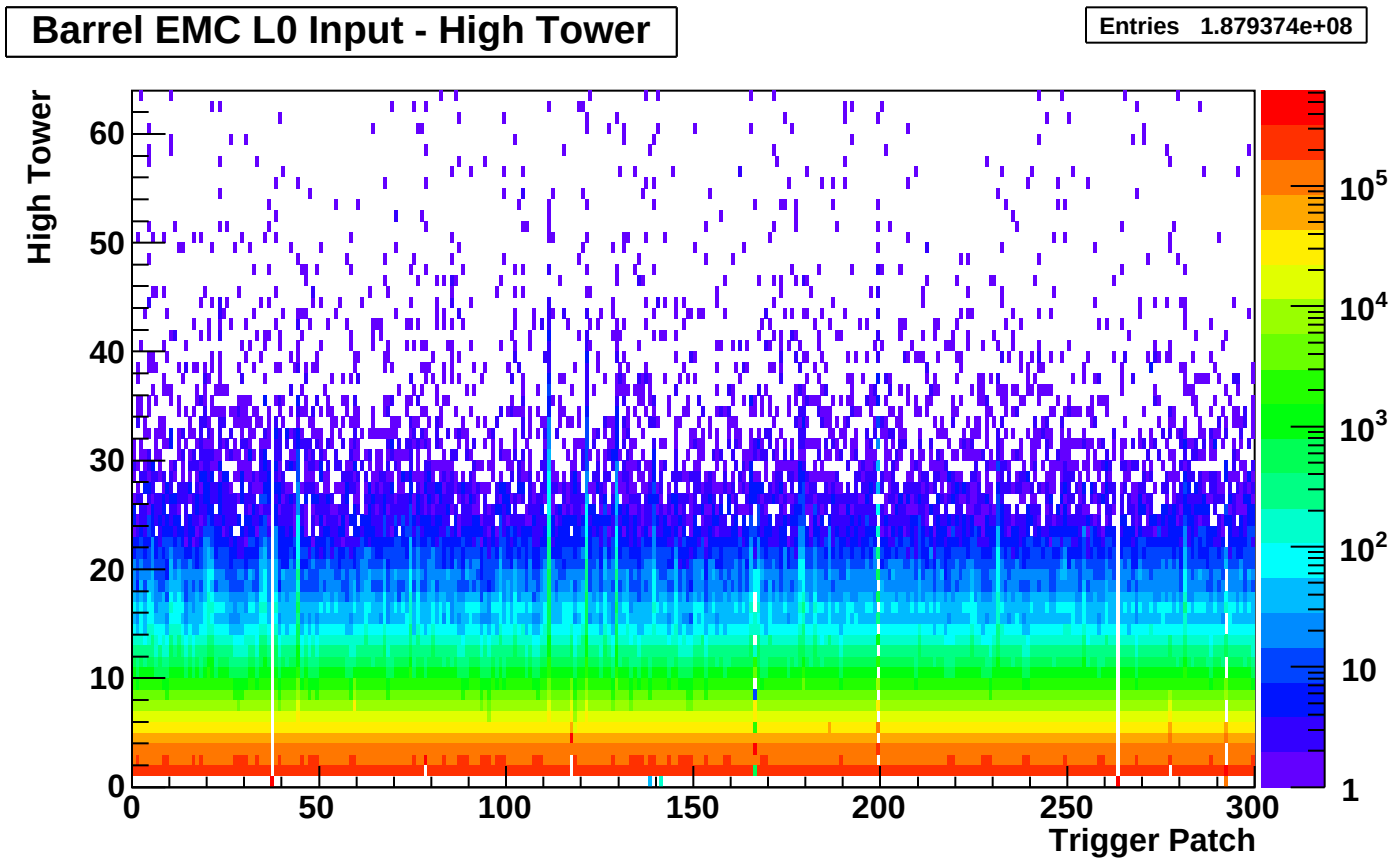

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

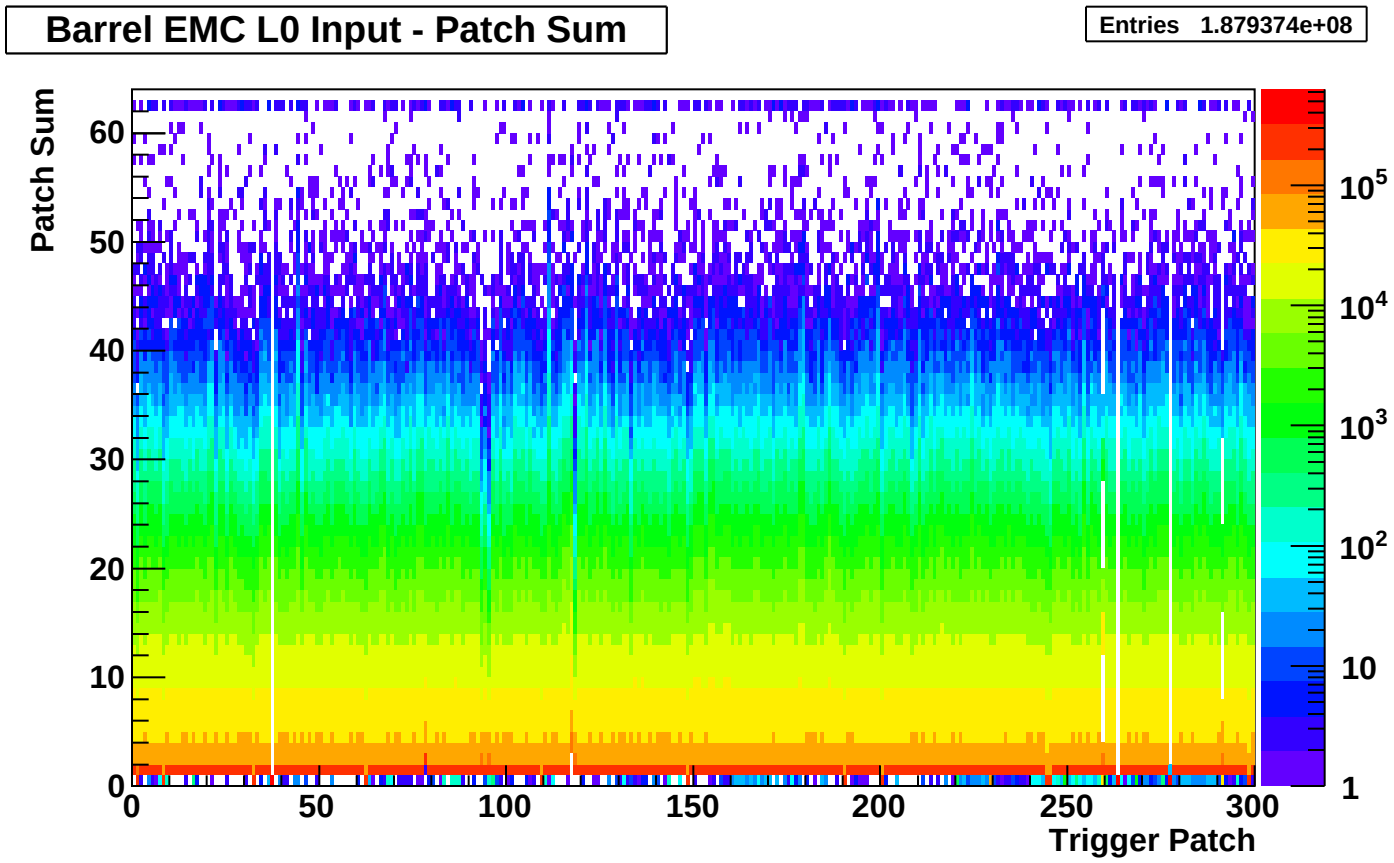

BunchId7Bit (BHT3)
Entries 4664
10^2
10
1
0
20
40
60
80
100
120

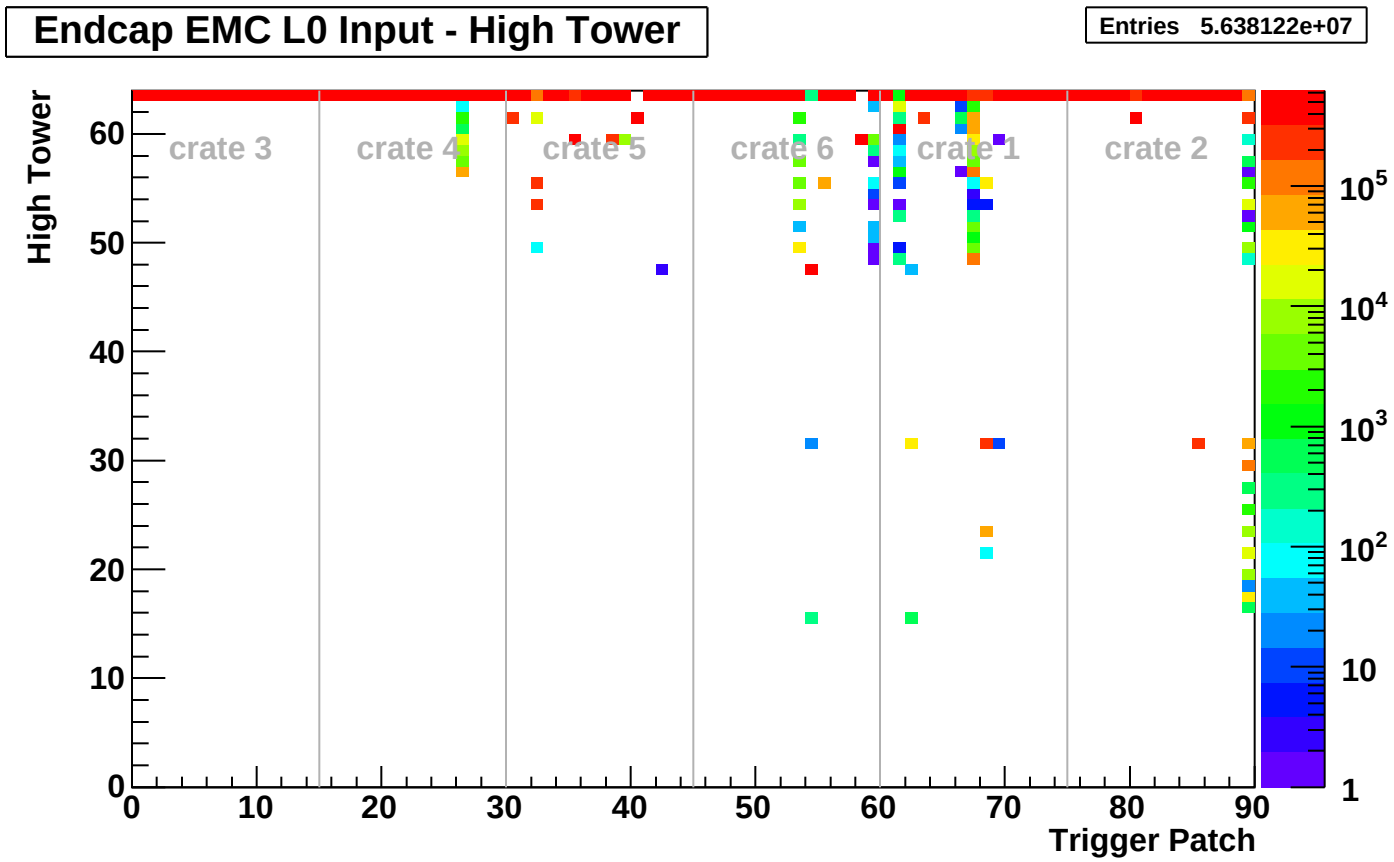

BunchId7Bit (all events)
Entries 626458
10^4
10^3
10^2
10
0
20
40
60
80
100
120

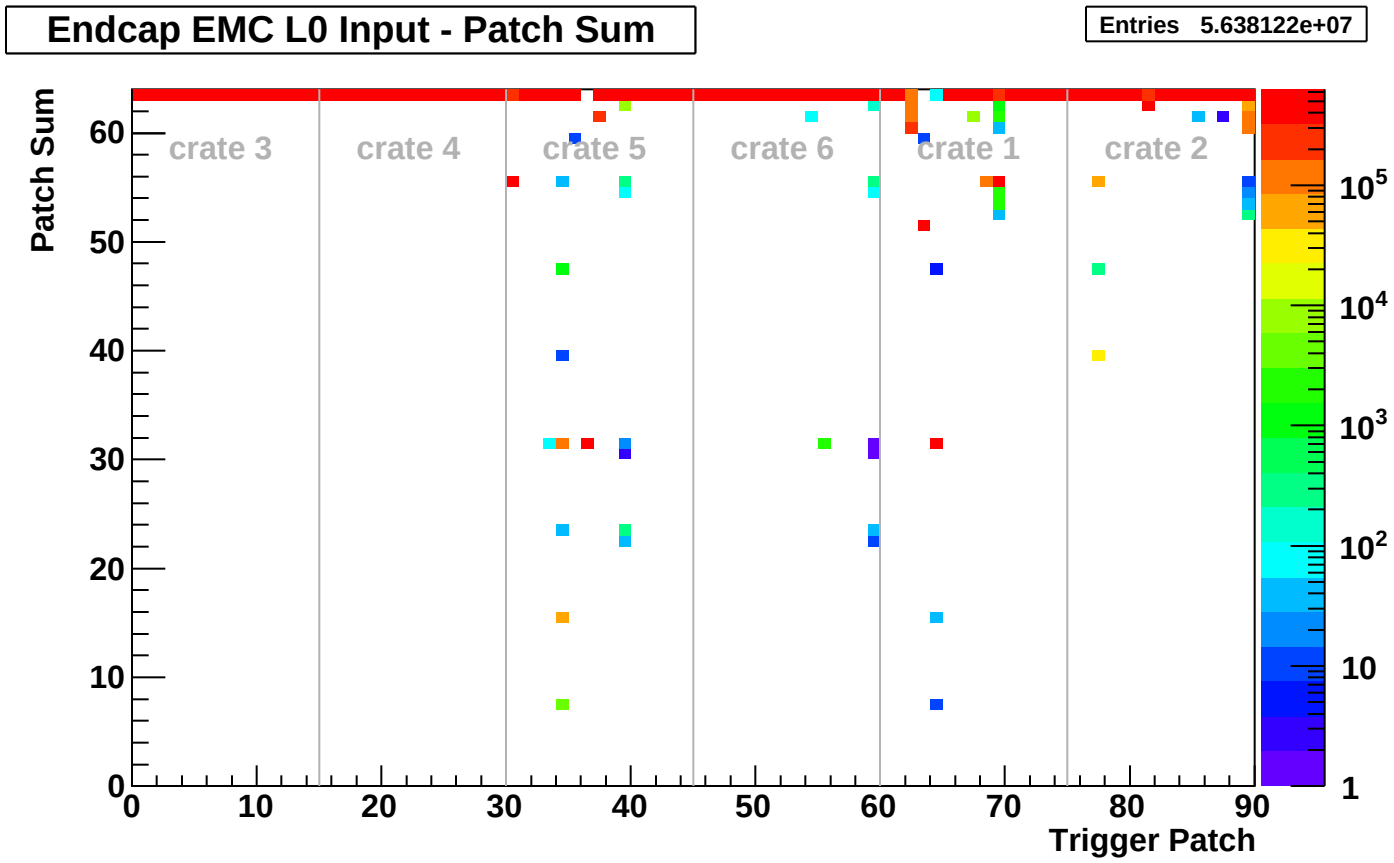

BunchId7Bit (JP1)
Entries 626458
10^4
10^3
10^2
10
0
20
40
60
80
100
120

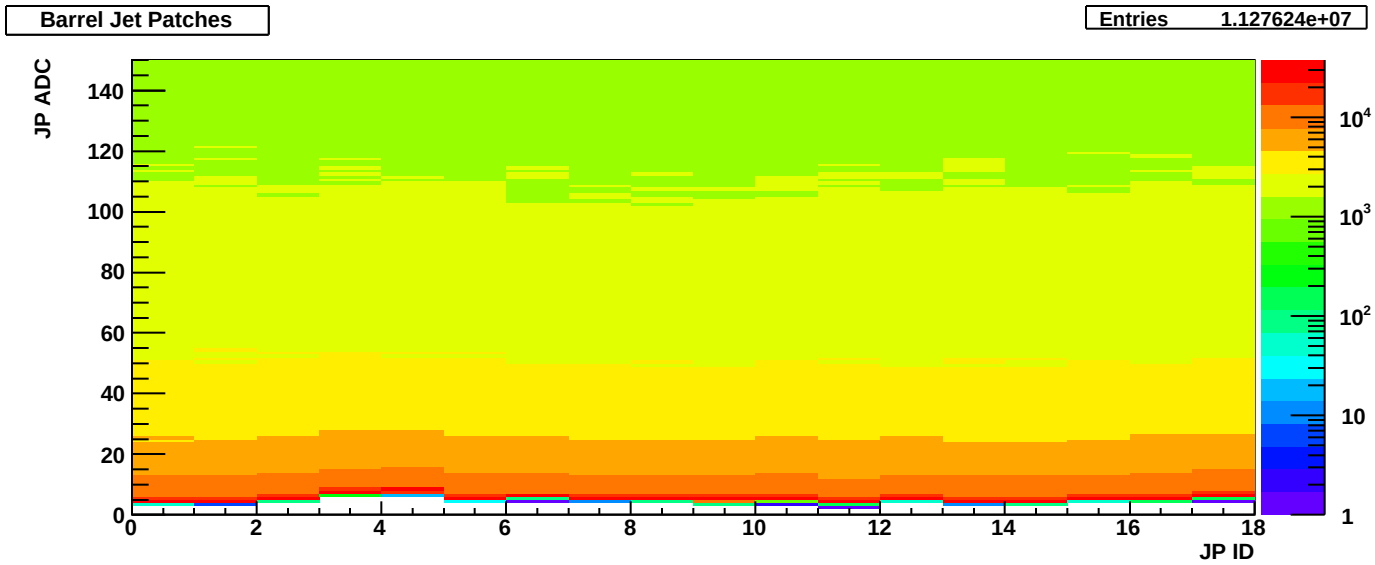

BunchId7Bit (JP2||AJP)
Entries 626458
10^4
10^3
10^2
10
0
20
40
60
80
100
120

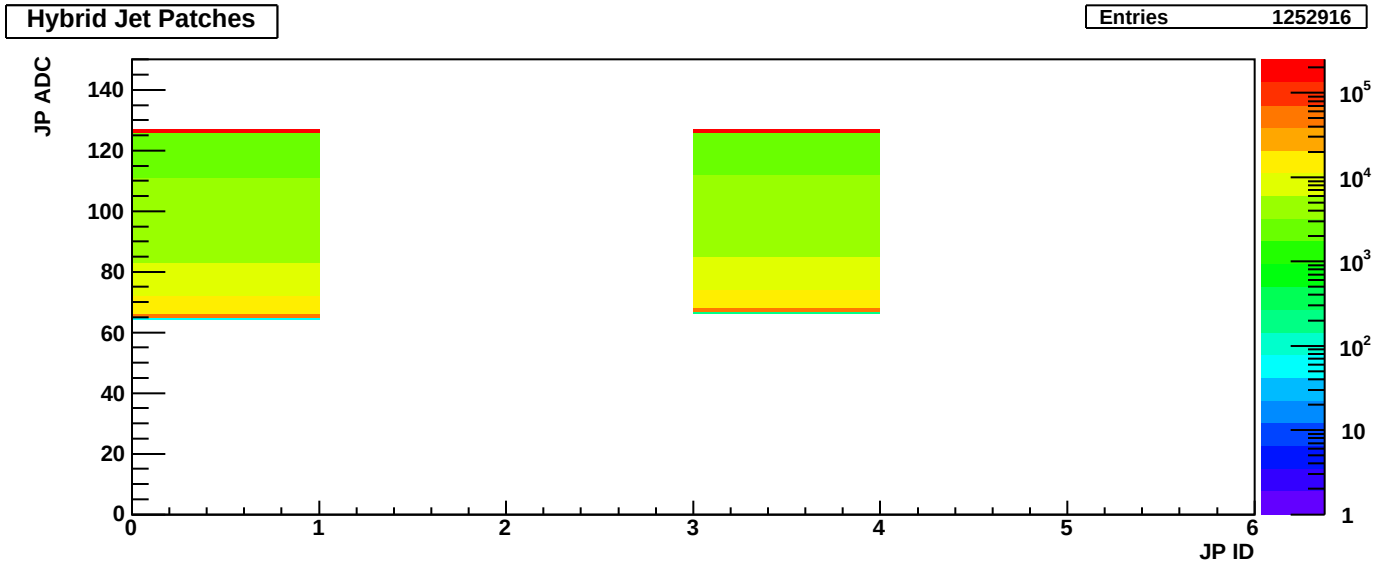

Barrel EMC L0 Input - High Tower
Entries 1.879374e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

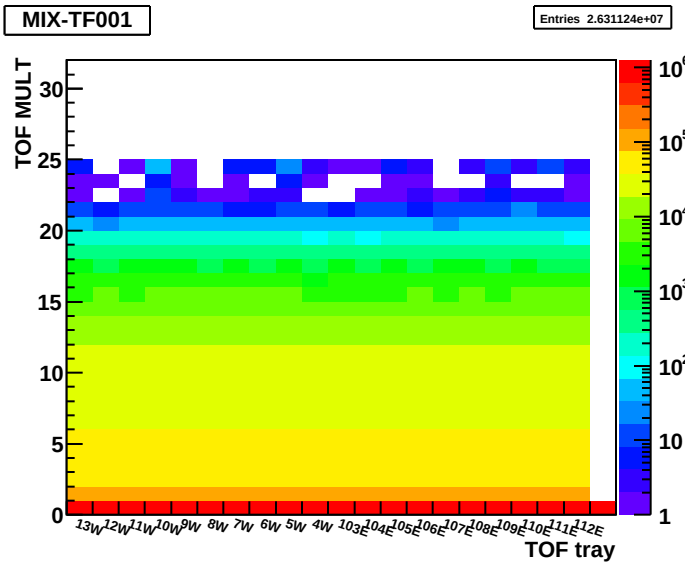

Barrel EMC L0 Input - Patch Sum
Entries 1.879374e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

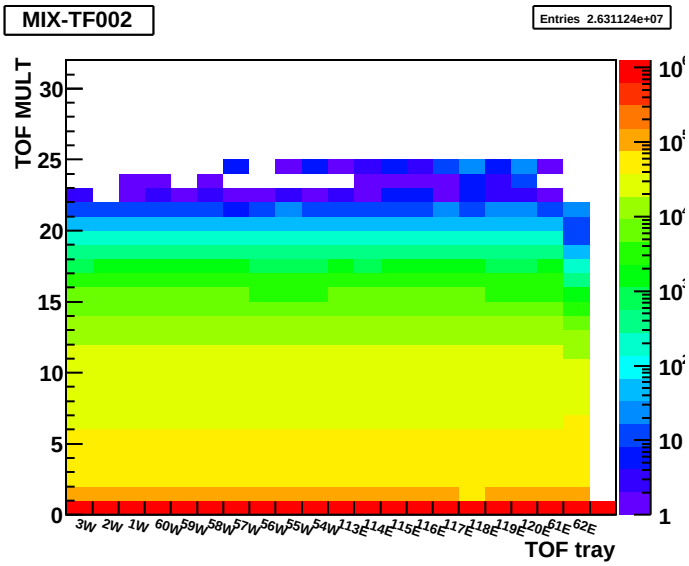

Endcap EMC L0 Input - High Tower
Entries 5.638122e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

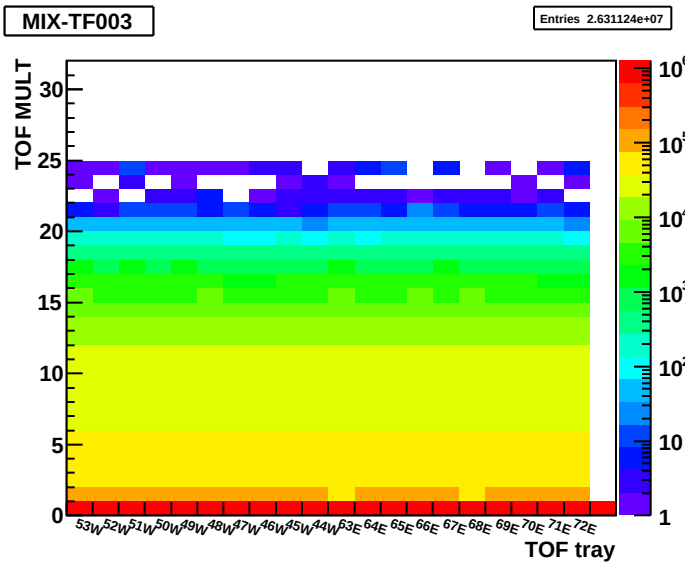

Endcap EMC L0 Input - Patch Sum
Entries 5.638122e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

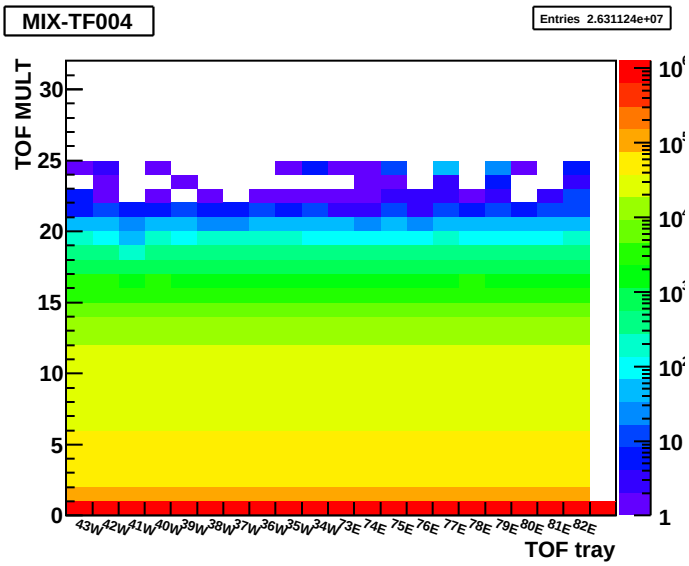

Barrel Jet Patches
Entries 1.127624e+07
JP ADC
JP ID

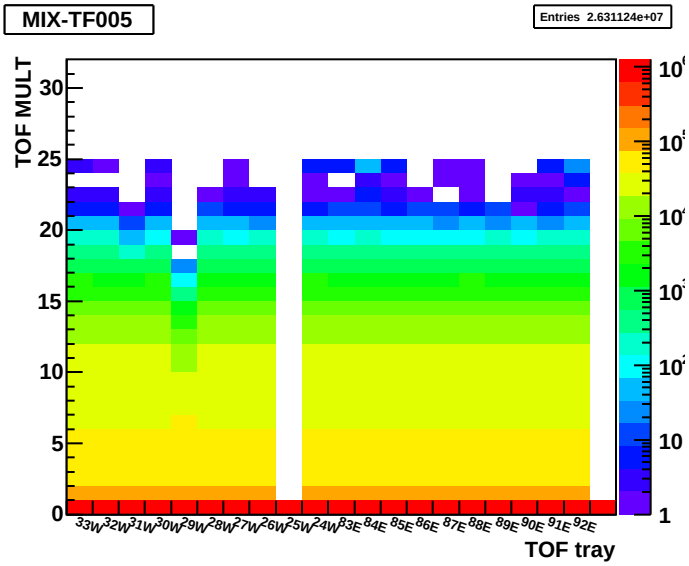

Hybrid Jet Patches
Entries 1252916
JP ADC
JP ID

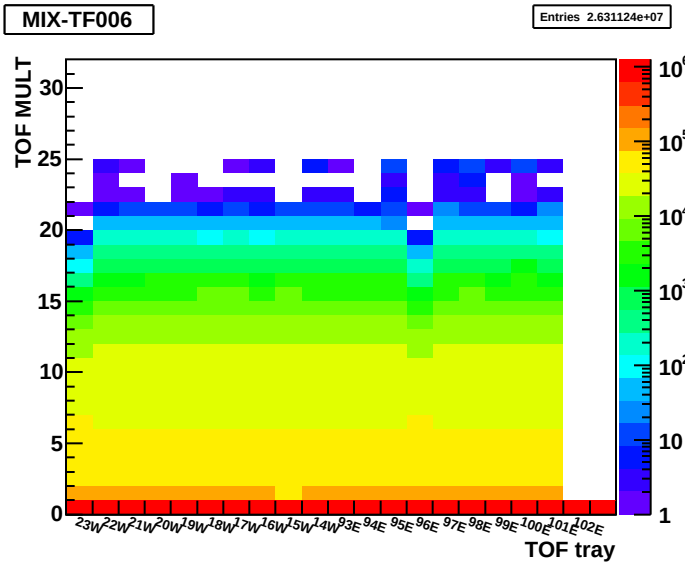

MIX-TF001
Entries 2.631124e+07
TOF MULT
TOF tray

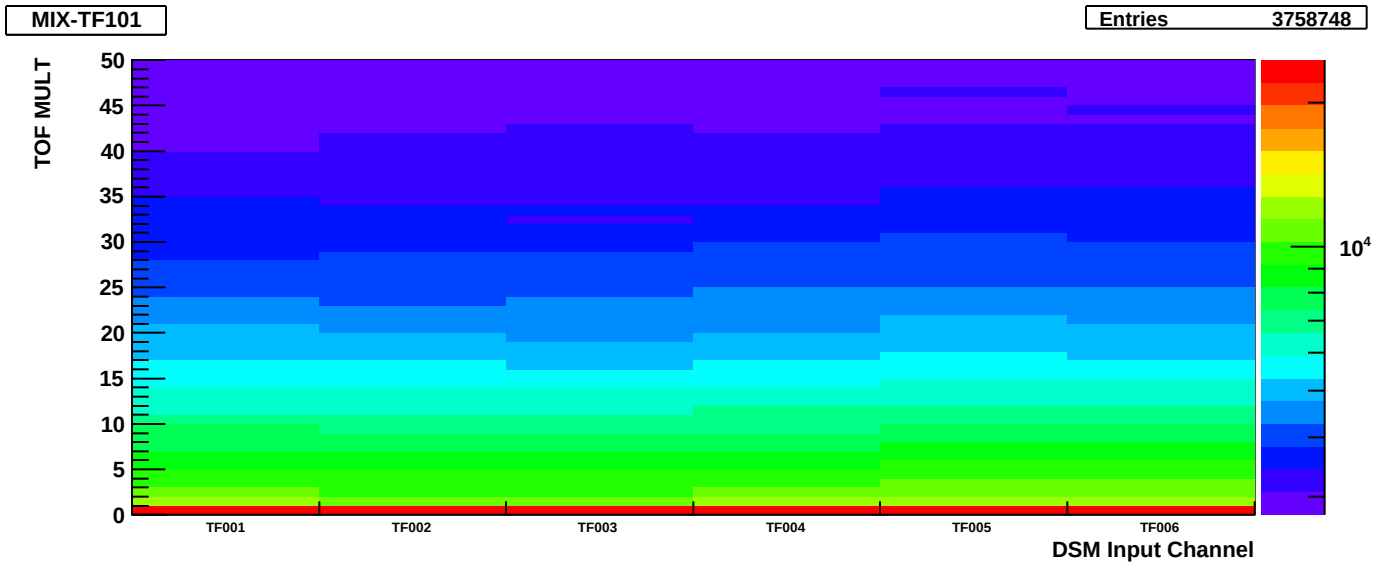

MIX-TF002
Entries 2.631124e+07
TOF MULT
TOF tray

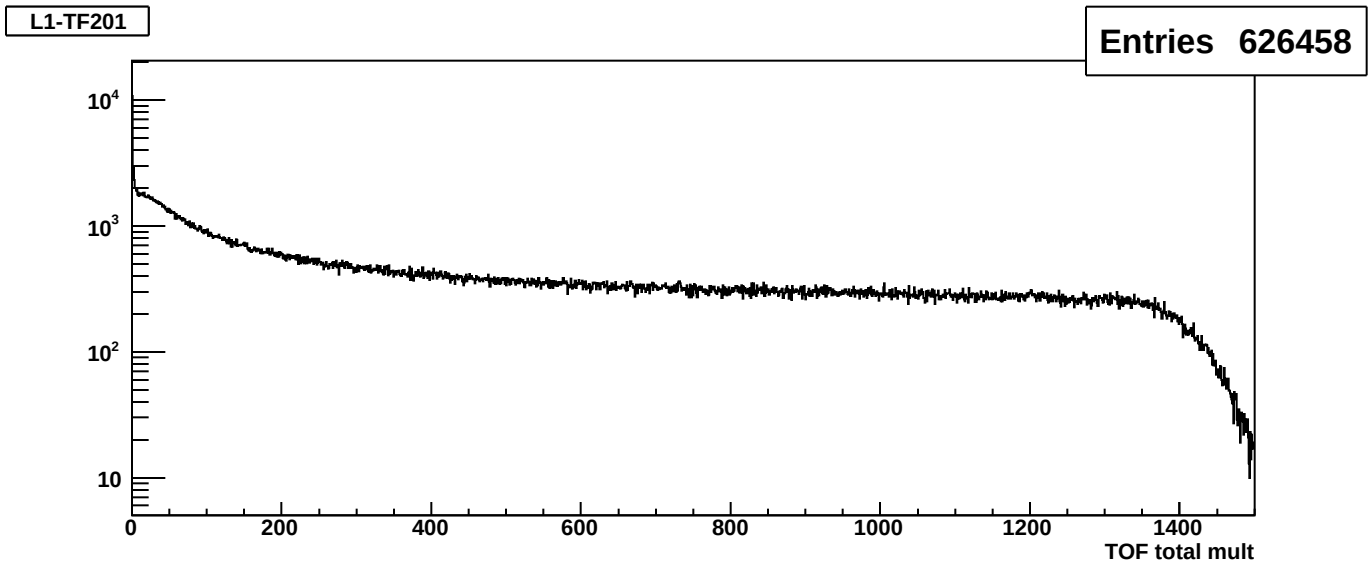

MIX-TF003
Entries 2.631124e+07
TOF MULT
TOF tray

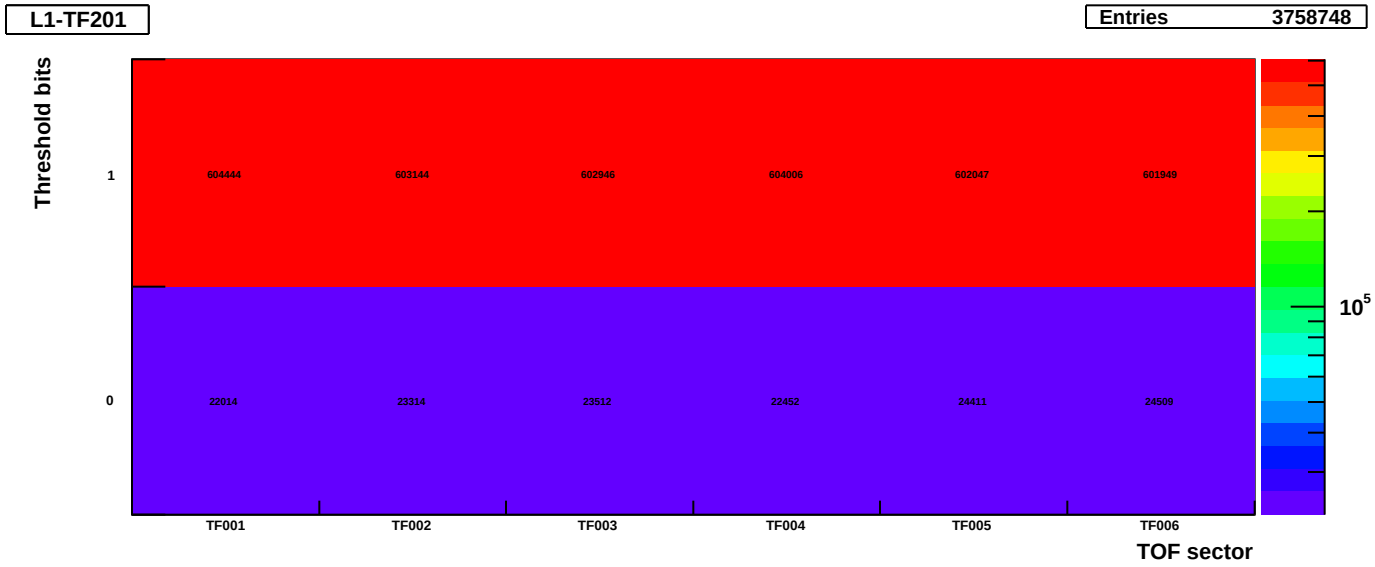

MIX-TF004
Entries 2.631124e+07
TOF MULT
TOF tray

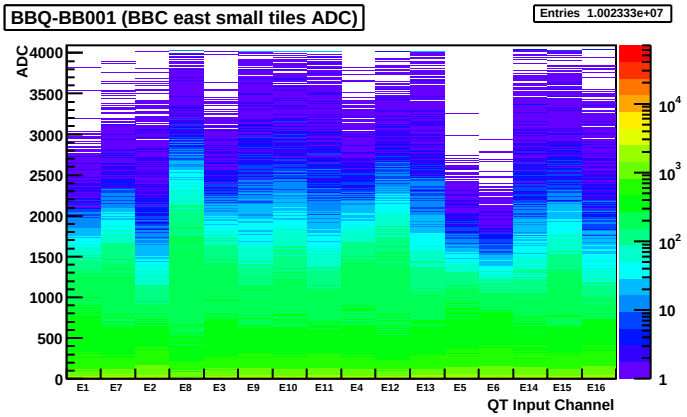

MIX-TF005
Entries 2.631124e+07
TOF MULT
TOF tray

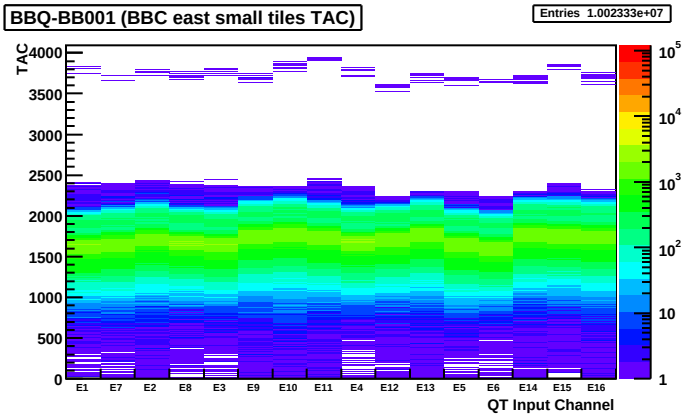

MIX-TF006
Entries 2.631124e+07
TOF MULT
TOF tray

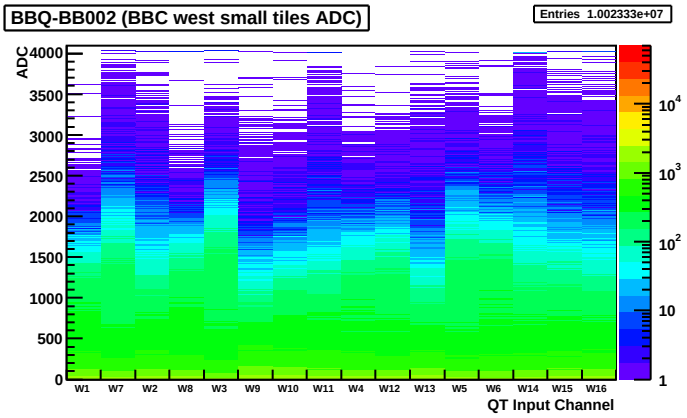

MIX-TF101
Entries 3758748
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

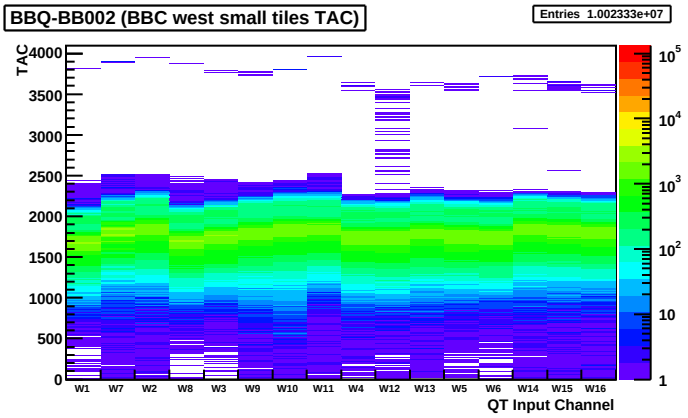

L1-TF201
Entries 626458
10^4
10^3
10^2
10
0
200
400
600
800
1000
1200
1400
TOF total mult

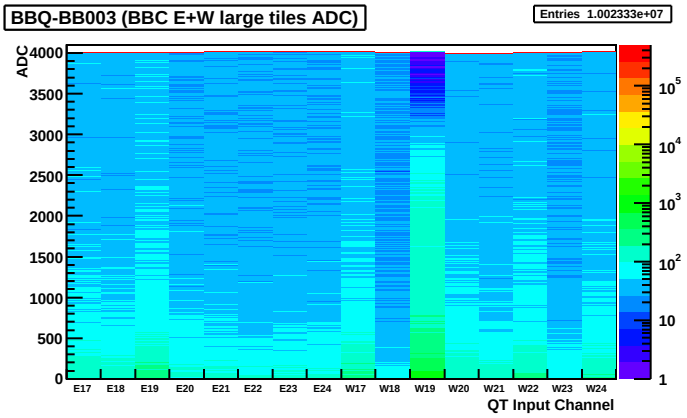

L1-TF201
Entries 3758748
Threshold bits
1
0
604444
603144
602946
604006
602047
601949
22014
23314
23512
22452
24411
24509
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

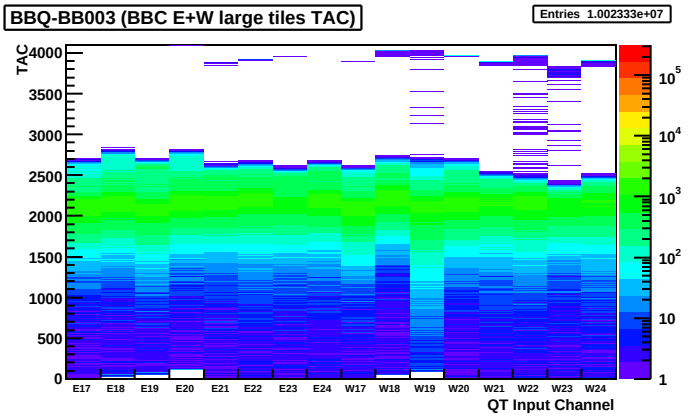

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.002333e+07
ADC
QT Input Channel

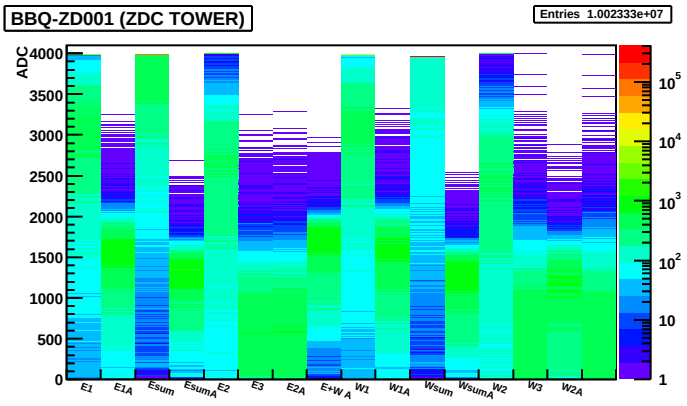

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.002333e+07
TAC
QT Input Channel

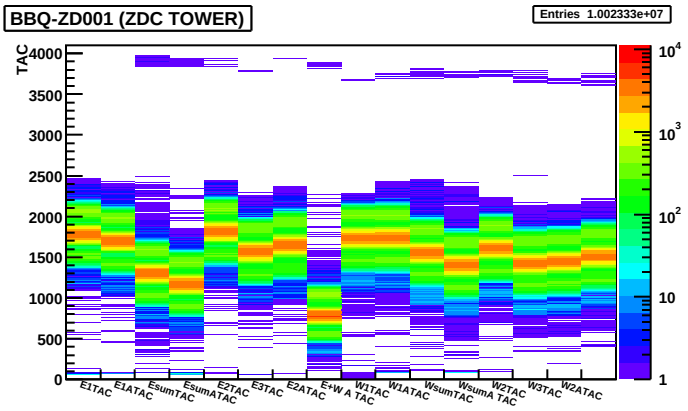

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.002333e+07
ADC
QT Input Channel

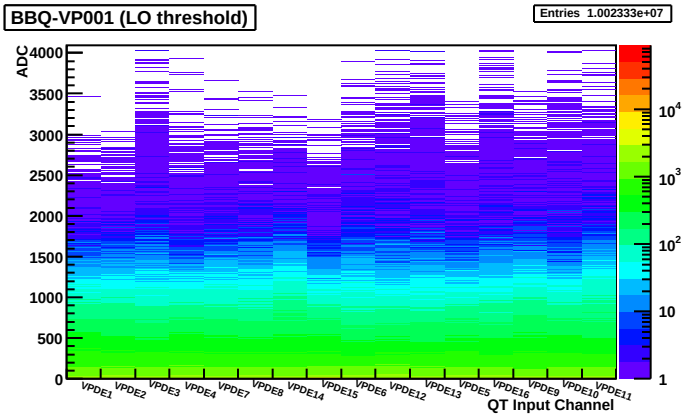

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.002333e+07
TAC
QT Input Channel

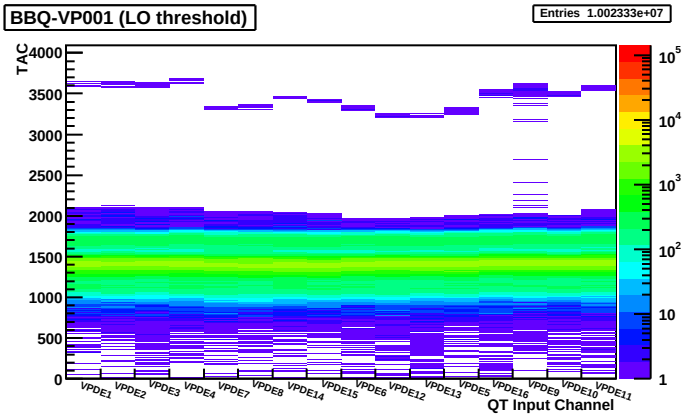

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.002333e+07
ADC
QT Input Channel

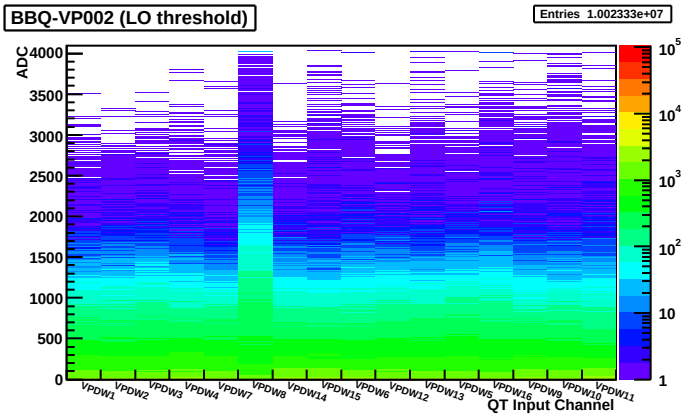

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.002333e+07
TAC
QT Input Channel

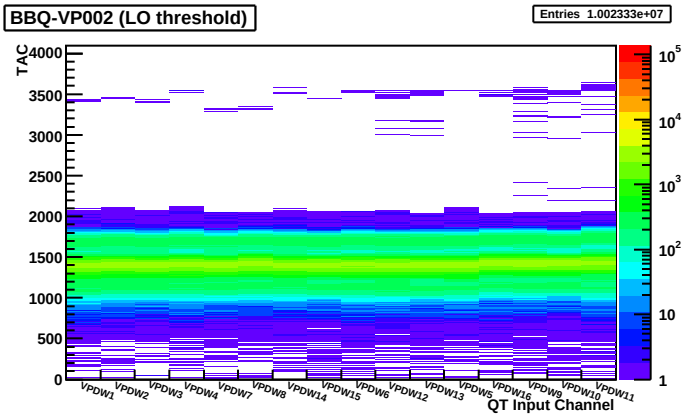

BBQ-ZD001 (ZDC TOWER)
Entries 1.002333e+07
ADC

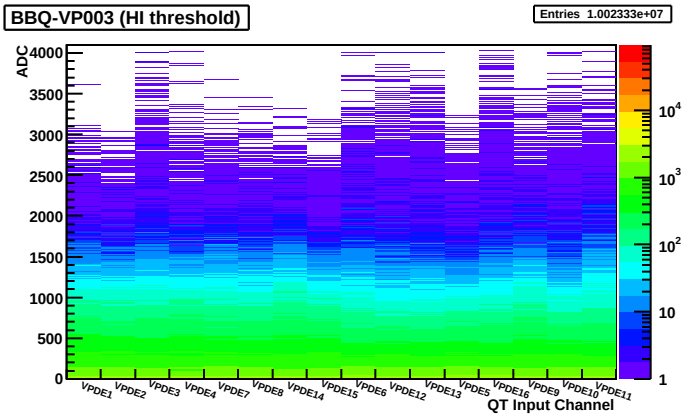

BBQ-ZD001 (ZDC TOWER)
Entries 1.002333e+07
TAC

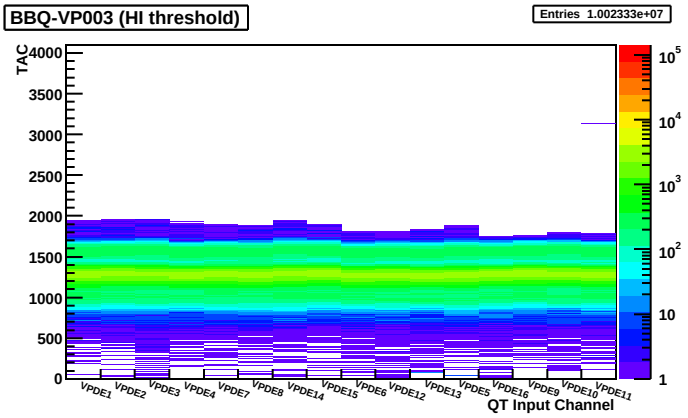

BBQ-VP001 (LO threshold)
Entries 1.002333e+07
ADC
QT Input Channel

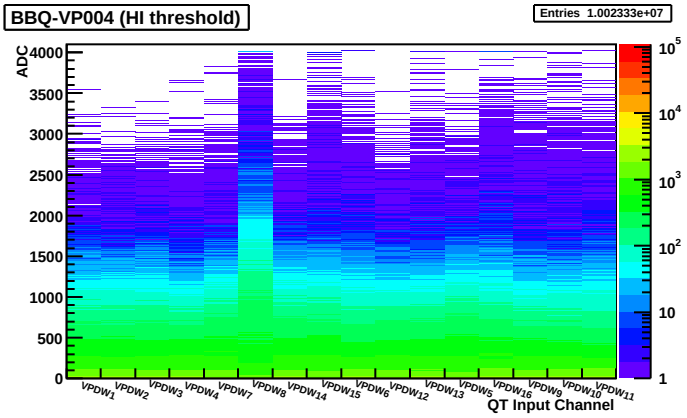

BBQ-VP001 (LO threshold)
Entries 1.002333e+07
TAC
QT Input Channel

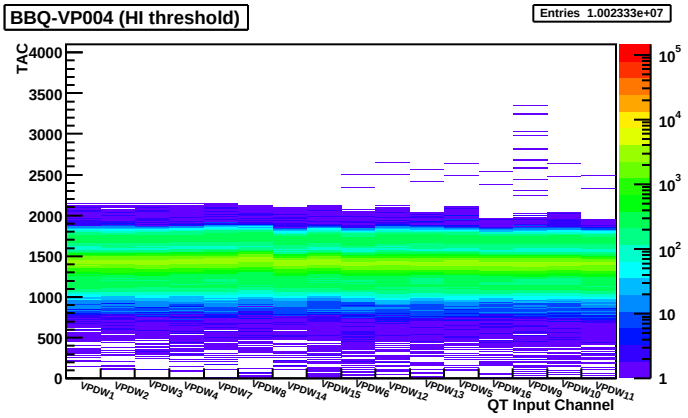

BBQ-VP002 (LO threshold)
Entries 1.002333e+07
ADC
QT Input Channel

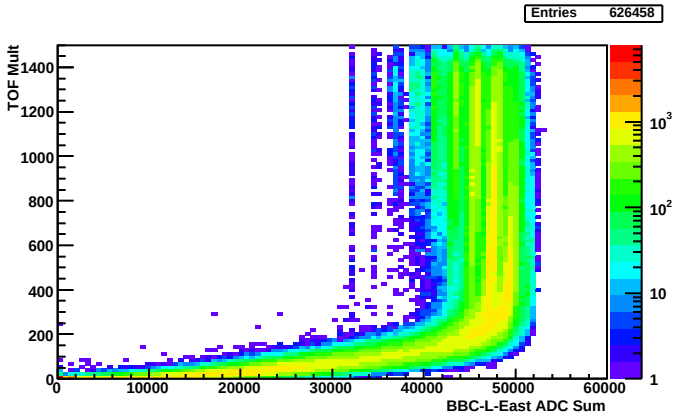

BBQ-VP002 (LO threshold)
Entries 1.002333e+07
TAC
QT Input Channel

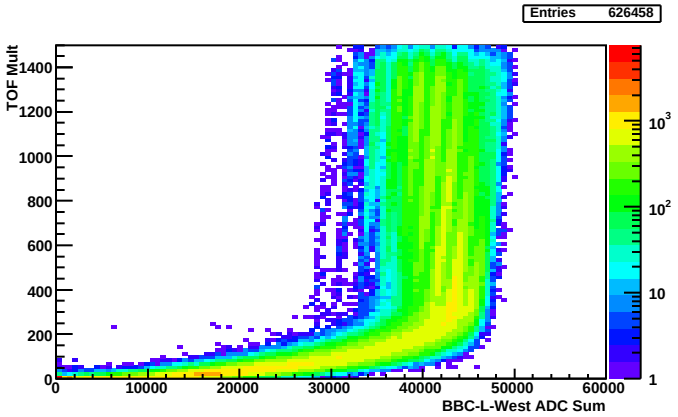

BBQ-VP003 (HI threshold)
Entries 1.002333e+07
ADC
QT Input Channel

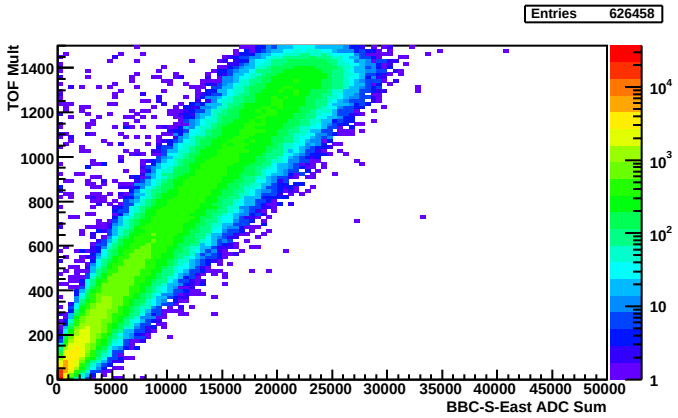

BBQ-VP003 (HI threshold)
Entries 1.002333e+07
TAC
QT Input Channel

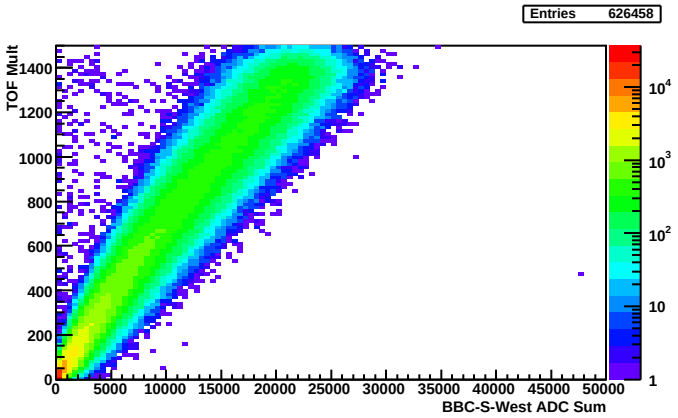

BBQ-VP004 (HI threshold)
Entries 1.002333e+07
ADC
QT Input Channel

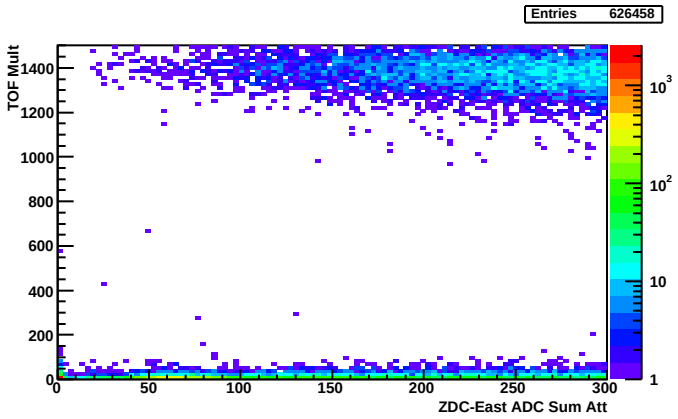

BBQ-VP004 (HI threshold)
Entries 1.002333e+07
TAC
QT Input Channel

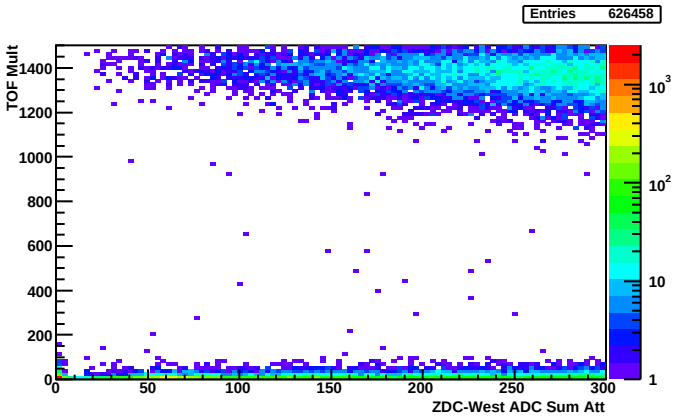

Entries 626458
TOF Mult
BBC-L-East ADC Sum

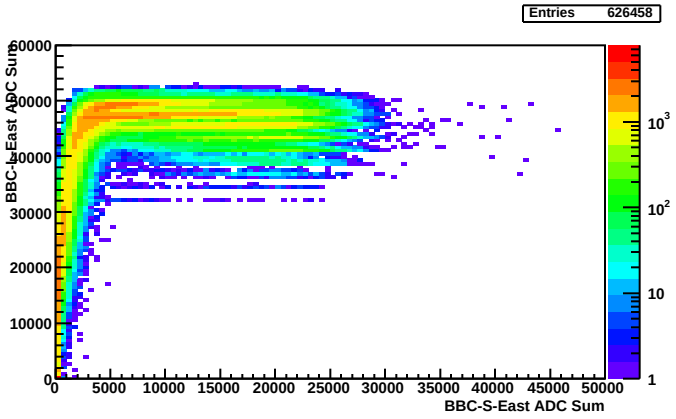

Entries 626458
TOF Mult
BBC-L-West ADC Sum

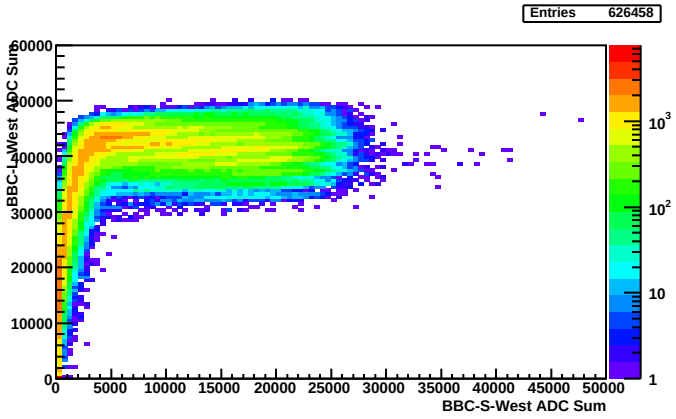

Entries 626458
TOF Mult
BBC-S-East ADC Sum

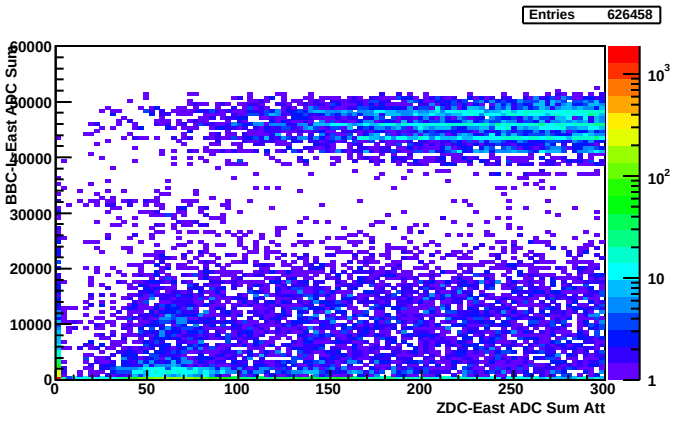

Entries 626458
TOF Mult
BBC-S-West ADC Sum

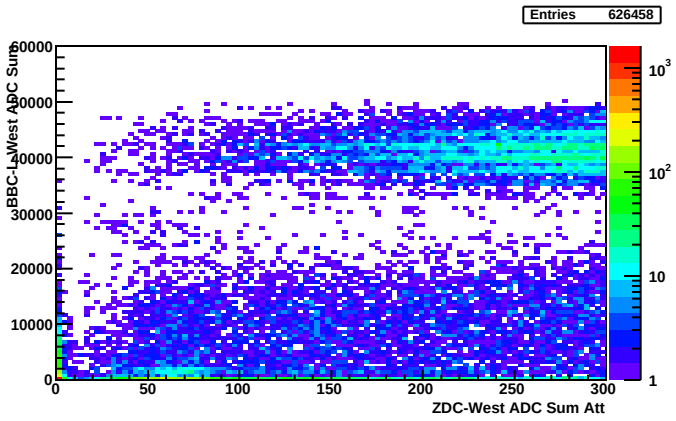

Entries 626458
TOF Mult
ZDC-East ADC Sum Att

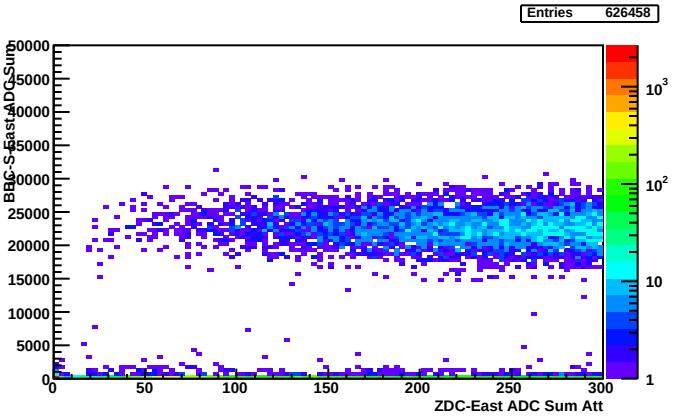

Entries 626458
TOF Mult
ZDC-West ADC Sum Att

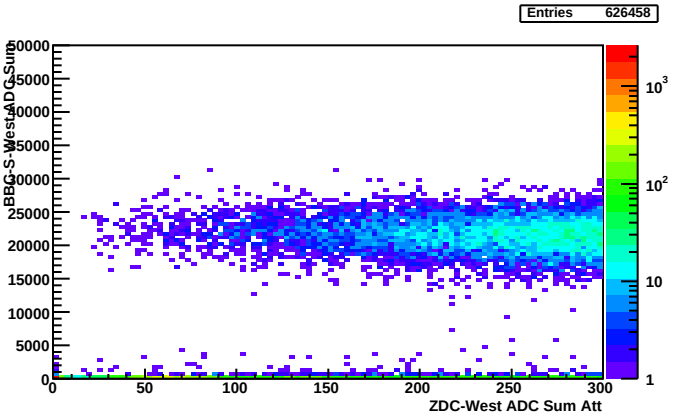

Entries 626458
BBC-L-East ADC Sum
BBC-S-East ADC Sum

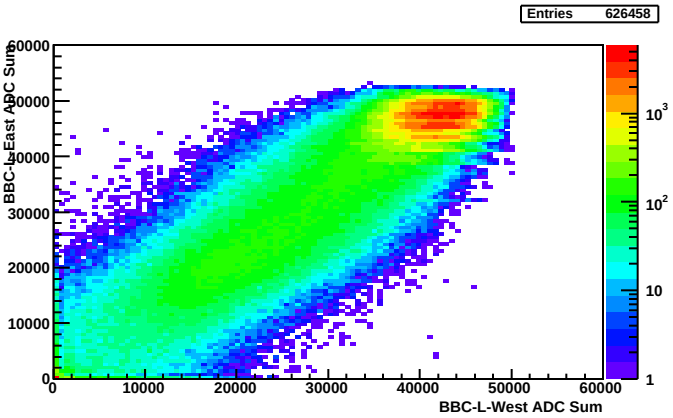

Entries 626458
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 626458
BBC-L-East ADC Sum
ZDC-East ADC Sum Att


Entries 626458
BBC-L-West ADC Sum
ZDC-West ADC Sum Att


Entries 626458
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 626458
BBC-S-West ADC Sum
ZDC-West ADC Sum Att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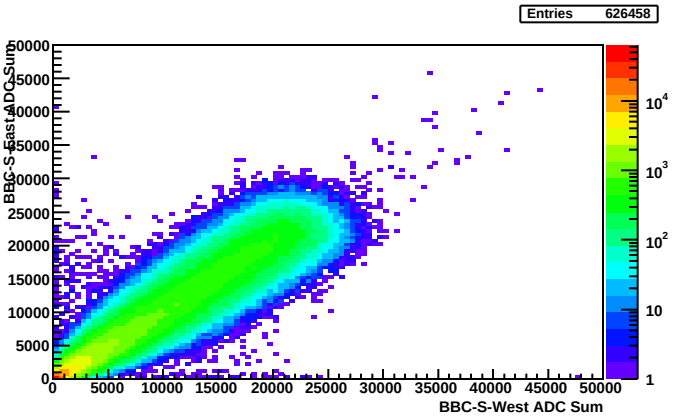
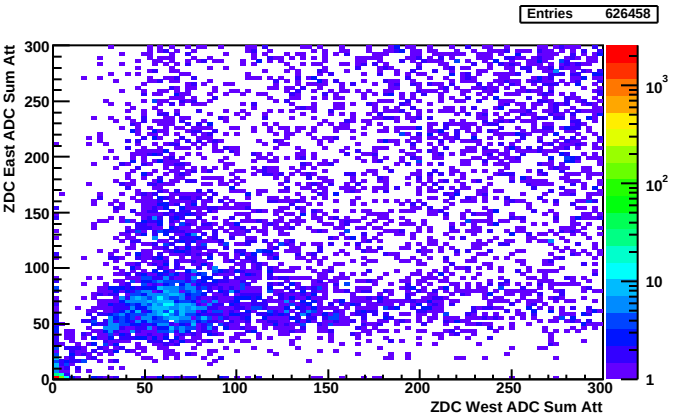
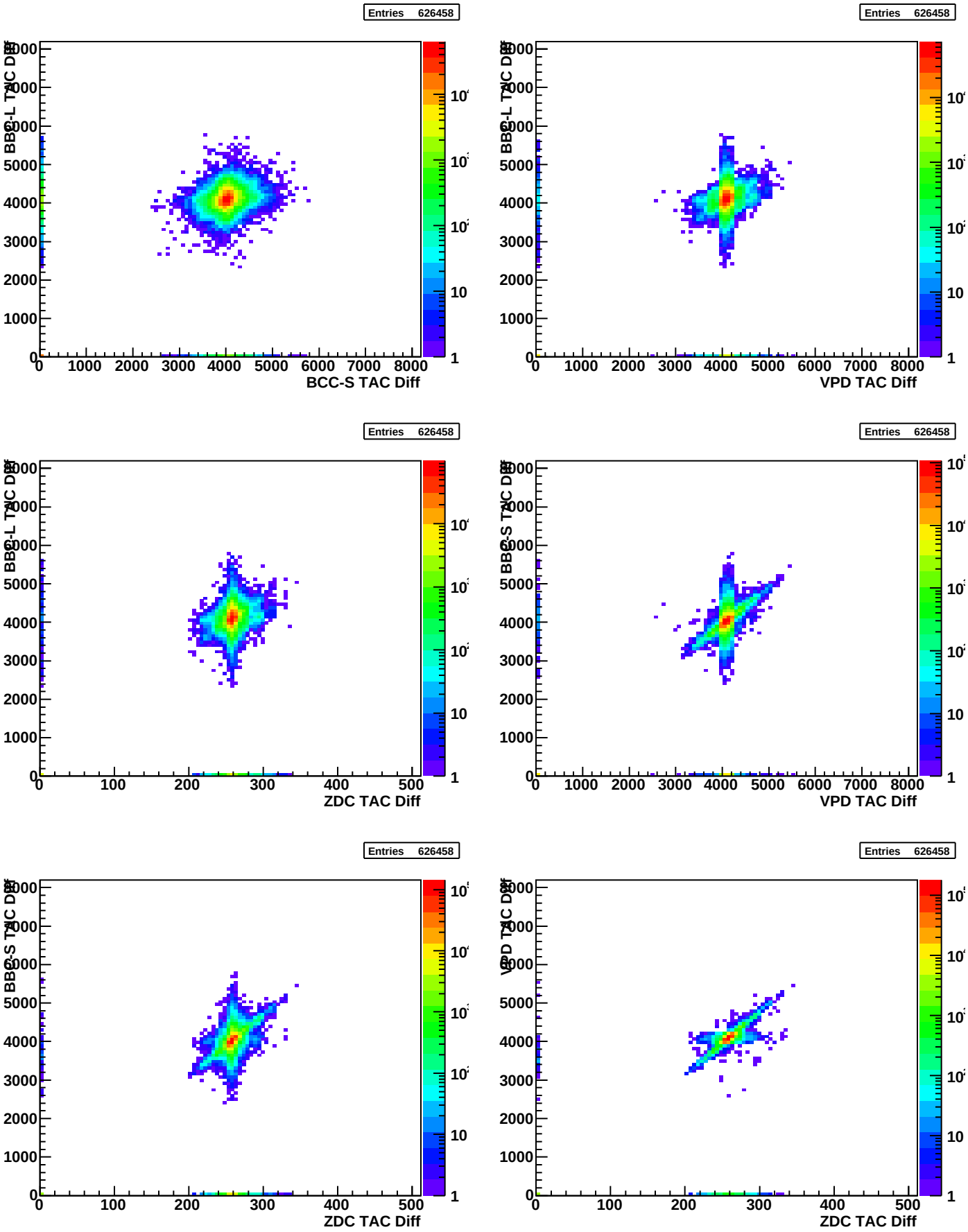
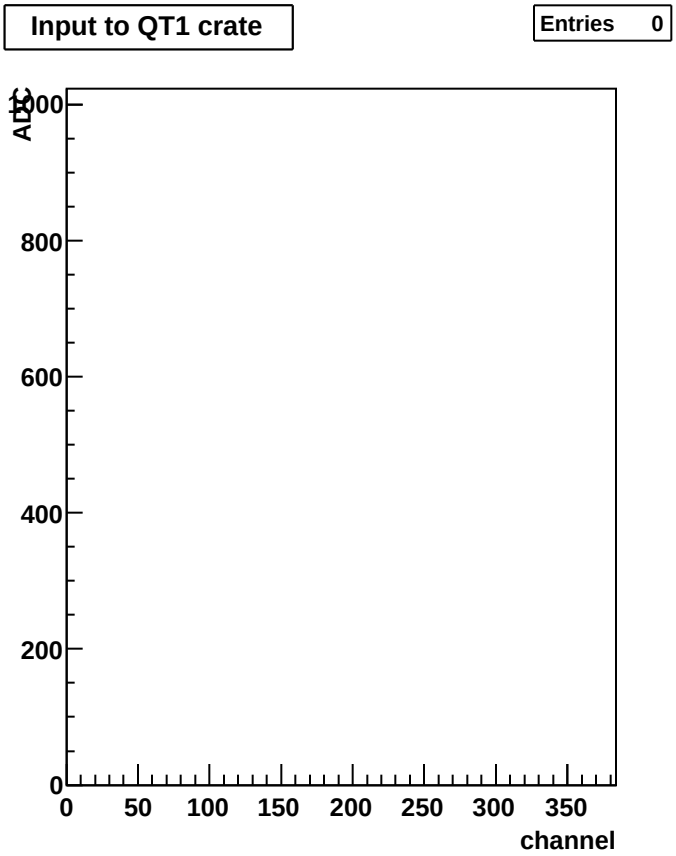
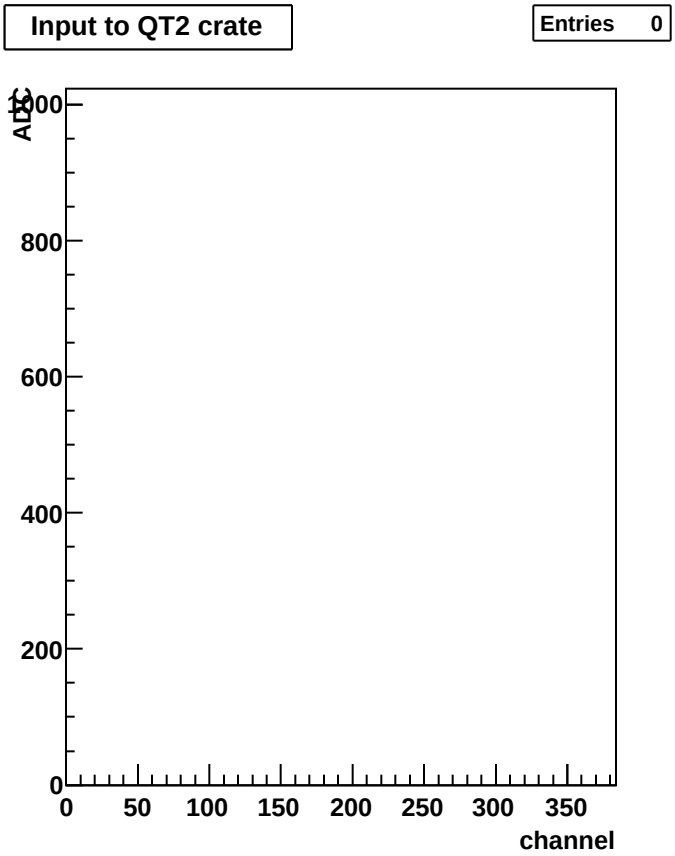
BBC-L-East ADC Sum
BBC-L-West ADC Sum

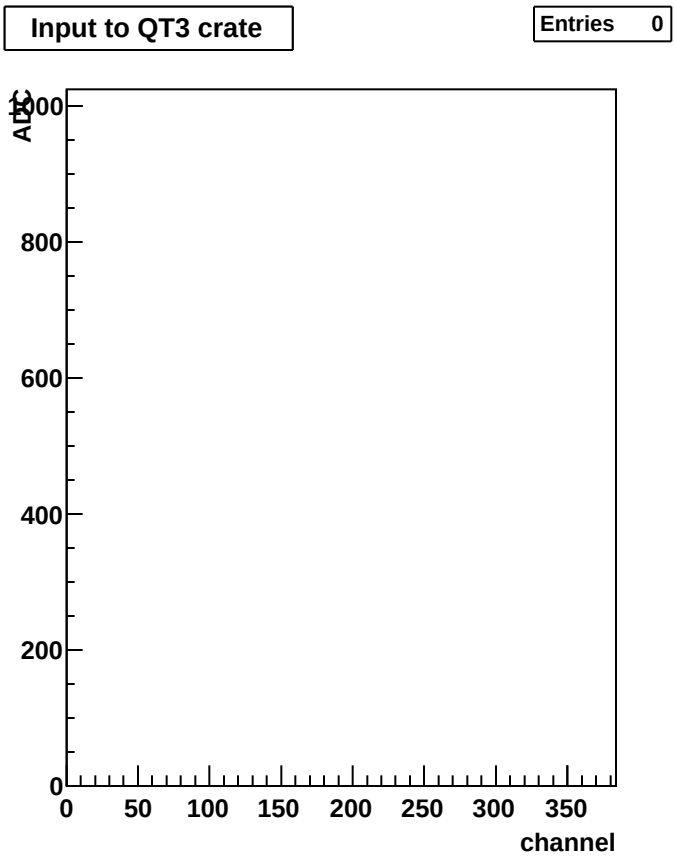

Entries 626458
BBC-S-East ADC Sum
BBC-S-West ADC Sum

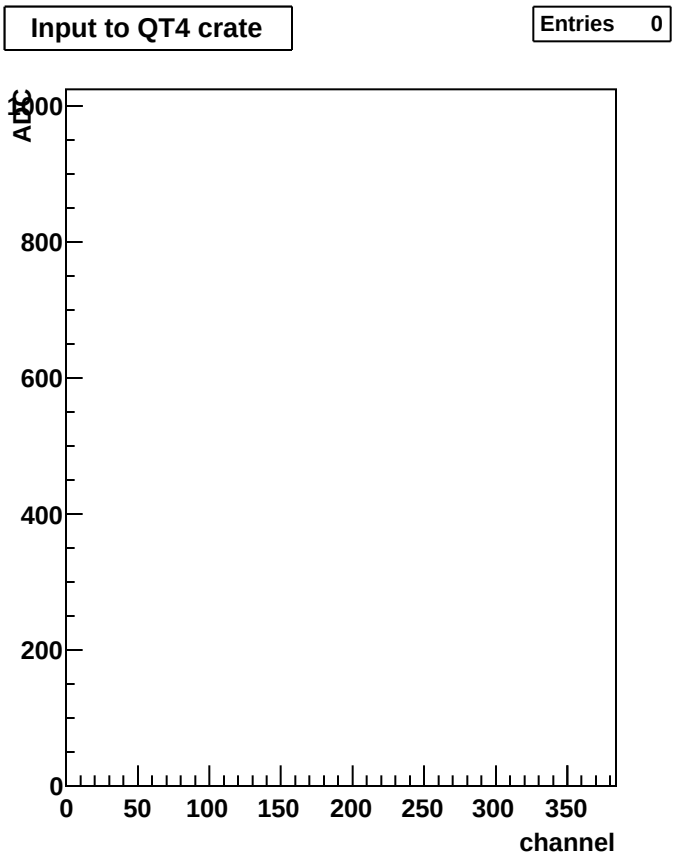

Entries 626458
ZDC East ADC Sum Att
ZDC West ADC Sum Att

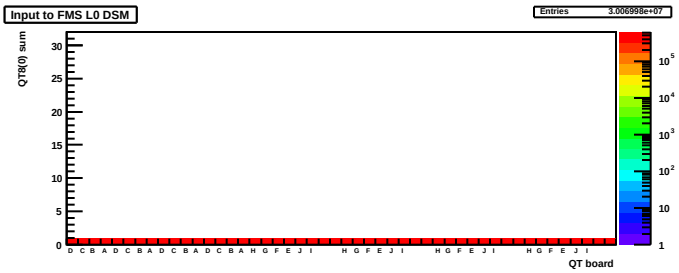

Entries 626458
BBC-L TAC Diff
BCC-S TAC Diff
Entries 626458
BBC-L TAC Diff
VPD TAC Diff
Entries 626458
BBC-L TAC Diff
ZDC TAC Diff
Entries 626458
BBC-S TAC Diff
VPD TAC Diff
Entries 626458
BBC-S TAC Diff
ZDC TAC Diff
Entries 626458
VPD TAC Diff
ZDC TAC Diff

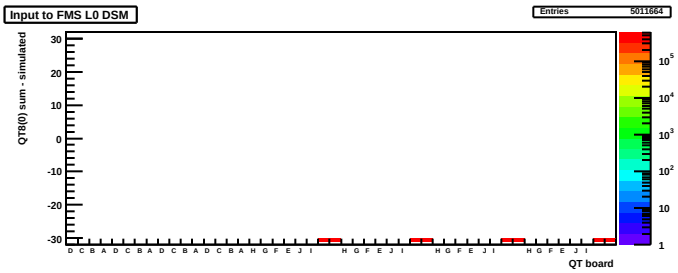

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

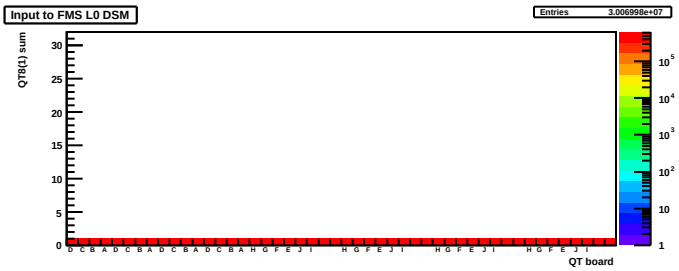

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

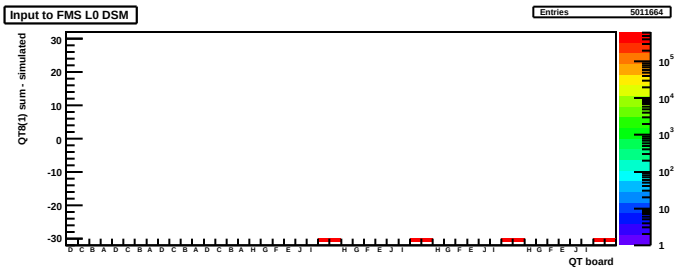

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

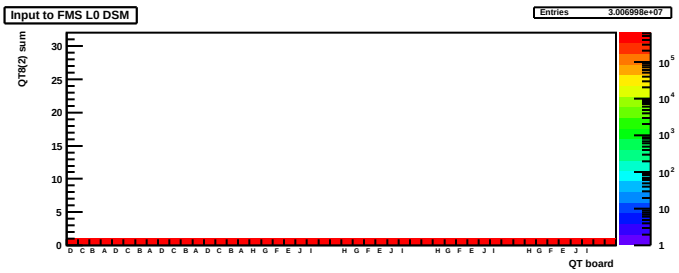

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

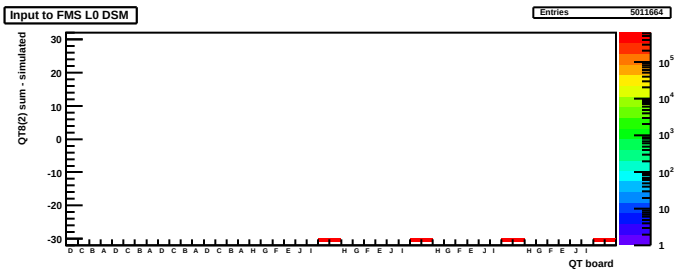

Input to FMS L0 DSM
Entries 3.006998e+07
QT8(0) sum
QT board

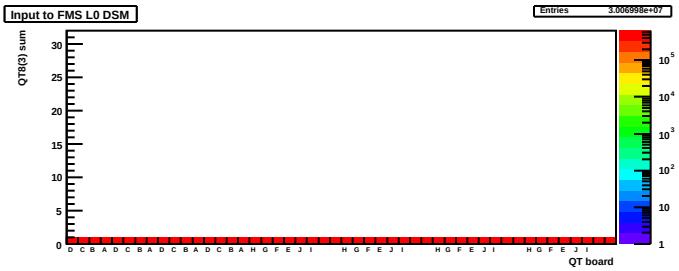

Input to FMS L0 DSM
Entries 5011664
QT8(0) sum - simulated
QT board

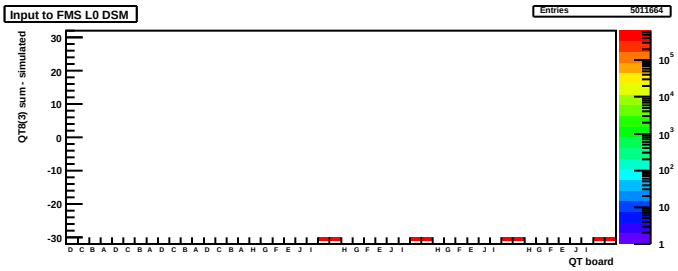

Input to FMS L0 DSM
Entries 3.006998e+07
QT8(1) sum
QT board

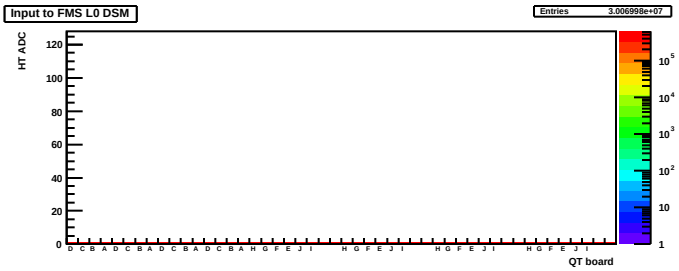

Input to FMS L0 DSM
Entries 5011664
QT8(1) sum - simulated
QT board

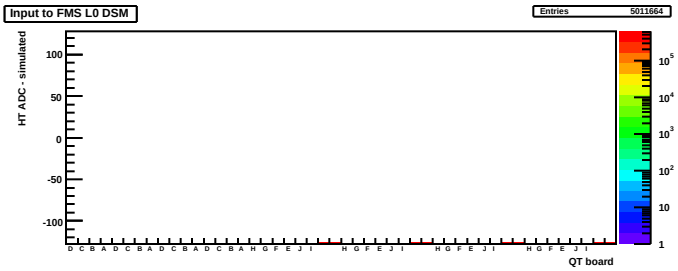

Input to FMS L0 DSM
Entries 3.006998e+07
QT8(2) sum
QT board

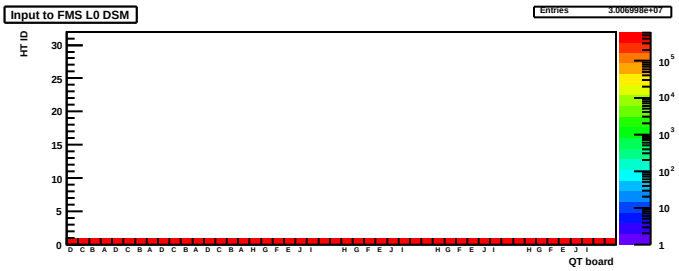

Input to FMS L0 DSM
Entries 5011664
QT8(2) sum - simulated
QT board

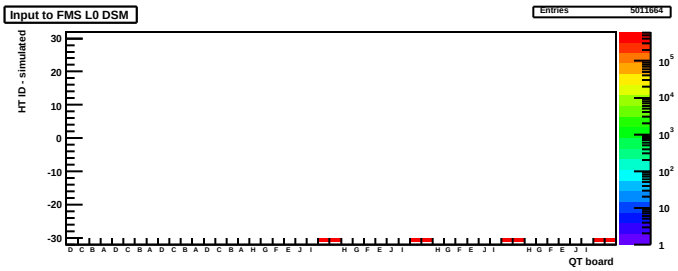

Input to FMS L0 DSM
Entries 3.006998e+07
QT8(3) sum
QT board

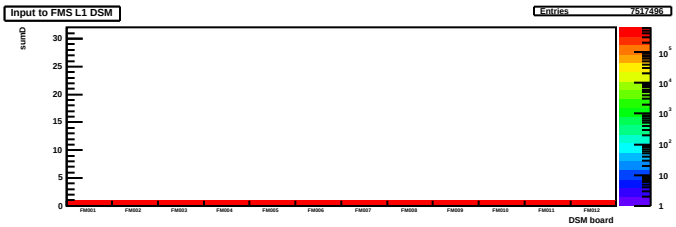

Input to FMS L0 DSM
Entries 5011664
QT8(3) sum - simulated
QT board

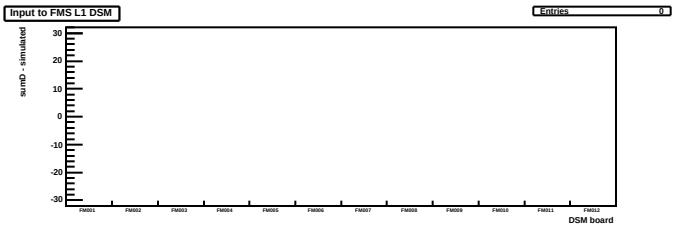

Input to FMS L0 DSM
Entries 3.006998e+07
HT ADC
QT board

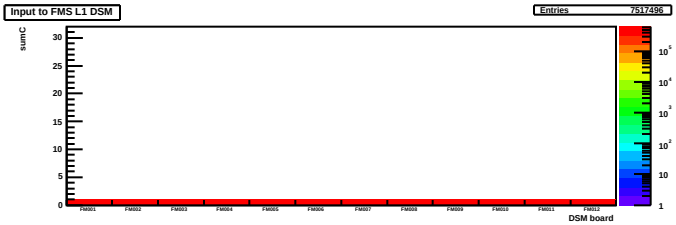

Input to FMS L0 DSM
Entries 5011664
HT ADC - simulated
QT board

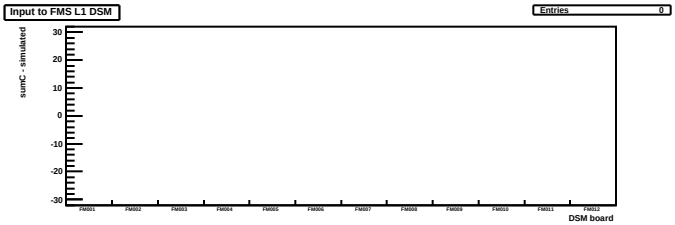

Input to FMS L0 DSM
Entries 3.006998e+07
HT ID
QT board

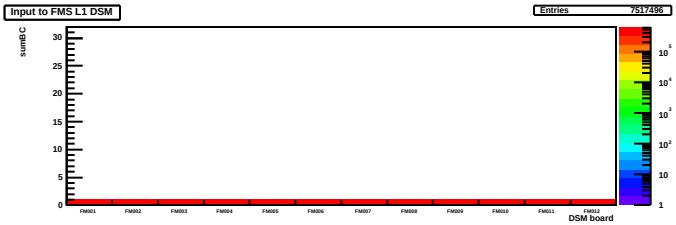

Input to FMS L0 DSM
Entries 5011664
HT ID - simulated
QT board

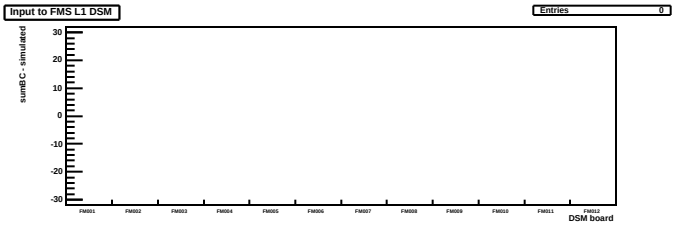

Input to FMS L1 DSM
Entries 7517496
sumD
DSM board

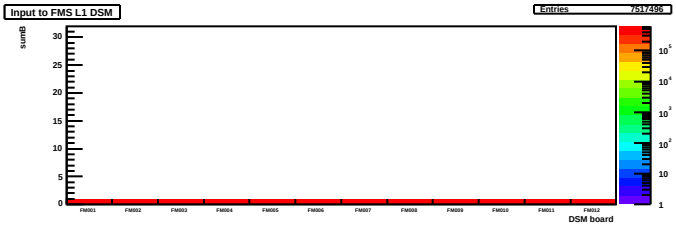

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

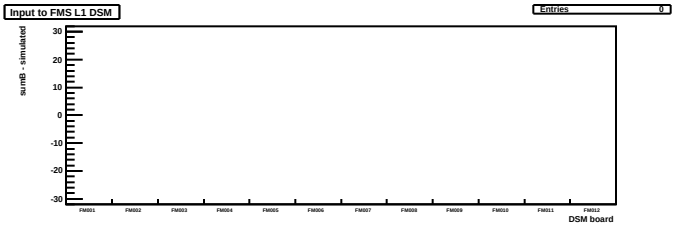

Input to FMS L1 DSM
Entries 7517496
sumC
DSM board

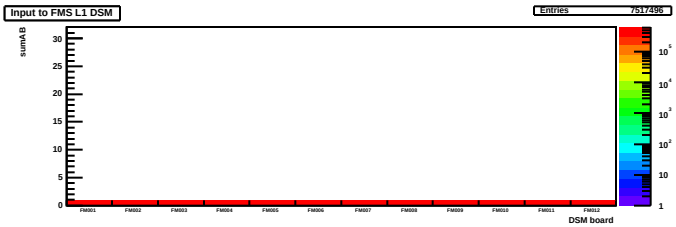

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

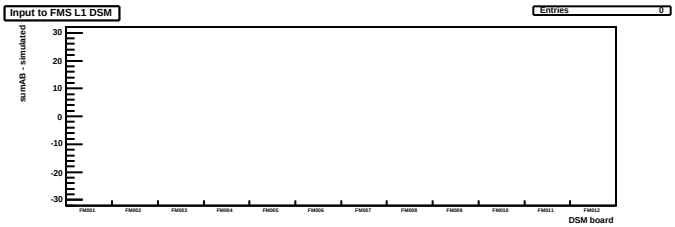

Input to FMS L1 DSM
Entries 7517496
sumBC
DSM board

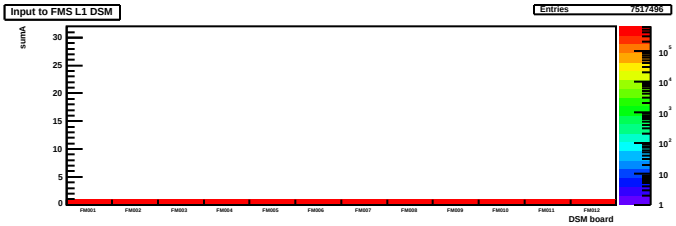

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

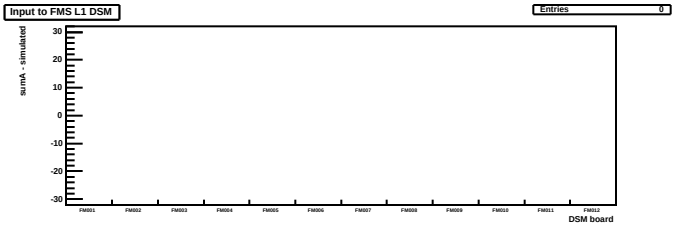

Input to FMS L1 DSM
Entries 7517496
sumB
DSM board

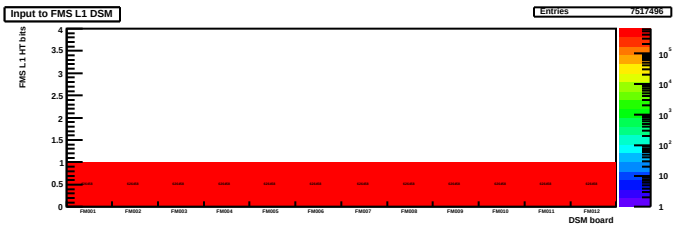

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

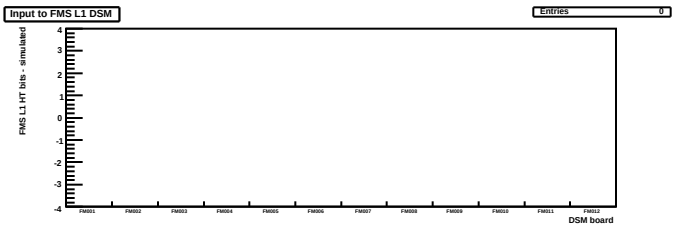

Input to FMS L1 DSM
Entries 7517496
sumAB
DSM board

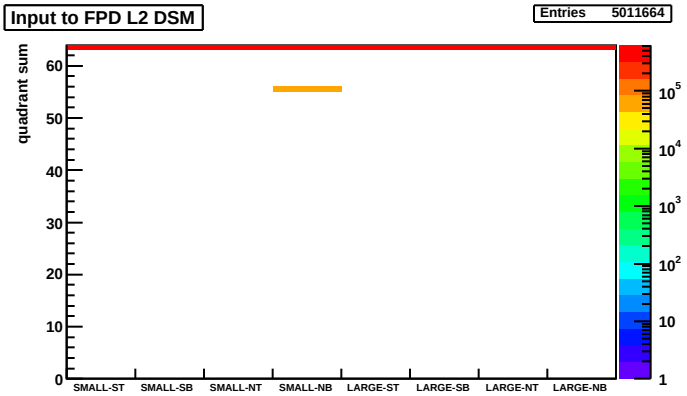

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

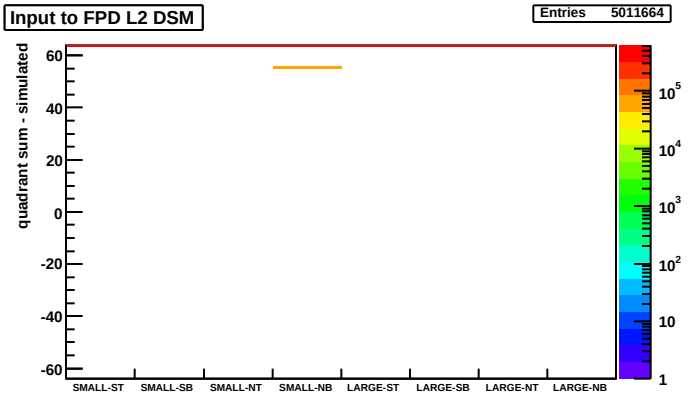

Input to FMS L1 DSM
Entries 7517496
sumA
DSM board

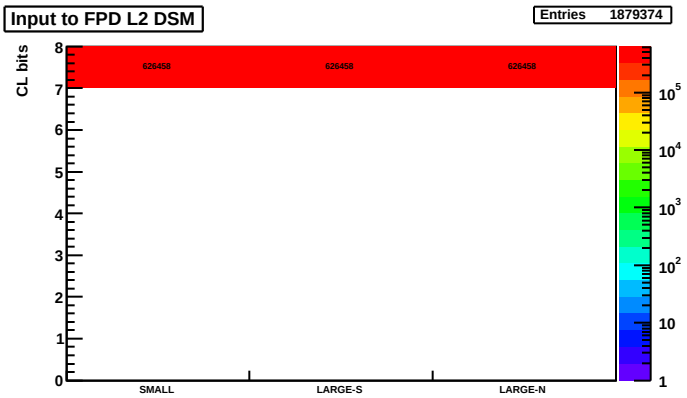

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

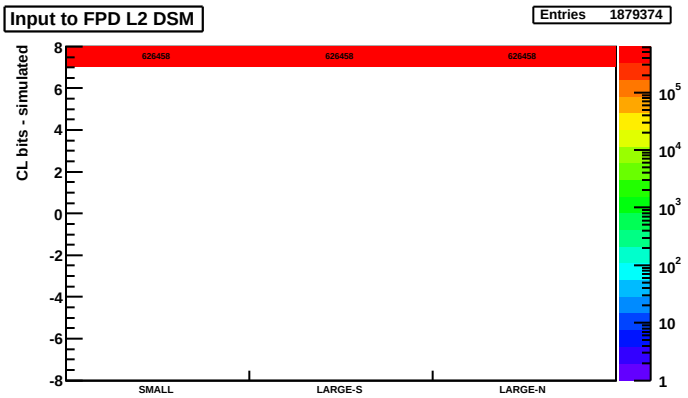

Input to FMS L1 DSM
Entries 7517496
FMS L1 HT bits
DSM board

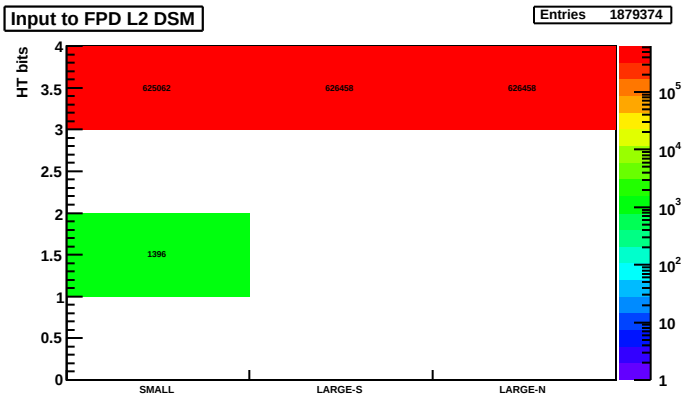

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

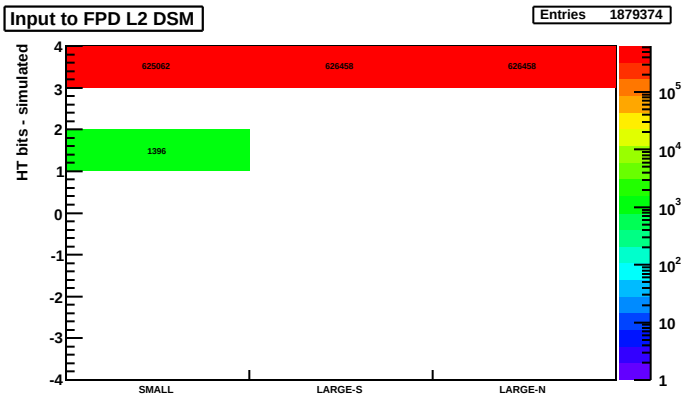

Input to FPD L2 DSM
Entries 5011664
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

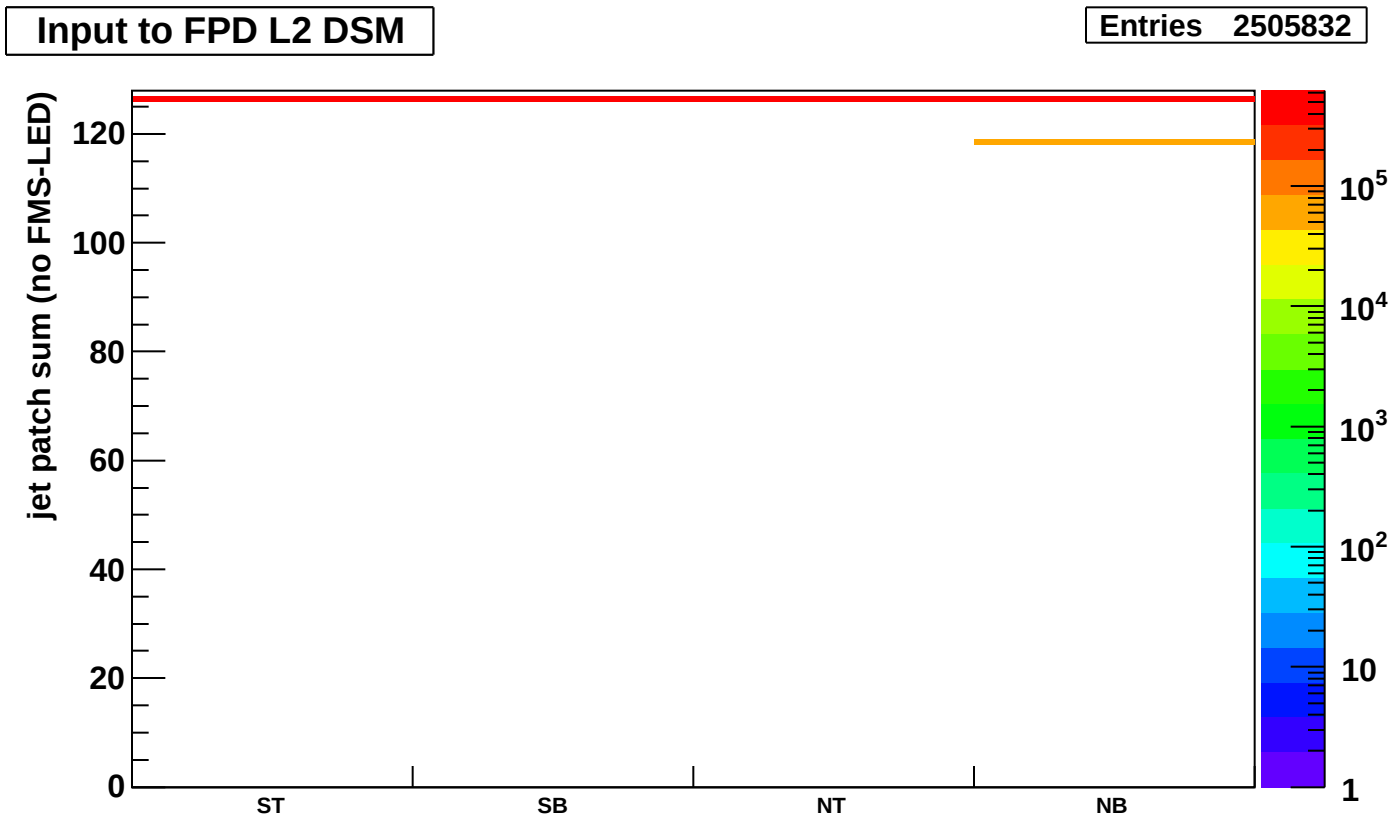

Input to FPD L2 DSM
Entries 5011664
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

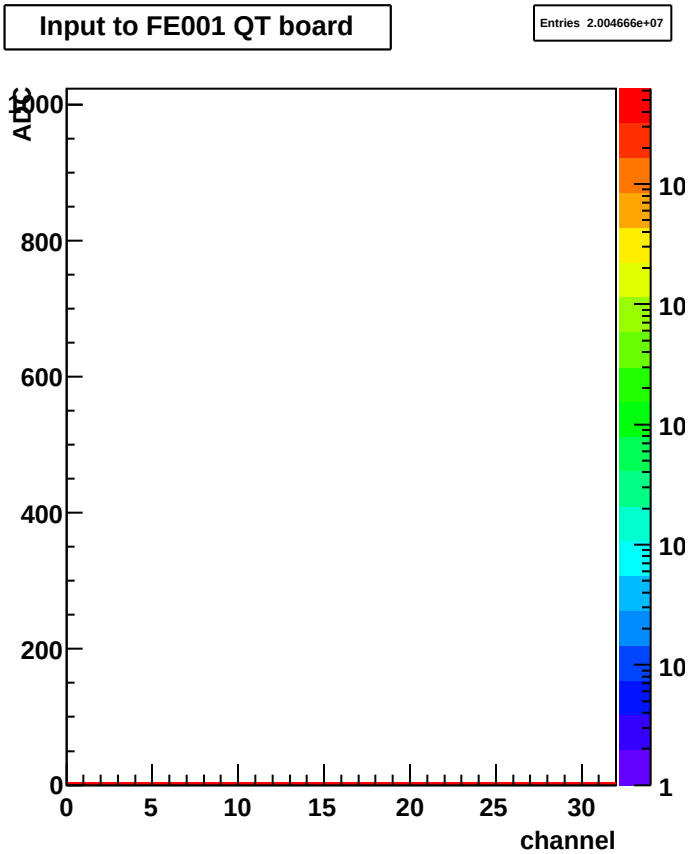

Input to FPD L2 DSM
Entries 1879374
CL bits
626458
626458
626458
SMALL
LARGE-S
LARGE-N

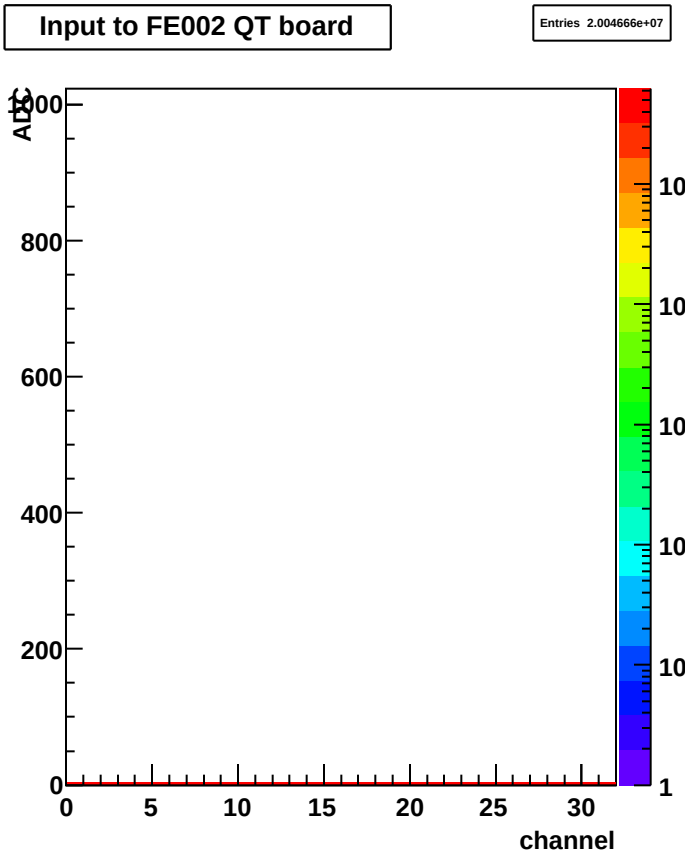

Input to FPD L2 DSM
Entries 1879374
CL bits - simulated
626458
626458
626458
SMALL
LARGE-S
LARGE-N

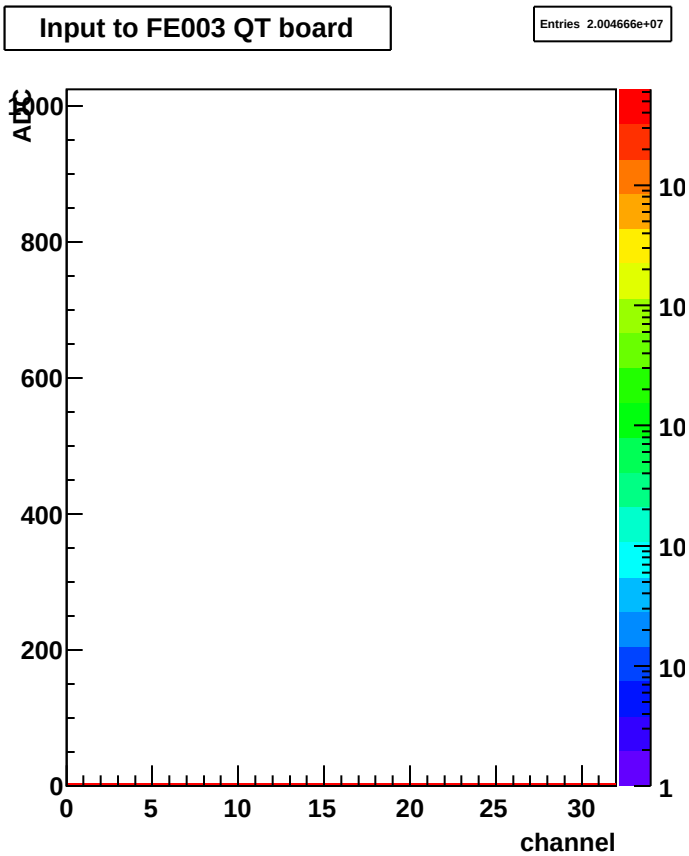

Input to FPD L2 DSM
Entries 1879374
HT bits
625062
626458
626458
1396
SMALL
LARGE-S
LARGE-N

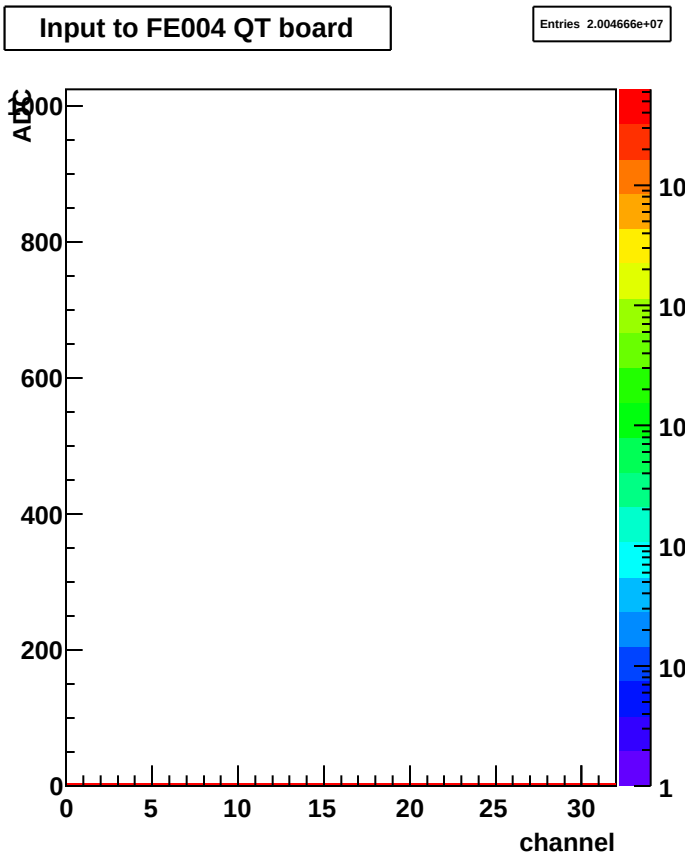

Input to FPD L2 DSM
Entries 1879374
HT bits - simulated
625062
626458
626458
1396
SMALL
LARGE-S
LARGE-N

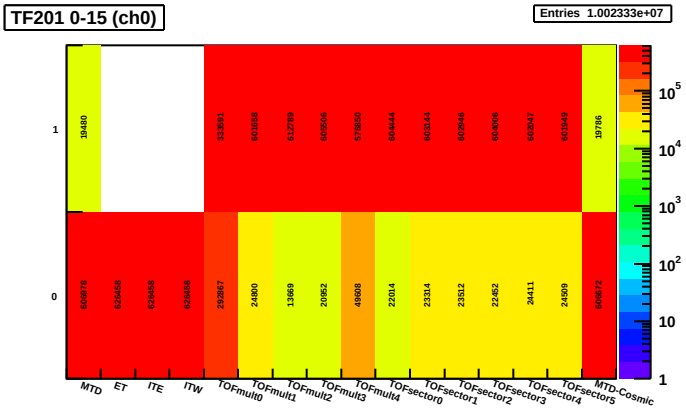

Input to FPD L2 DSM
Entries 2505832
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

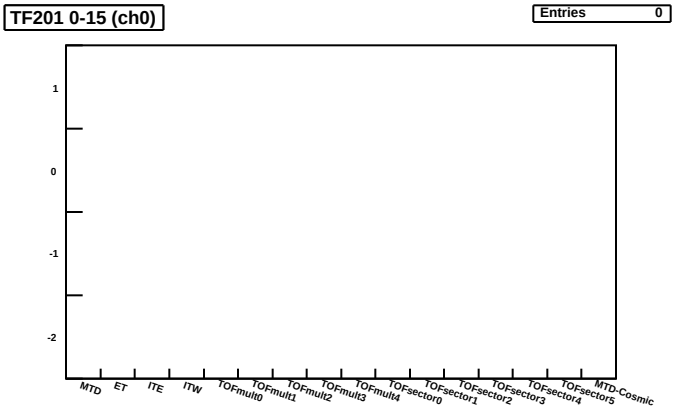

Input to FE001 QT board
Entries 2.004666e+07
ADC
channel

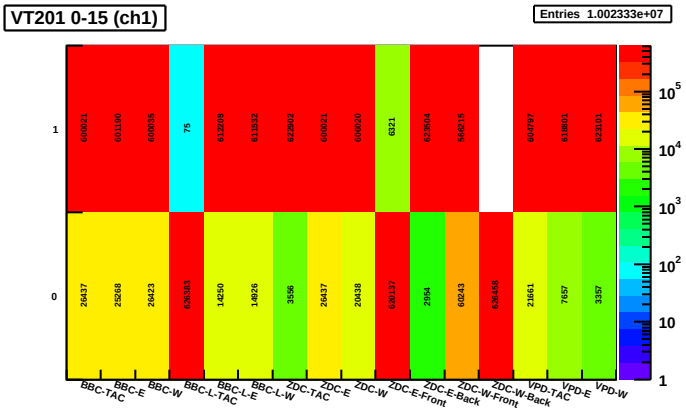

Input to FE002 QT board
Entries 2.004666e+07
ADC
channel

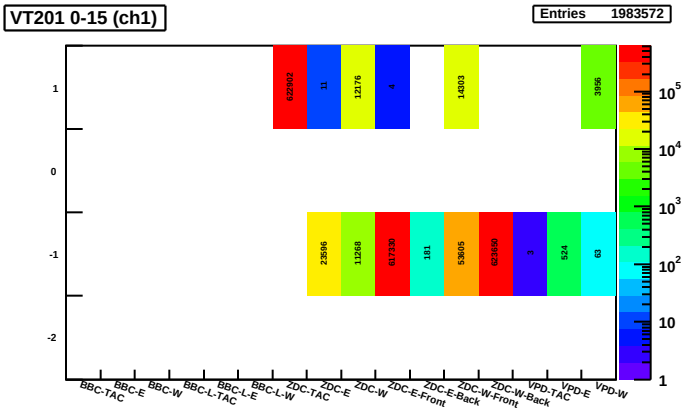

Input to FE003 QT board
Entries 2.004666e+07
ADC
channel

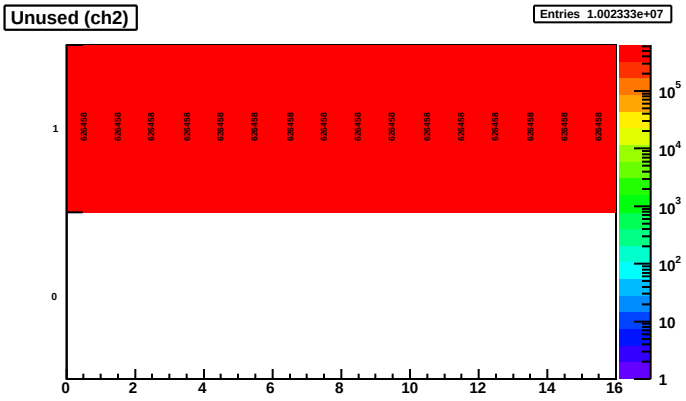

Input to FE004 QT board
Entries 2.004666e+07
ADC
channel

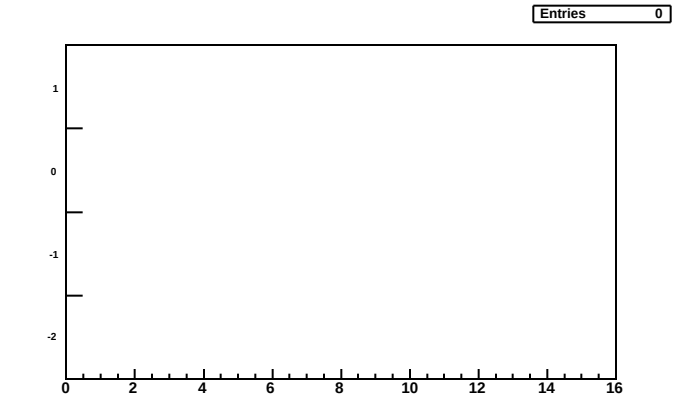

TF201 0-15 (ch0)
Entries 1.002333e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

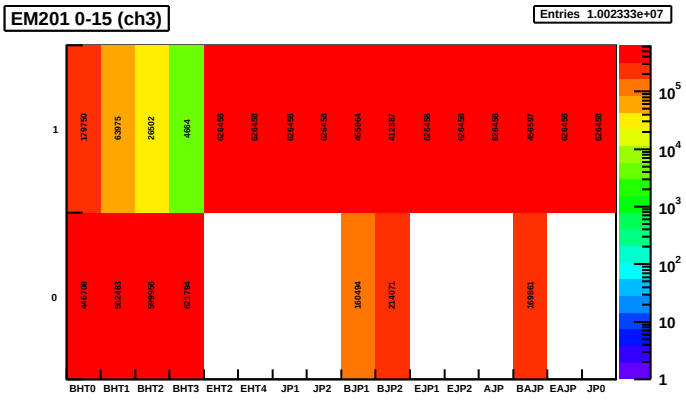

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

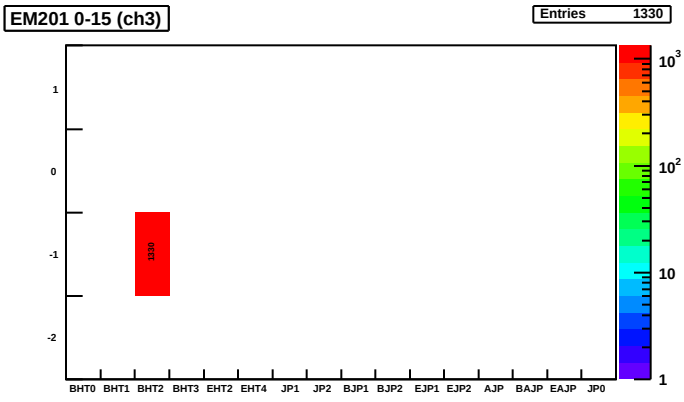

VT201 0-15 (ch1)
Entries 1.002333e+07
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 1983572
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

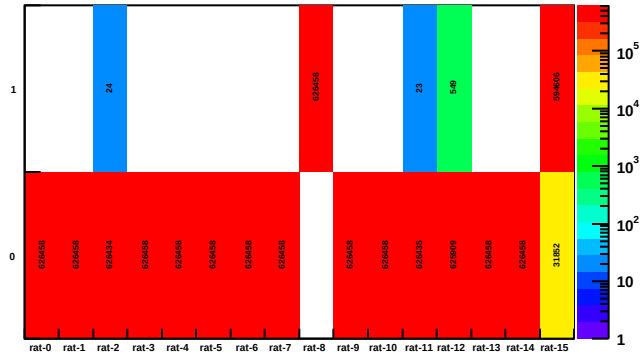

Unused (ch2)
Entries 1.002333e+07

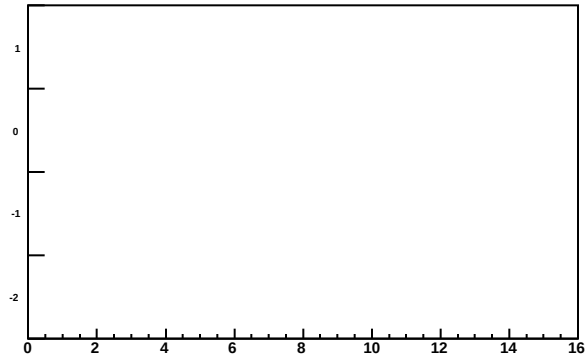

Entries 0


EM201 0-15 (ch3)
Entries 1.002333e+07
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

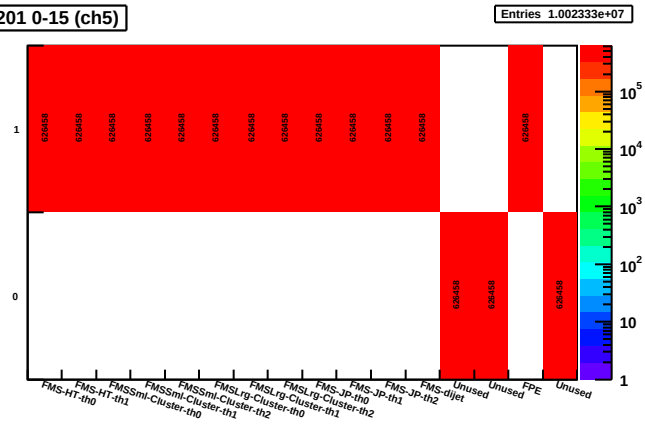

EM201 0-15 (ch3)
Entries 1330
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

RAT board (ch4)

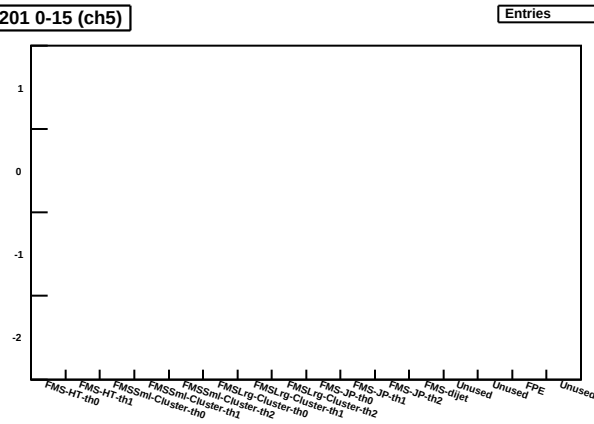




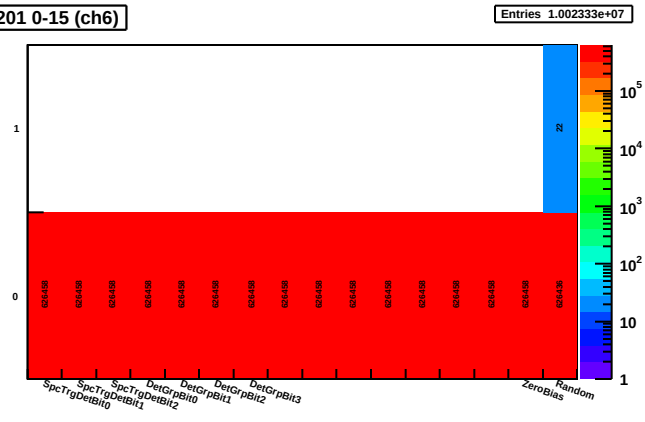
FP201 0-15 (ch5)

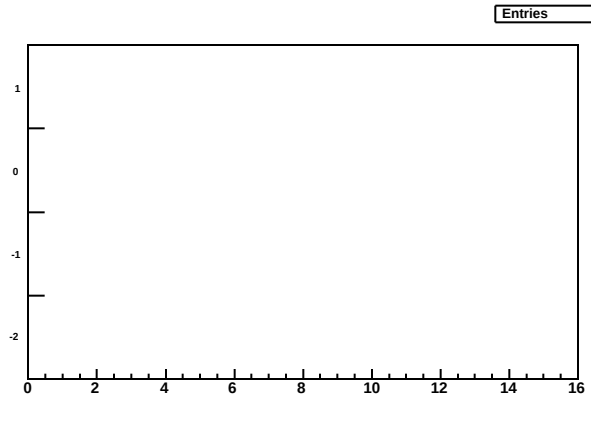
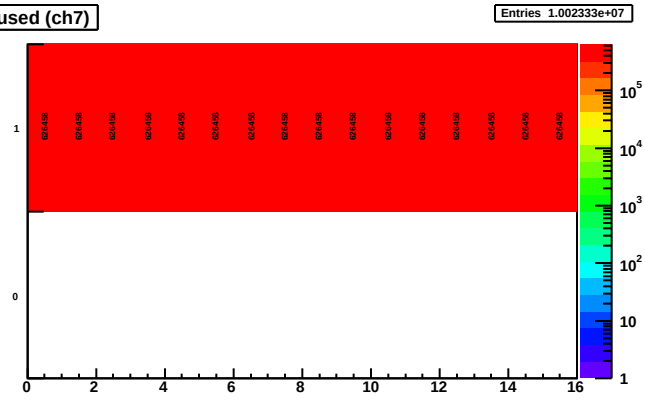
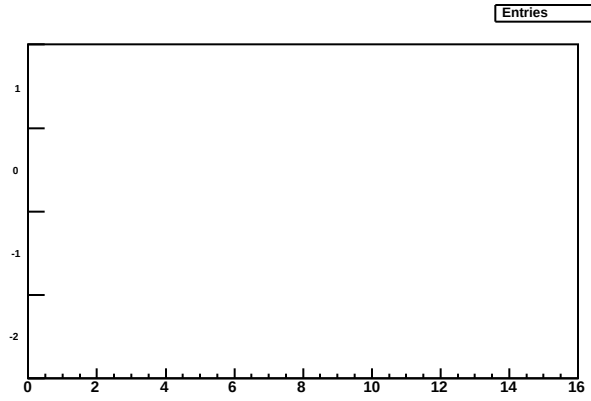


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)