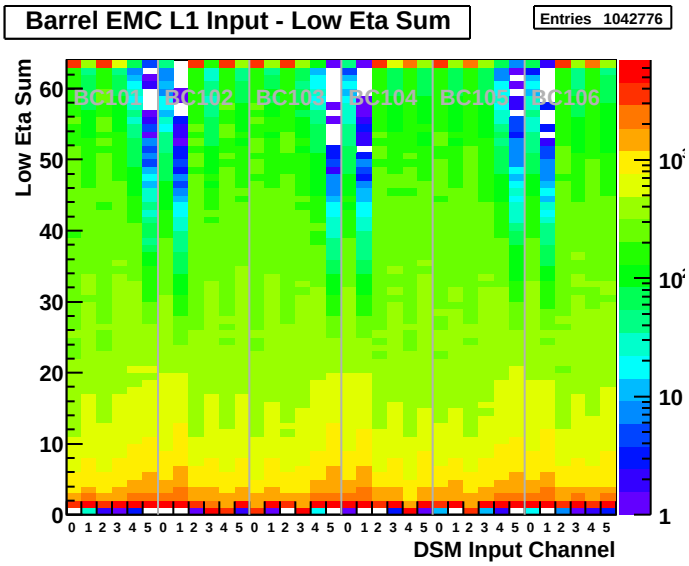
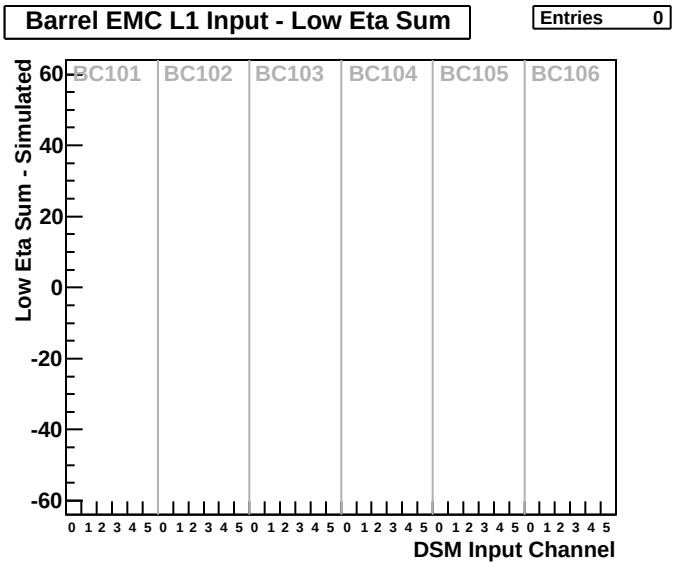
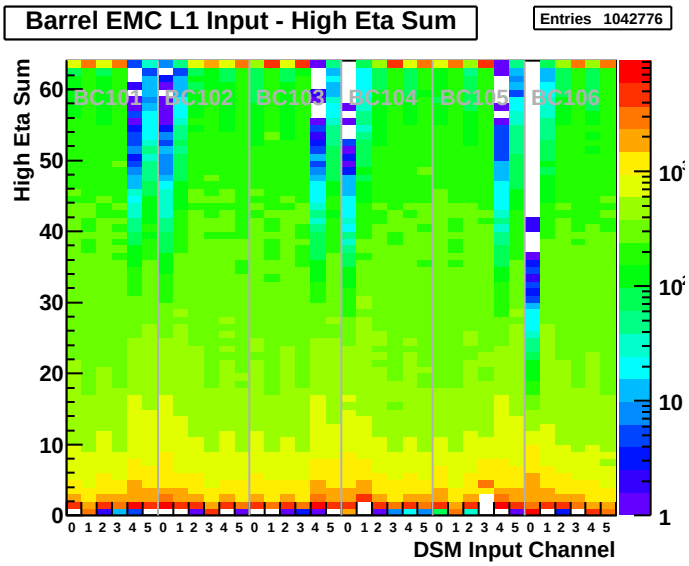
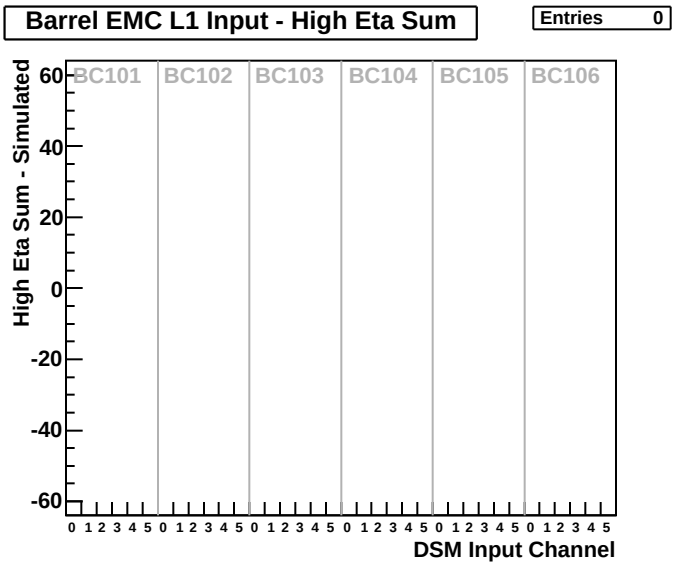
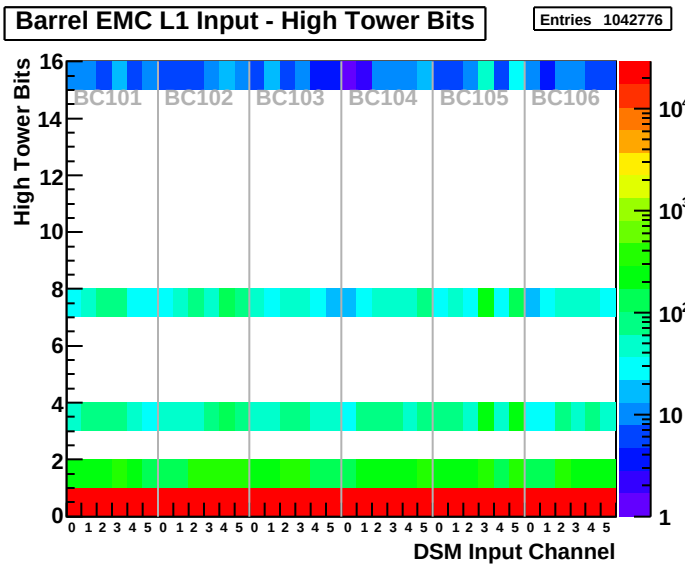
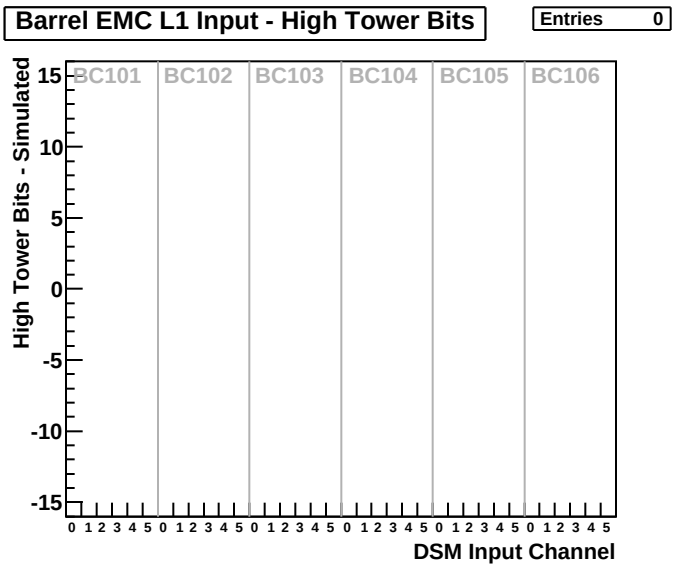

Barrel EMC L1 Input - Low Eta Sum
Entries 1042776
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

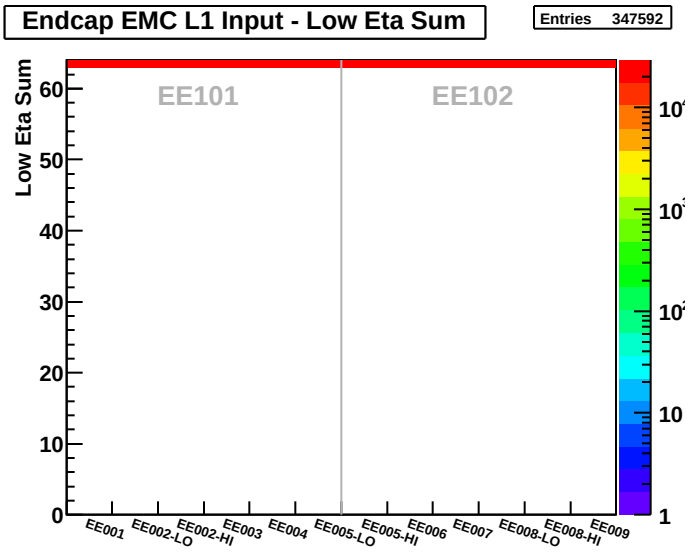

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

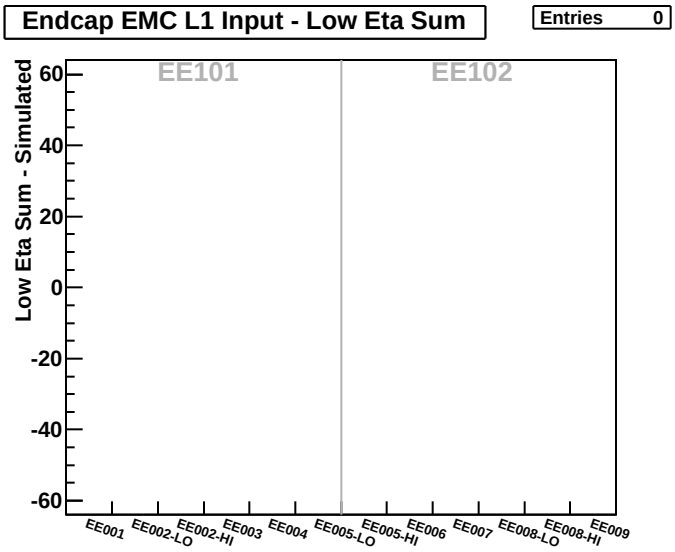

Barrel EMC L1 Input - High Eta Sum
Entries 1042776
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

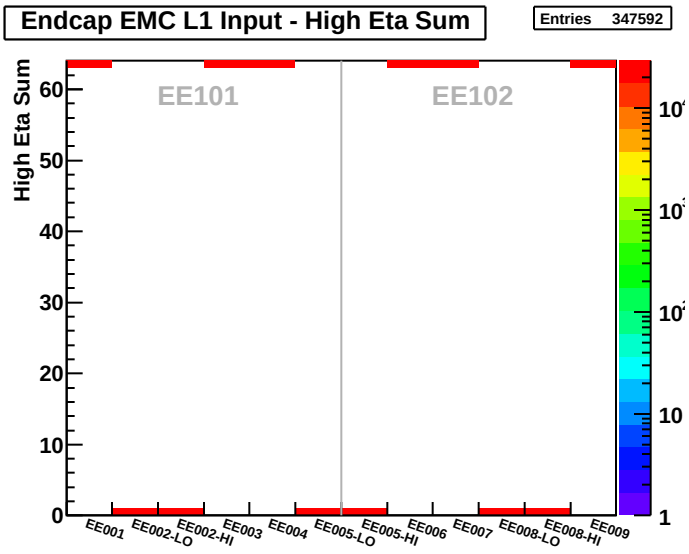

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

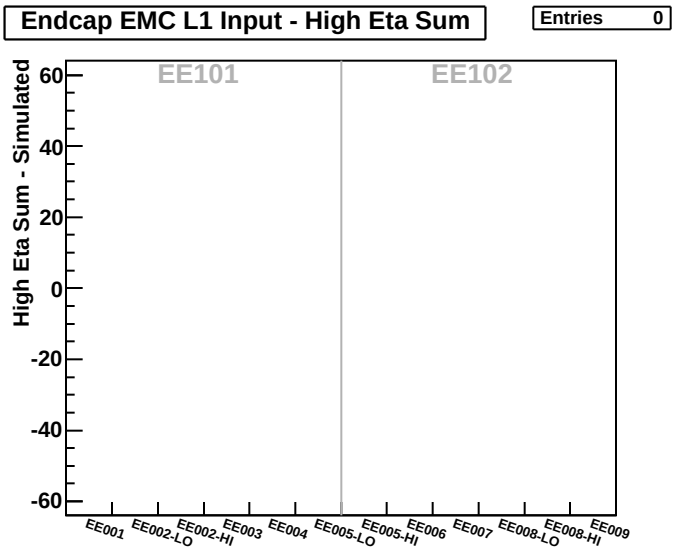

Barrel EMC L1 Input - High Tower Bits
Entries 1042776
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

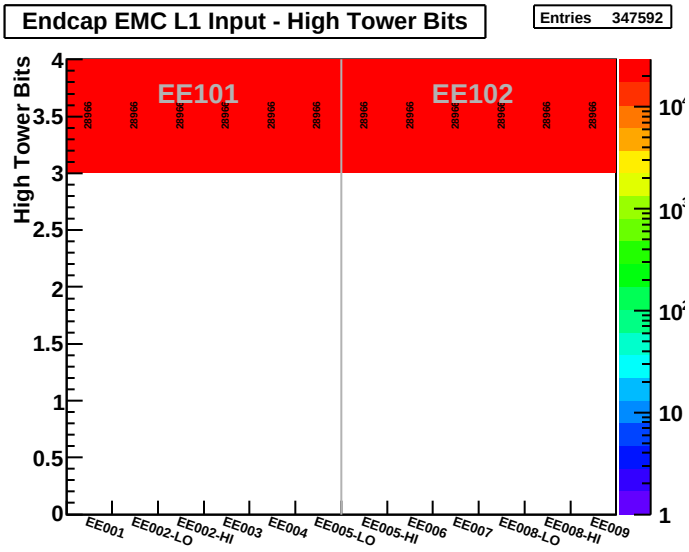

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

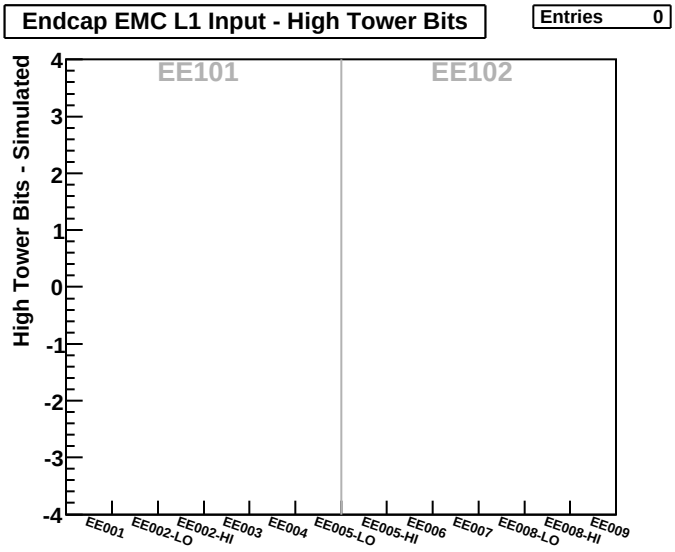

Endcap EMC L1 Input - Low Eta Sum
Entries 347592
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

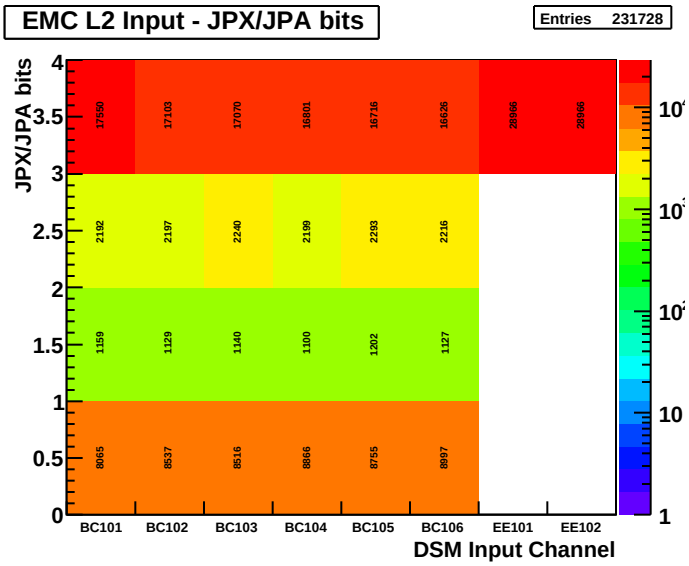

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

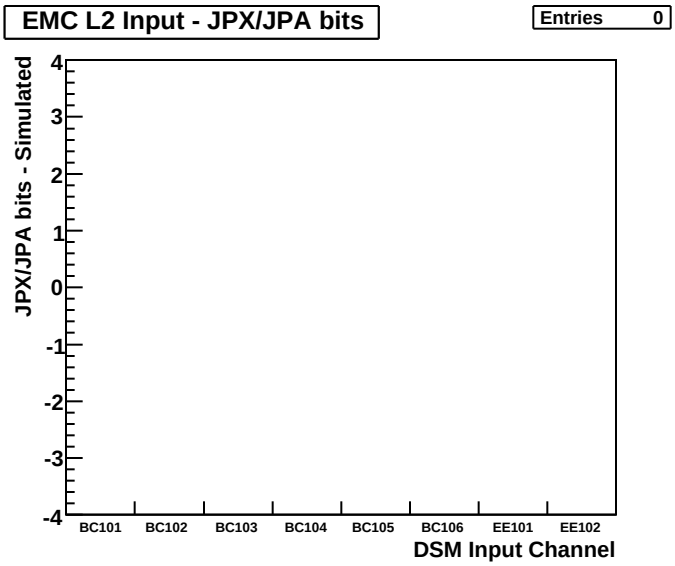

Endcap EMC L1 Input - High Eta Sum
Entries 347592
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

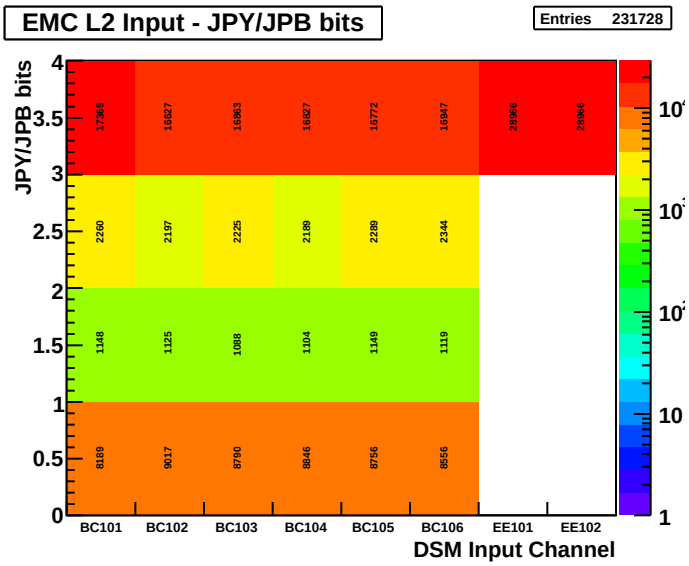

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

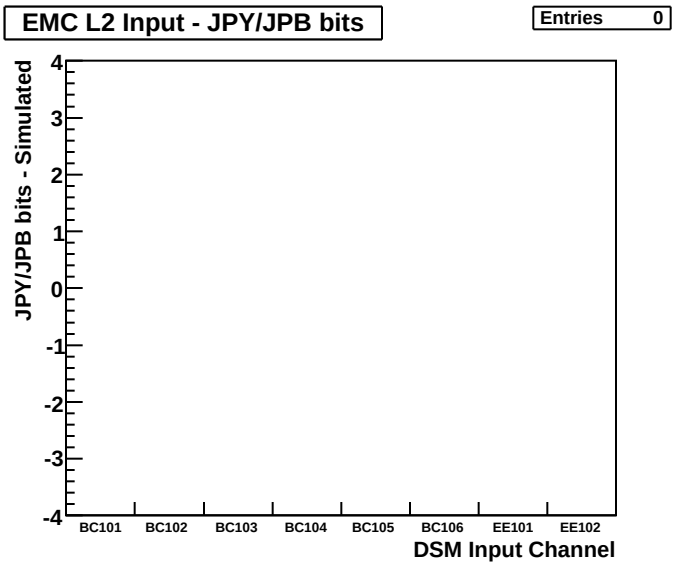

Endcap EMC L1 Input - High Tower Bits
Entries 347592
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

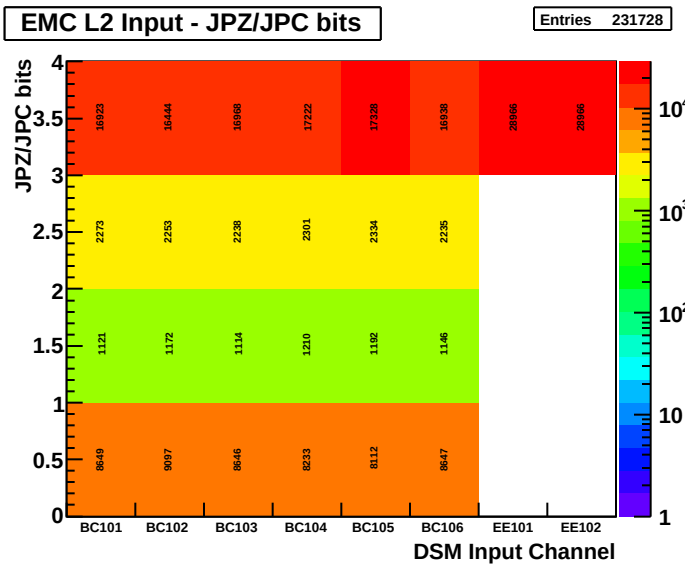

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

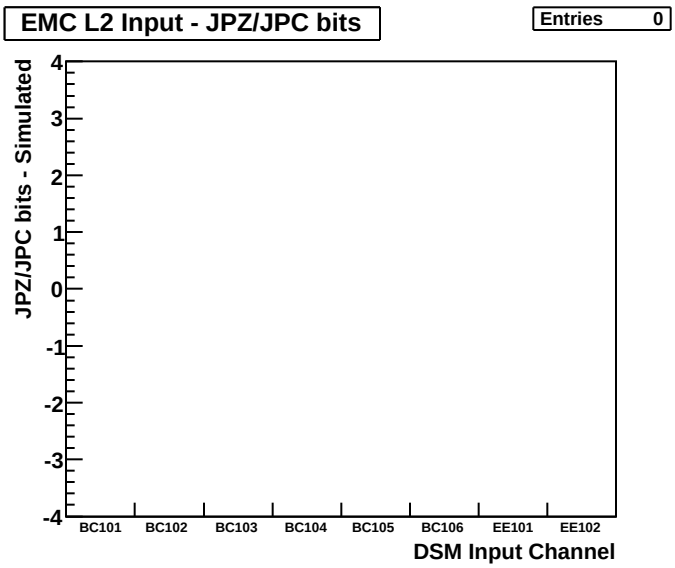

EMC L2 Input - JPX/JPA bits
Entries 231728
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
17550 17103 17070 16801 16716 16626 28966 28966
2192 2197 2240 2199 2293 2216
1159 1129 1140 1100 1202 1127
8065 8537 8516 8866 8755 8997

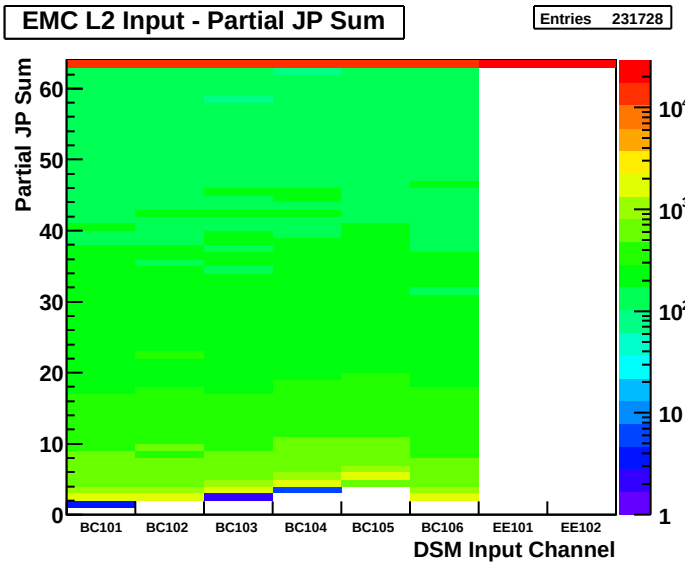

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

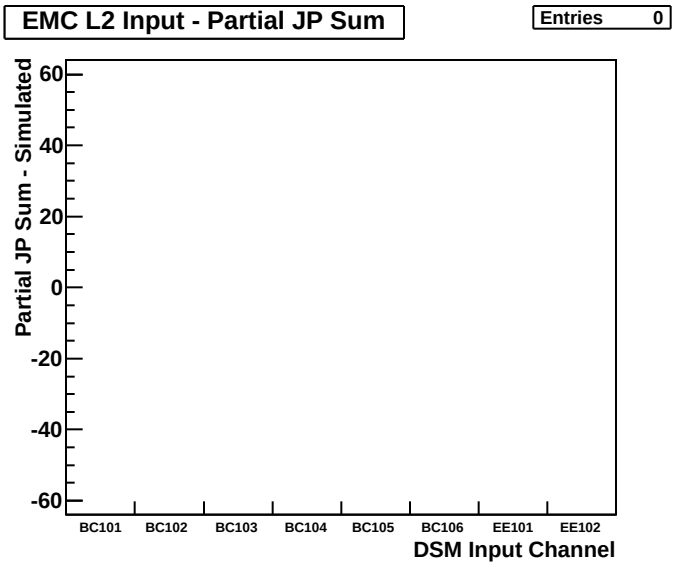

EMC L2 Input - JPY/JPB bits
Entries 231728
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
17369 16627 16863 16827 16772 16947 28966 28966
2260 2197 2225 2189 2289 2344
1148 1125 1088 1104 1149 1119
8189 9017 8790 8846 8756 8556

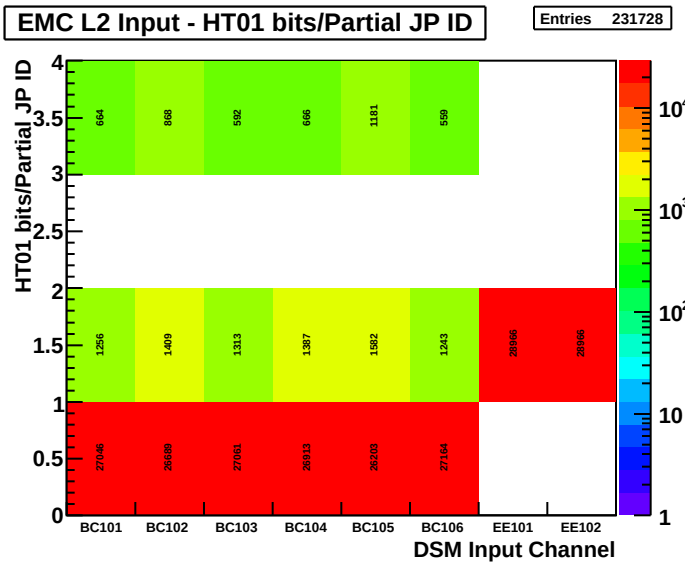

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

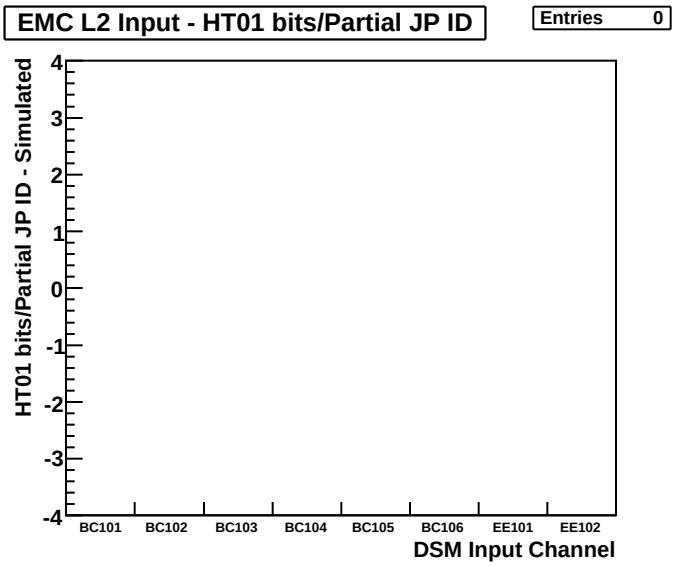

EMC L2 Input - JPZ/JPC bits
Entries 231728
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
16923 16444 16968 17222 17328 16938 28966 28966
2273 2253 2230 2301 2334 2235
1121 1172 1114 1210 1192 1146
8649 9097 8646 8233 8112 8647

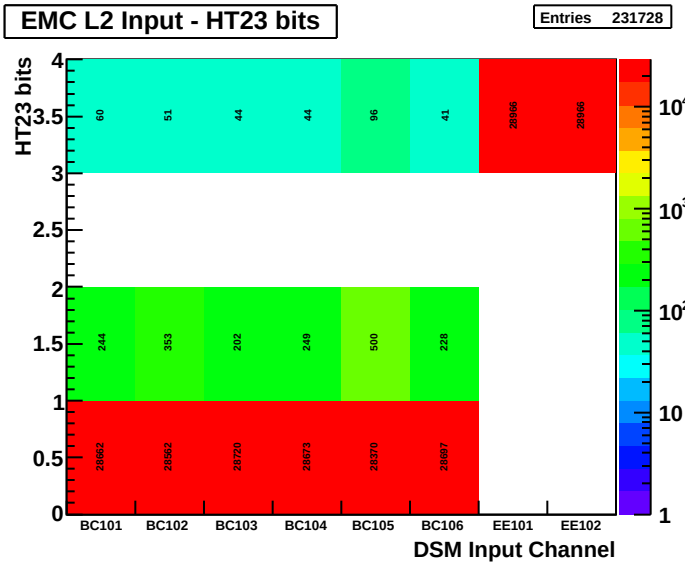

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

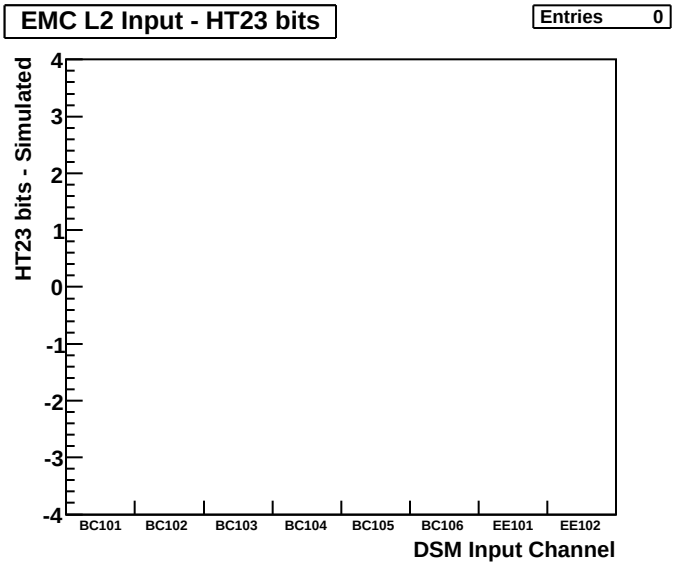

EMC L2 Input - Partial JP Sum
Entries 231728
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

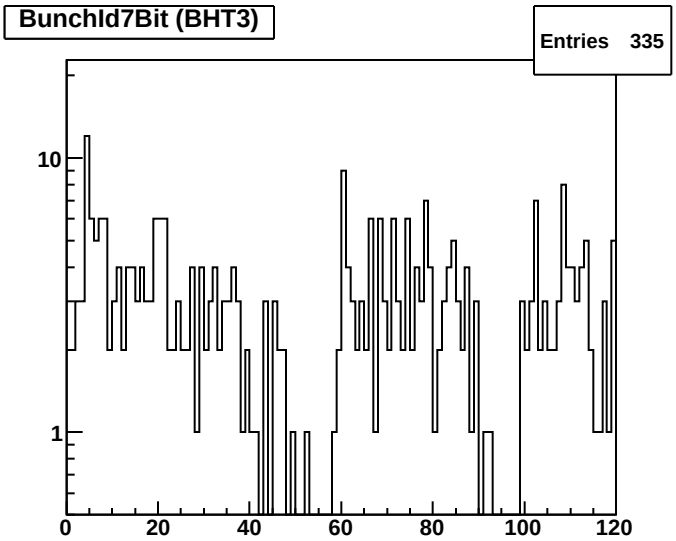

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

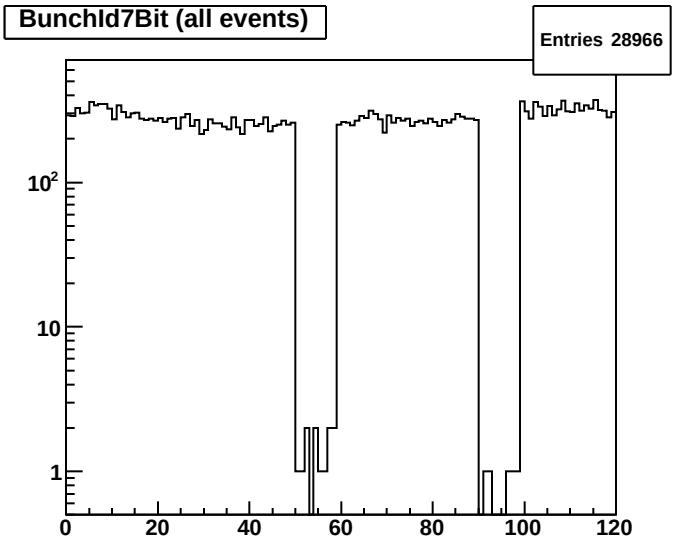

EMC L2 Input - HT01 bits/Partial JP ID
Entries 231728
HT01 bits/Partial JP ID
664 868 592 666 1181 559
1256 1409 1313 1387 1382 1243 28966 28966
27046 26689 27061 26913 26203 27164
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

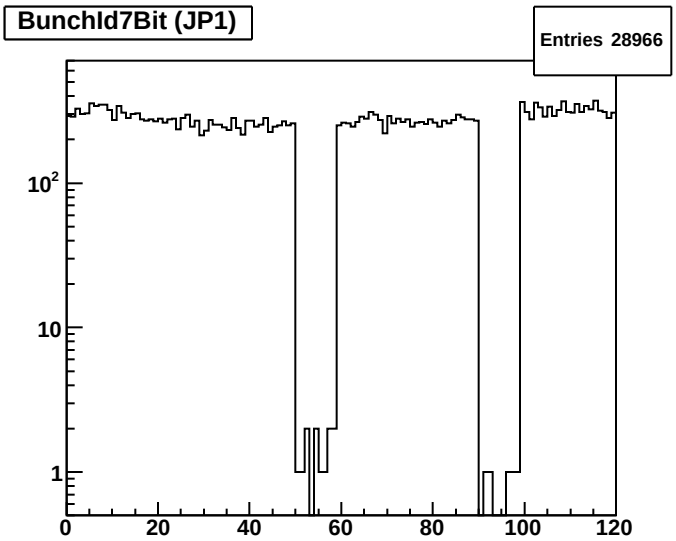

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

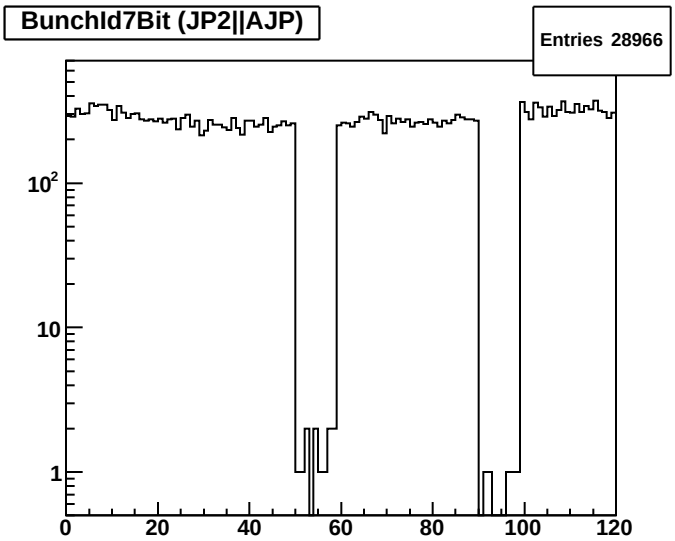

EMC L2 Input - HT23 bits
Entries 231728
HT23 bits
60 51 44 44 96 41 28966 28966
244 353 202 249 500 228
28662 28562 28720 28673 28370 28697
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

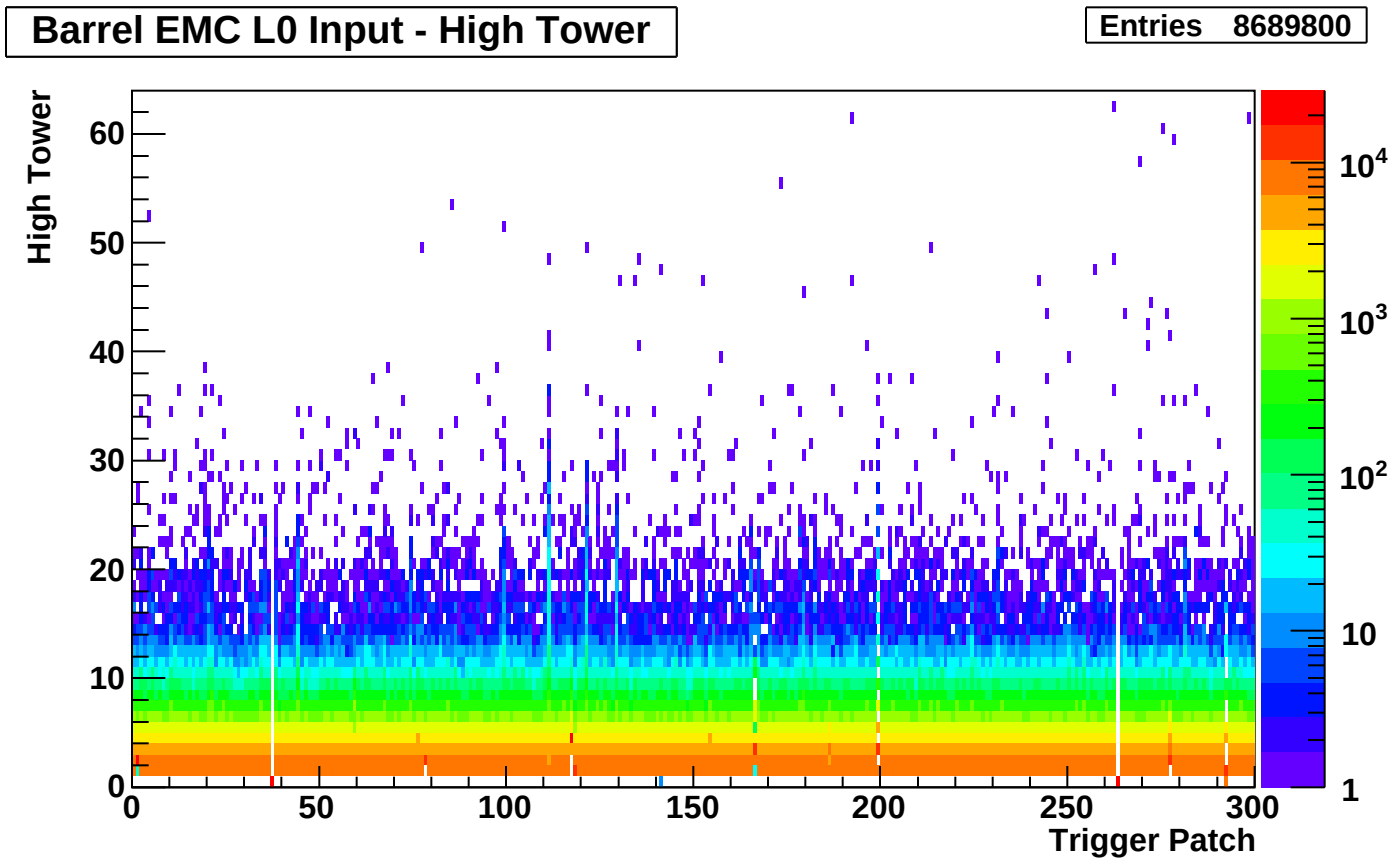

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

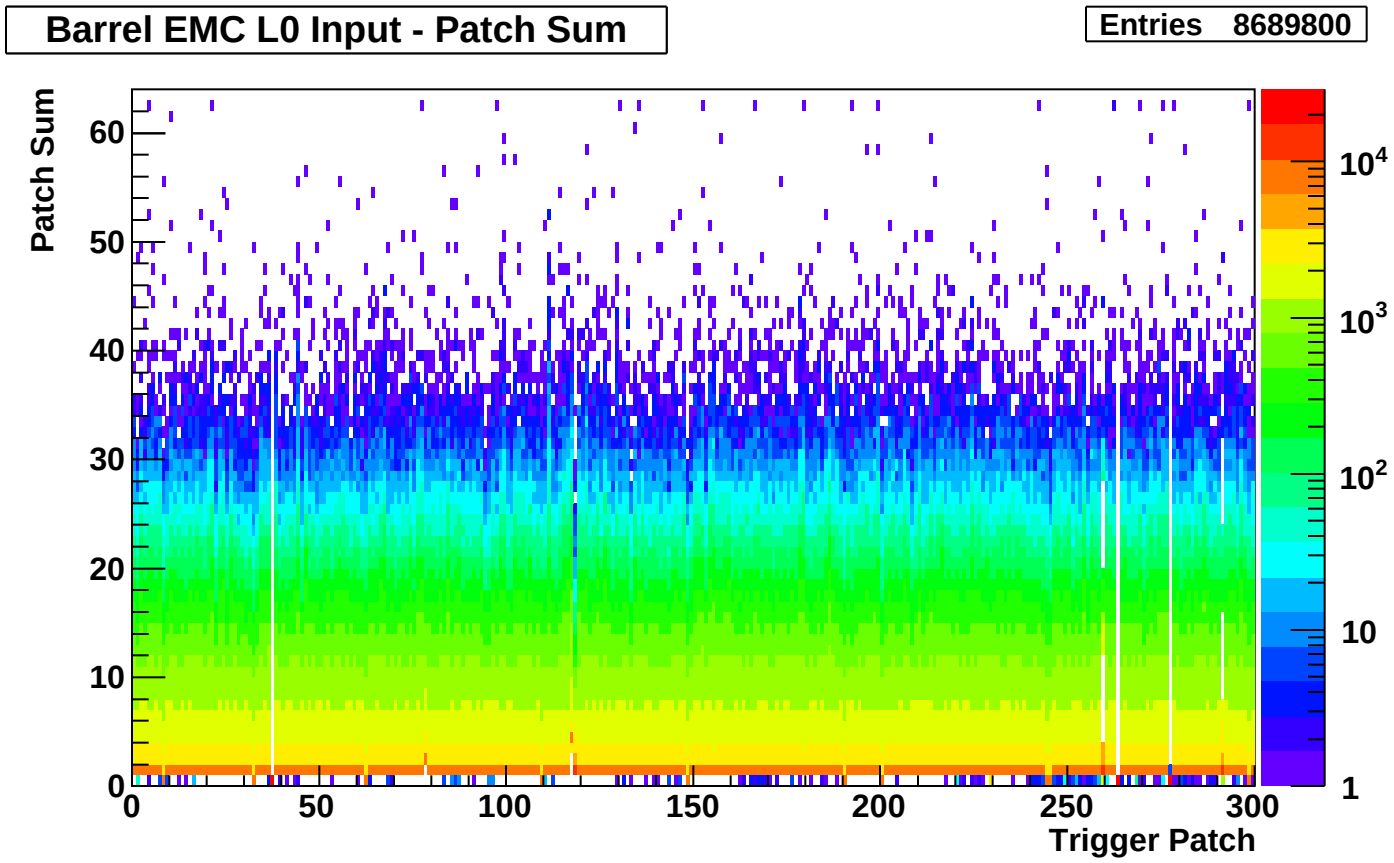

BunchId7Bit (BHT3)
Entries 335
10
1
0
20
40
60
80
100
120

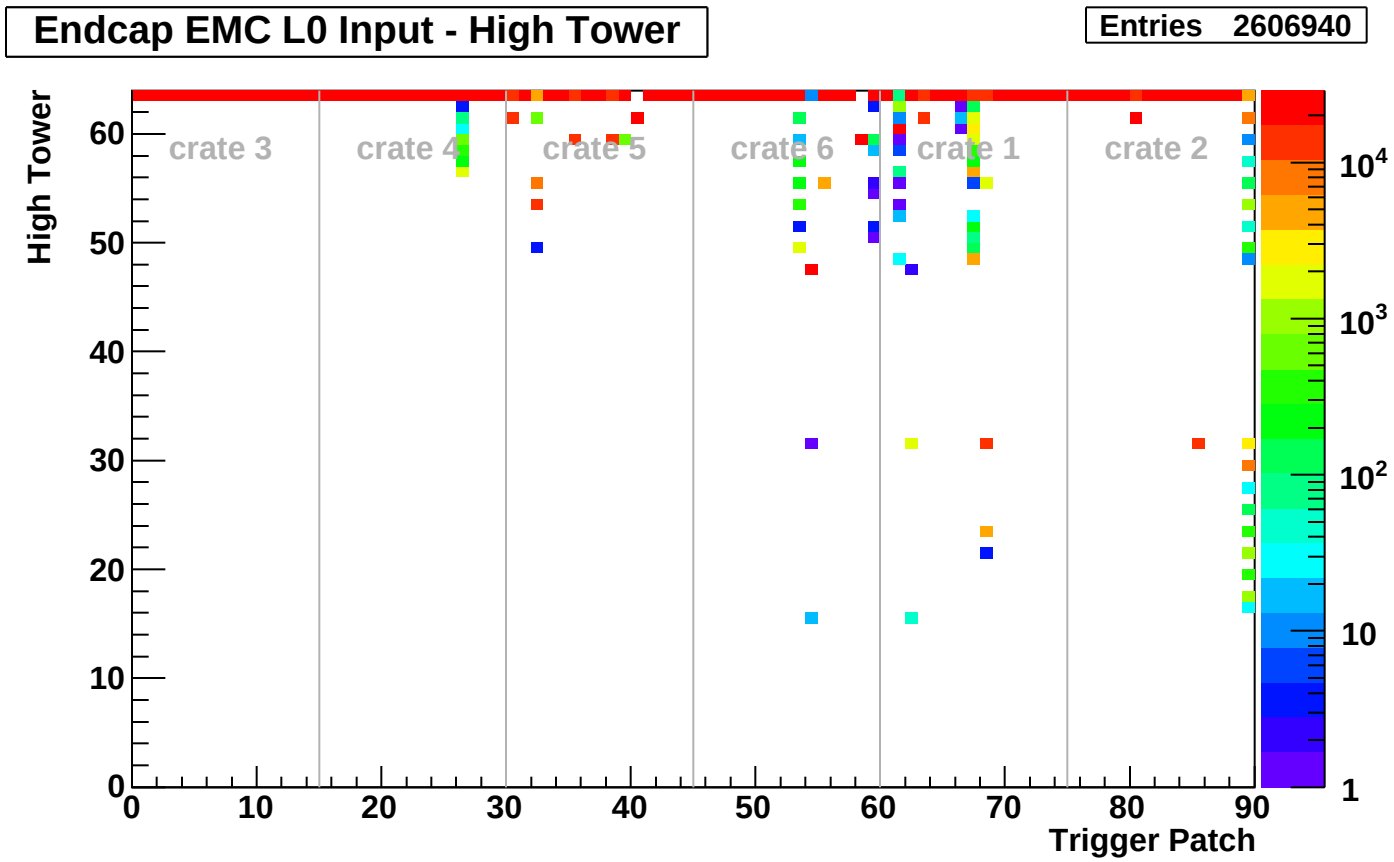

BunchId7Bit (all events)
Entries 28966
$10^2$
10
1
0
20
40
60
80
100
120

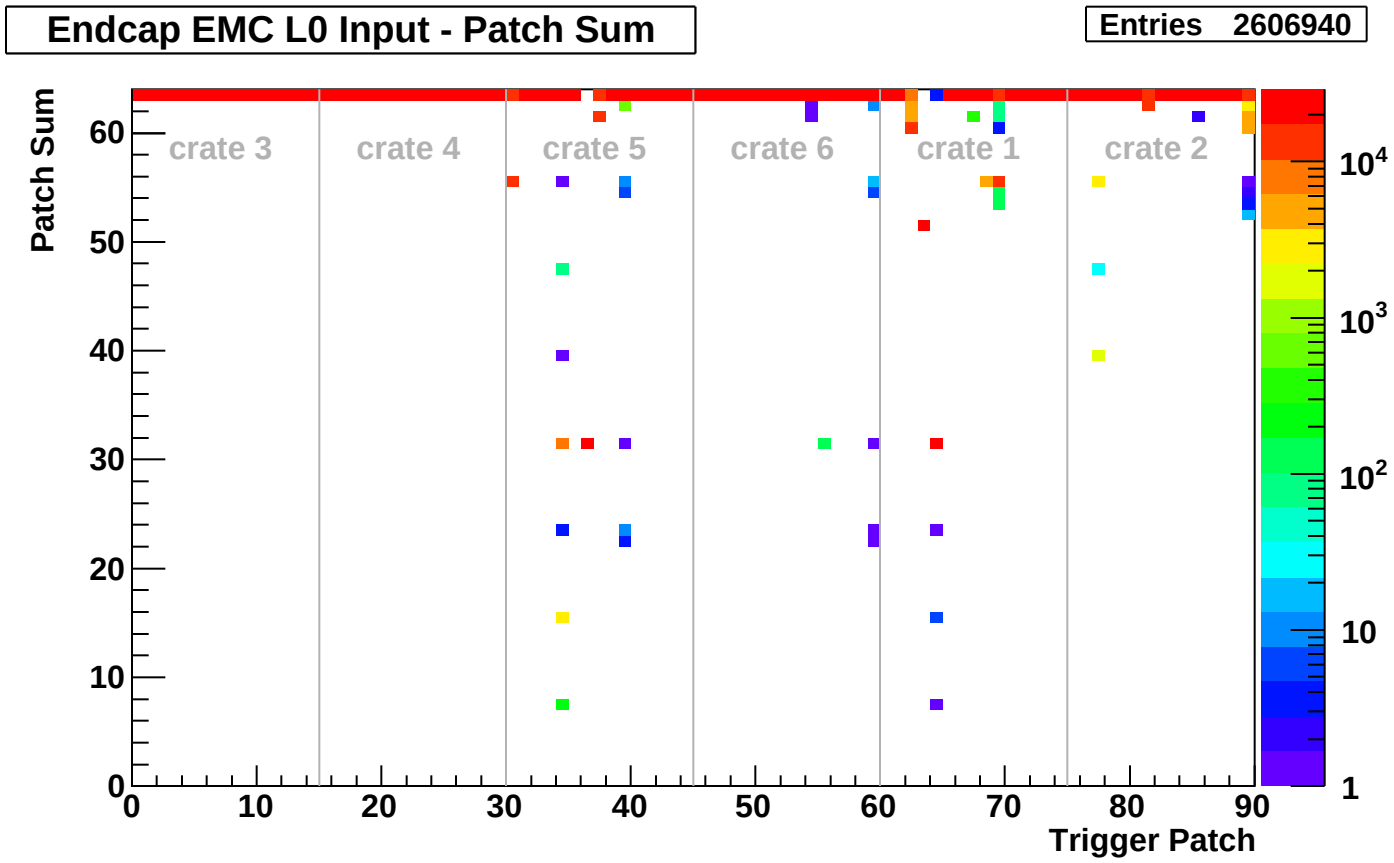

BunchId7Bit (JP1)
Entries 28966
$10^2$
10
1
0
20
40
60
80
100
120

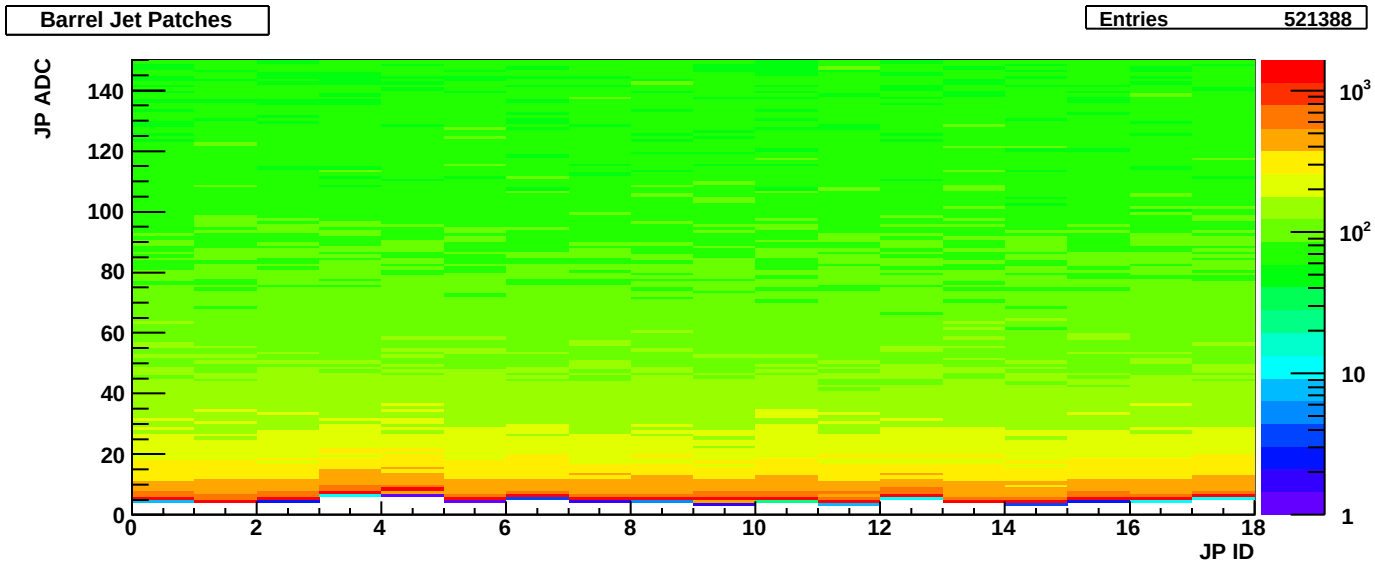

BunchId7Bit (JP2||AJP)
Entries 28966
$10^2$
10
1
0
20
40
60
80
100
120

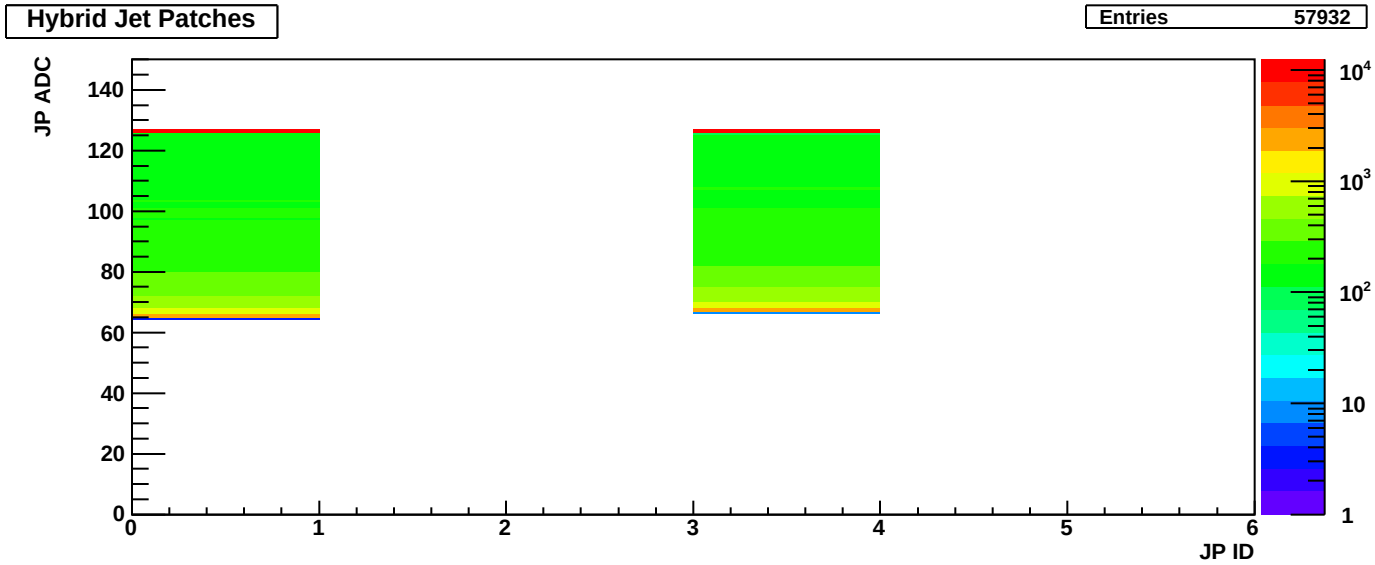

Barrel EMC L0 Input - High Tower
Entries 8689800
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$
$10^4$

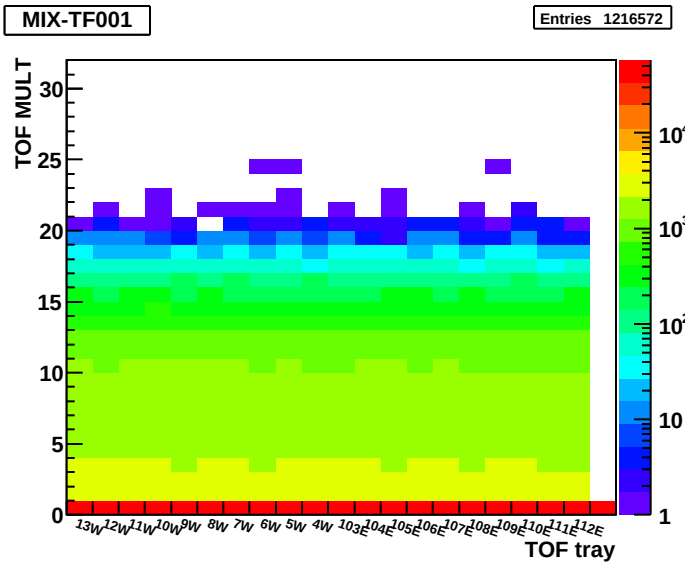

Barrel EMC L0 Input - Patch Sum
Entries 8689800
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$
$10^4$

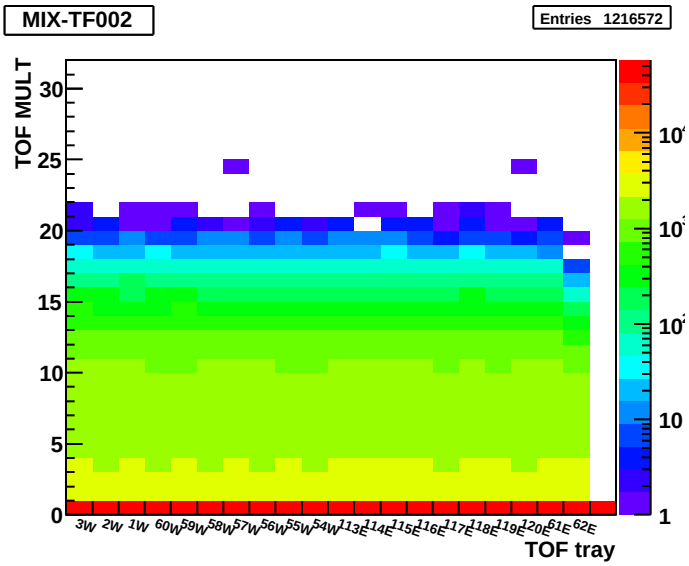

Endcap EMC L0 Input - High Tower
Entries 2606940
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

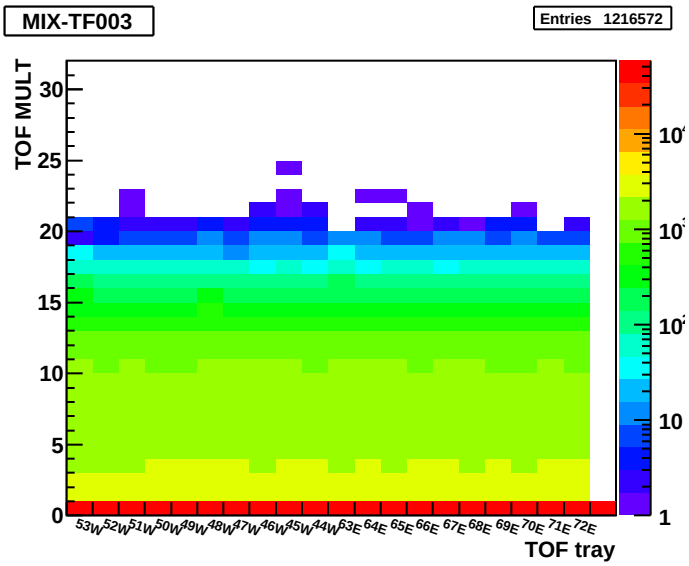

Endcap EMC L0 Input - Patch Sum
Entries 2606940
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

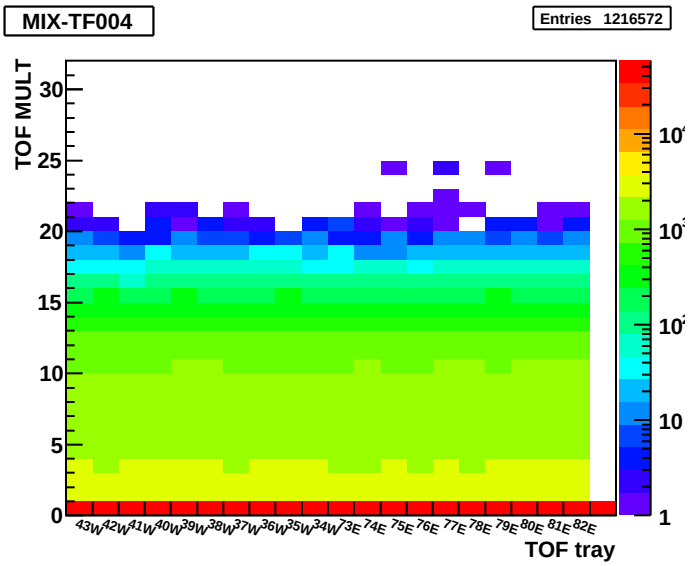

Barrel Jet Patches
Entries 521388
JP ADC
JP ID

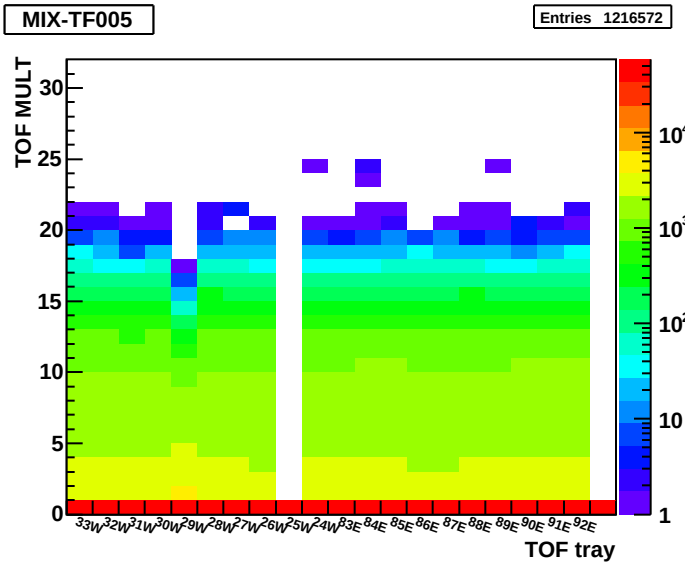

Hybrid Jet Patches
Entries 57932
JP ADC
JP ID

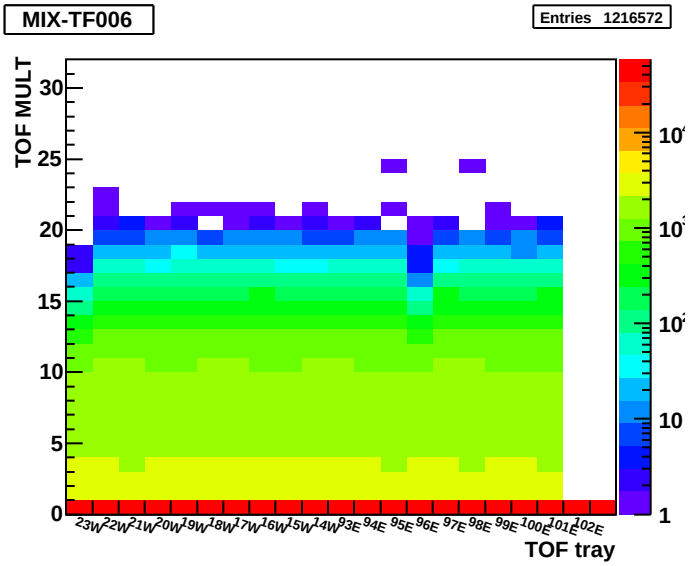

MIX-TF001
Entries 1216572
TOF MULT
TOF tray

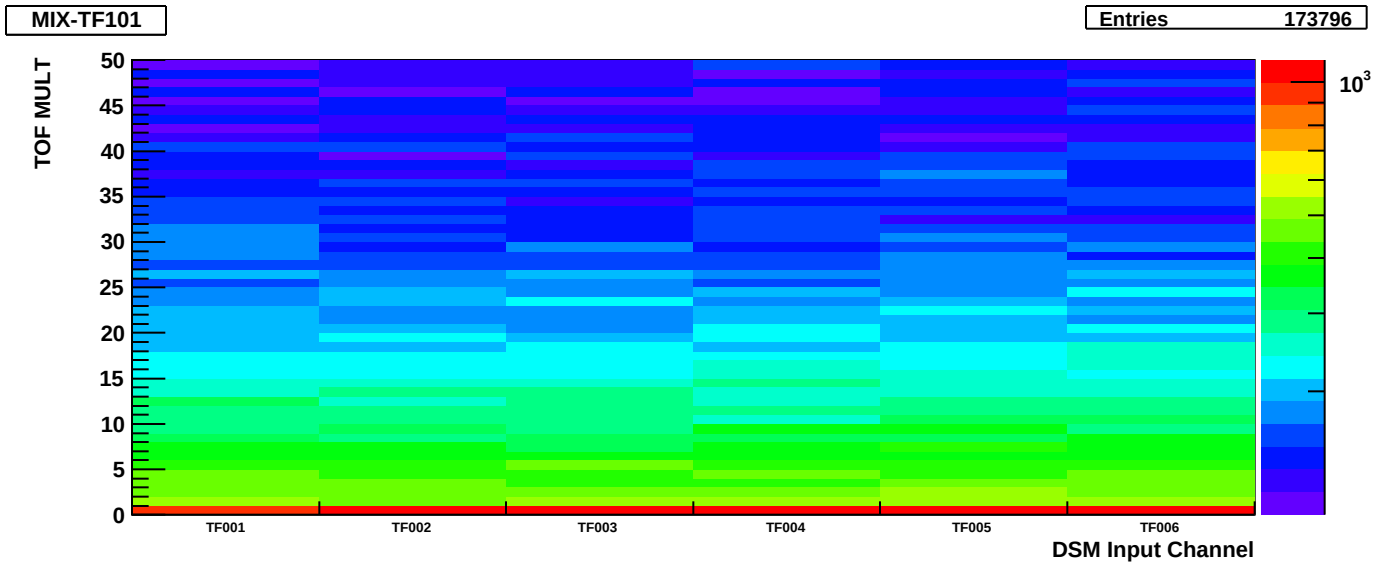

MIX-TF002
Entries 1216572
TOF MULT
TOF tray

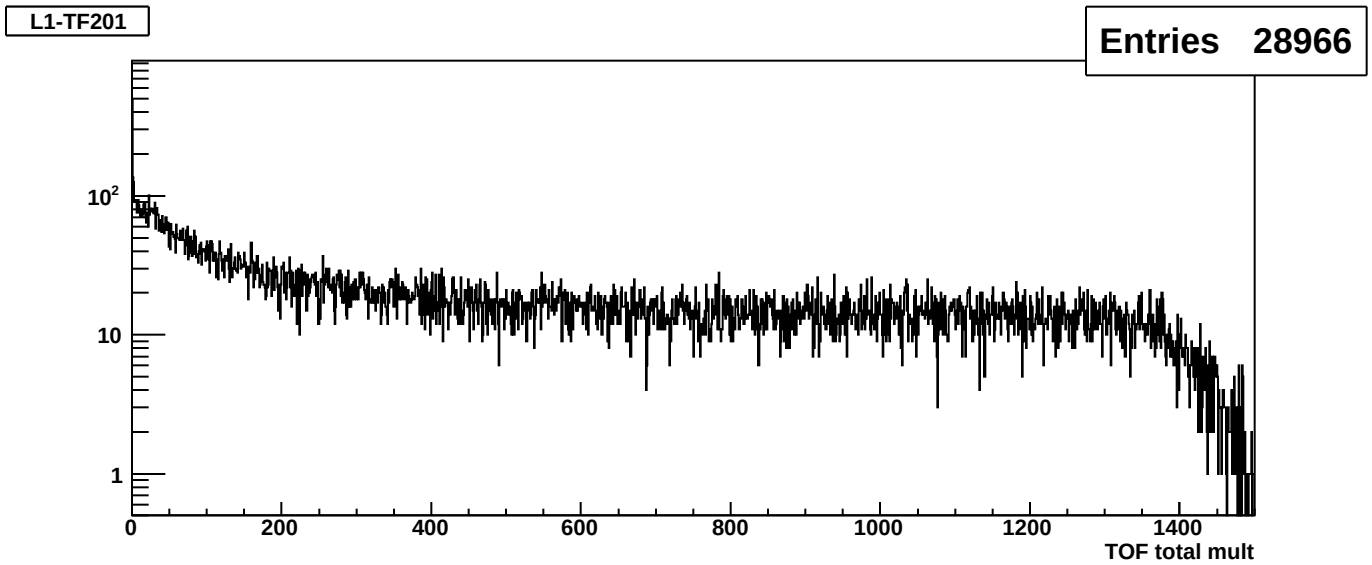

MIX-TF003
Entries 1216572
TOF MULT
TOF tray

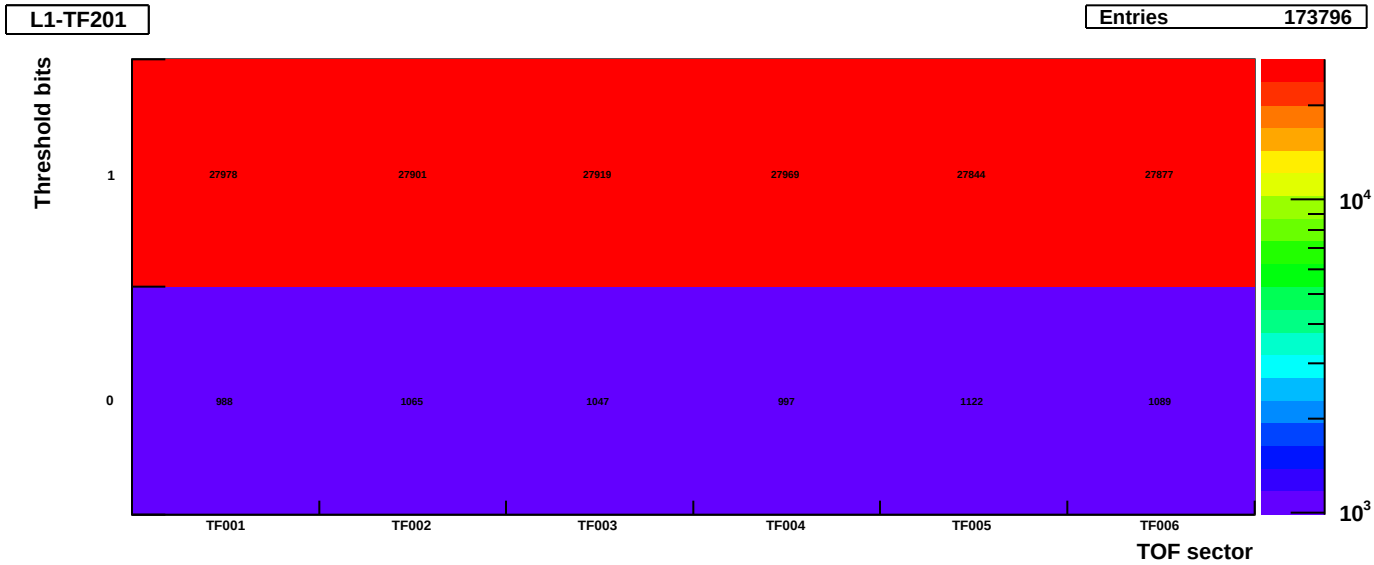

MIX-TF004
Entries 1216572
TOF MULT
TOF tray

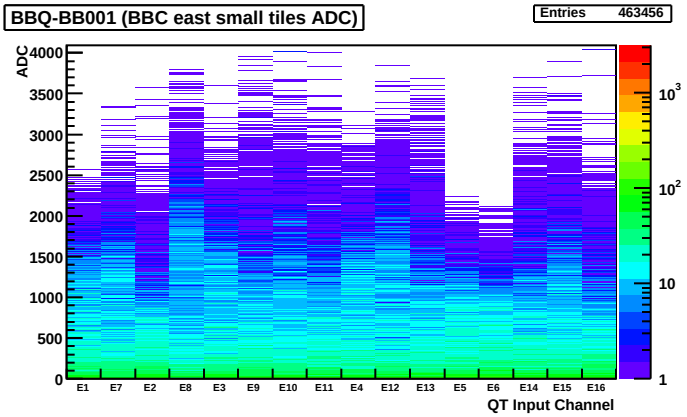

MIX-TF005
Entries 1216572
TOF MULT
TOF tray

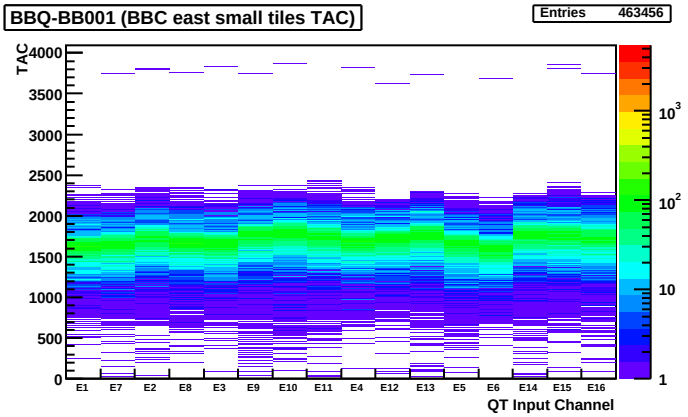

MIX-TF006
Entries 1216572
TOF MULT
TOF tray

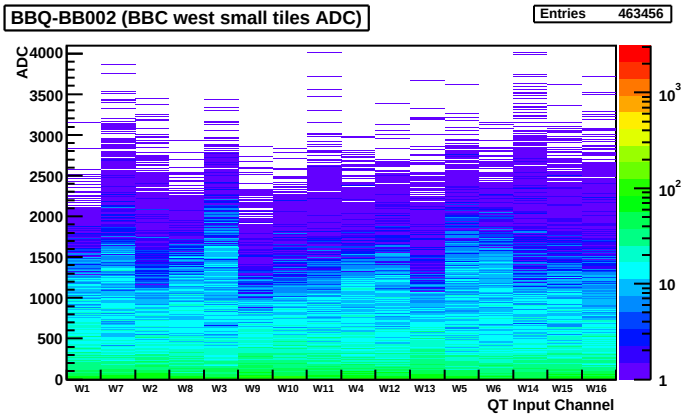

MIX-TF101
Entries 173796
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
$10^3$

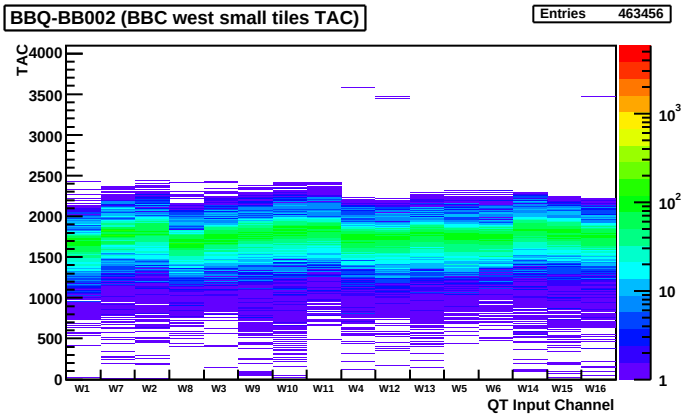

L1-TF201
Entries 28966
$10^2$
10
1
0
200
400
600
800
1000
1200
1400
TOF total mult

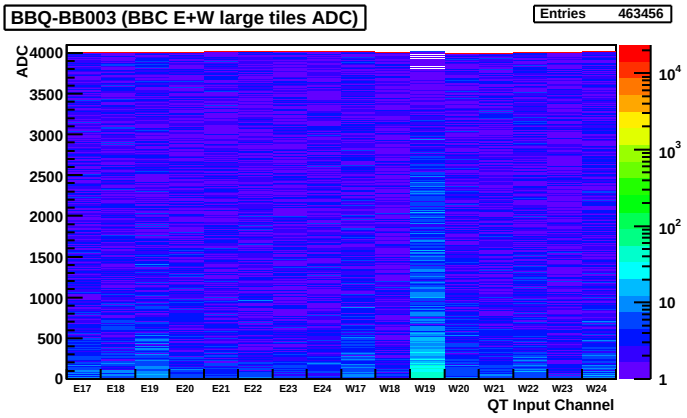

L1-TF201
Entries 173796
Threshold bits
1
0
27978
27901
27919
27969
27844
27877
988
1065
1047
997
1122
1089
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
$10^4$
$10^3$

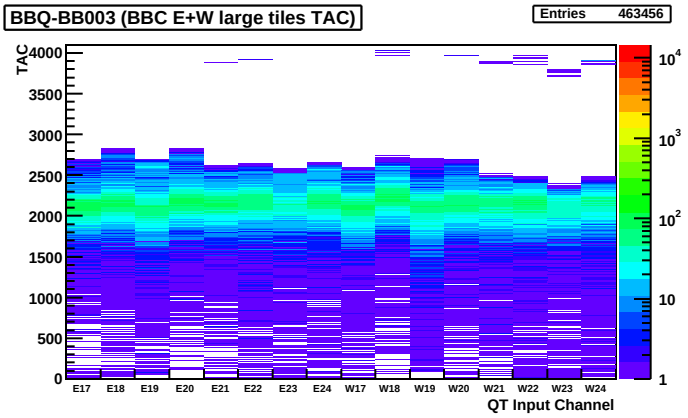

BBQ-BB001 (BBC east small tiles ADC)
Entries 463456
ADC
QT Input Channel

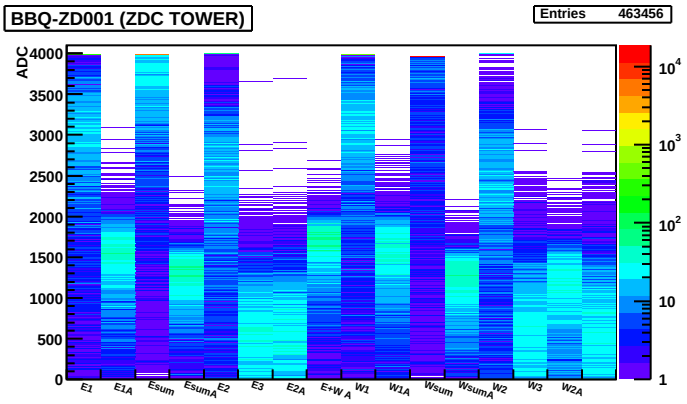

BBQ-BB001 (BBC east small tiles TAC)
Entries 463456
TAC
QT Input Channel

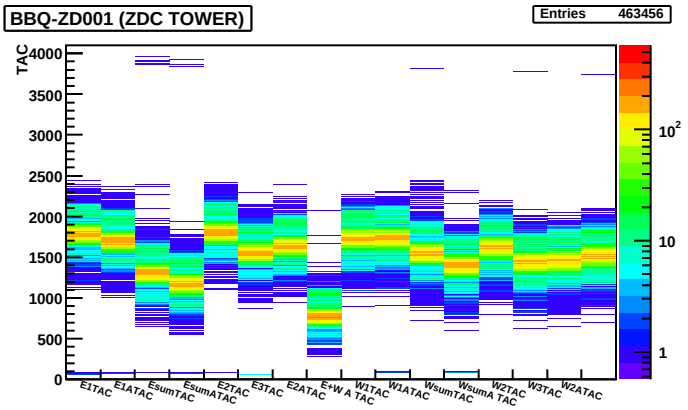

BBQ-BB002 (BBC west small tiles ADC)
Entries 463456
ADC
QT Input Channel

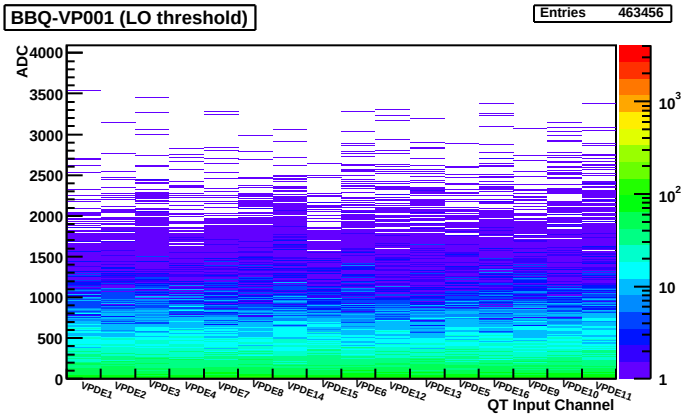

BBQ-BB002 (BBC west small tiles TAC)
Entries 463456
TAC
QT Input Channel

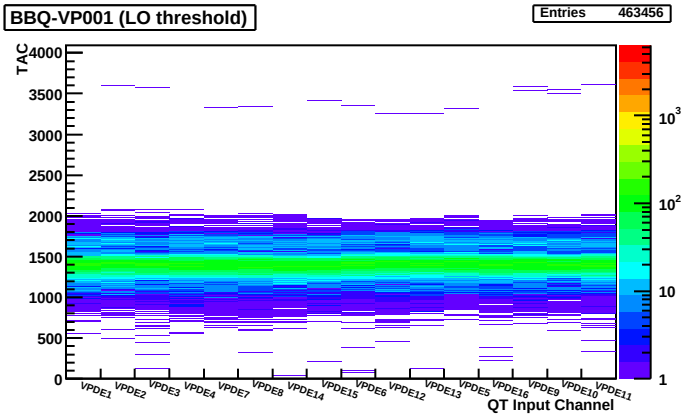

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 463456
ADC
QT Input Channel

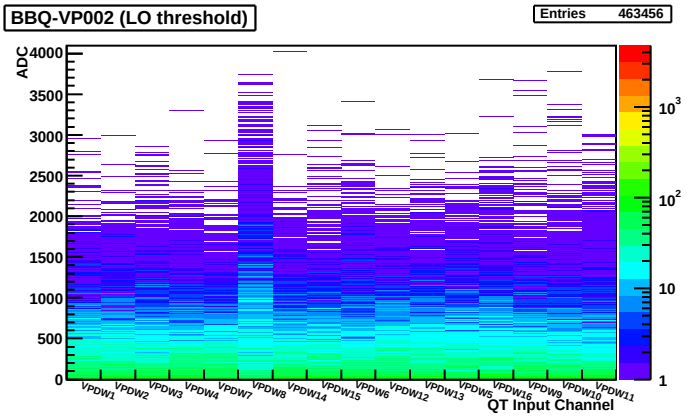

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 463456
TAC
QT Input Channel

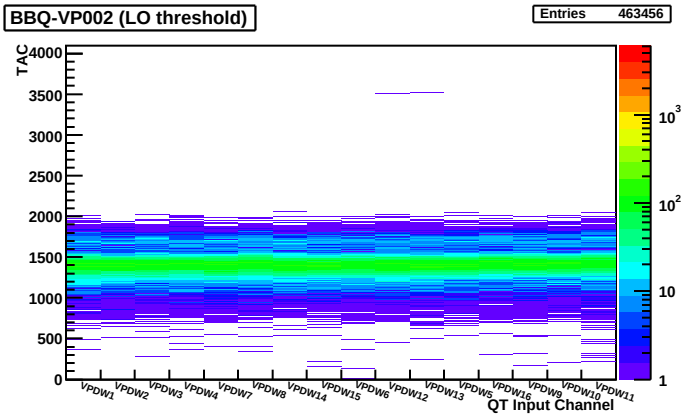

BBQ-ZD001 (ZDC TOWER)
Entries 463456
ADC

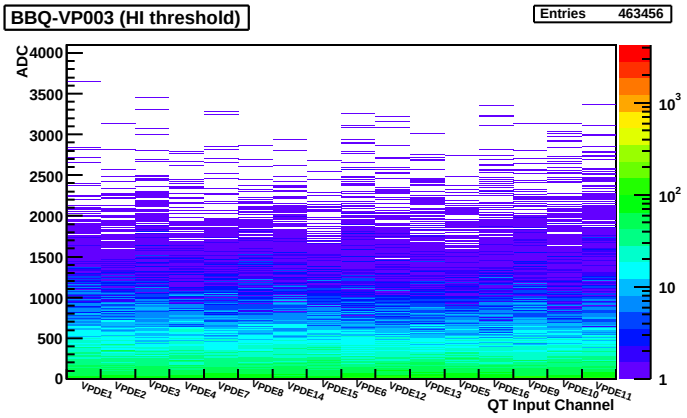

BBQ-ZD001 (ZDC TOWER)
Entries 463456
TAC

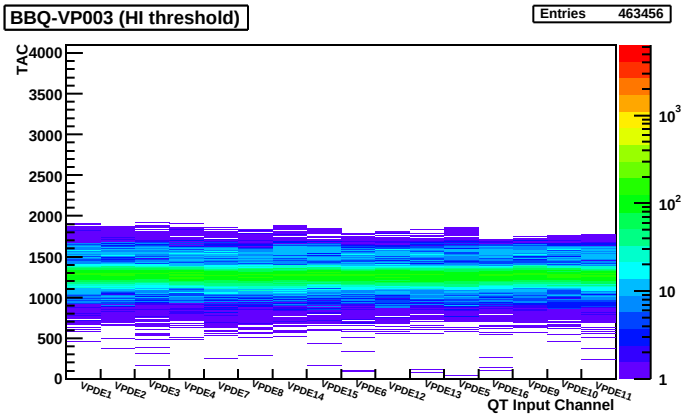

BBQ-VP001 (LO threshold)
Entries 463456
ADC
QT Input Channel

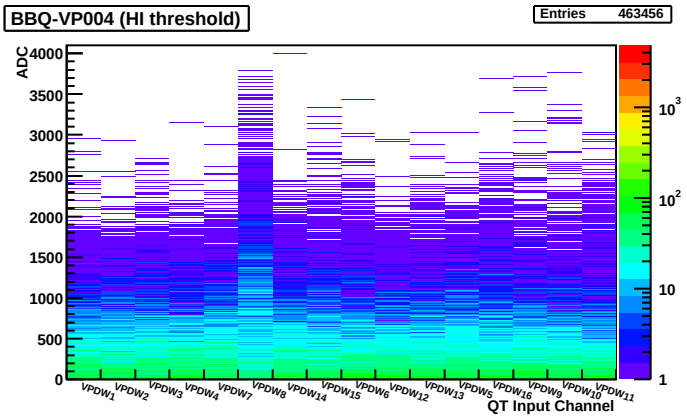

BBQ-VP001 (LO threshold)
Entries 463456
TAC
QT Input Channel

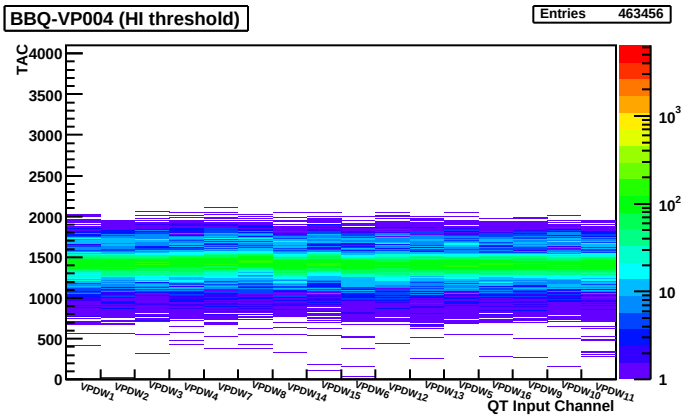

BBQ-VP002 (LO threshold)
Entries 463456
ADC
QT Input Channel

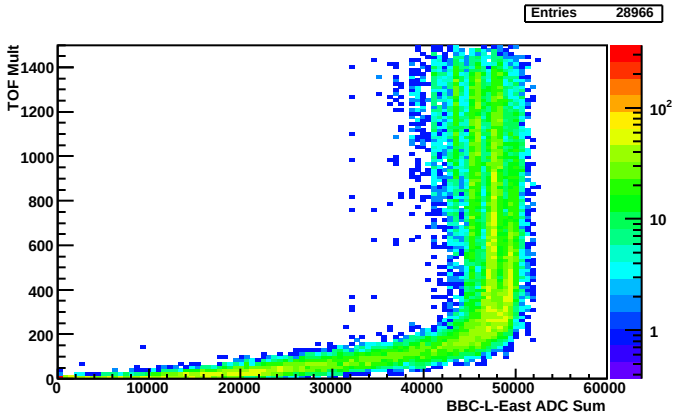

BBQ-VP002 (LO threshold)
Entries 463456
TAC
QT Input Channel

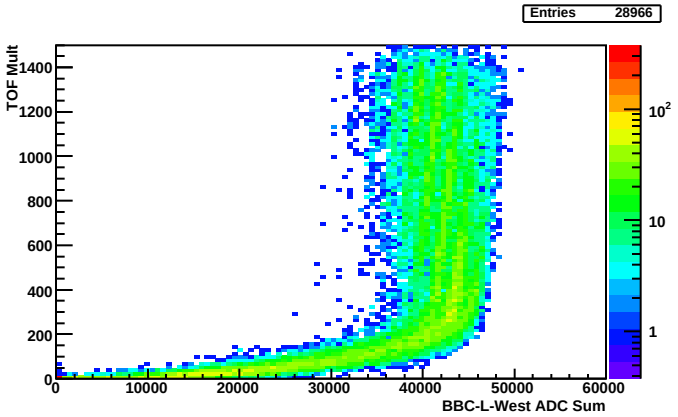

BBQ-VP003 (HI threshold)
Entries 463456
ADC
QT Input Channel

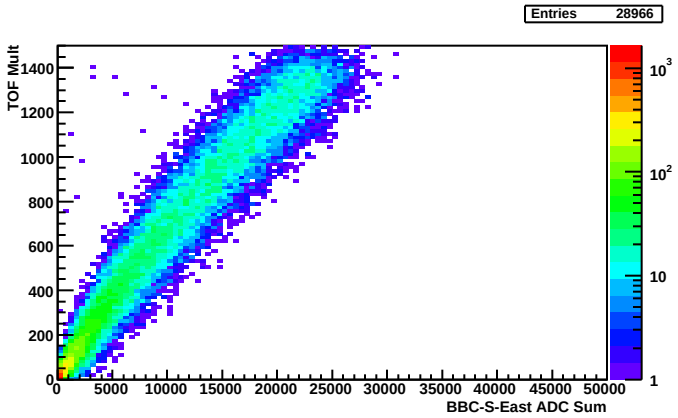

BBQ-VP003 (HI threshold)
Entries 463456
TAC
QT Input Channel

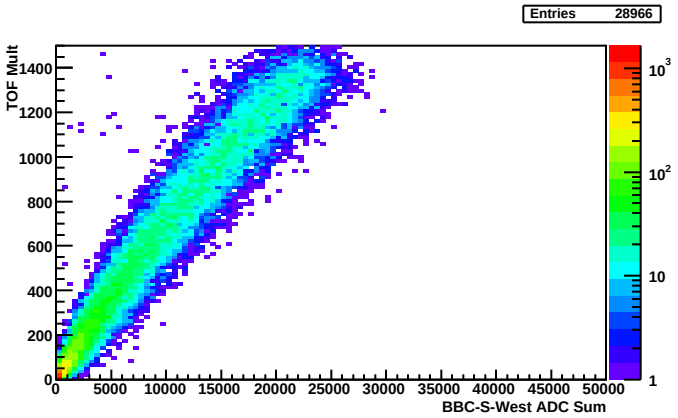

BBQ-VP004 (HI threshold)
Entries 463456
ADC
QT Input Channel

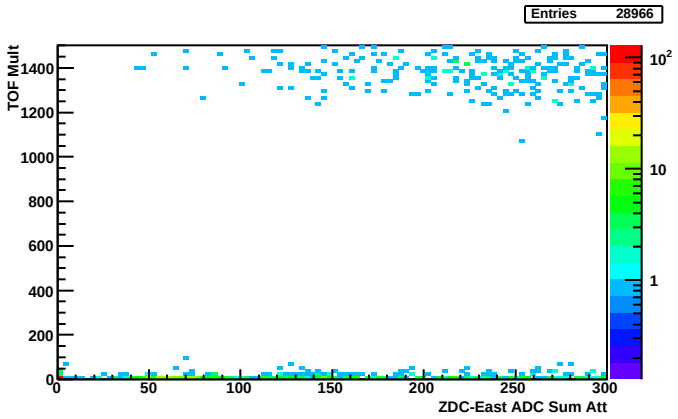

BBQ-VP004 (HI threshold)
Entries 463456
TAC
QT Input Channel

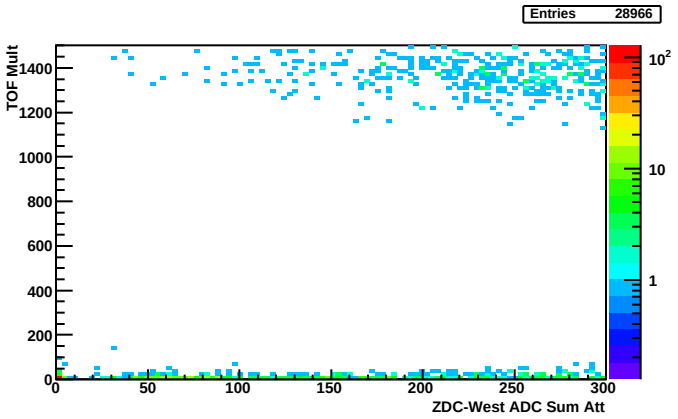

Entries 28966
TOF Mult
BBC-L-East ADC Sum

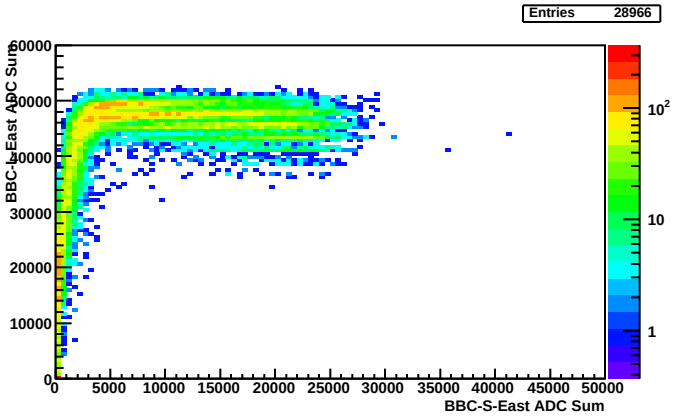

Entries 28966
TOF Mult
BBC-L-West ADC Sum

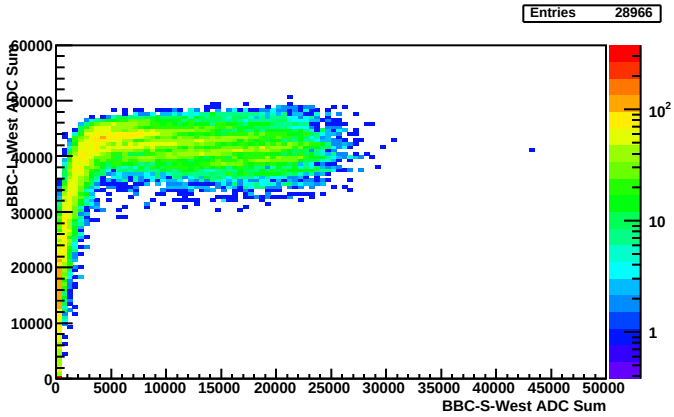

Entries 28966
TOF Mult
BBC-S-East ADC Sum

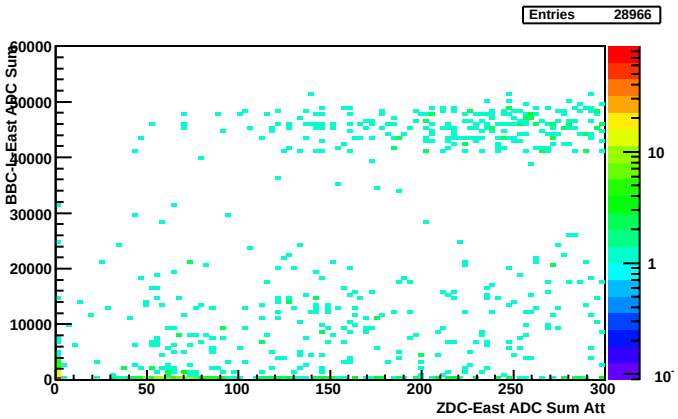

Entries 28966
TOF Mult
BBC-S-West ADC Sum

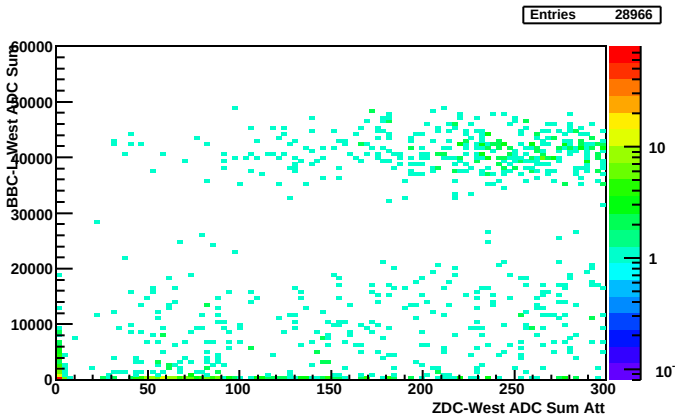

Entries 28966
TOF Mult
ZDC-East ADC Sum Att

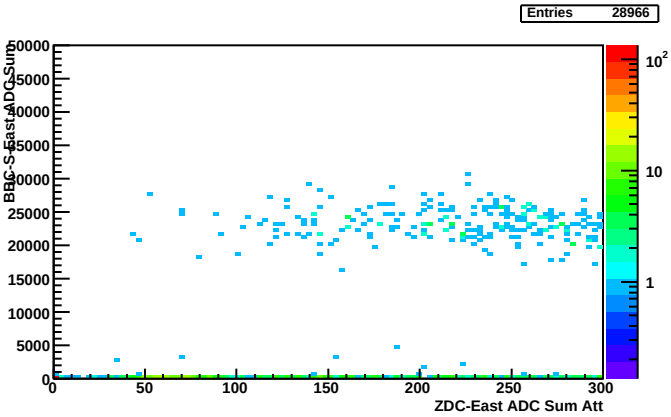

Entries 28966
TOF Mult
ZDC-West ADC Sum Att

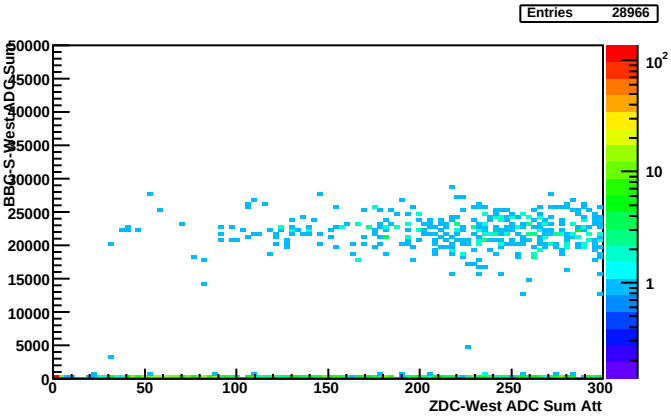

Entries 28966
BBC-L-East ADC Sum
BBC-S-East ADC Sum

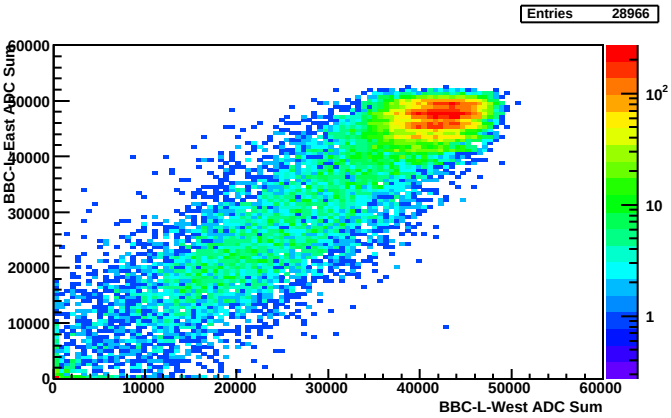

Entries 28966
BBC-L-West ADC Sum
BBC-S-West ADC Sum

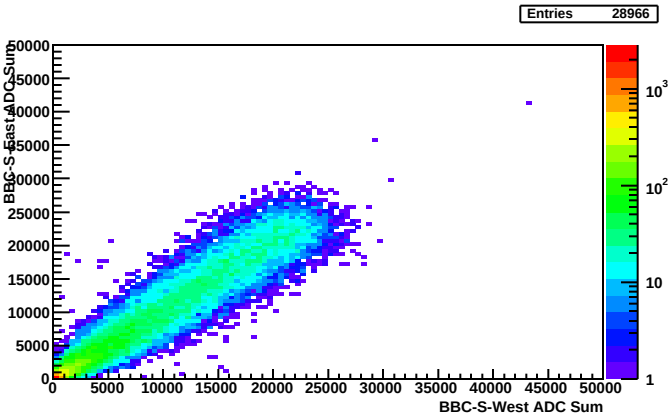

Entries 28966
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

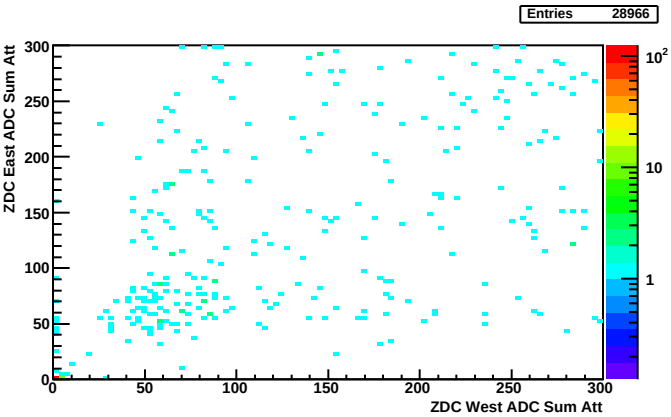

Entries 28966
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

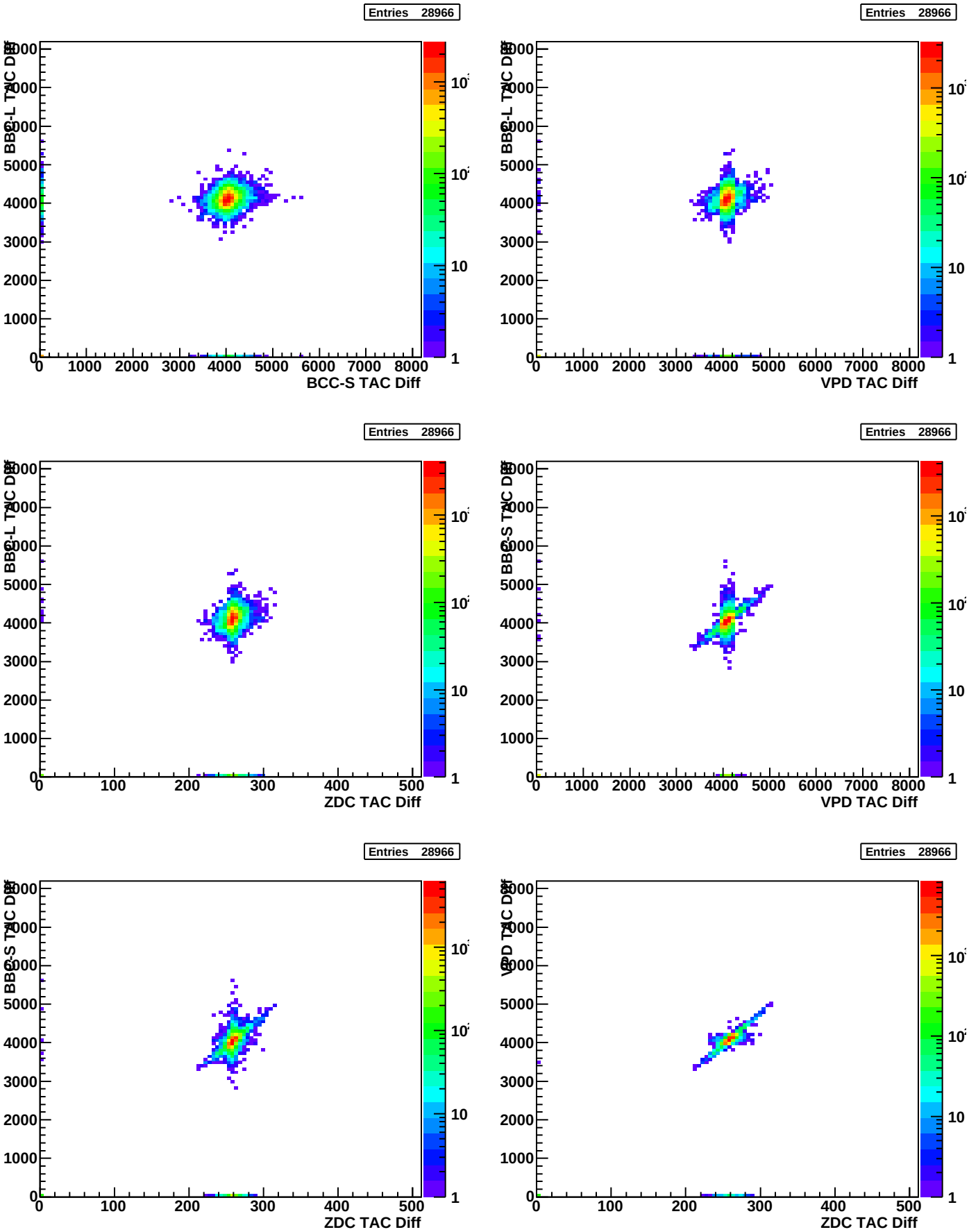

Entries 28966
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

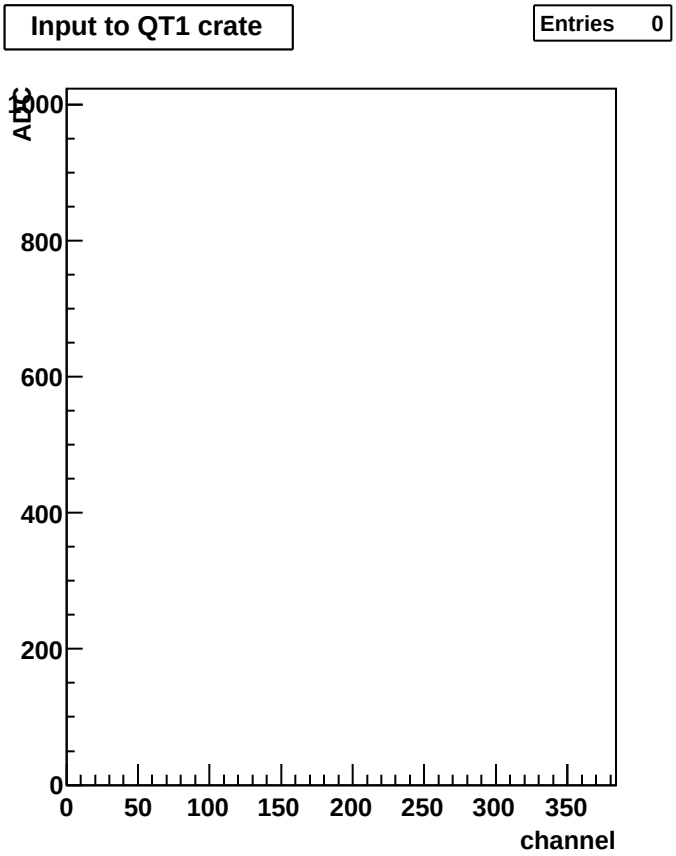

Entries 28966
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

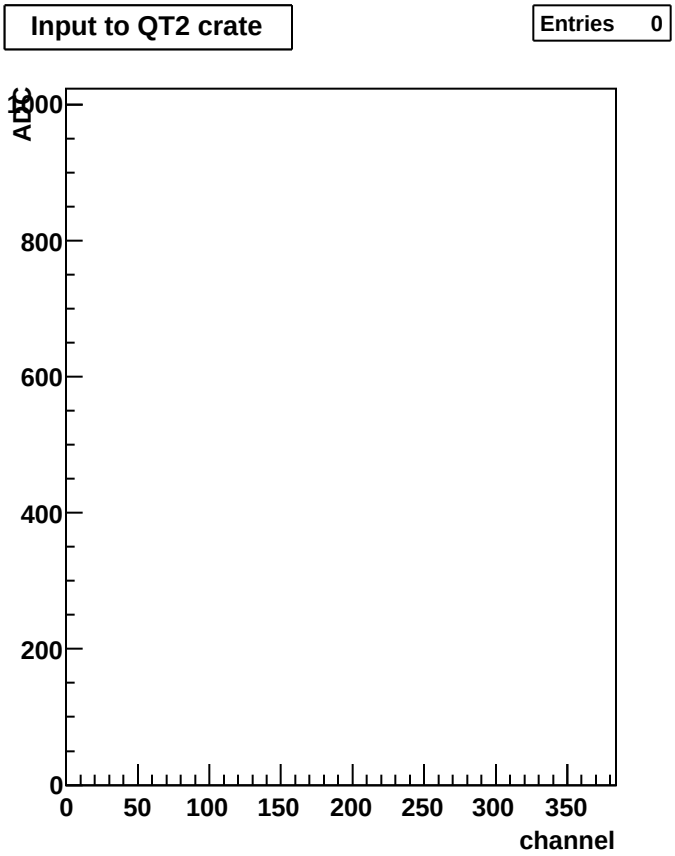

Entries 28966
BBC-L-East ADC Sum
BBC-L-West ADC Sum

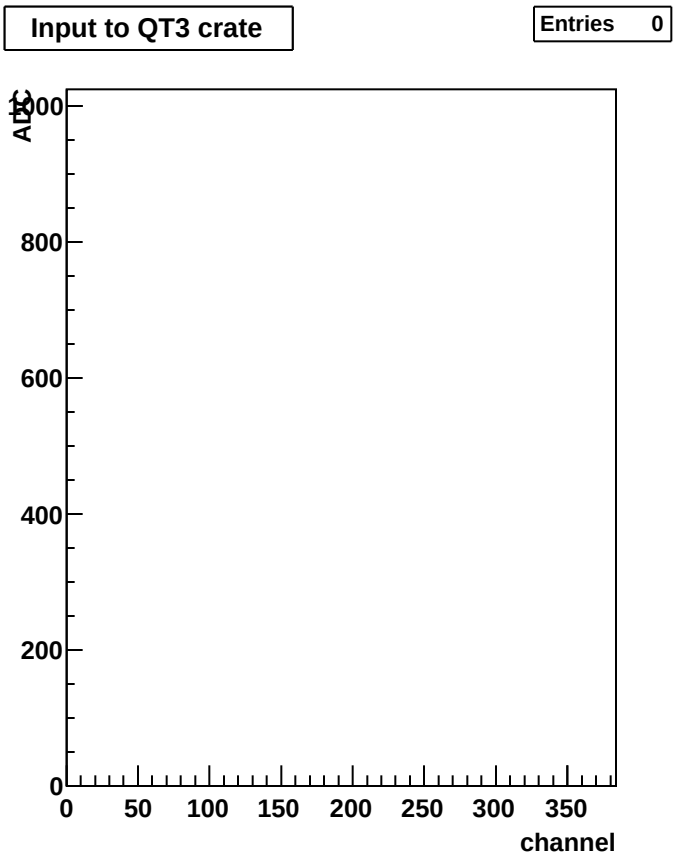

Entries 28966
BBC-S-East ADC Sum
BBC-S-West ADC Sum

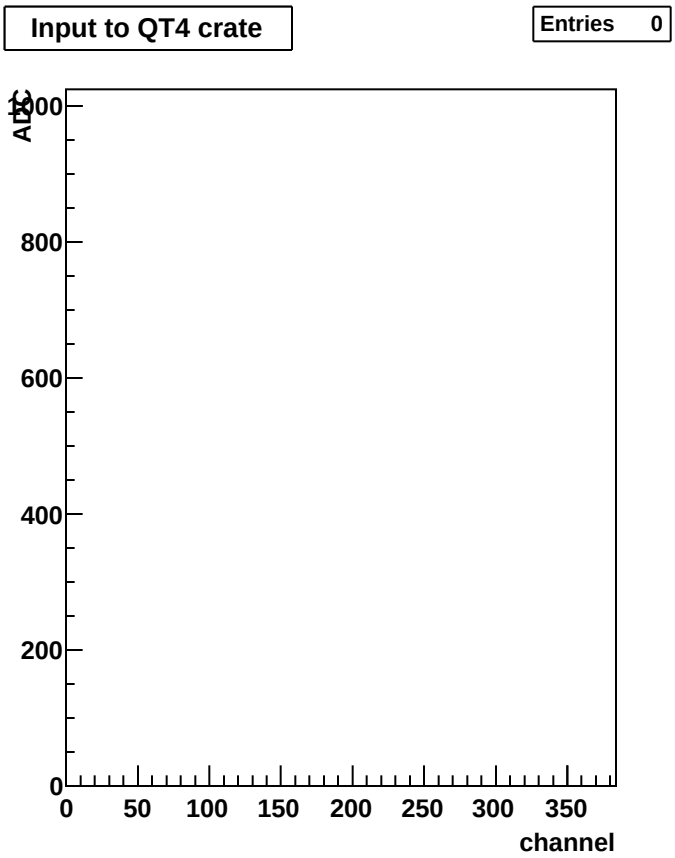

Entries 28966
ZDC East ADC Sum Att
ZDC West ADC Sum Att

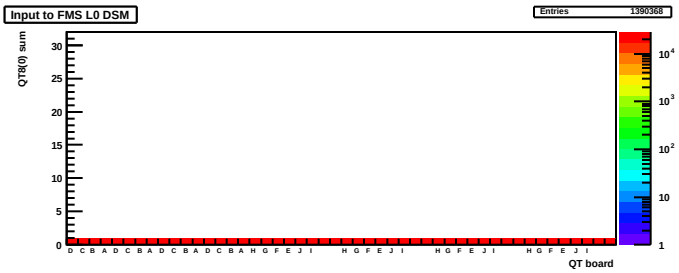

Entries 28966
BBC-L TAC Diff
BCC-S TAC Diff
VPD TAC Diff
ZDC TAC Diff
BBC-S TAC Diff

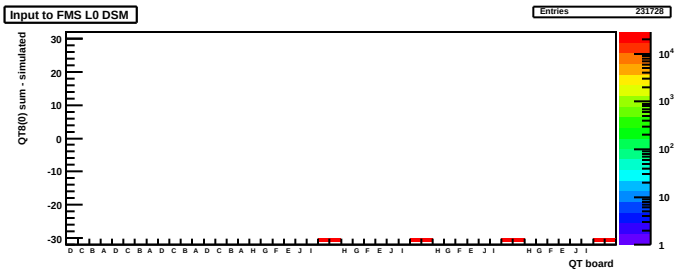

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

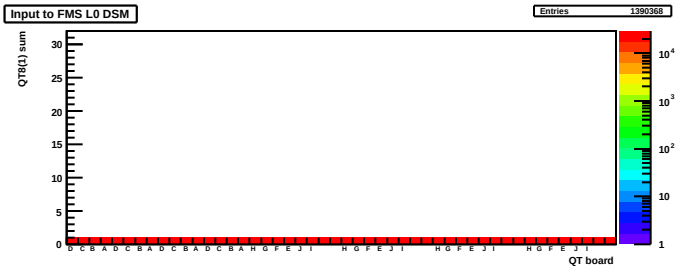

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

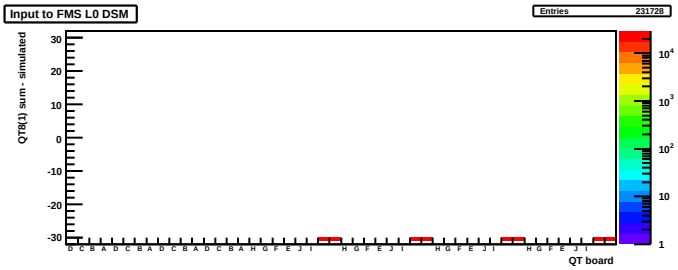

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

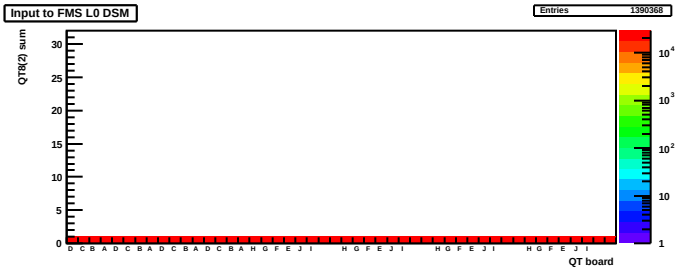

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

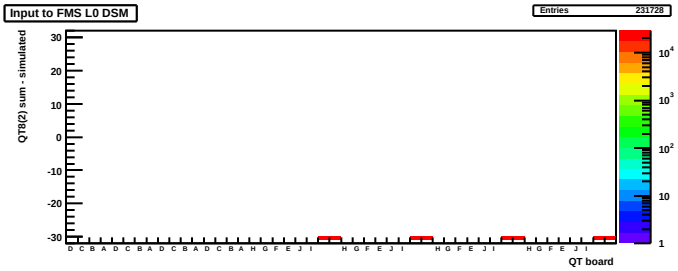

Input to FMS L0 DSM
Entries 1390368
QT8(0) sum
QT board

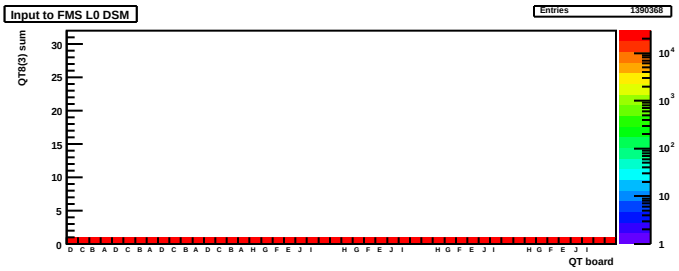

Input to FMS L0 DSM
Entries 231728
QT8(0) sum - simulated
QT board

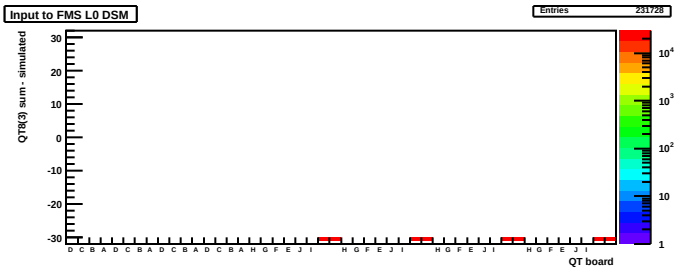

Input to FMS L0 DSM
Entries 1390368
QT8(1) sum
QT board

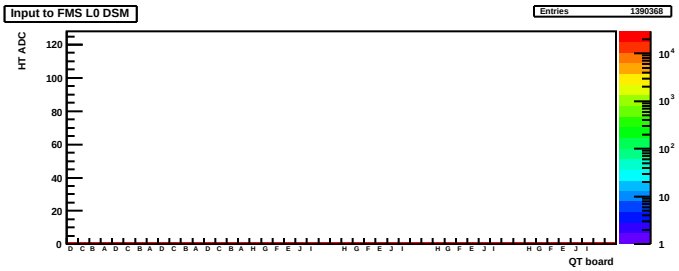

Input to FMS L0 DSM
Entries 231728
QT8(1) sum - simulated
QT board

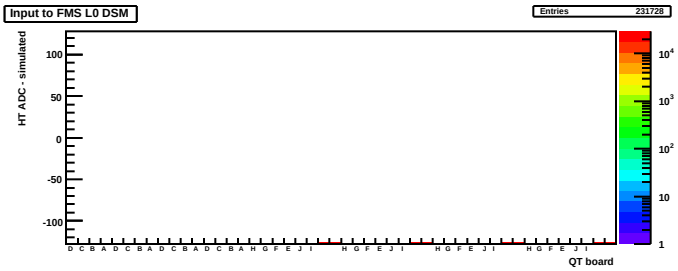

Input to FMS L0 DSM
Entries 1390368
QT8(2) sum
QT board

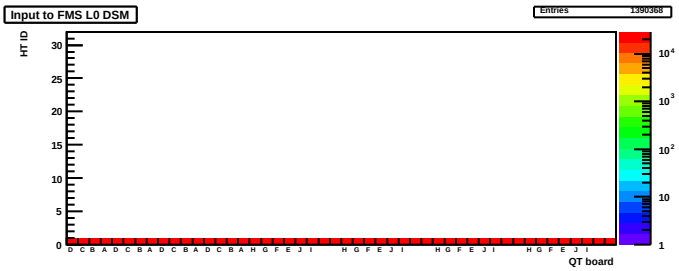

Input to FMS L0 DSM
Entries 231728
QT8(2) sum - simulated
QT board

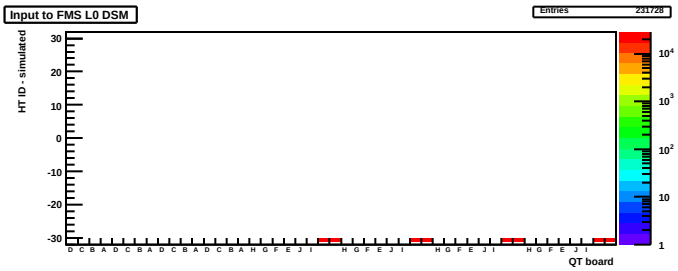

Input to FMS L0 DSM
Entries 1390368
QT8(3) sum
QT board

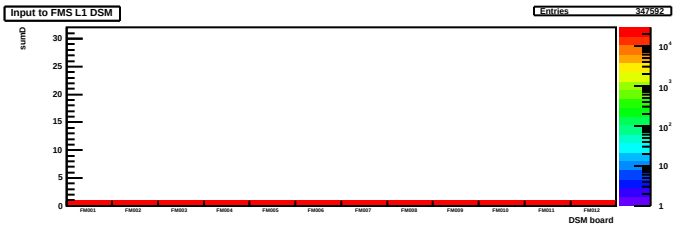

Input to FMS L0 DSM
Entries 231728
QT8(3) sum - simulated
QT board

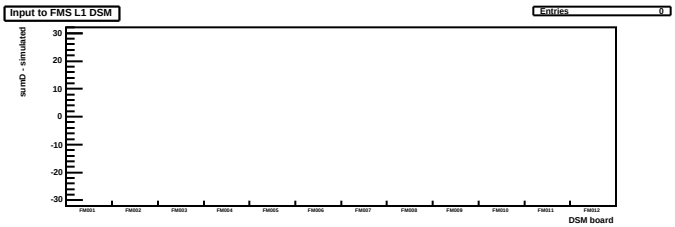

Input to FMS L0 DSM
Entries 1390368
HT ADC
QT board

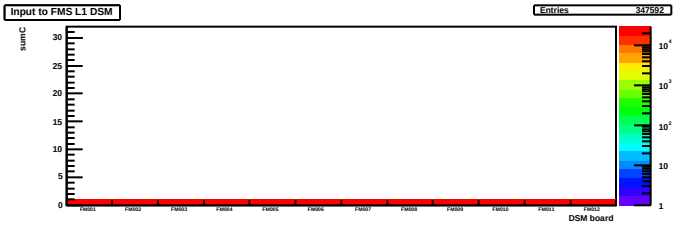

Input to FMS L0 DSM
Entries 231728
HT ADC - simulated
QT board

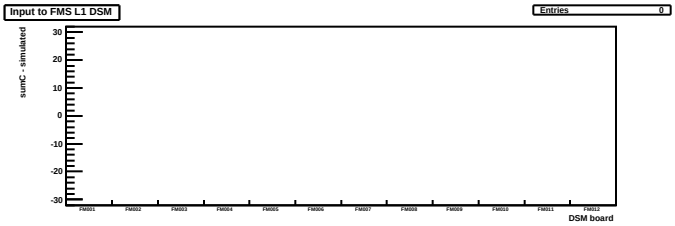

Input to FMS L0 DSM
Entries 1390368
HT ID
QT board

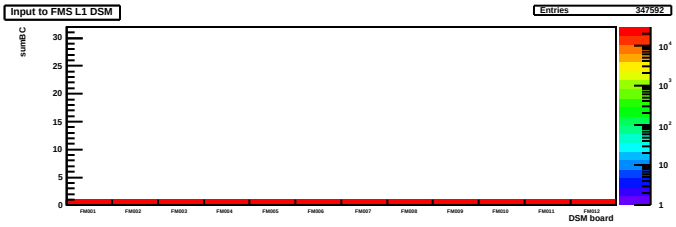

Input to FMS L0 DSM
Entries 231728
HT ID - simulated
QT board

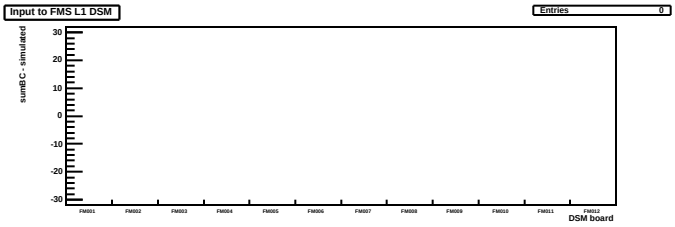

Input to FMS L1 DSM
Entries 347592
sumD
DSM board

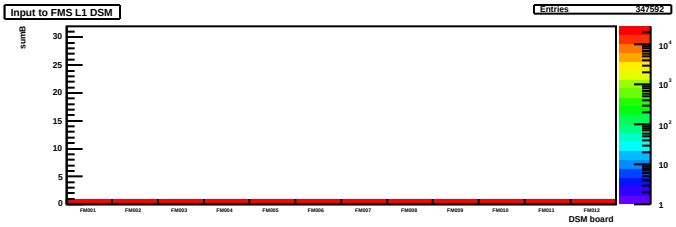

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

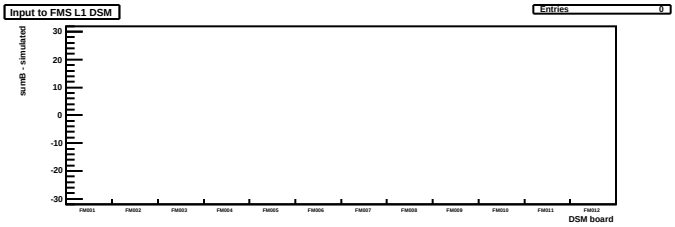

Input to FMS L1 DSM
Entries 347592
sumC
DSM board

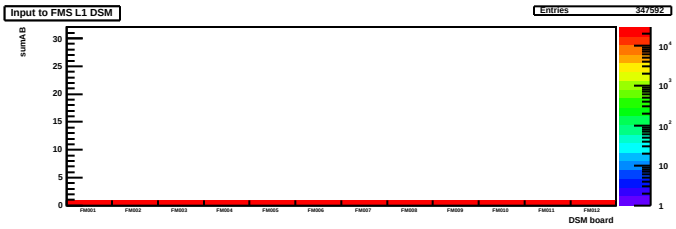

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

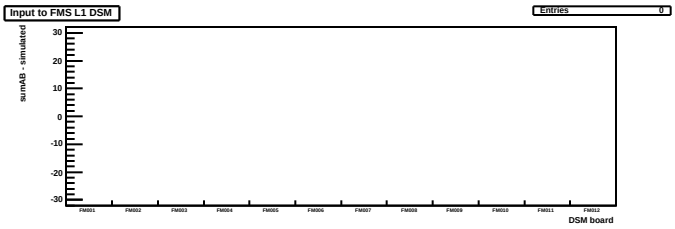

Input to FMS L1 DSM
Entries 347592
sumBC
DSM board

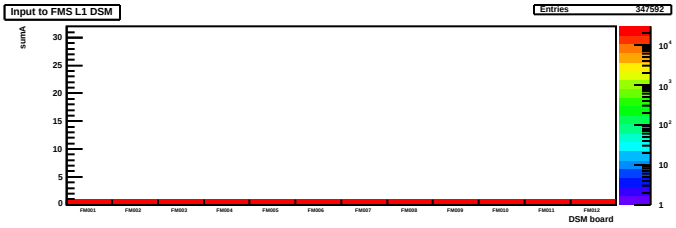

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

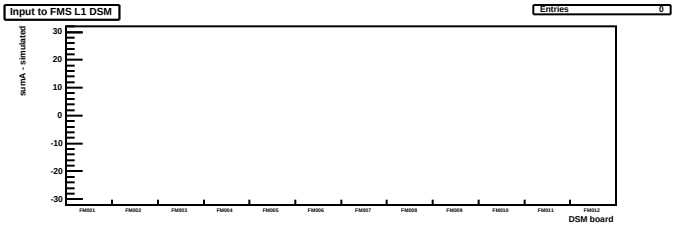

Input to FMS L1 DSM
Entries 347592
sumB
DSM board

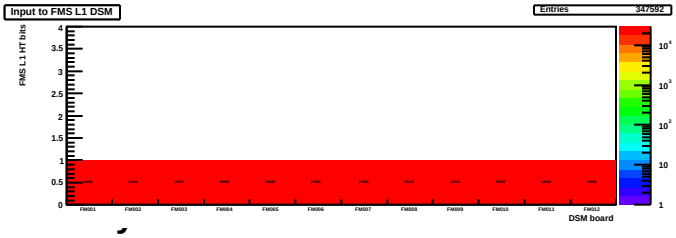

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

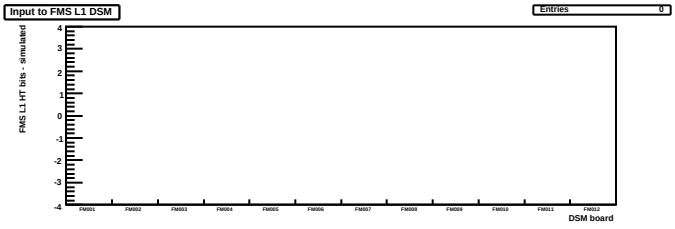

Input to FMS L1 DSM
Entries 347592
sumAB
DSM board

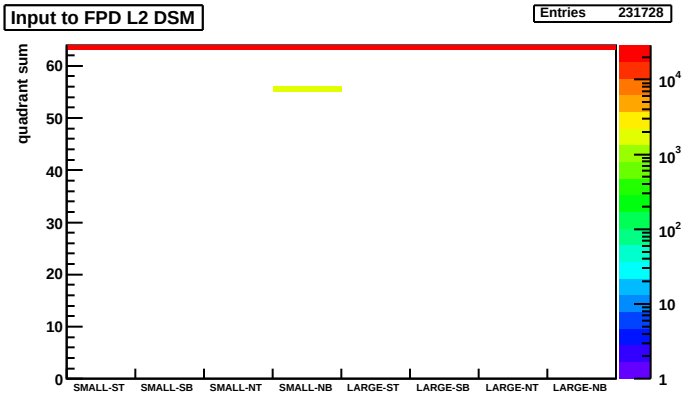

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

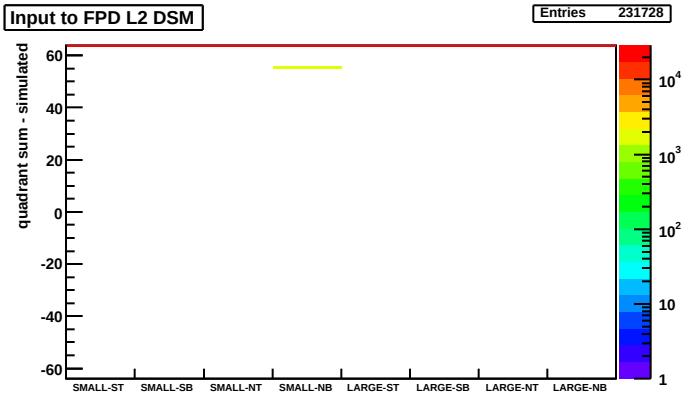

Input to FMS L1 DSM
Entries 347592
sumA
DSM board

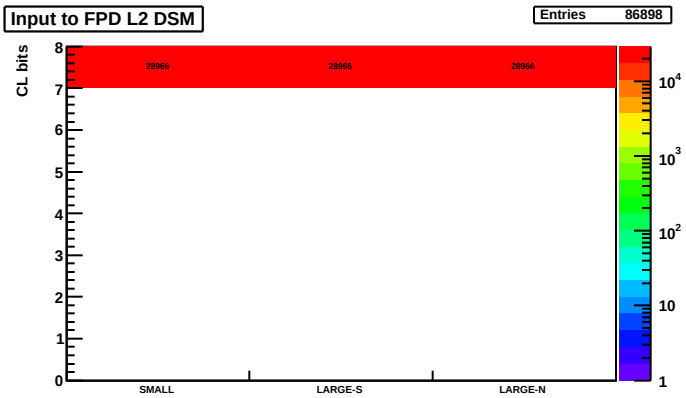

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

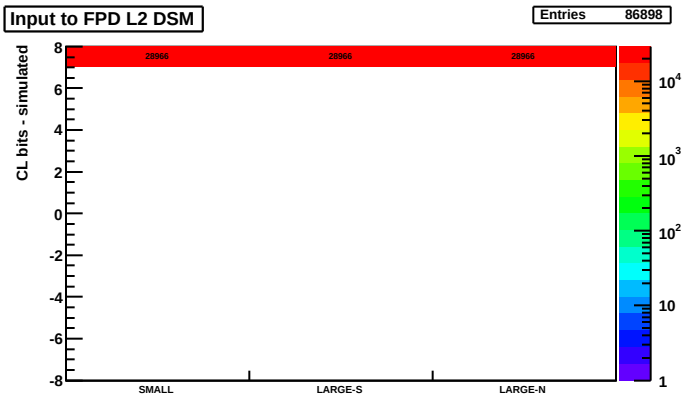

Input to FMS L1 DSM
Entries 347592
FMS L1 HT bits
DSM board

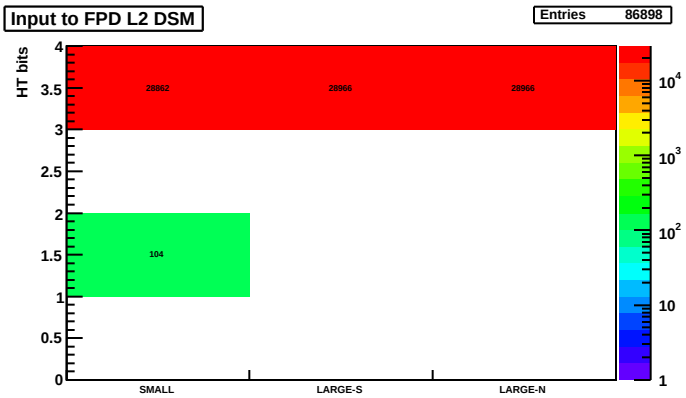

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

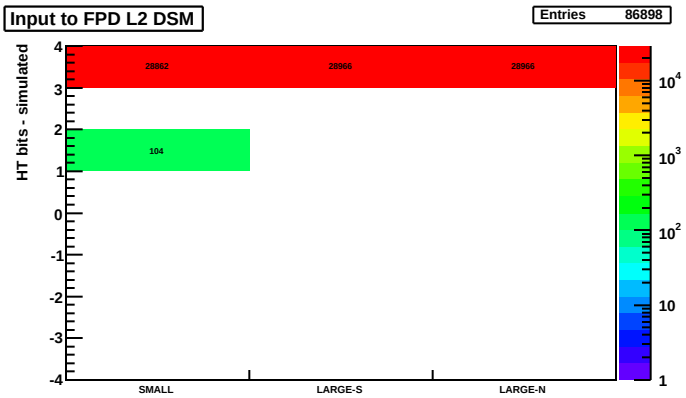

Input to FPD L2 DSM
Entries 231728
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

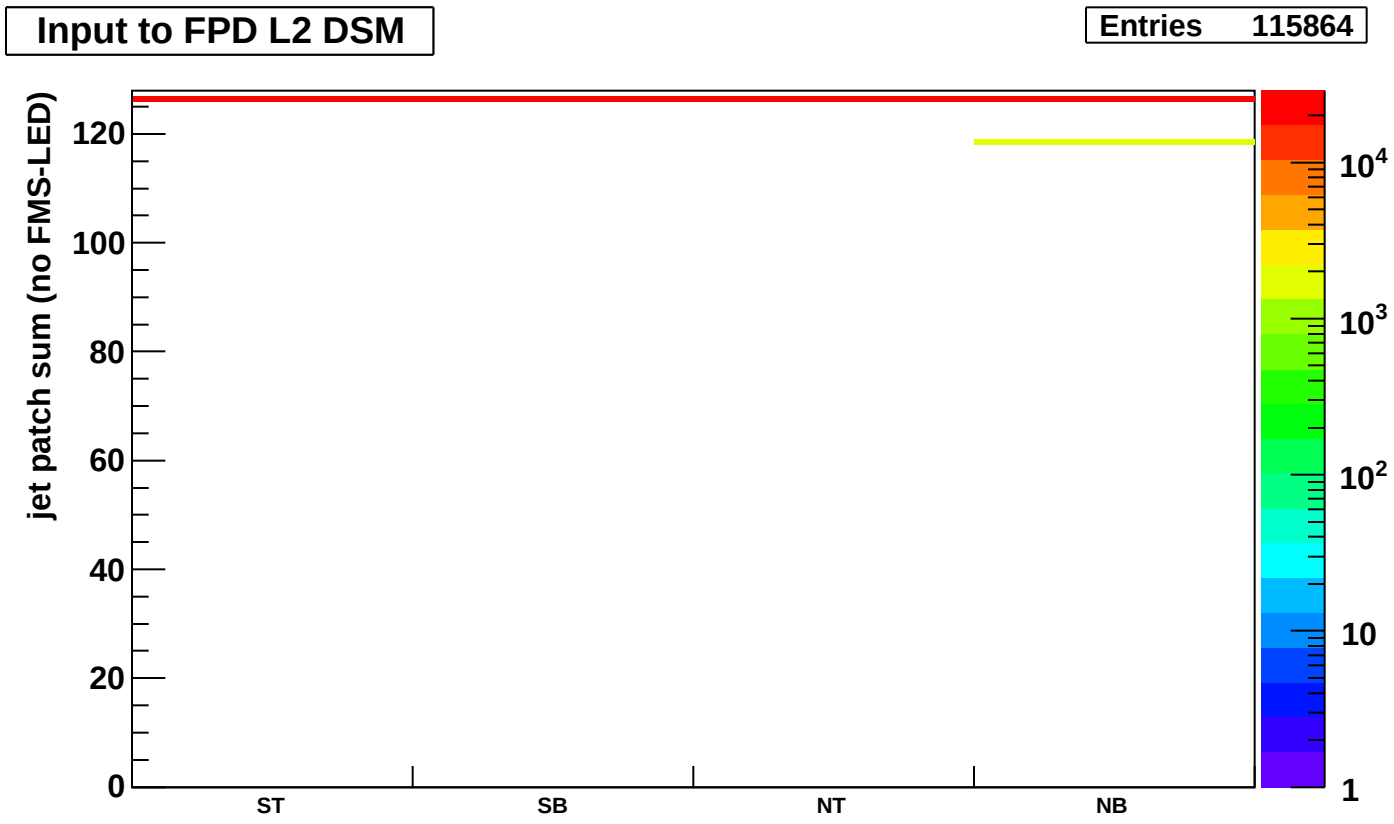

Input to FPD L2 DSM
Entries 231728
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

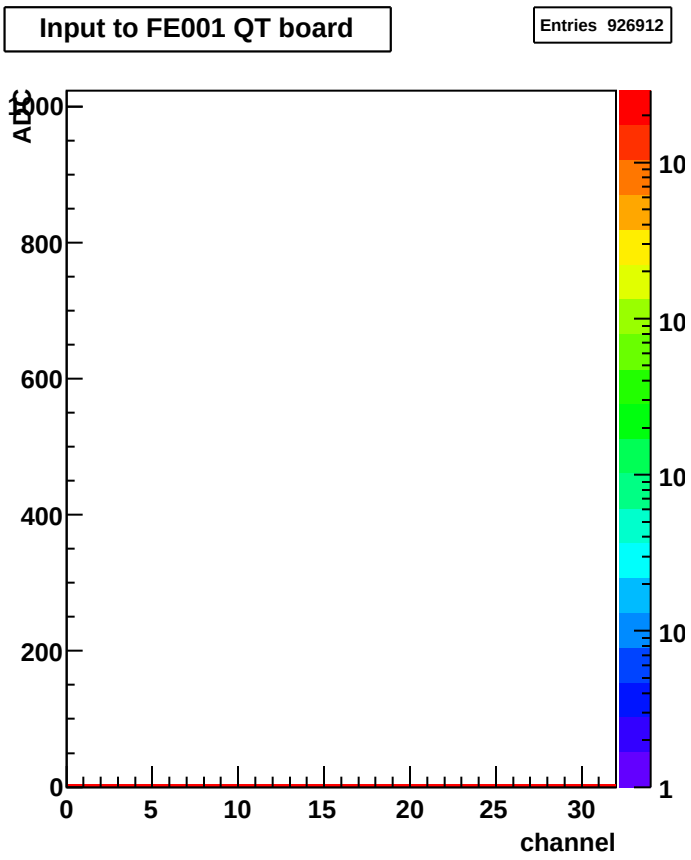

Input to FPD L2 DSM
Entries 86898
CL bits
28966
28966
28966
SMALL
LARGE-S
LARGE-N

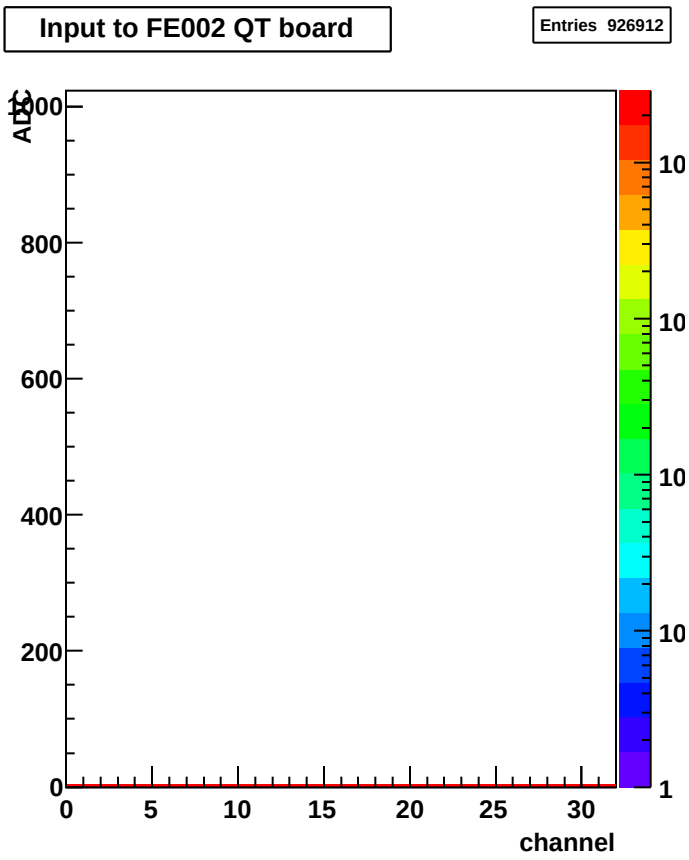

Input to FPD L2 DSM
Entries 86898
CL bits - simulated
28966
28966
28966
SMALL
LARGE-S
LARGE-N

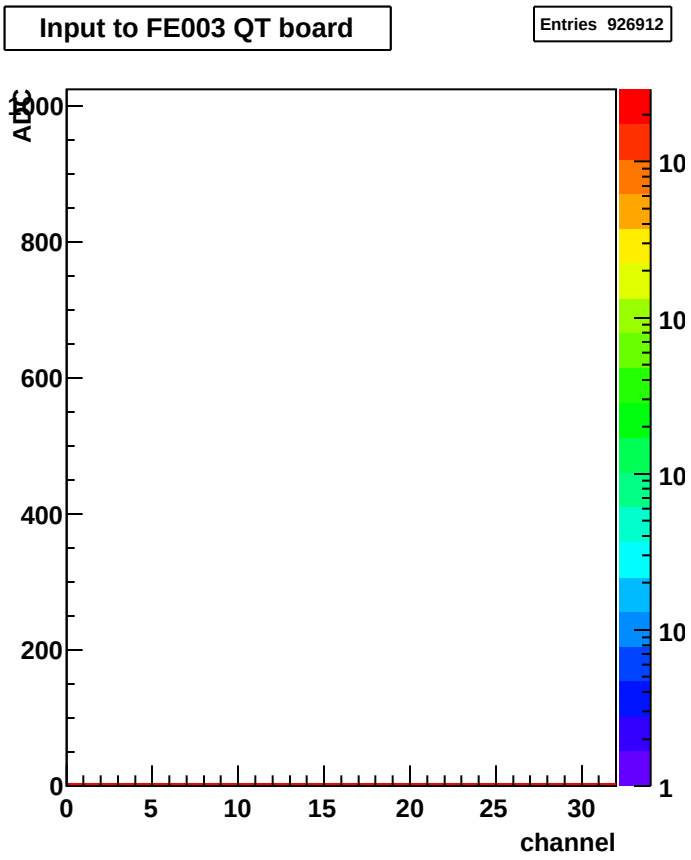

Input to FPD L2 DSM
Entries 86898
HT bits
28862
28966
28966
104
SMALL
LARGE-S
LARGE-N

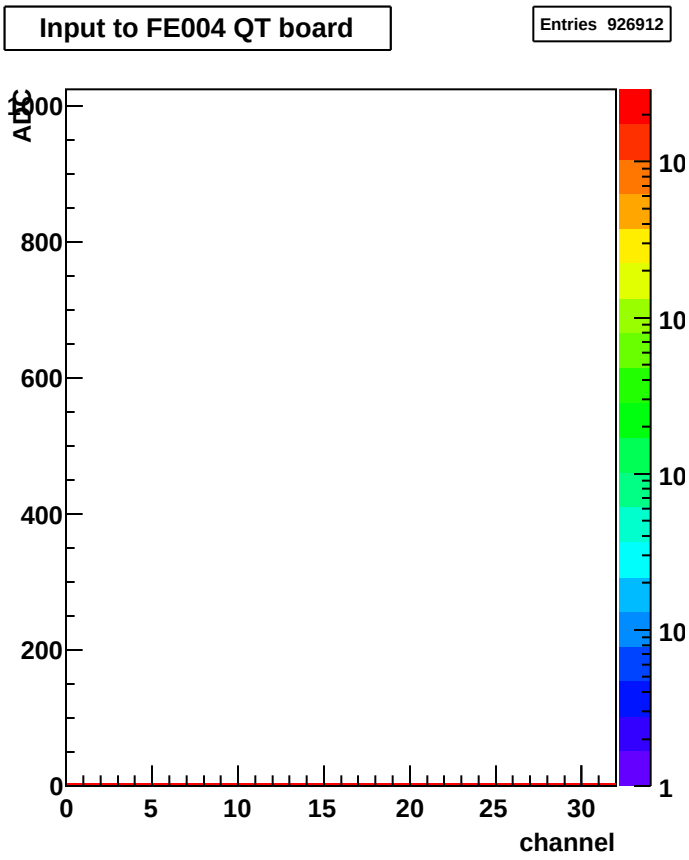

Input to FPD L2 DSM
Entries 86898
HT bits - simulated
28862
28966
28966
104
SMALL
LARGE-S
LARGE-N


Input to FPD L2 DSM
Entries 115864
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
$10^2$
$10^3$
$10^4$

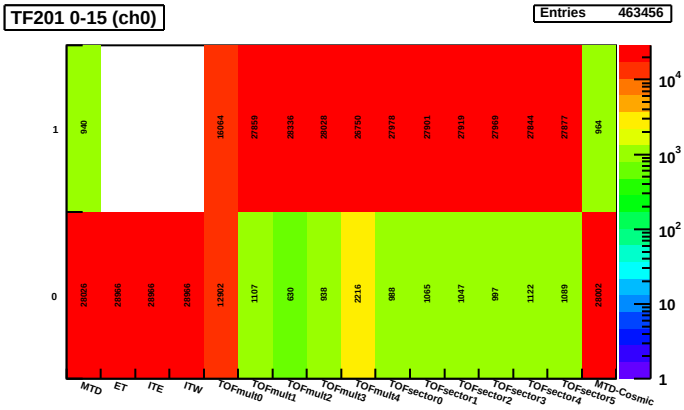

Input to FE001 QT board
Entries 926912
ADC
channel


Input to FE002 QT board
Entries 926912
ADC
channel

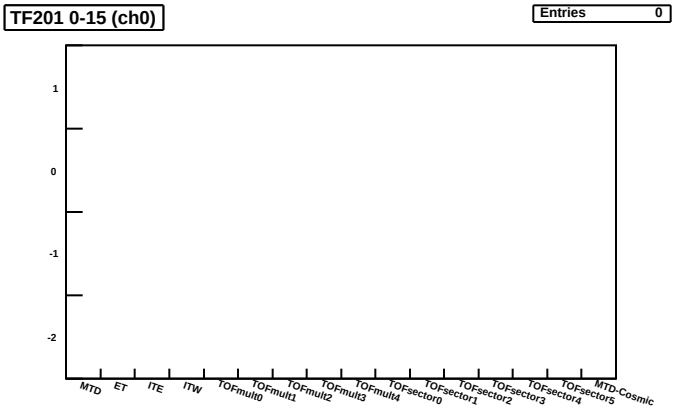

Input to FE003 QT board
Entries 926912
ADC
channel


Input to FE004 QT board
Entries 926912
ADC
channel

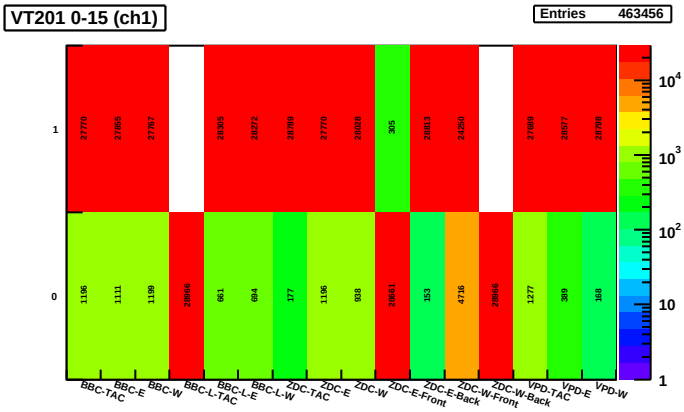
TF201 0-15 (ch0)



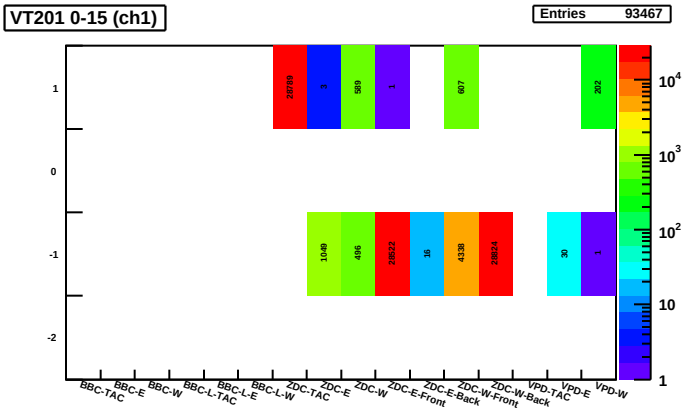

TF201 0-15 (ch0)



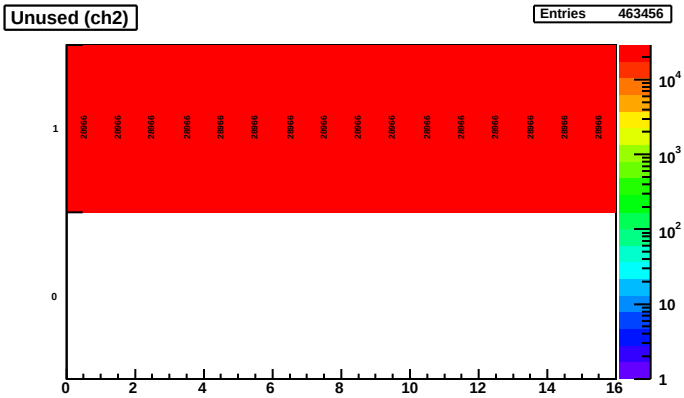

VT201 0-15 (ch1)

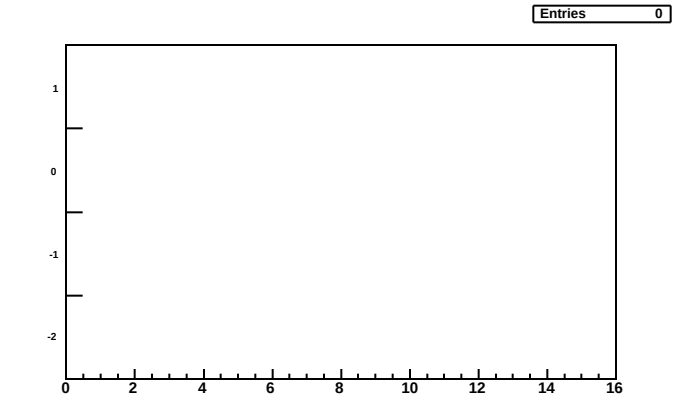



VT201 0-15 (ch1)

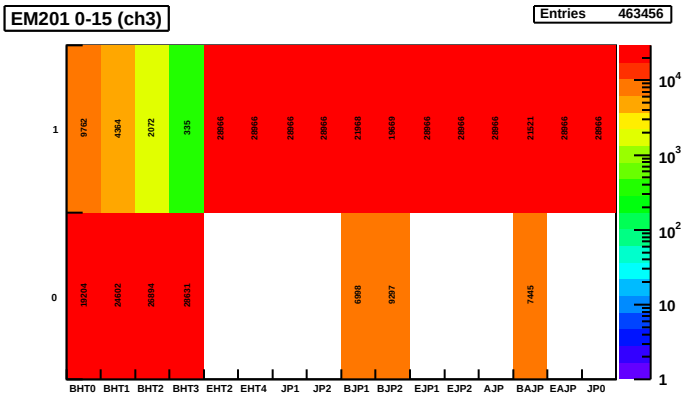



Unused (ch2)

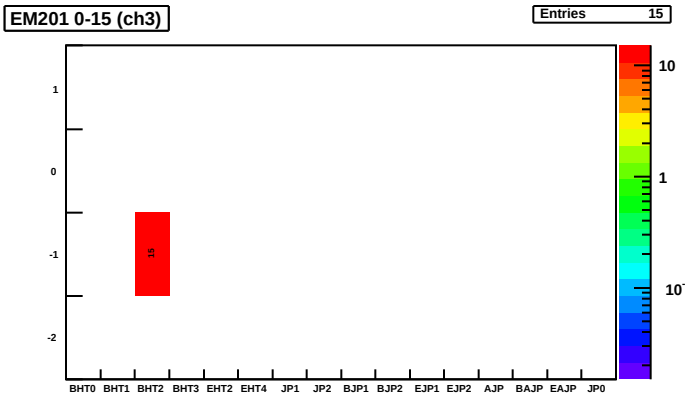


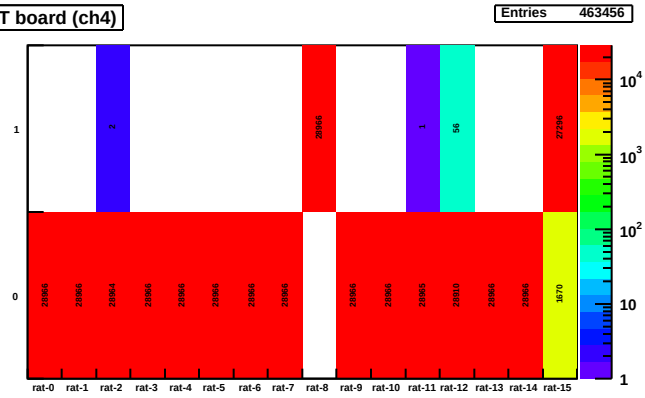

EM201 0-15 (ch3)

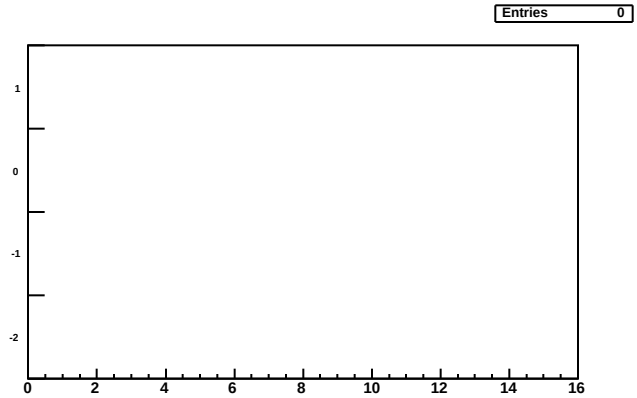



EM201 0-15 (ch3)

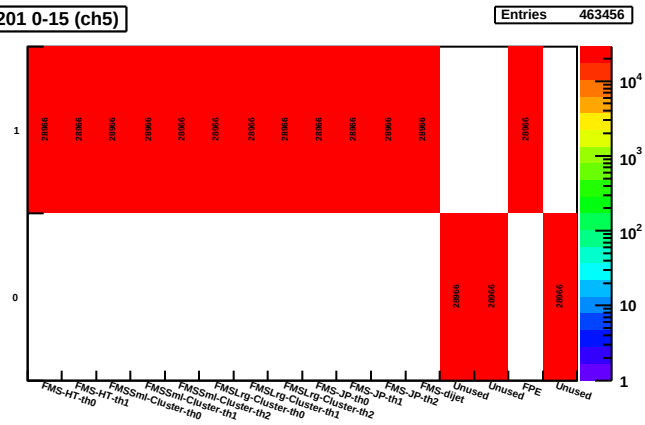


**RAT board (ch4)**

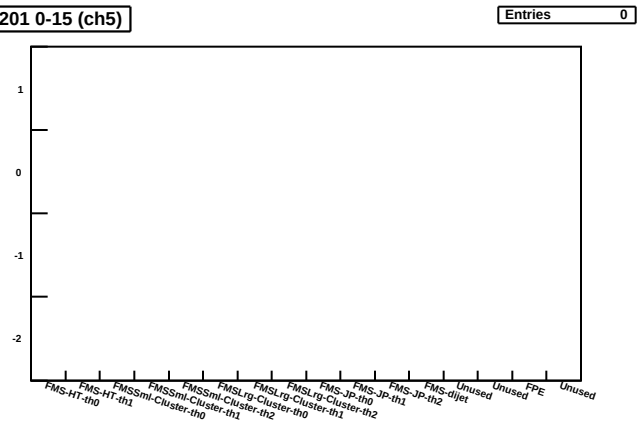


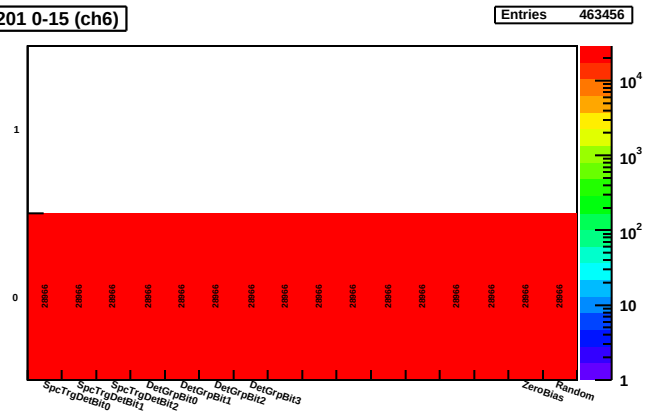

**FP201 0-15 (ch5)**

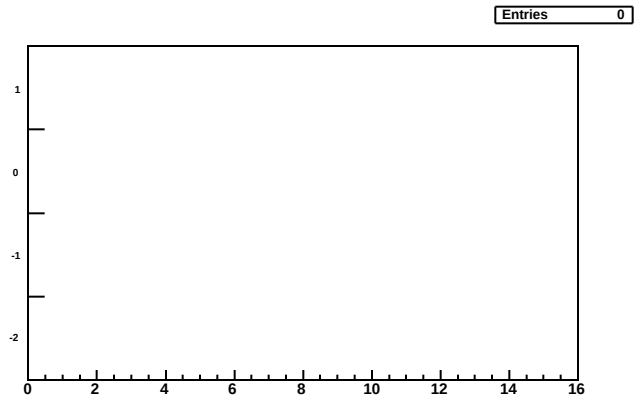


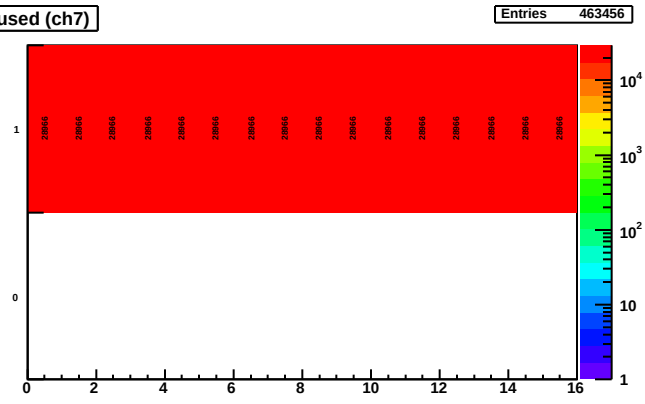
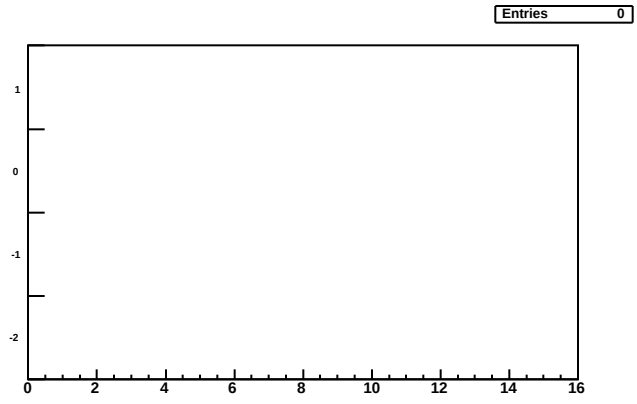

**FP201 0-15 (ch5)**




**ST201 0-15 (ch6)**




**Unused (ch7)**