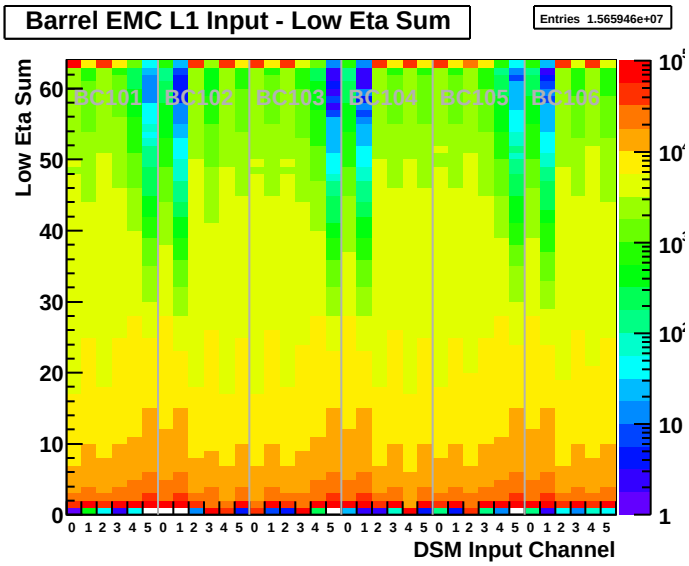
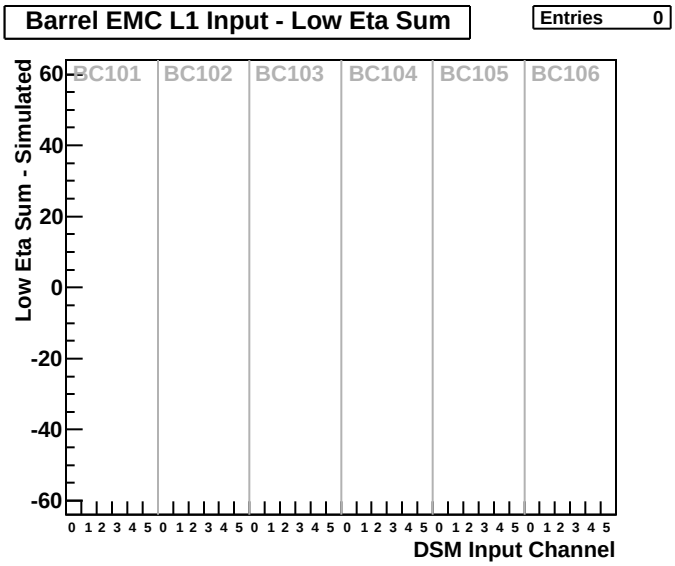
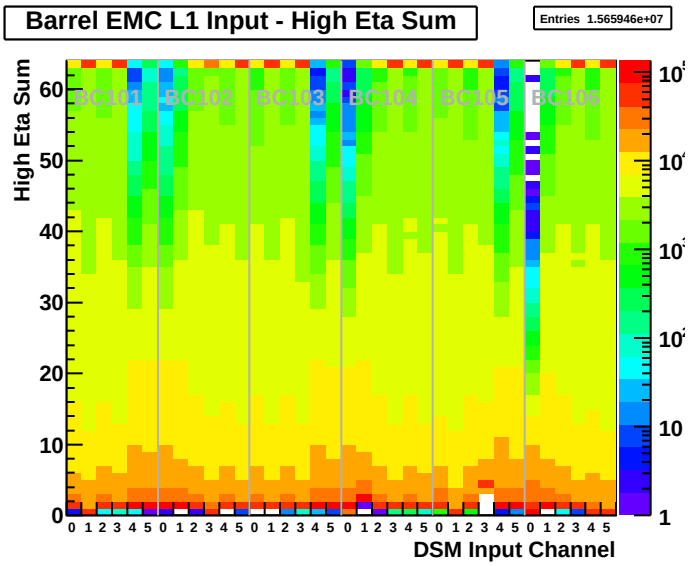
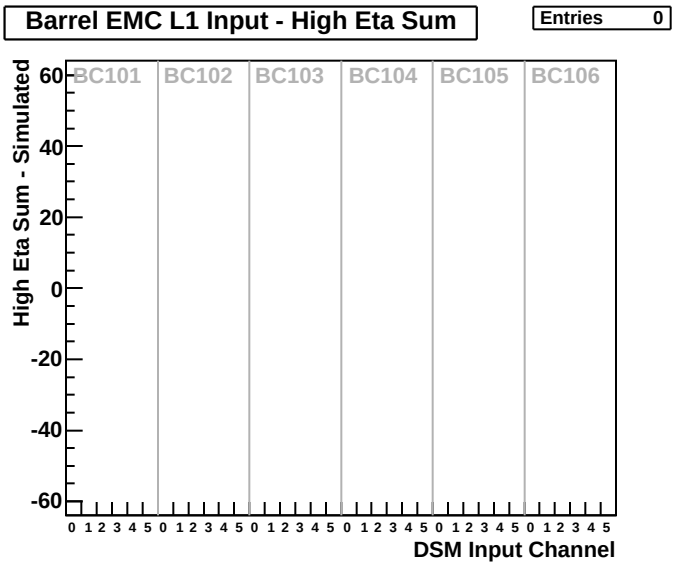
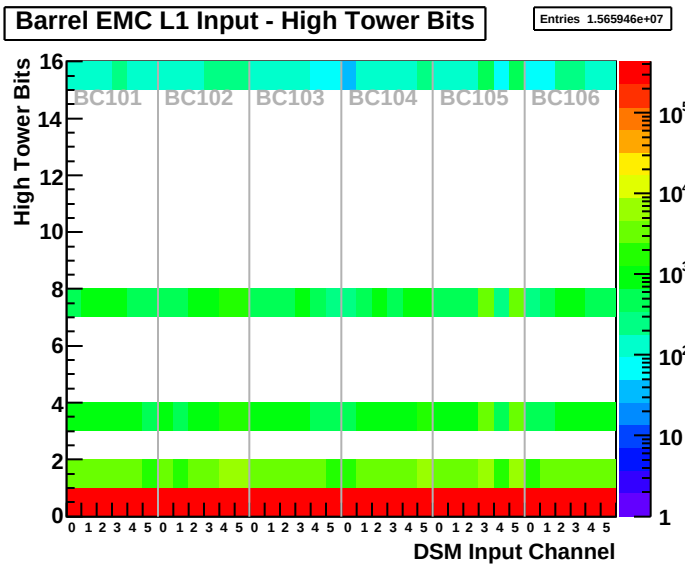
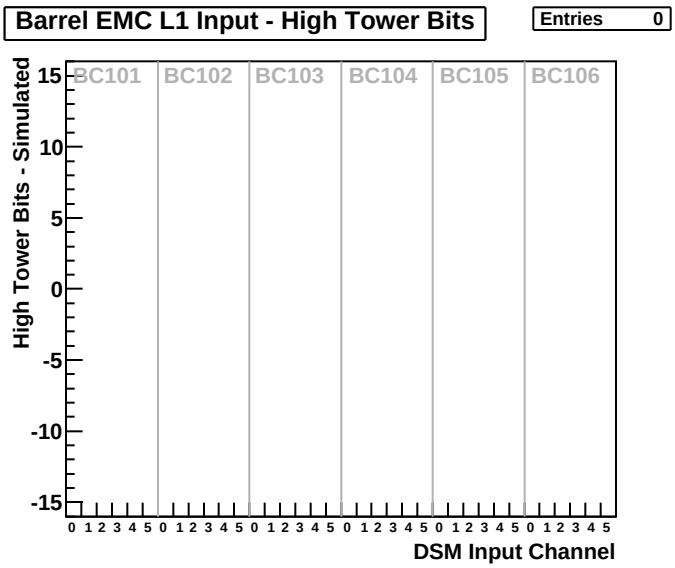

Barrel EMC L1 Input - Low Eta Sum
Entries 1.565946e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

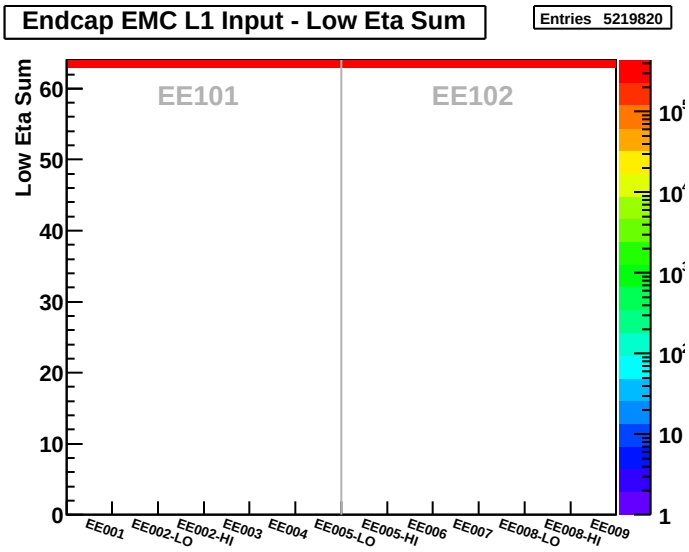

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

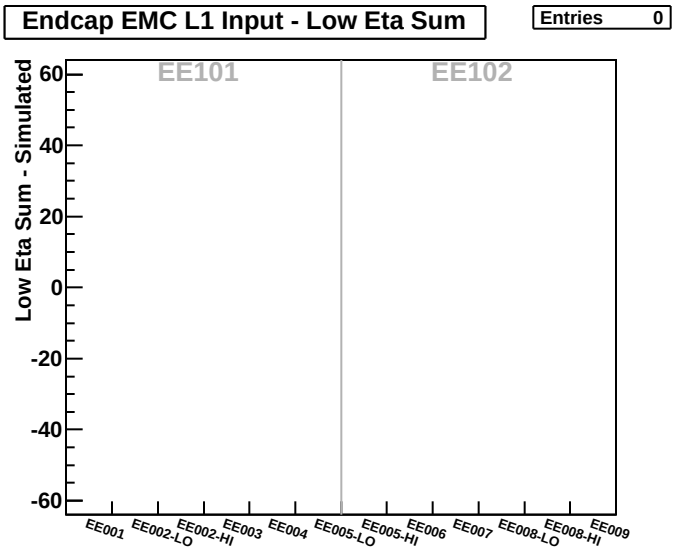

Barrel EMC L1 Input - High Eta Sum
Entries 1.565946e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

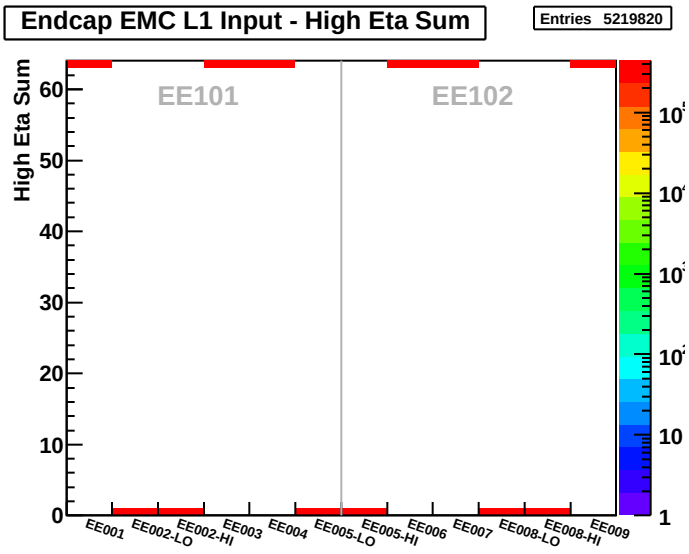

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

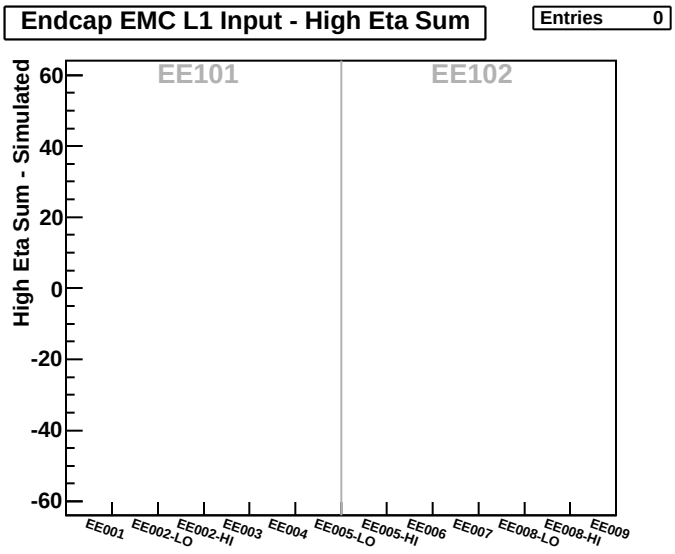

Barrel EMC L1 Input - High Tower Bits
Entries 1.565946e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

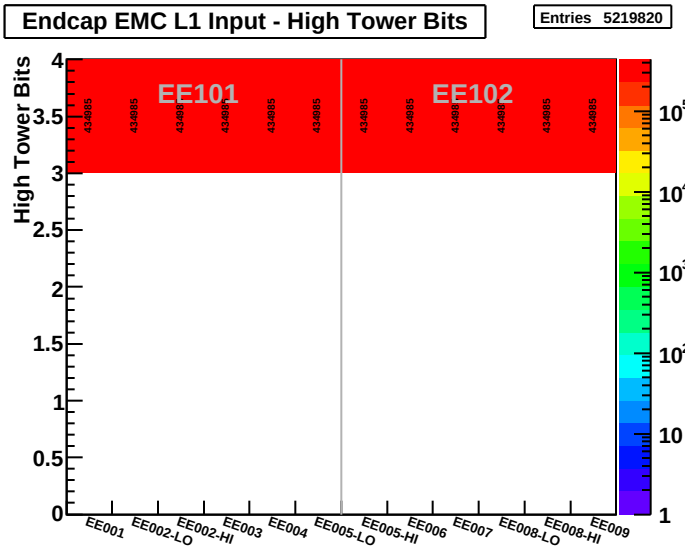

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

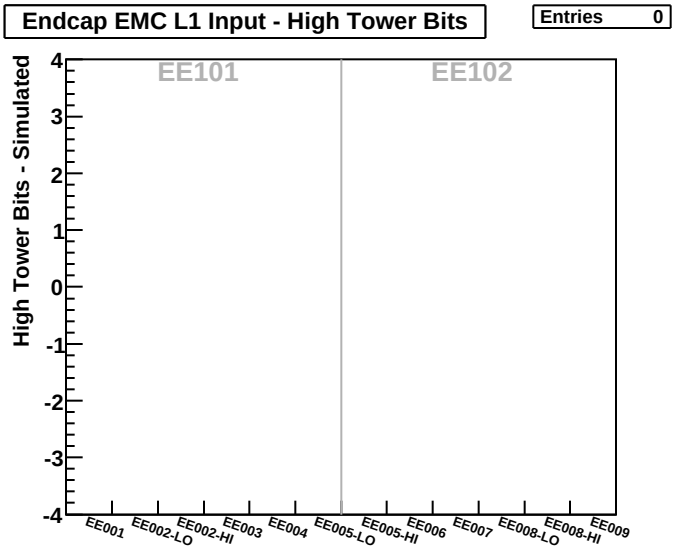

Endcap EMC L1 Input - Low Eta Sum
Entries 5219820
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

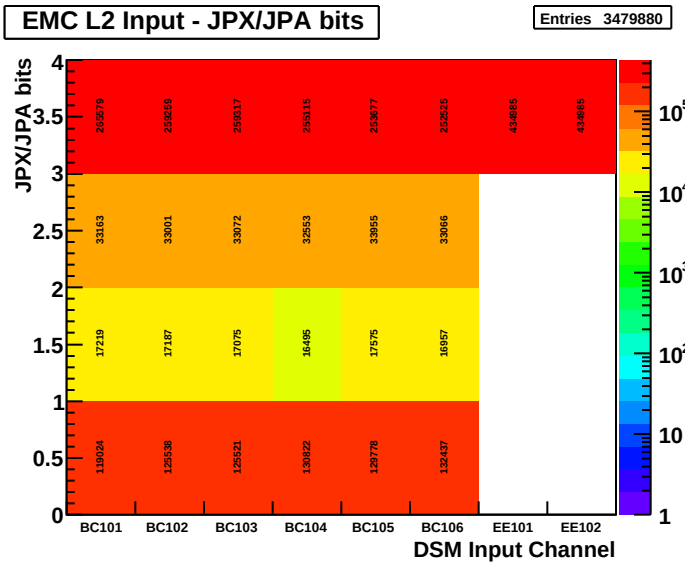

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

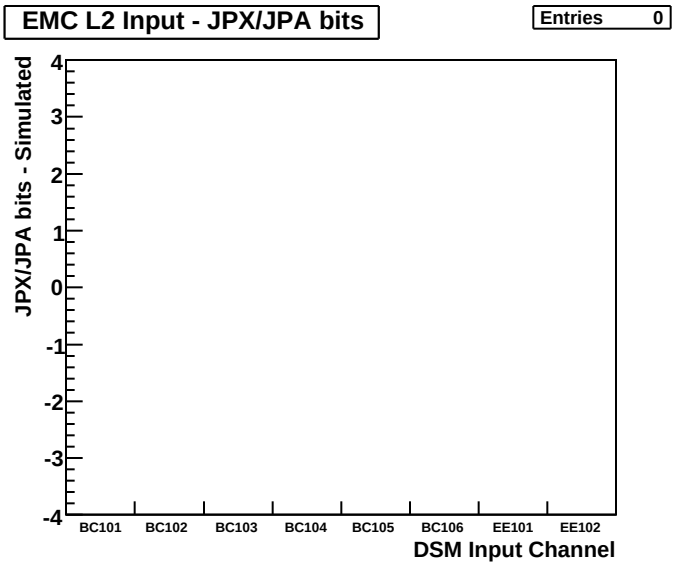

Endcap EMC L1 Input - High Eta Sum
Entries 5219820
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

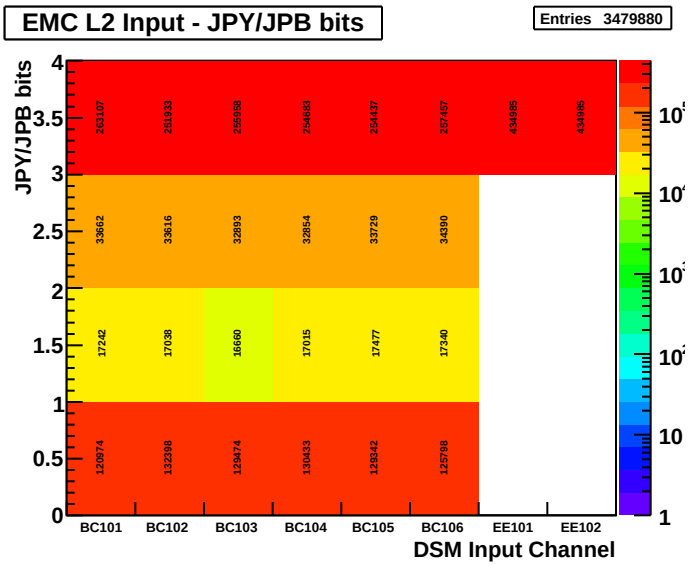

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

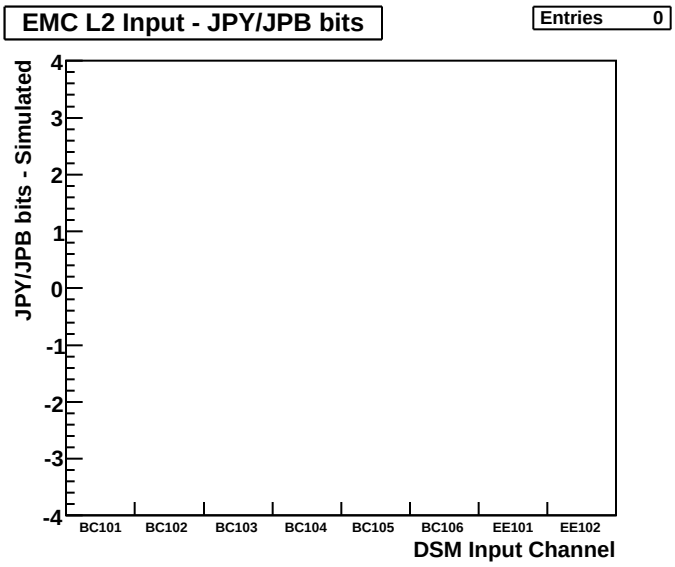

Endcap EMC L1 Input - High Tower Bits
Entries 5219820
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

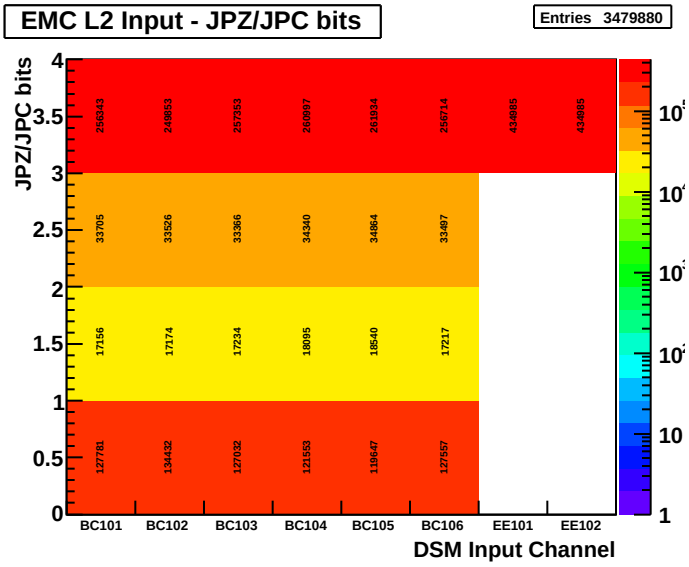

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

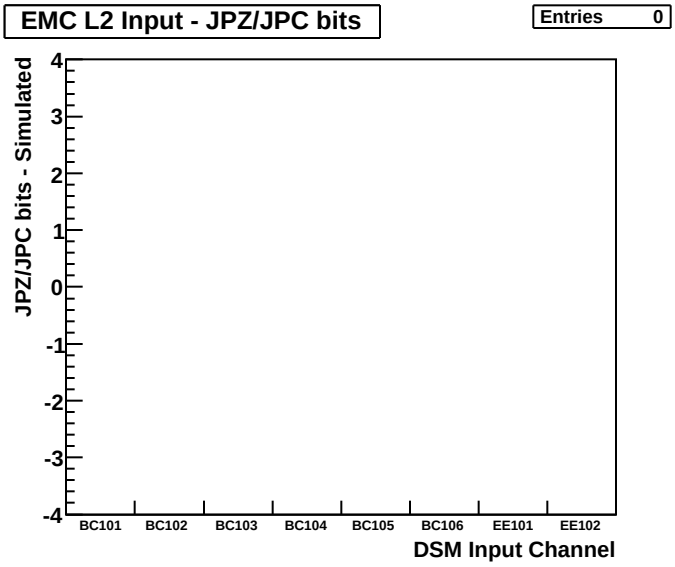

EMC L2 Input - JPX/JPA bits
Entries 3479880
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
255579 259259 259217 255115 253877 252525 434985 434985
33163 33001 33072 32553 33955 33066
17219 17187 17075 16495 17575 16957
119024 125538 125521 130822 129778 132437

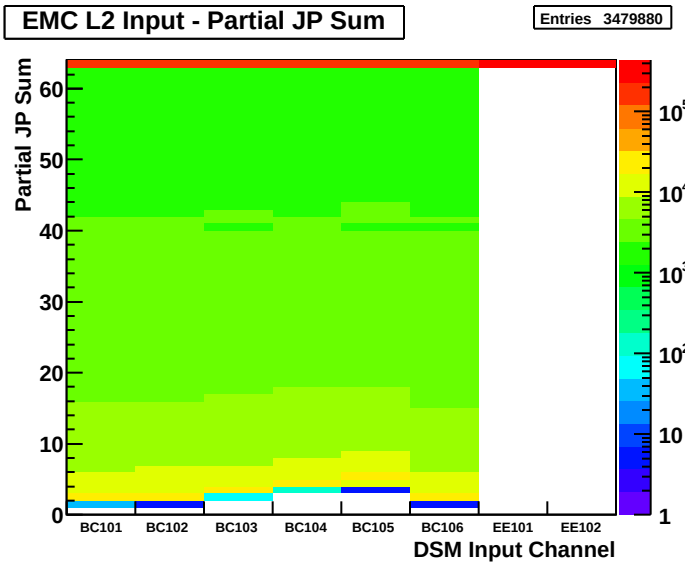

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

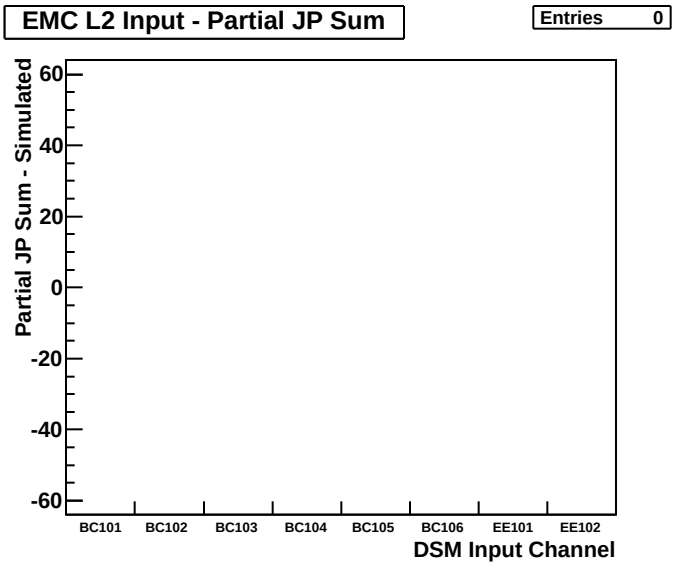

EMC L2 Input - JPY/JPB bits
Entries 3479880
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
263107 251933 255958 254683 254437 257457 434985 434985
33662 33616 32893 32854 33729 34390
17242 17038 16660 17015 17477 17340
120974 132398 129474 130433 129342 125798

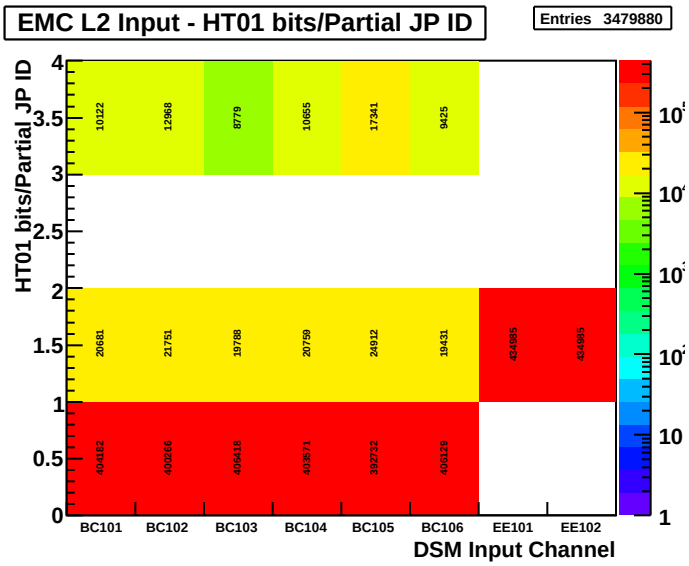

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

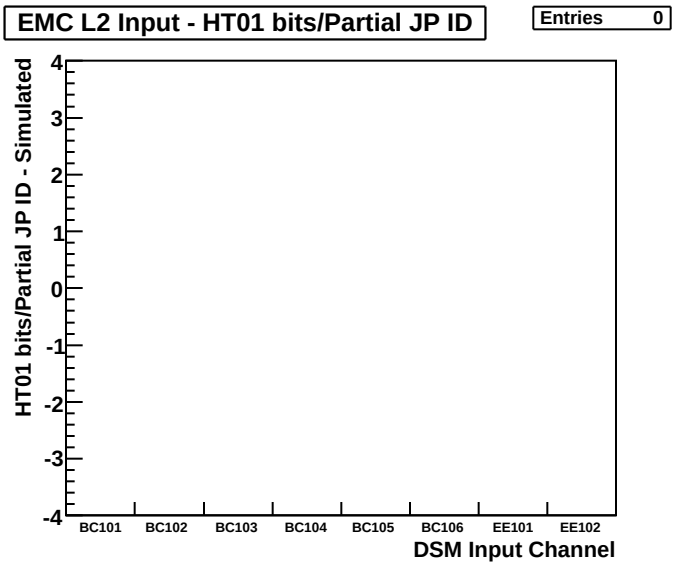

EMC L2 Input - JPZ/JPC bits
Entries 3479880
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
256343 248653 257353 260997 261934 256714 434985 434985
33705 33526 33366 34340 34864 33497
17156 17174 17234 18095 18540 17217
127781 134432 127032 121553 119647 127557

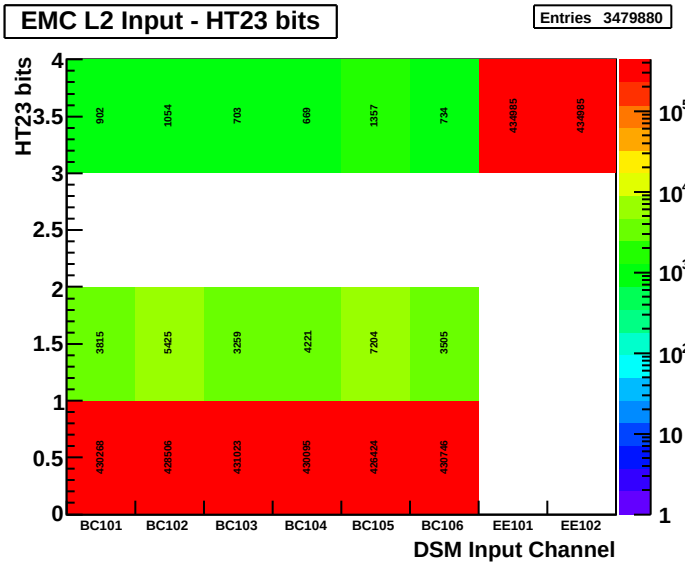

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

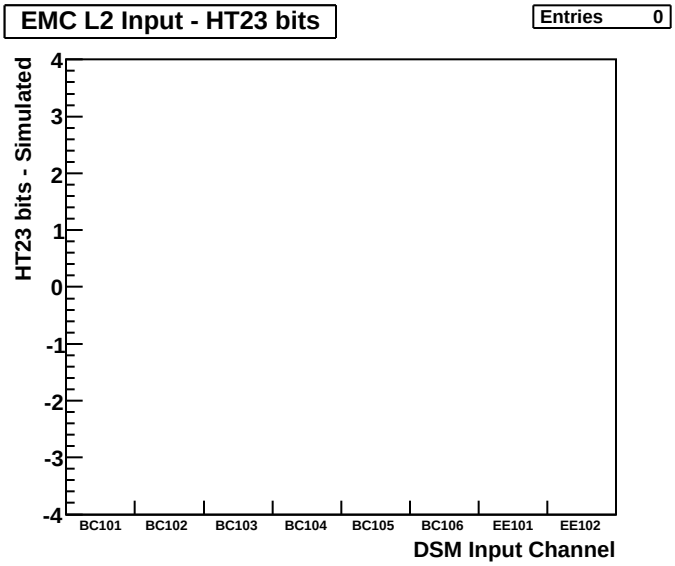

EMC L2 Input - Partial JP Sum
Entries 3479880
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

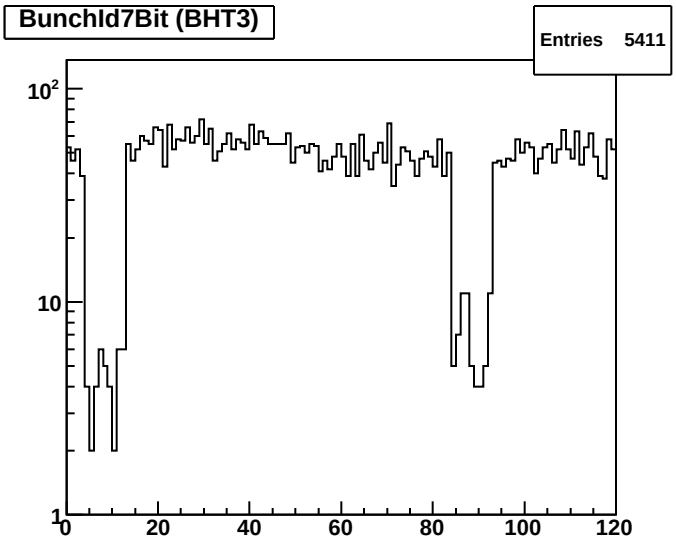

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

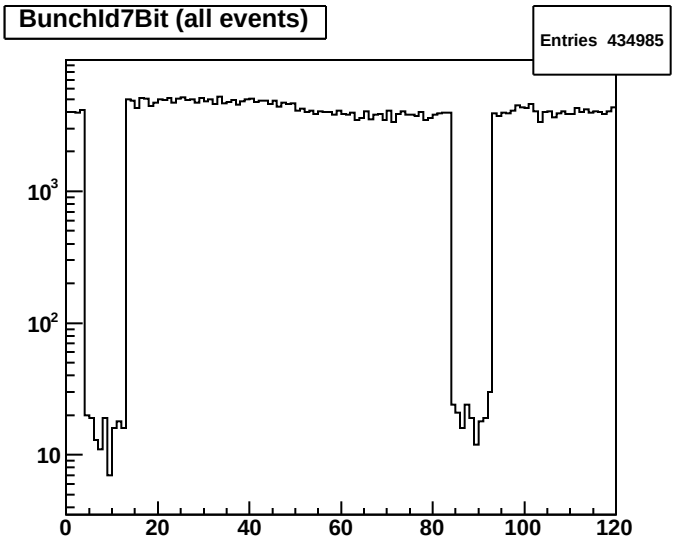

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3479880
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

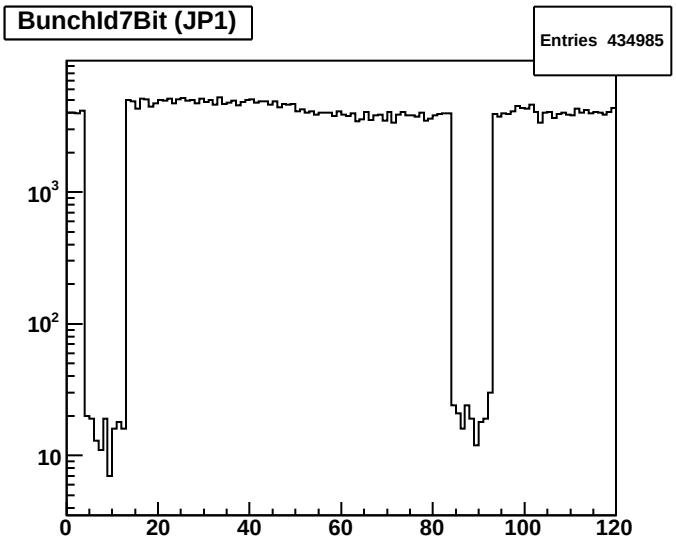

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

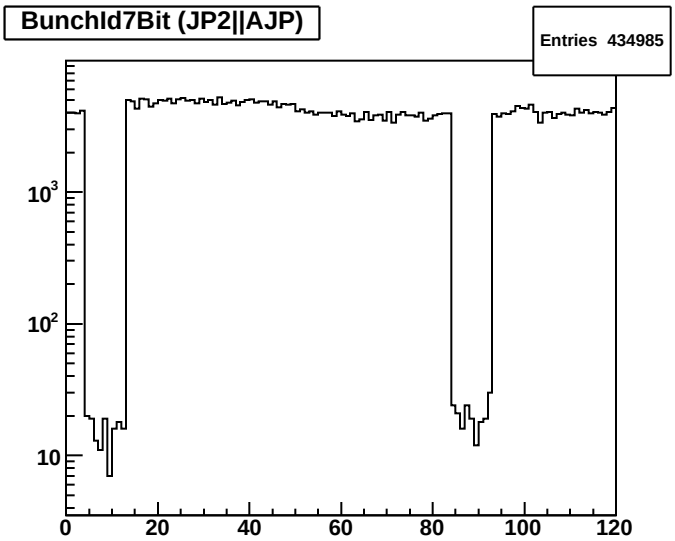

EMC L2 Input - HT23 bits
Entries 3479880
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

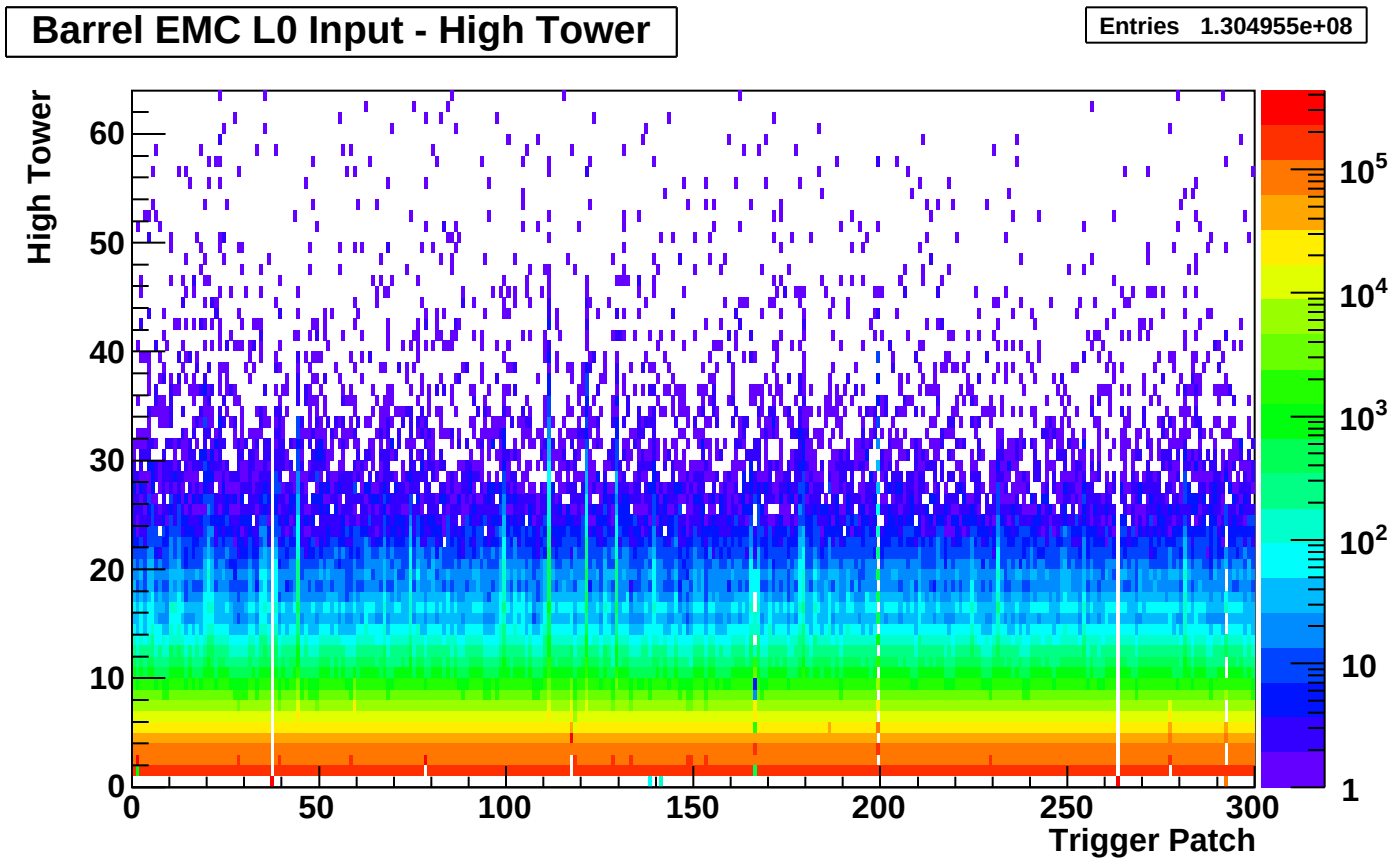

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

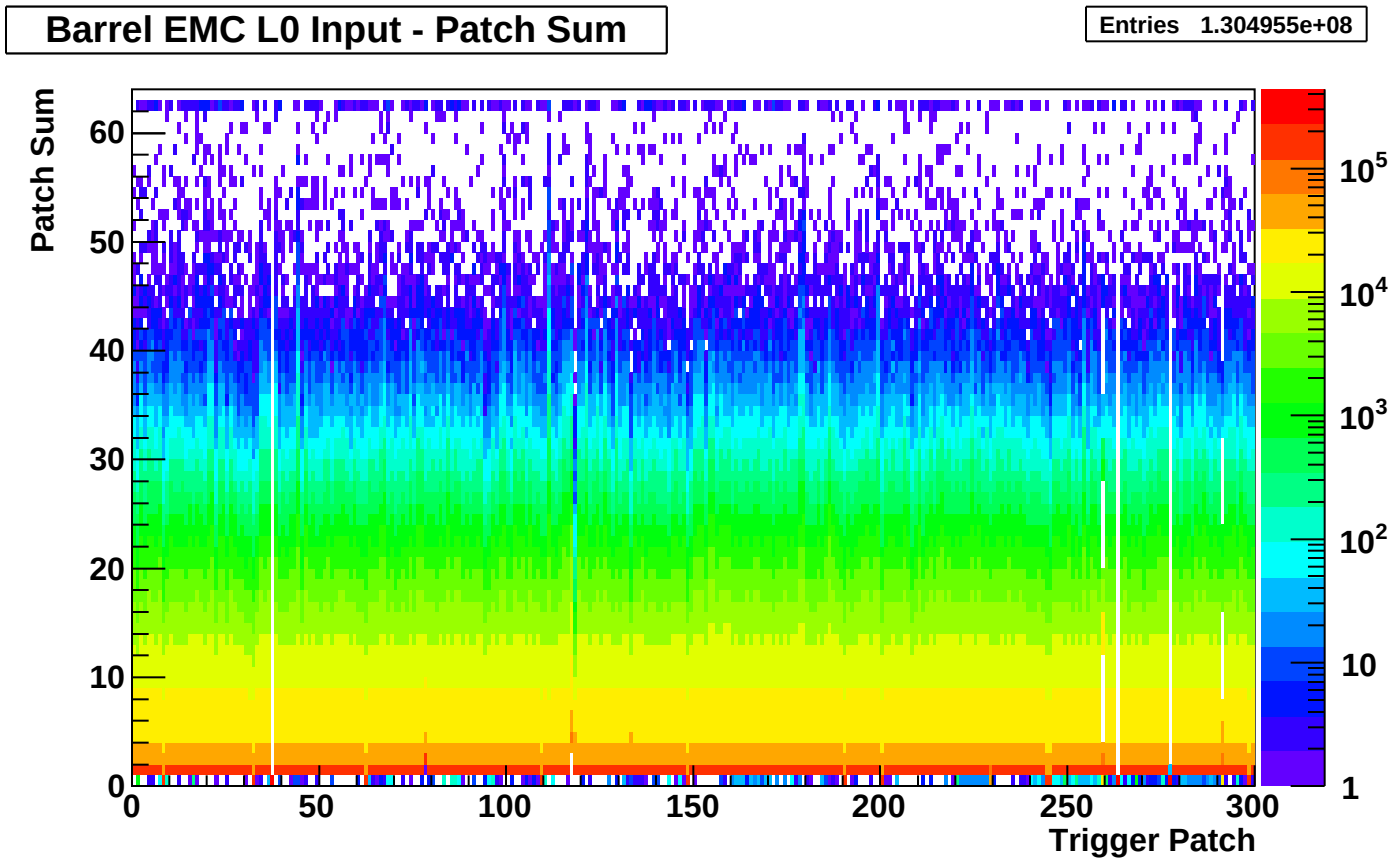

BunchId7Bit (BHT3)
Entries 5411
10^2
10
1
0
20
40
60
80
100
120

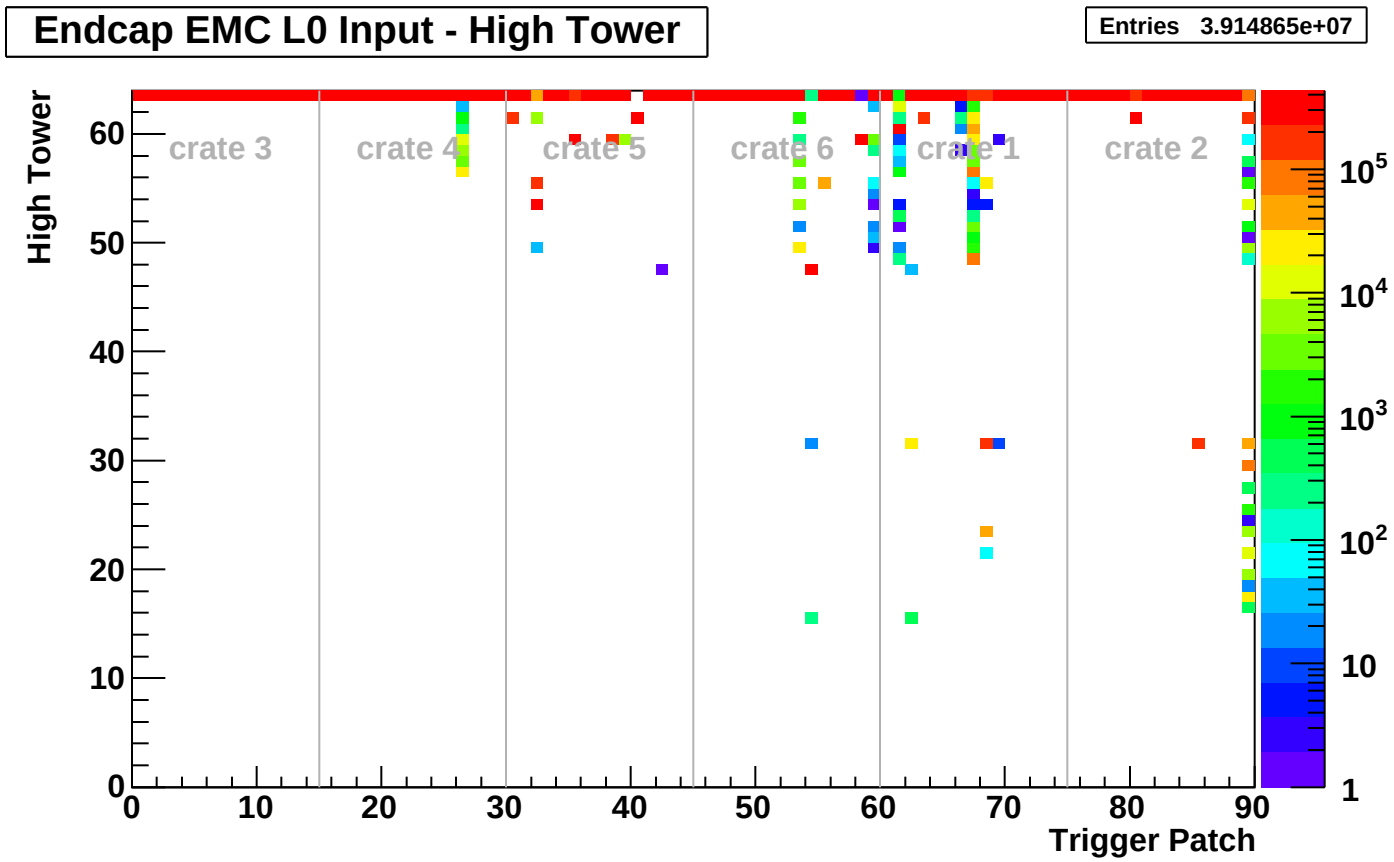

BunchId7Bit (all events)
Entries 434985
10^3
10^2
10
0
20
40
60
80
100
120

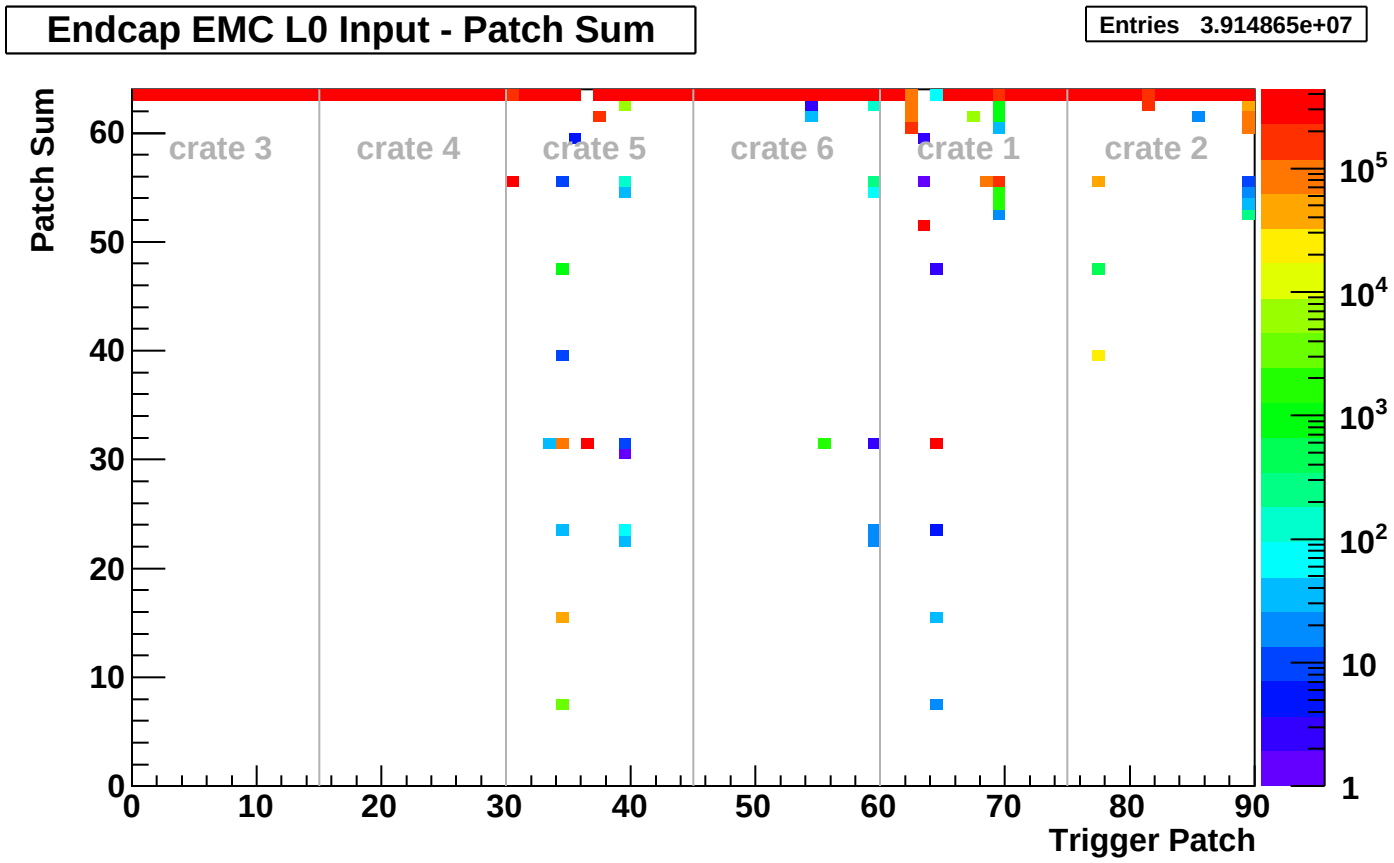

BunchId7Bit (JP1)
Entries 434985
10^3
10^2
10
0
20
40
60
80
100
120

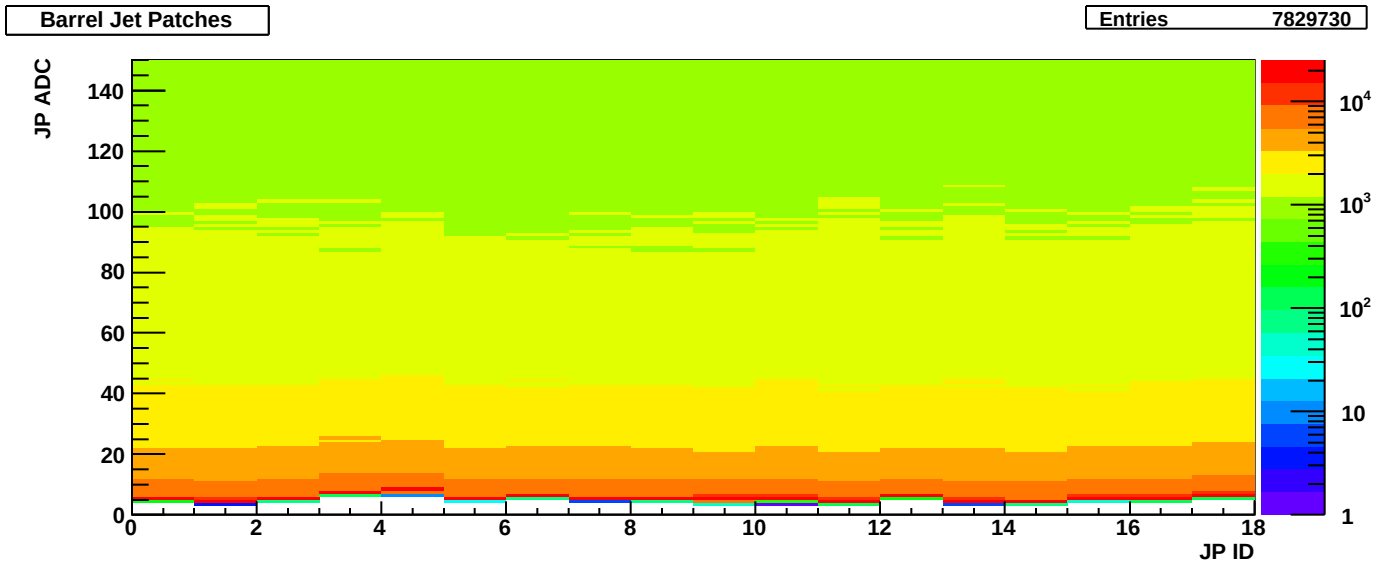

BunchId7Bit (JP2||AJP)
Entries 434985
10^3
10^2
10
0
20
40
60
80
100
120

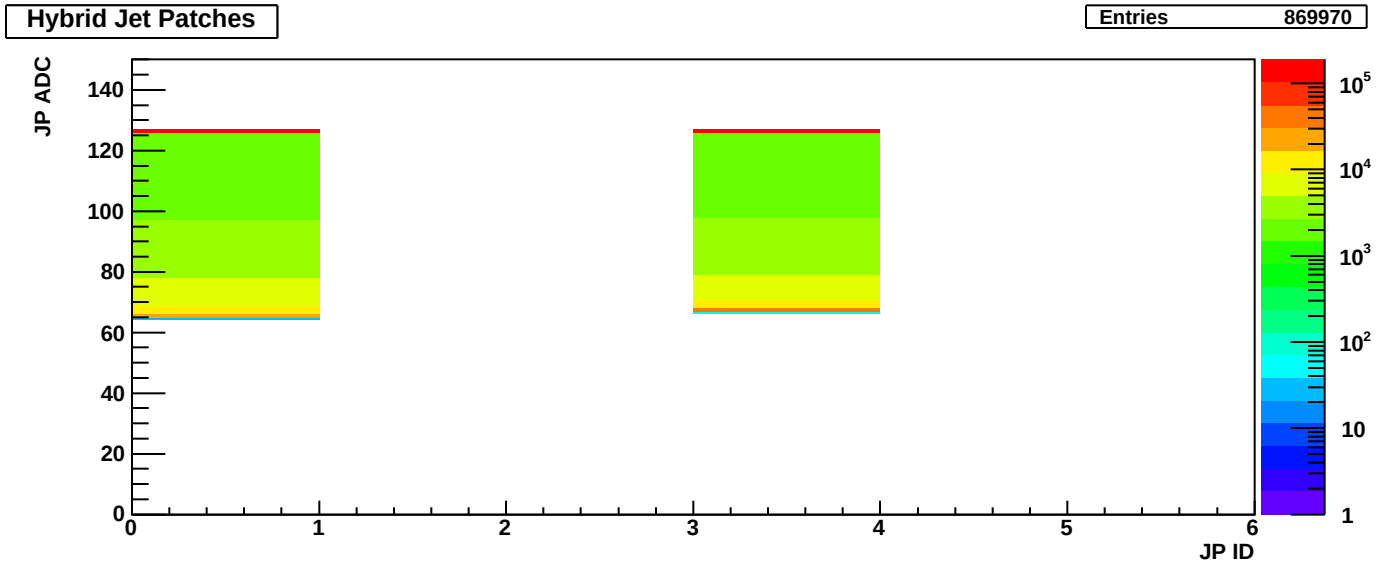

Barrel EMC L0 Input - High Tower
Entries 1.304955e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

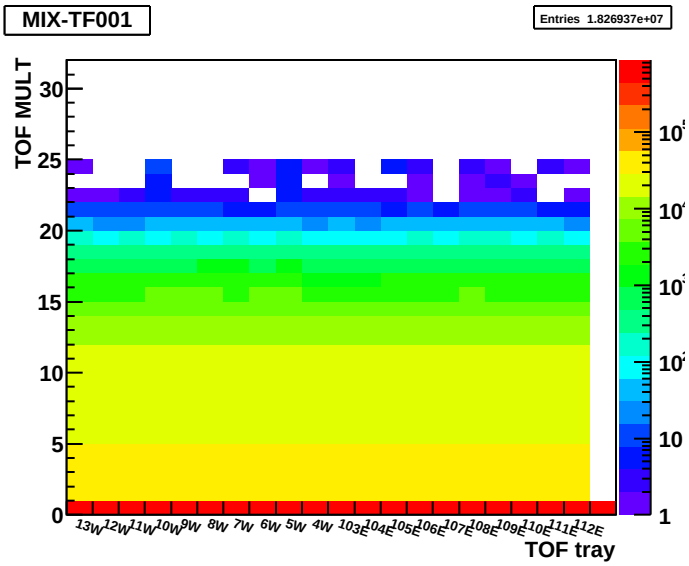

Barrel EMC L0 Input - Patch Sum
Entries 1.304955e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

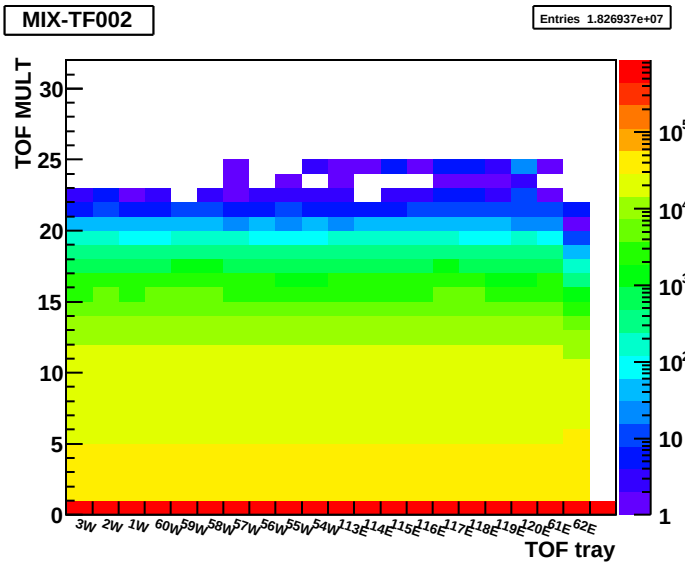

Endcap EMC L0 Input - High Tower
Entries 3.914865e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

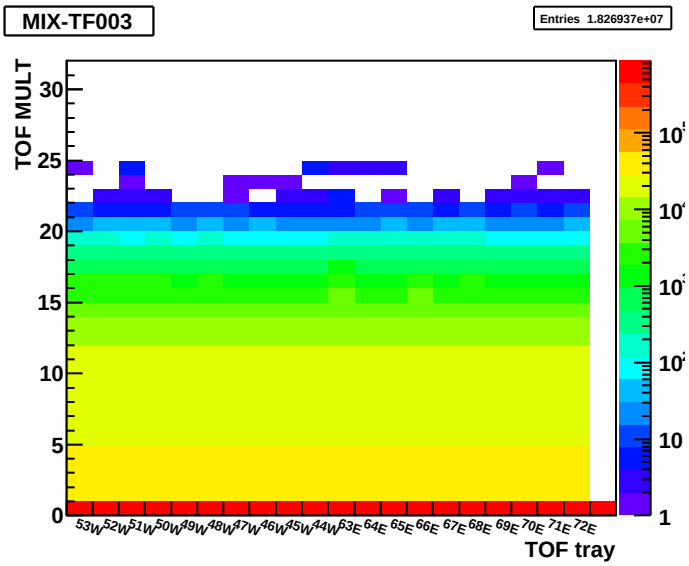

Endcap EMC L0 Input - Patch Sum
Entries 3.914865e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

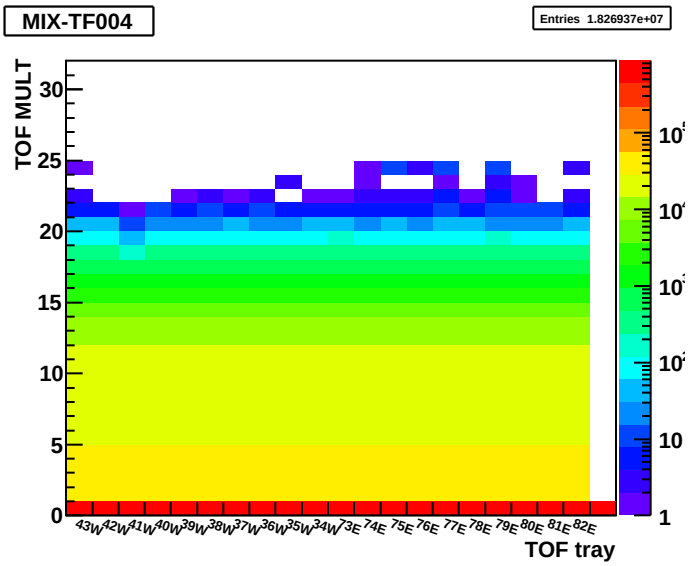

Barrel Jet Patches
Entries 7829730
JP ADC
JP ID

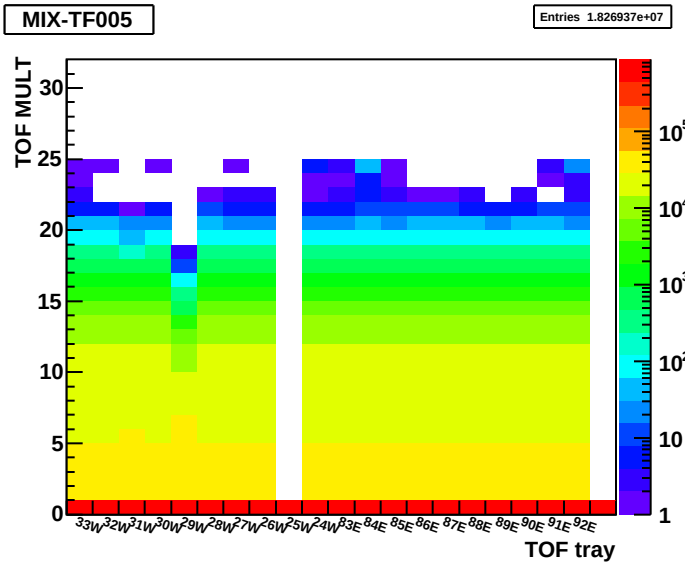

Hybrid Jet Patches
Entries 869970
JP ADC
JP ID

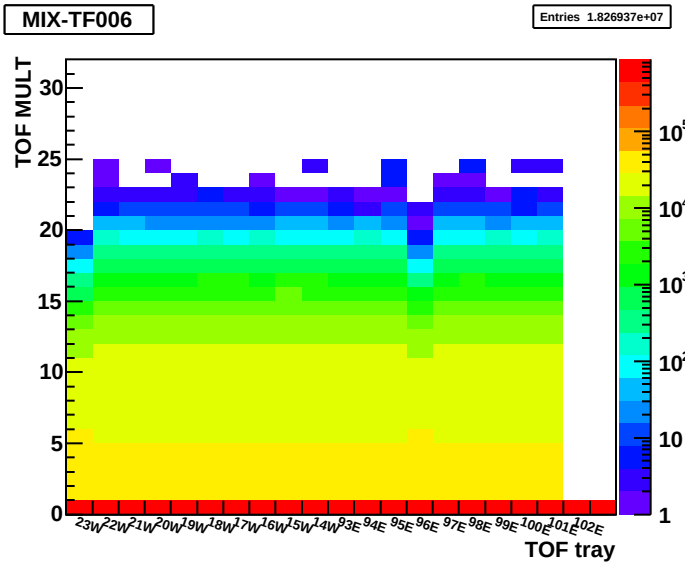

MIX-TF001
Entries 1.826937e+07
TOF MULT
TOF tray

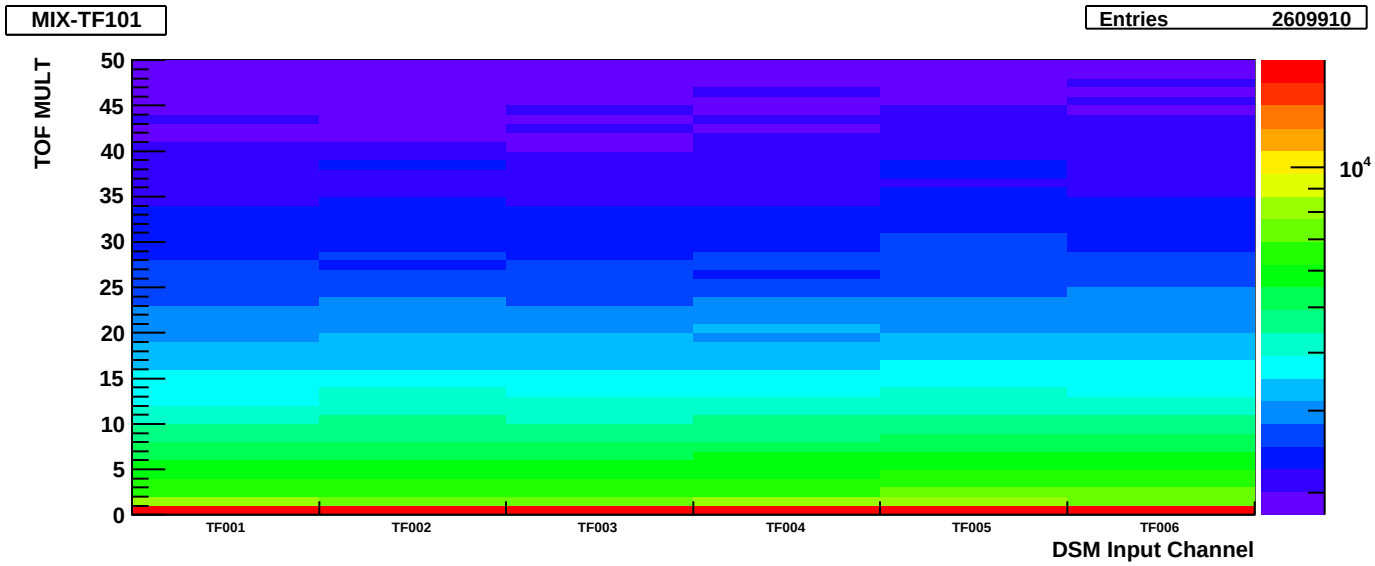

MIX-TF002
Entries 1.826937e+07
TOF MULT
TOF tray

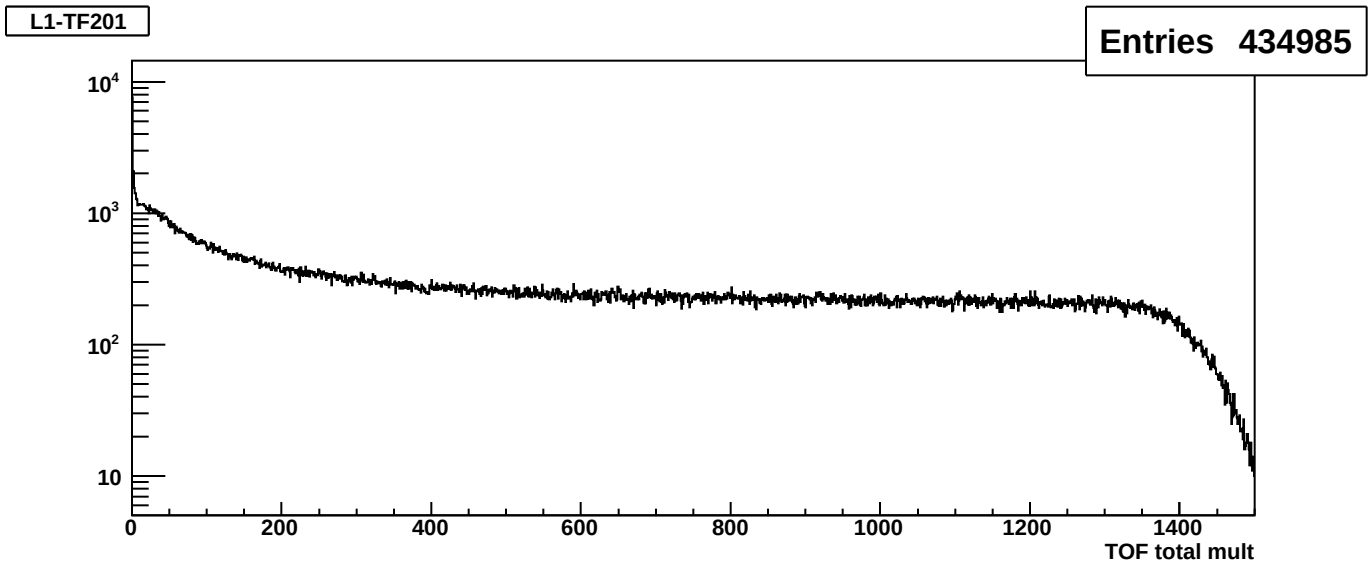

MIX-TF003
Entries 1.826937e+07
TOF MULT
TOF tray

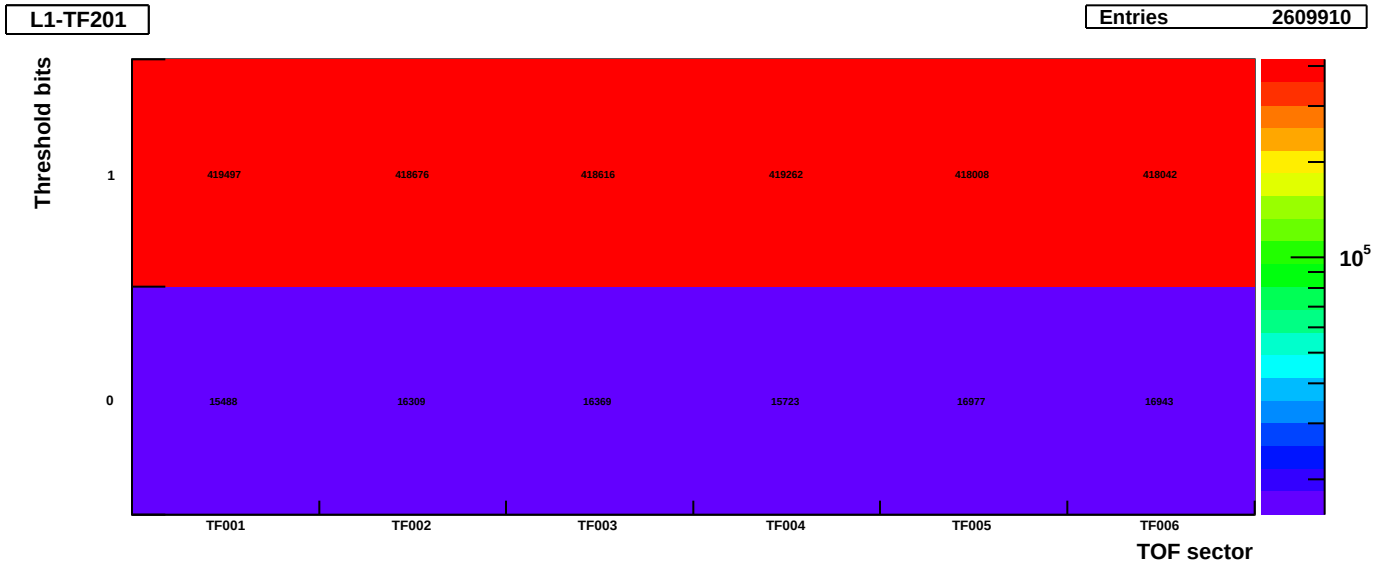

MIX-TF004
Entries 1.826937e+07
TOF MULT
TOF tray

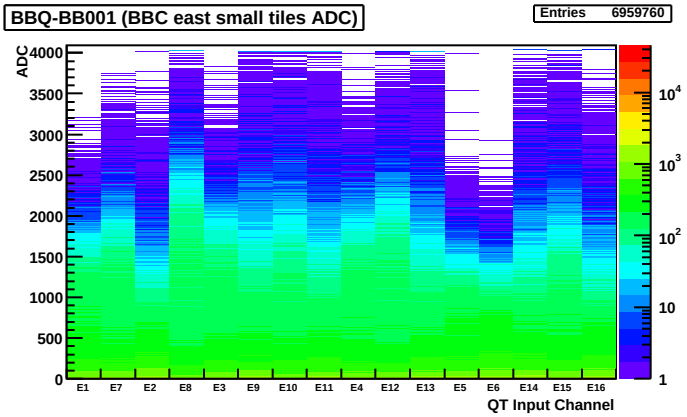

MIX-TF005
Entries 1.826937e+07
TOF MULT
TOF tray

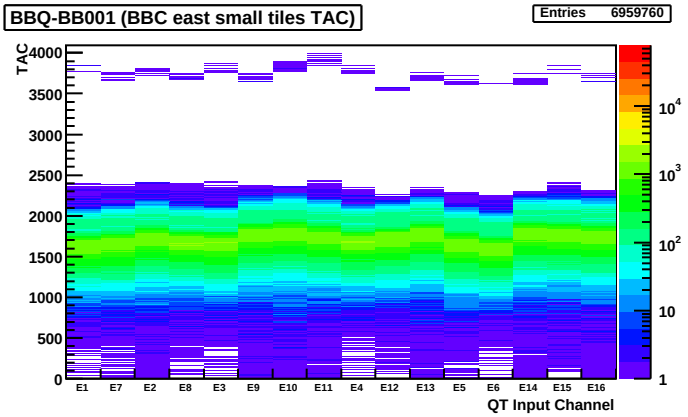

MIX-TF006
Entries 1.826937e+07
TOF MULT
TOF tray

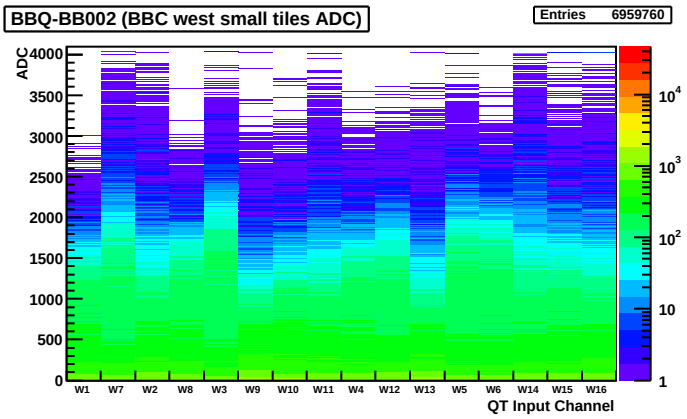

MIX-TF101
Entries 2609910
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

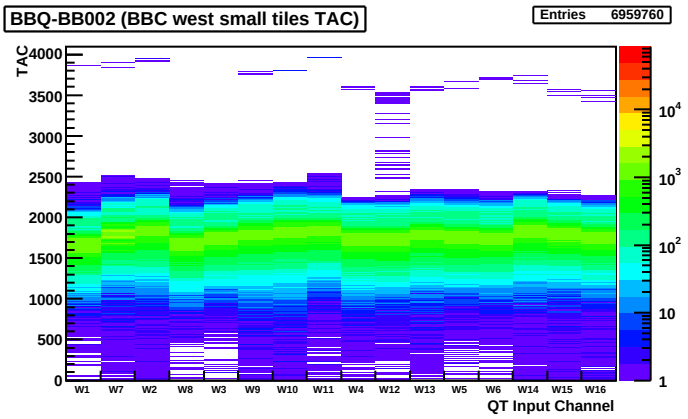

L1-TF201
Entries 434985
10^4
10^3
10^2
10
0
200
400
600
800
1000
1200
1400
TOF total mult

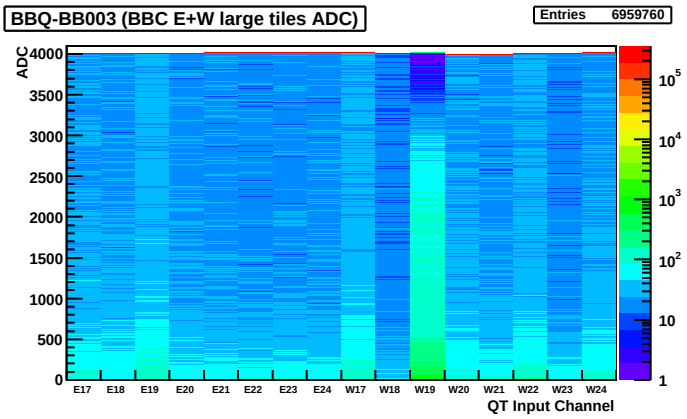

L1-TF201
Entries 2609910
Threshold bits
1
0
419497
418676
418616
419262
418008
418042
15488
16309
16369
15723
16977
16943
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

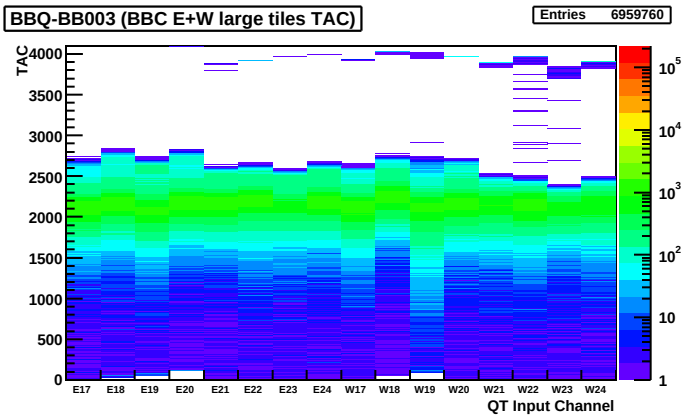

BBQ-BB001 (BBC east small tiles ADC)
Entries 6959760
ADC
QT Input Channel

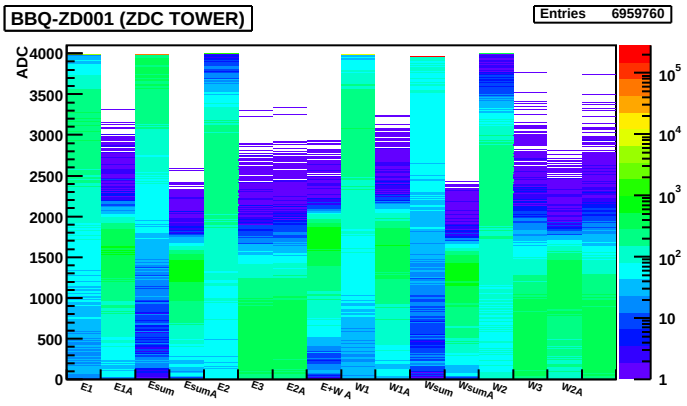

BBQ-BB001 (BBC east small tiles TAC)
Entries 6959760
TAC
QT Input Channel

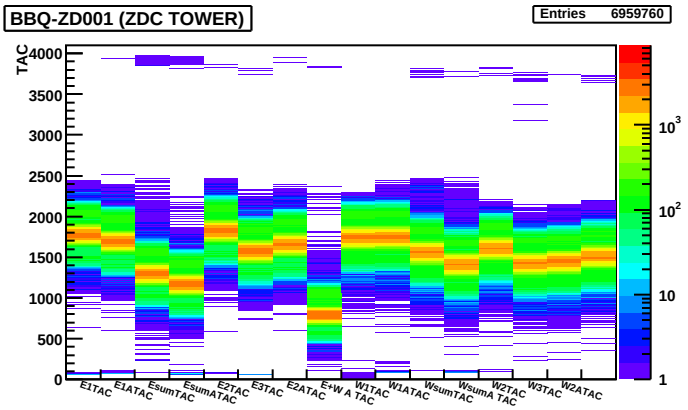

BBQ-BB002 (BBC west small tiles ADC)
Entries 6959760
ADC
QT Input Channel

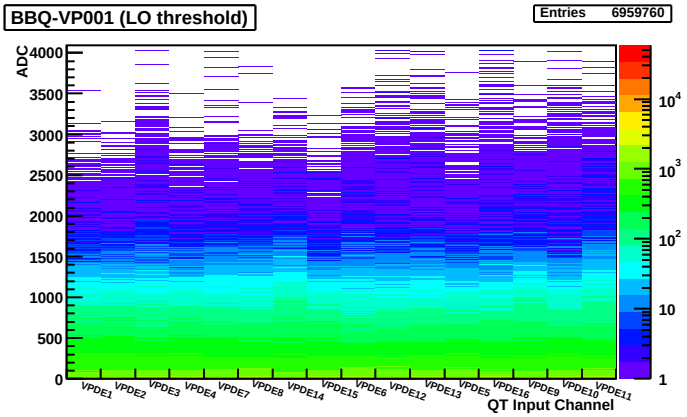

BBQ-BB002 (BBC west small tiles TAC)
Entries 6959760
TAC
QT Input Channel

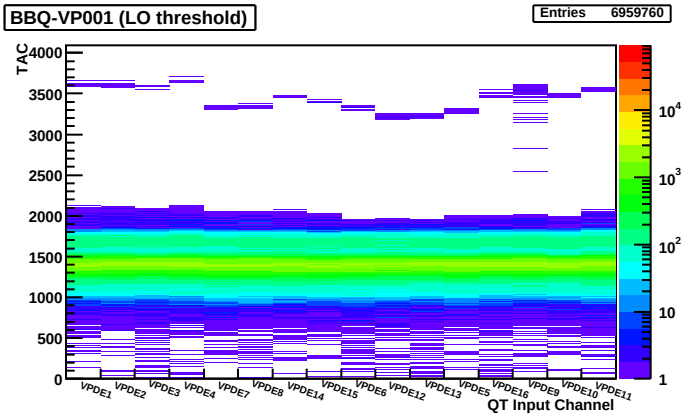

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 6959760
ADC
QT Input Channel

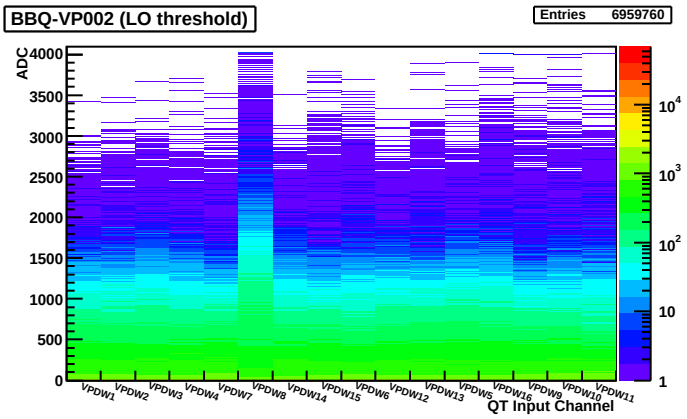

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 6959760
TAC
QT Input Channel

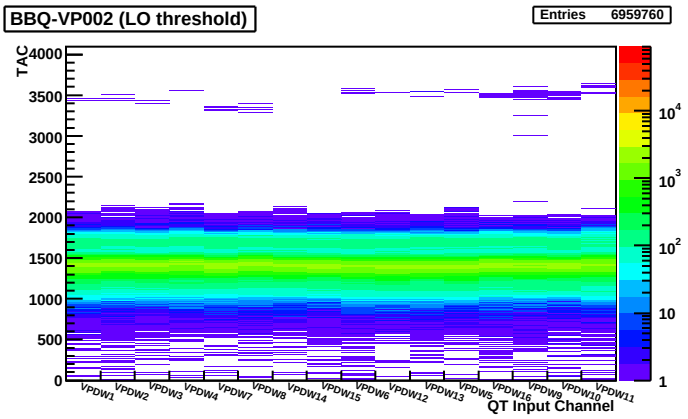

BBQ-ZD001 (ZDC TOWER)
Entries 6959760
ADC

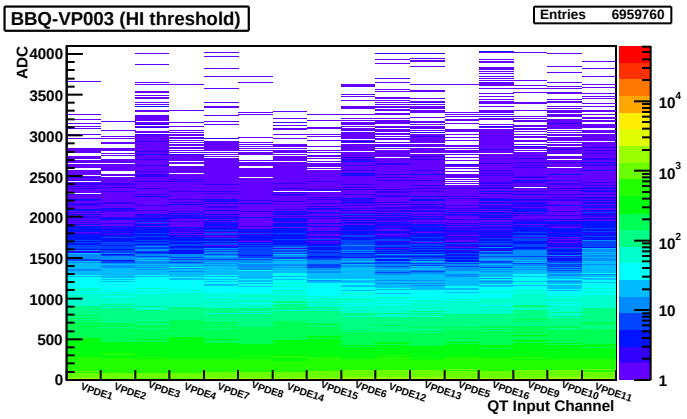

BBQ-ZD001 (ZDC TOWER)
Entries 6959760
TAC

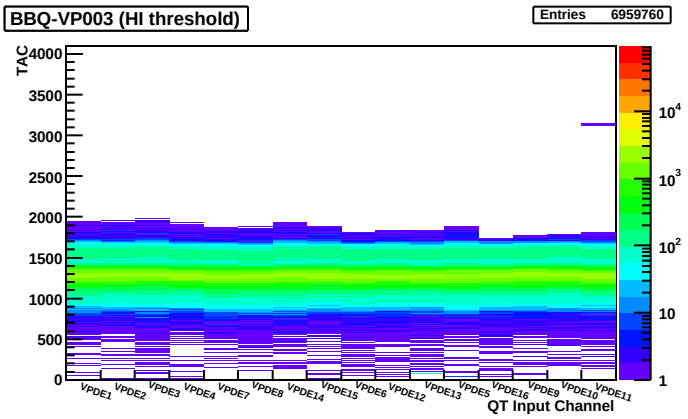

BBQ-VP001 (LO threshold)
Entries 6959760
ADC
QT Input Channel

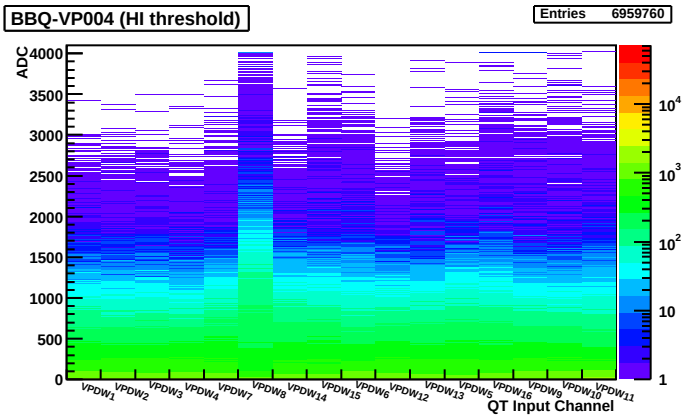

BBQ-VP001 (LO threshold)
Entries 6959760
TAC
QT Input Channel

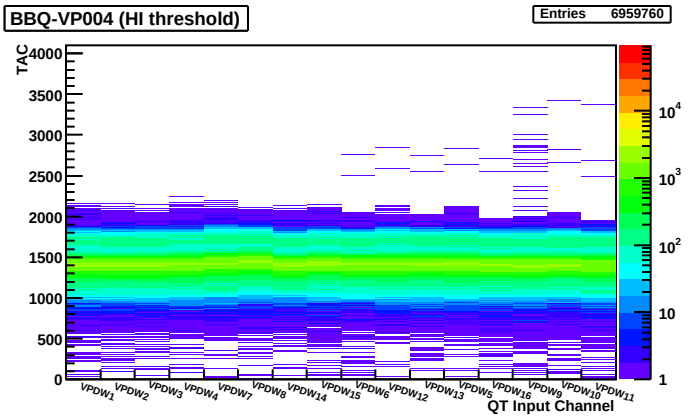

BBQ-VP002 (LO threshold)
Entries 6959760
ADC
QT Input Channel

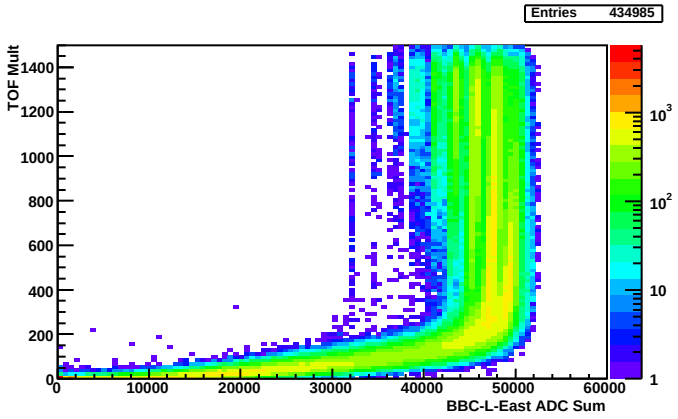

BBQ-VP002 (LO threshold)
Entries 6959760
TAC
QT Input Channel

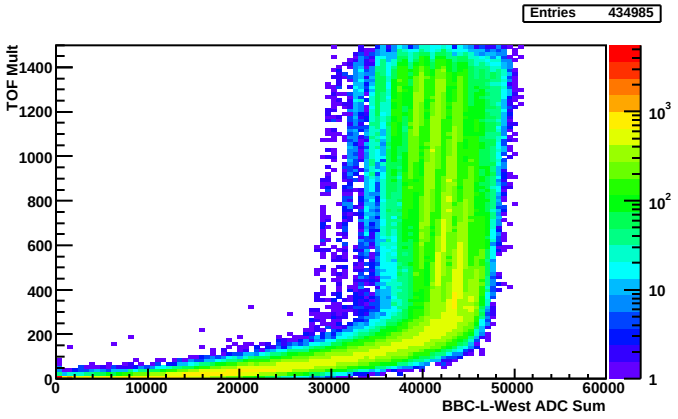

BBQ-VP003 (HI threshold)
Entries 6959760
ADC
QT Input Channel

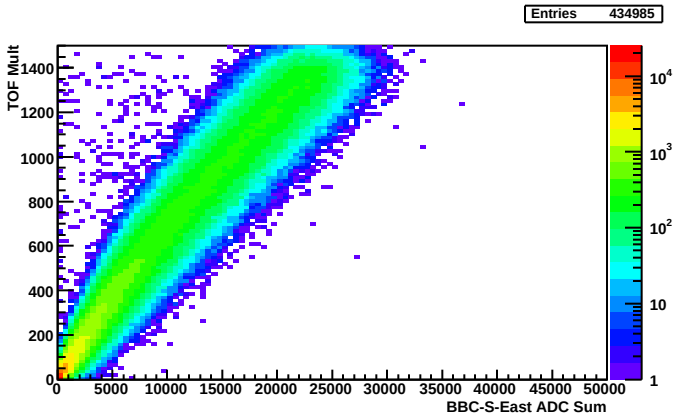

BBQ-VP003 (HI threshold)
Entries 6959760
TAC
QT Input Channel

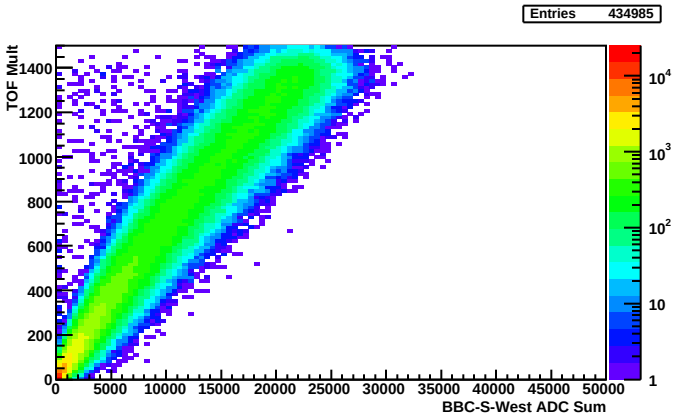

BBQ-VP004 (HI threshold)
Entries 6959760
ADC
QT Input Channel

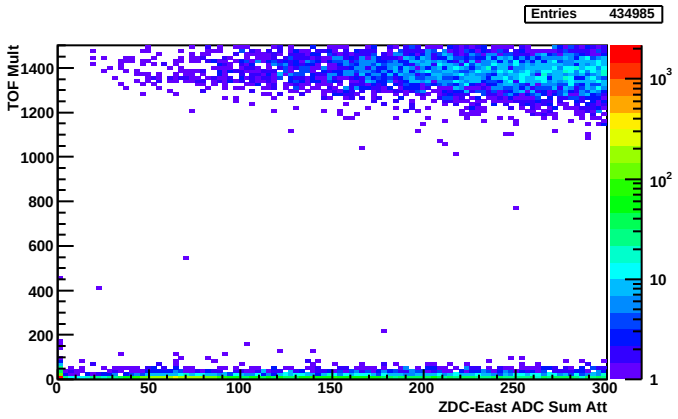

BBQ-VP004 (HI threshold)
Entries 6959760
TAC
QT Input Channel

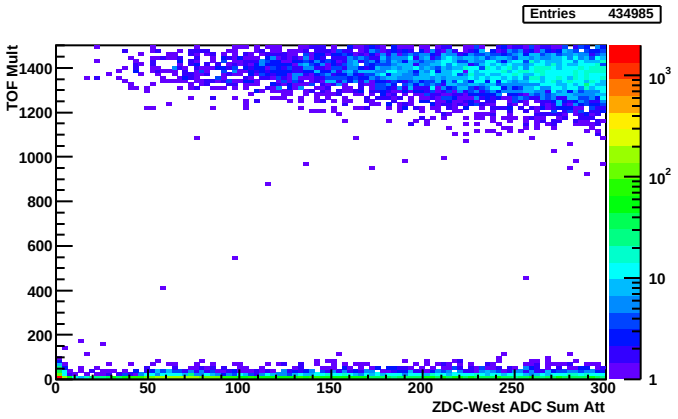

Entries 434985
TOF Mult
BBC-L-East ADC Sum

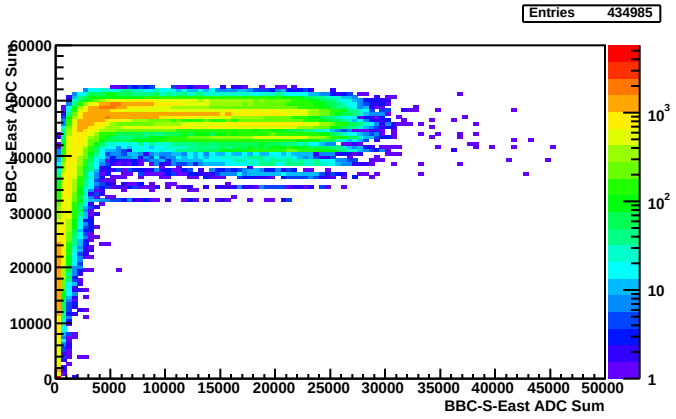

Entries 434985
TOF Mult
BBC-L-West ADC Sum

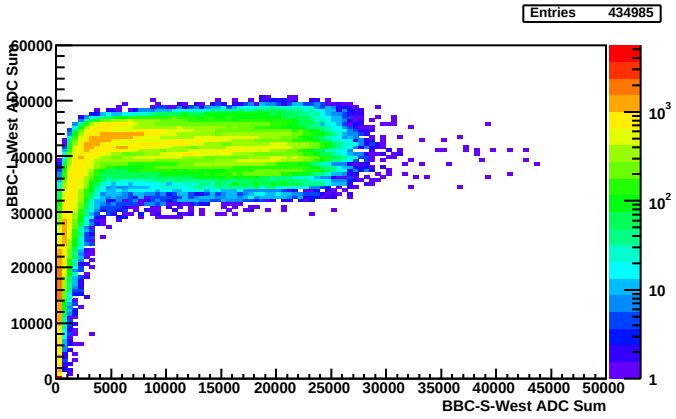

Entries 434985
TOF Mult
BBC-S-East ADC Sum

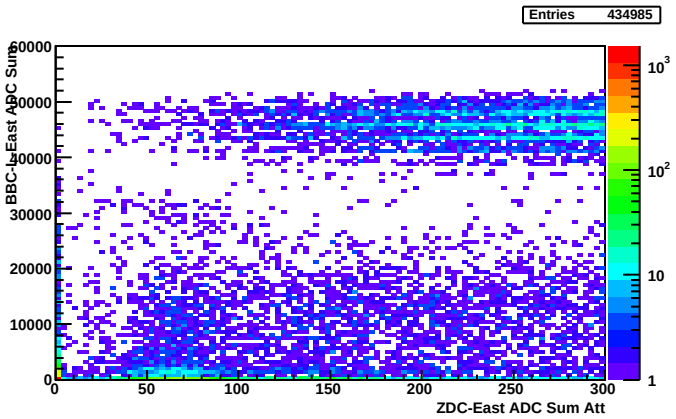

Entries 434985
TOF Mult
BBC-S-West ADC Sum

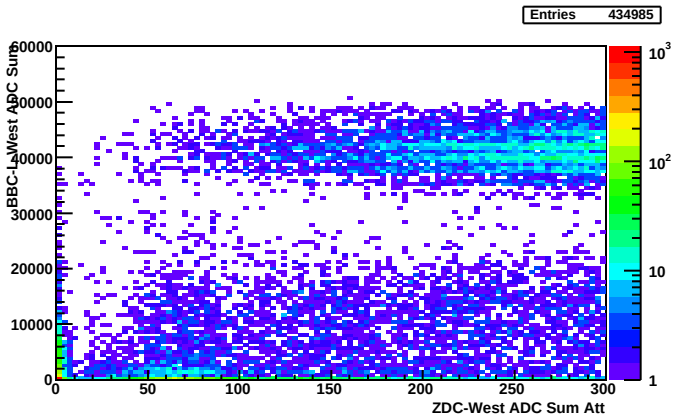

Entries 434985
TOF Mult
ZDC-East ADC Sum Att


Entries 434985
TOF Mult
ZDC-West ADC Sum Att


Entries 434985
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 434985
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 434985
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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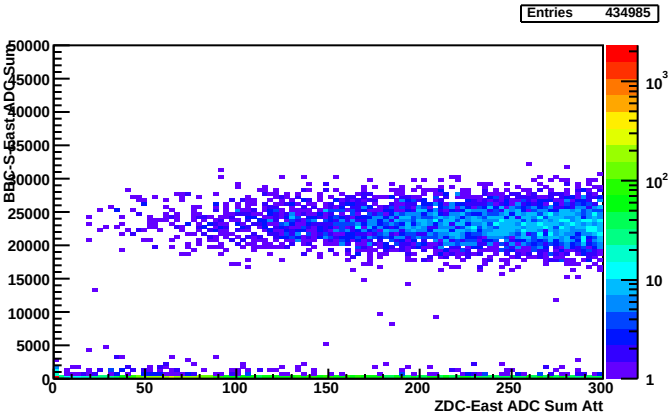
BBC-L-West ADC Sum
ZDC-West ADC Sum Att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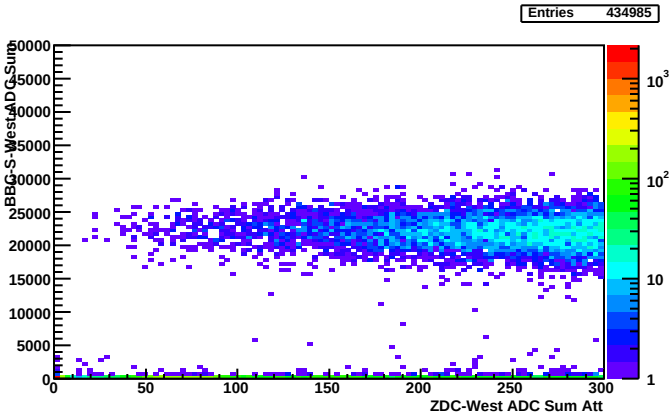
BBC-S-East ADC Sum
ZDC-East ADC Sum Att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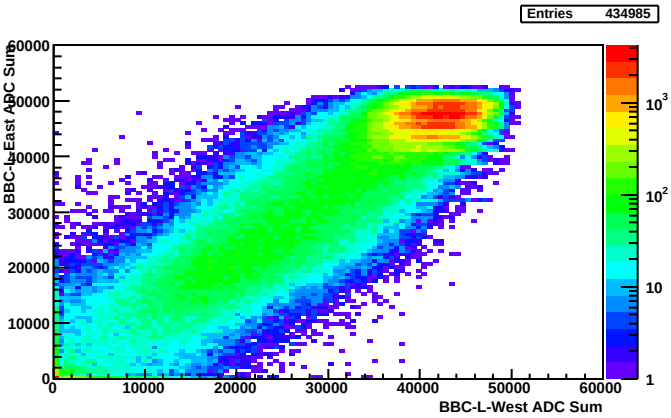
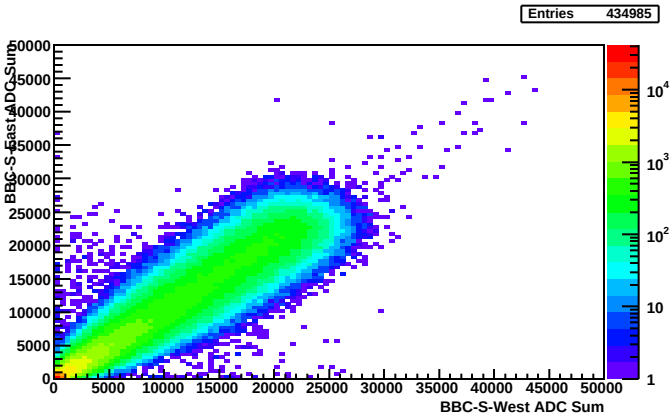
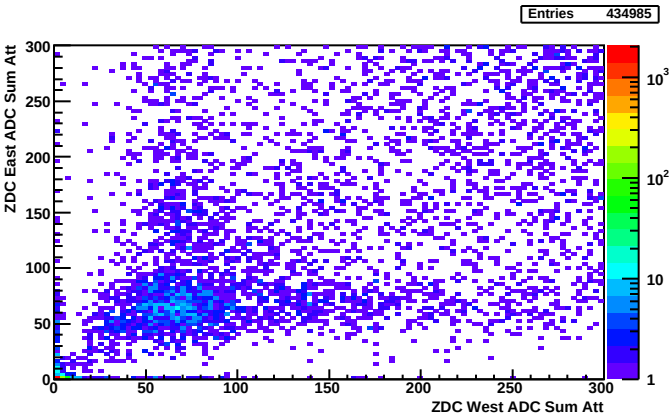
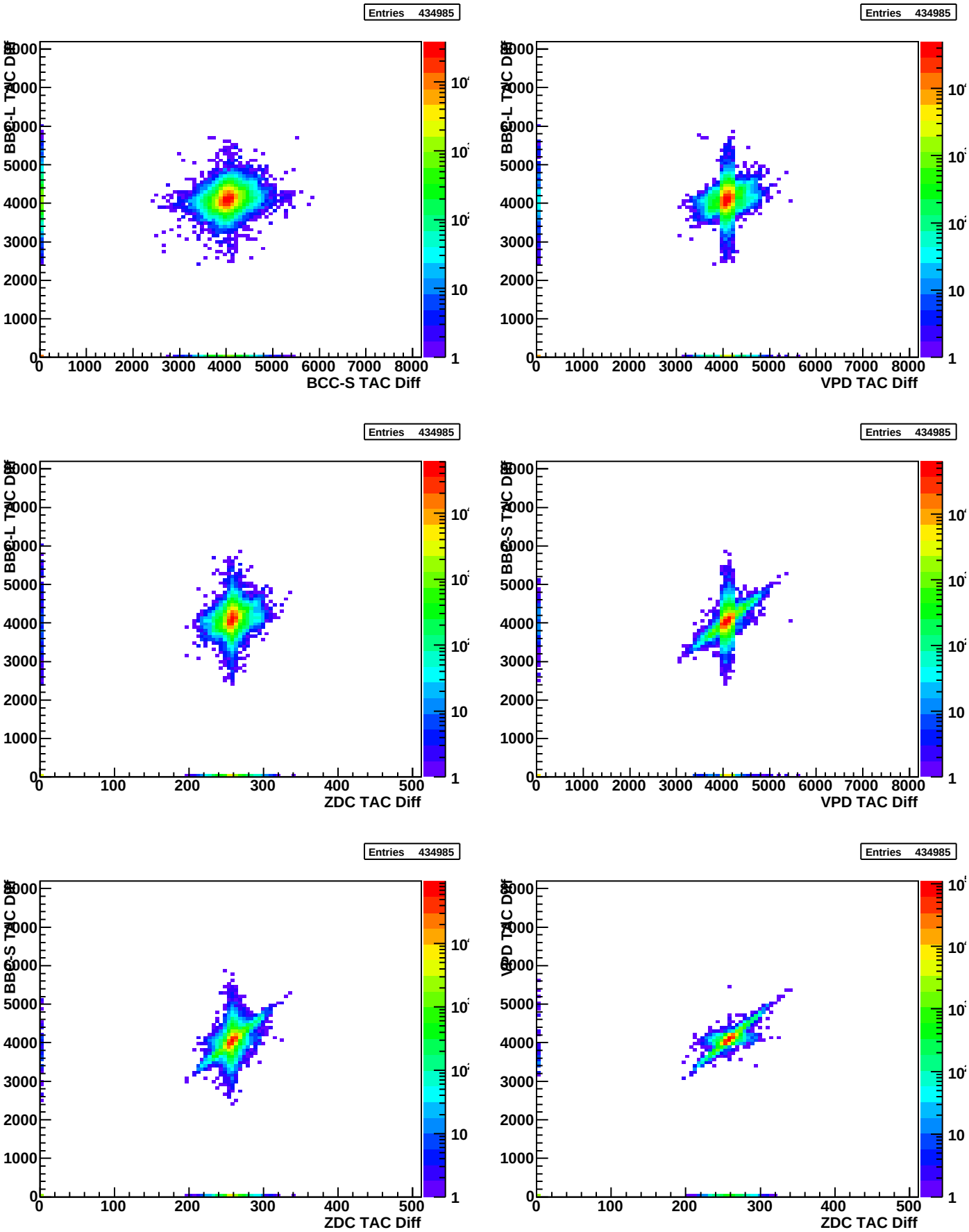
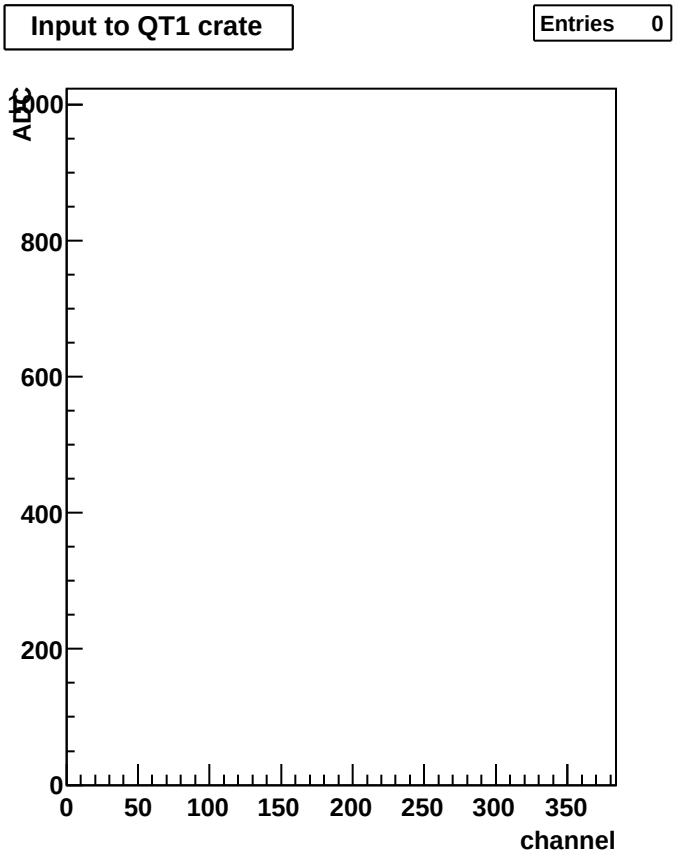
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

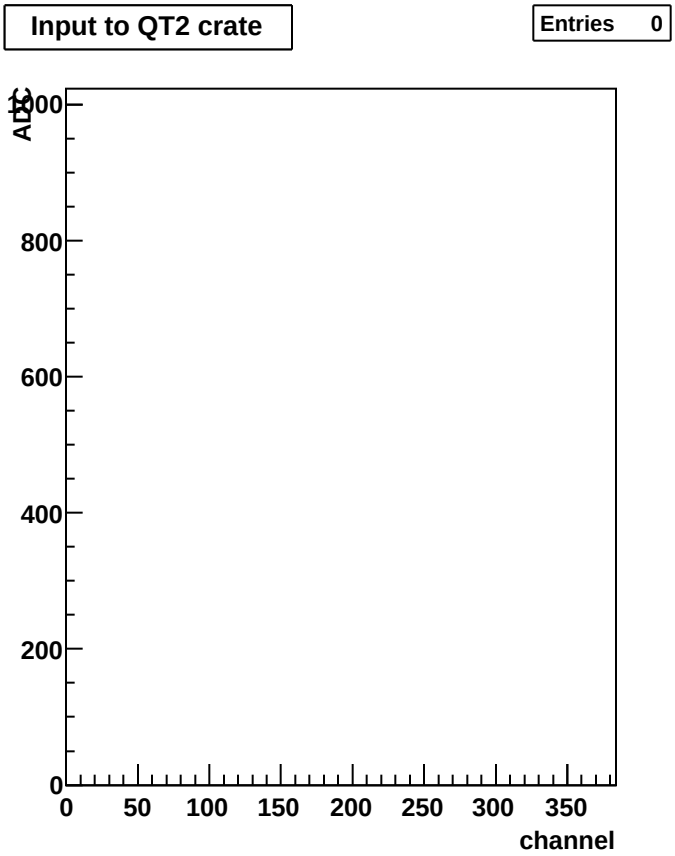

Entries 434985
BBC-L-East ADC Sum
BBC-L-West ADC Sum

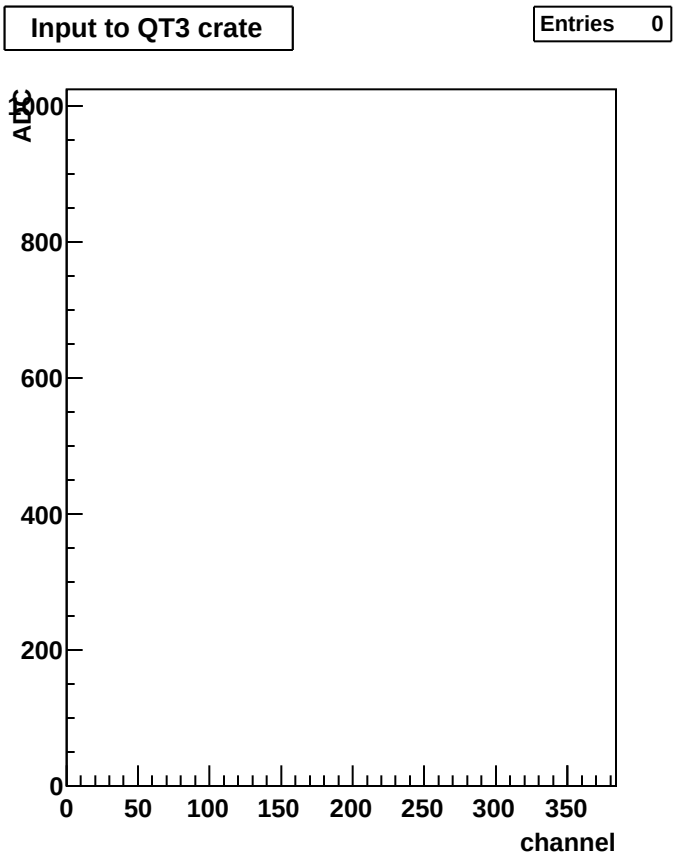

Entries 434985
BBC-S-East ADC Sum
BBC-S-West ADC Sum

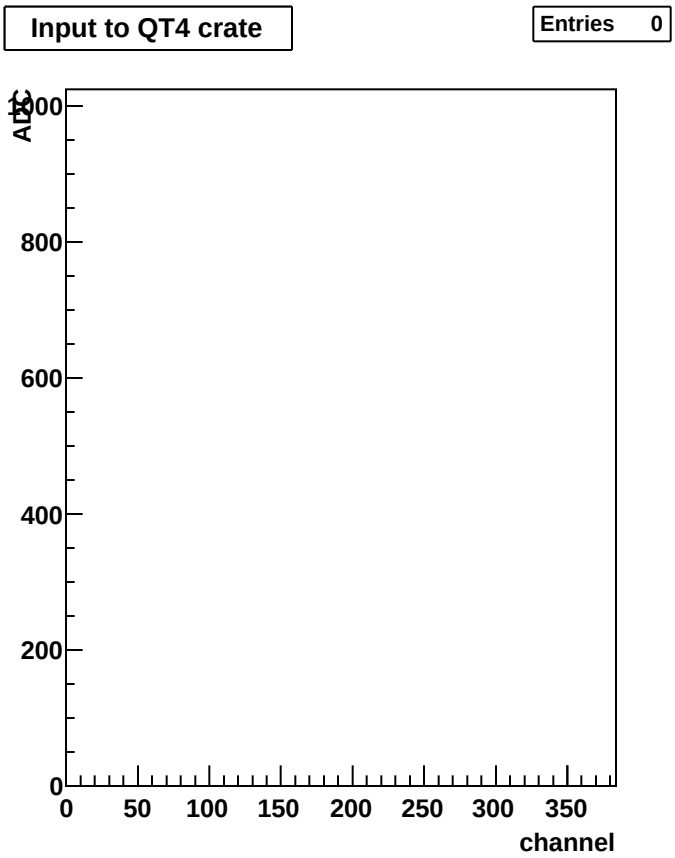

Entries 434985
ZDC East ADC Sum Att
ZDC West ADC Sum Att

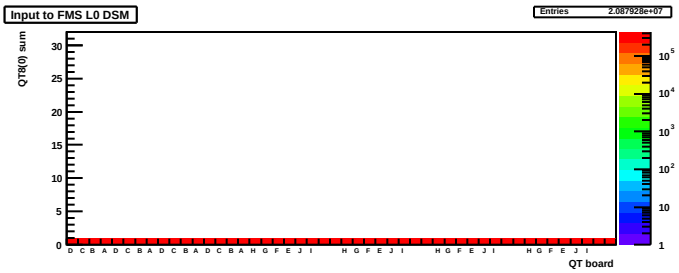

Entries 434985
BBC-L TAC Diff
BCC-S TAC Diff
Entries 434985
BBC-L TAC Diff
VPD TAC Diff
Entries 434985
BBC-L TAC Diff
ZDC TAC Diff
Entries 434985
BBC-S TAC Diff
VPD TAC Diff
Entries 434985
BBC-S TAC Diff
ZDC TAC Diff
Entries 434985
VPD TAC Diff
ZDC TAC Diff

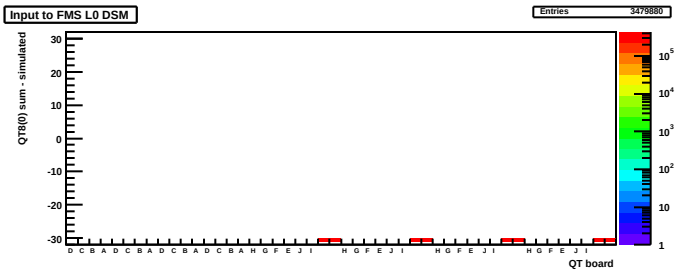

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

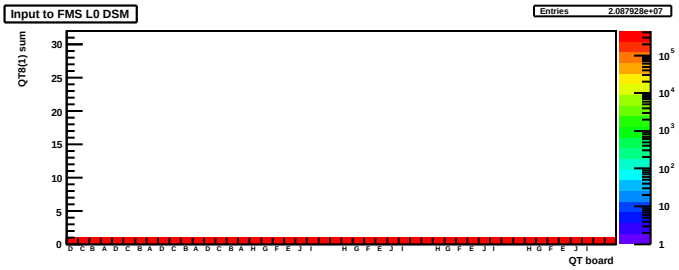

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

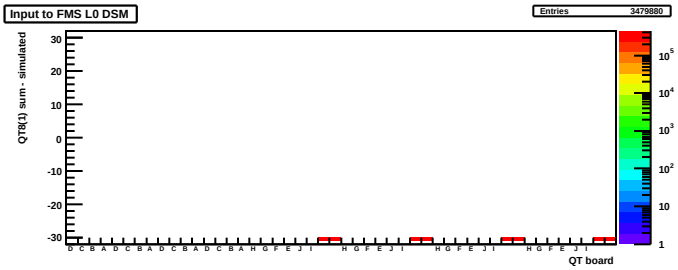

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

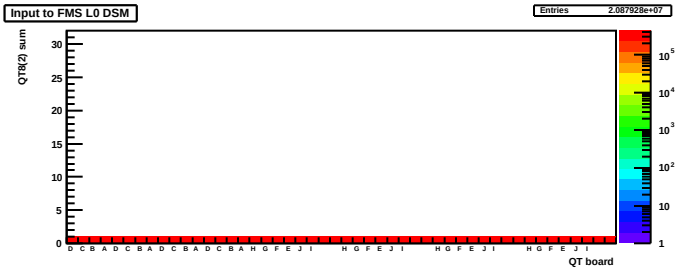

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

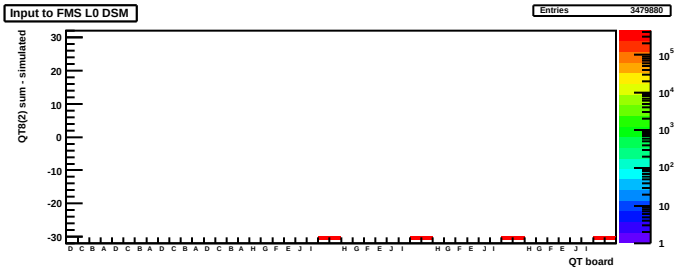

Input to FMS L0 DSM
Entries 2.087928e+07
QT8(0) sum
QT board

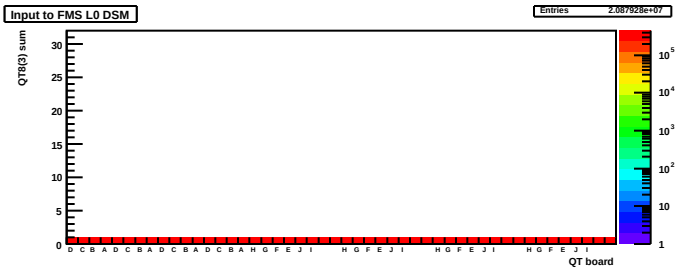

Input to FMS L0 DSM
Entries 3479880
QT8(0) sum - simulated
QT board

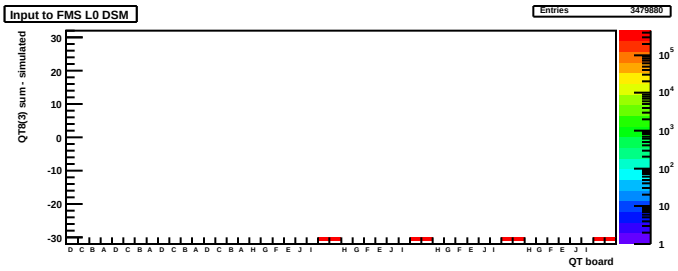

Input to FMS L0 DSM
Entries 2.087928e+07
QT8(1) sum
QT board

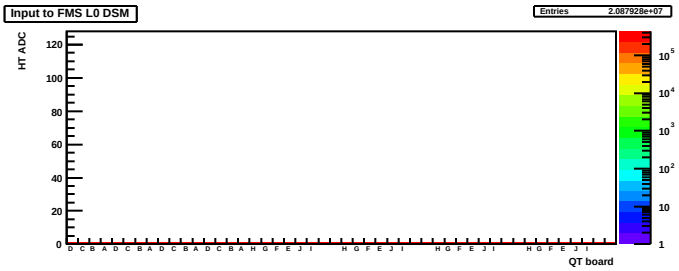

Input to FMS L0 DSM
Entries 3479880
QT8(1) sum - simulated
QT board

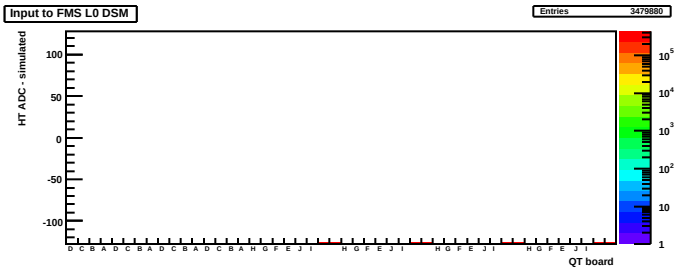

Input to FMS L0 DSM
Entries 2.087928e+07
QT8(2) sum
QT board

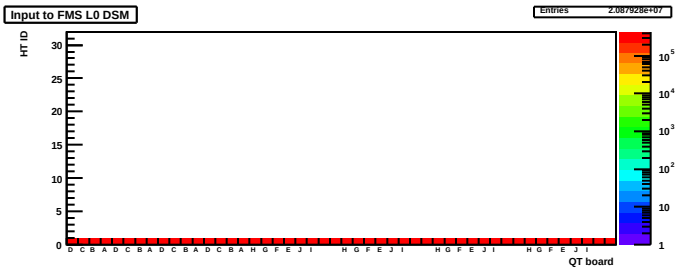

Input to FMS L0 DSM
Entries 3479880
QT8(2) sum - simulated
QT board

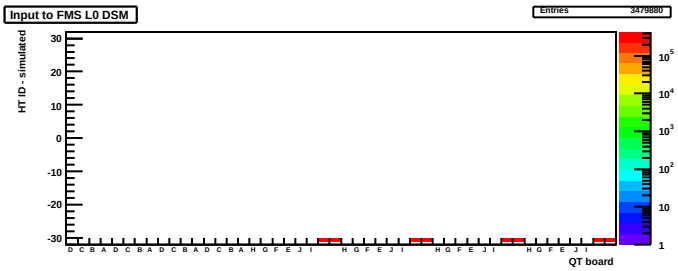

Input to FMS L0 DSM
Entries 2.087928e+07
QT8(3) sum
QT board

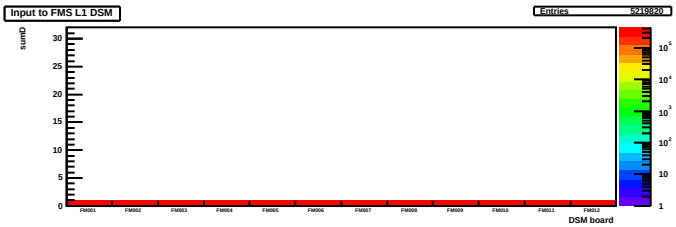

Input to FMS L0 DSM
Entries 3479880
QT8(3) sum - simulated
QT board

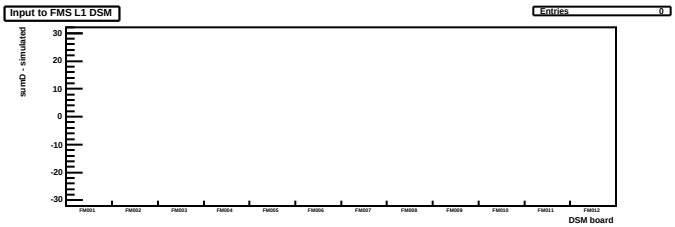

Input to FMS L0 DSM
Entries 2.087928e+07
HT ADC
QT board

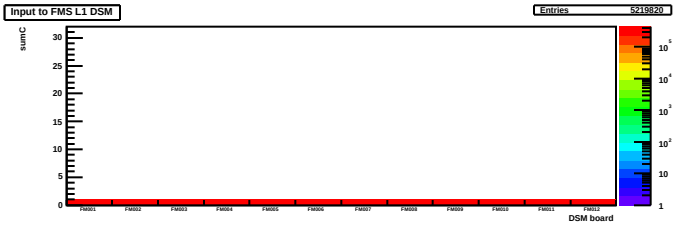

Input to FMS L0 DSM
Entries 3479880
HT ADC - simulated
QT board

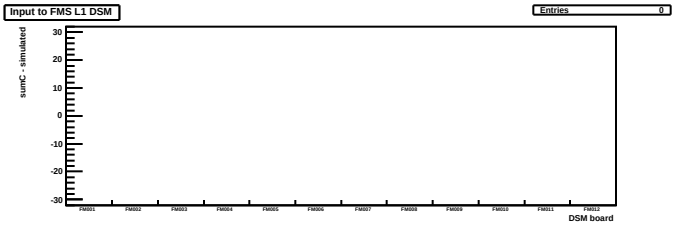

Input to FMS L0 DSM
Entries 2.087928e+07
HT ID
QT board

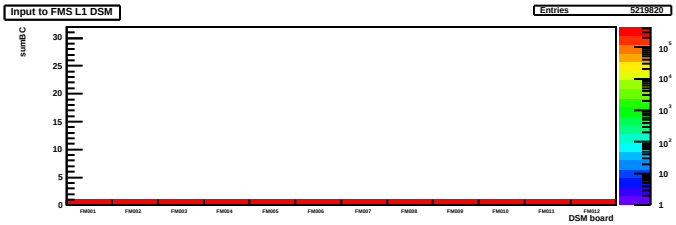

Input to FMS L0 DSM
Entries 3479880
HT ID - simulated
QT board

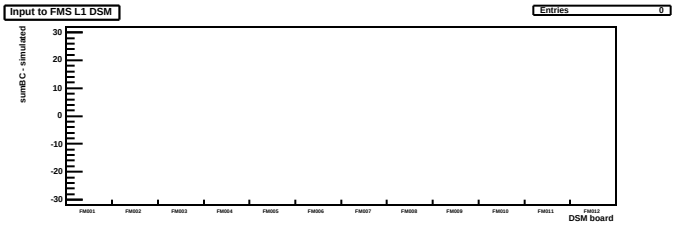

Input to FMS L1 DSM
Entries 5219820
sumD
DSM board

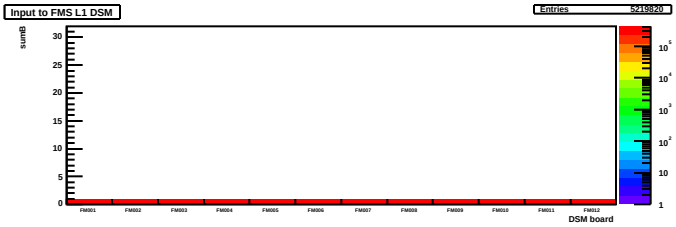

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

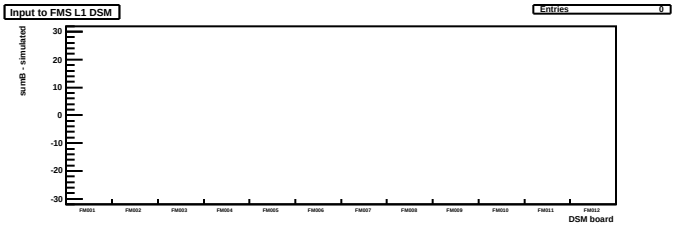

Input to FMS L1 DSM
Entries 5219820
sumC
DSM board

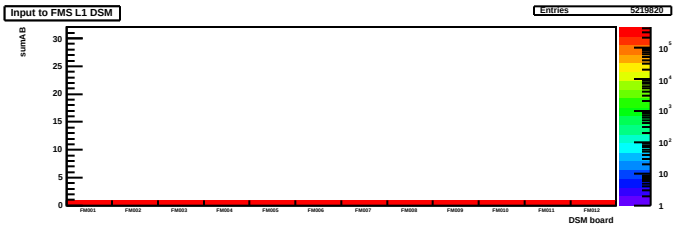

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

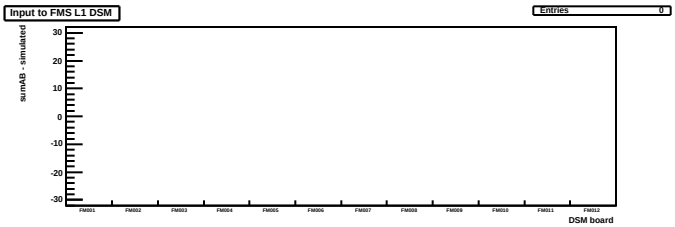

Input to FMS L1 DSM
Entries 5219820
sumBC
DSM board

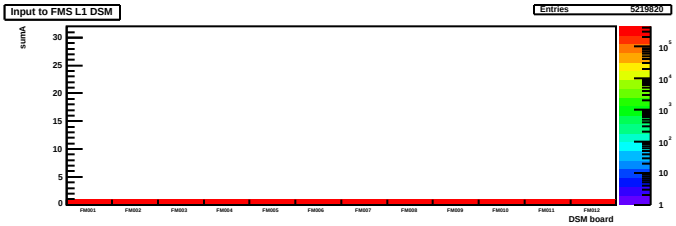

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

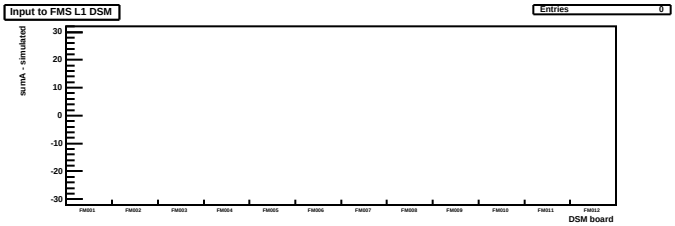

Input to FMS L1 DSM
Entries 5219820
sumB
DSM board

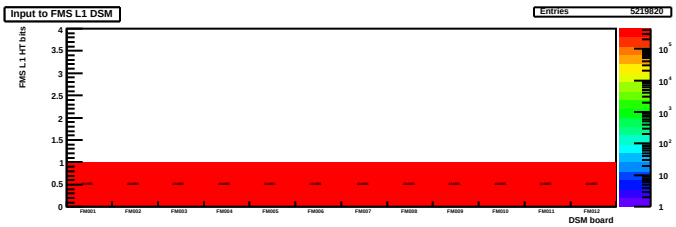

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

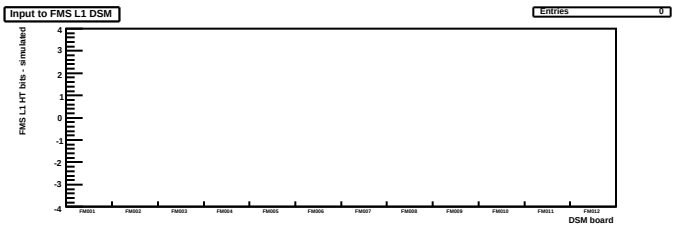

Input to FMS L1 DSM
Entries 5219820
sumAB
DSM board

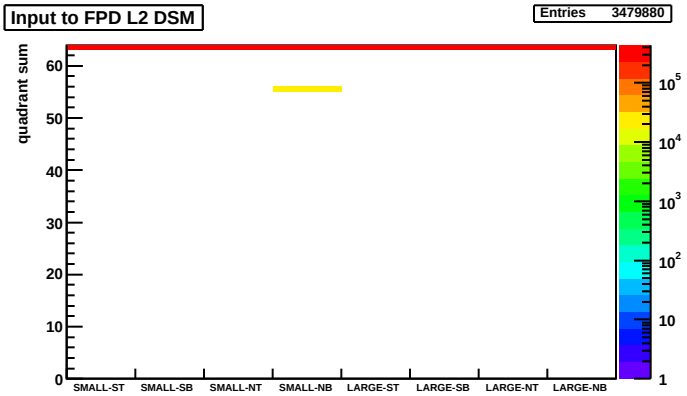

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

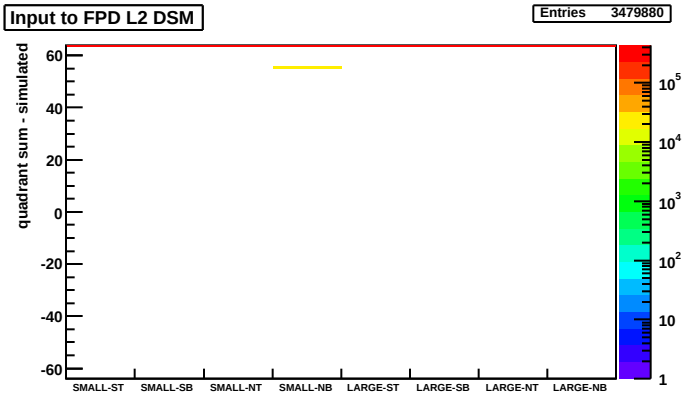

Input to FMS L1 DSM
Entries 5219820
sumA
DSM board

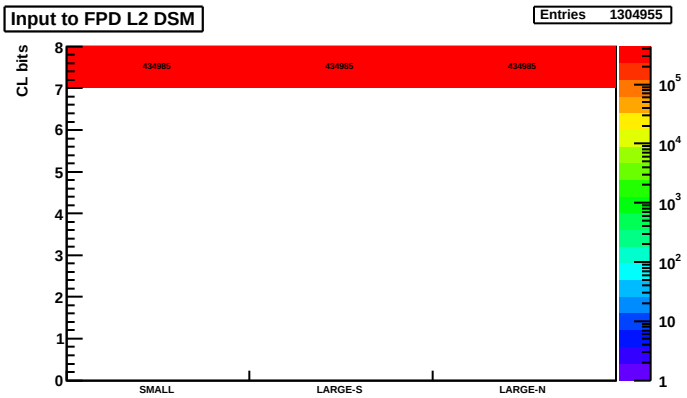

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

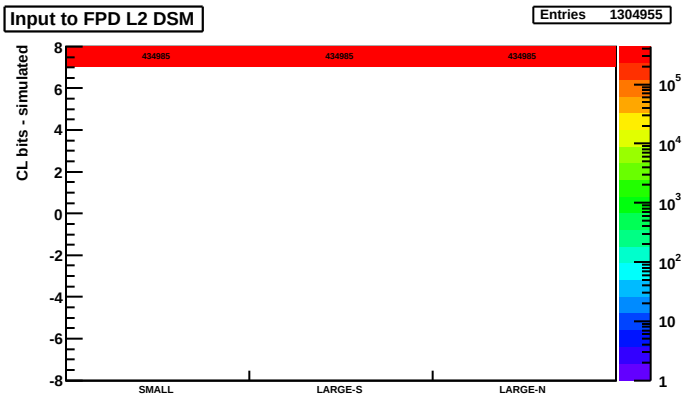

Input to FMS L1 DSM
Entries 5219820
FMS L1 HT bits
DSM board

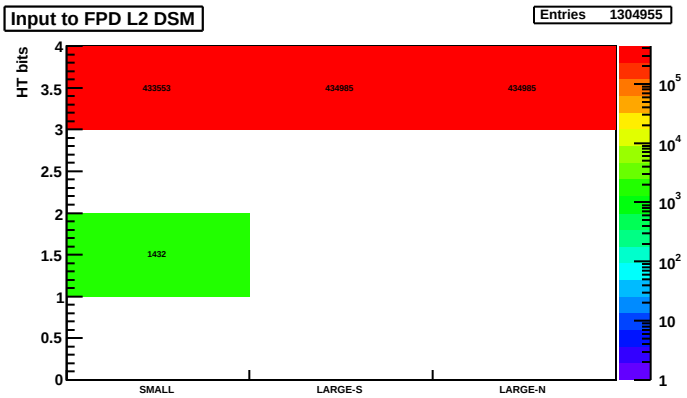

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

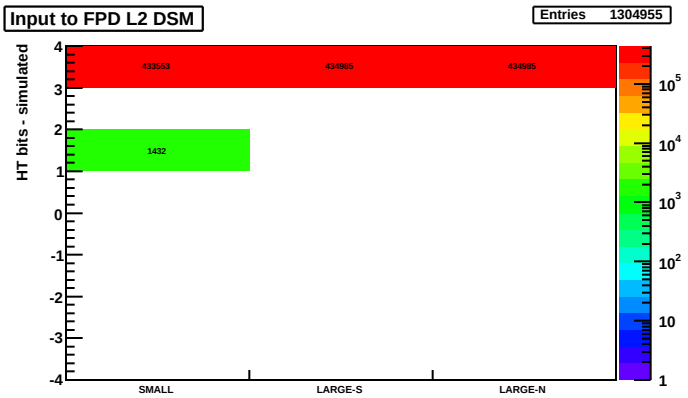

Input to FPD L2 DSM
Entries 3479880
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

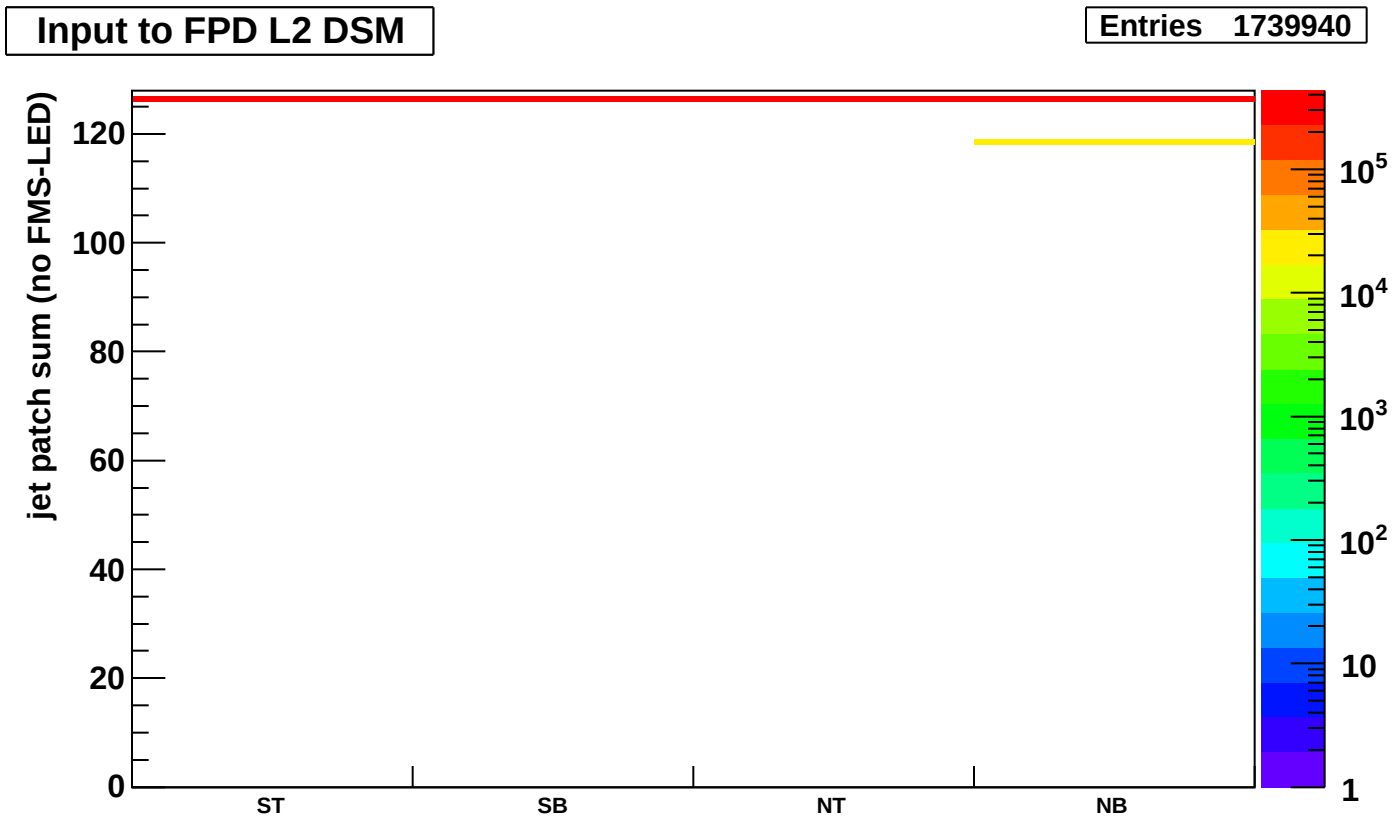

Input to FPD L2 DSM
Entries 3479880
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

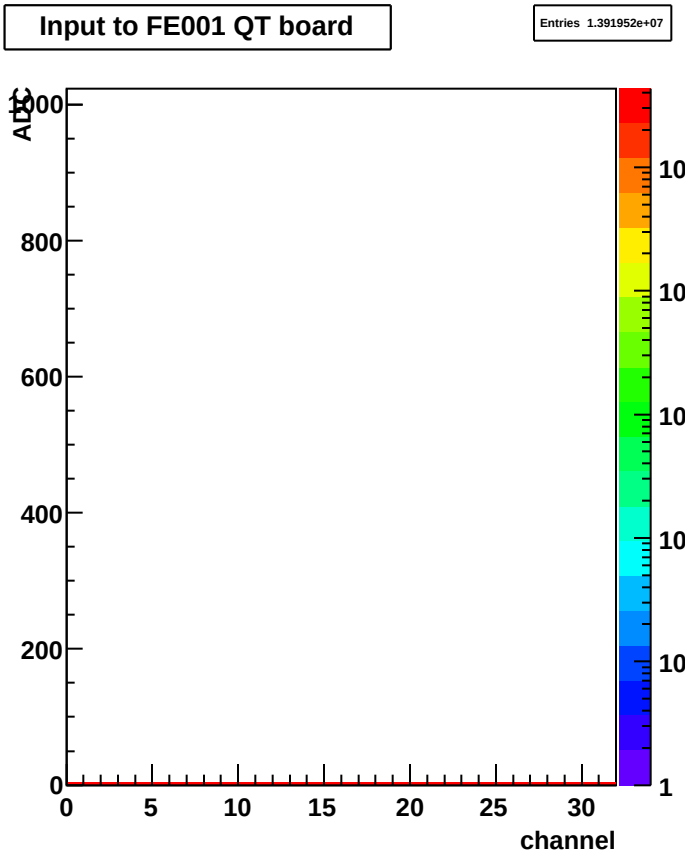

Input to FPD L2 DSM
Entries 1304955
CL bits
434985
434985
434985
SMALL
LARGE-S
LARGE-N

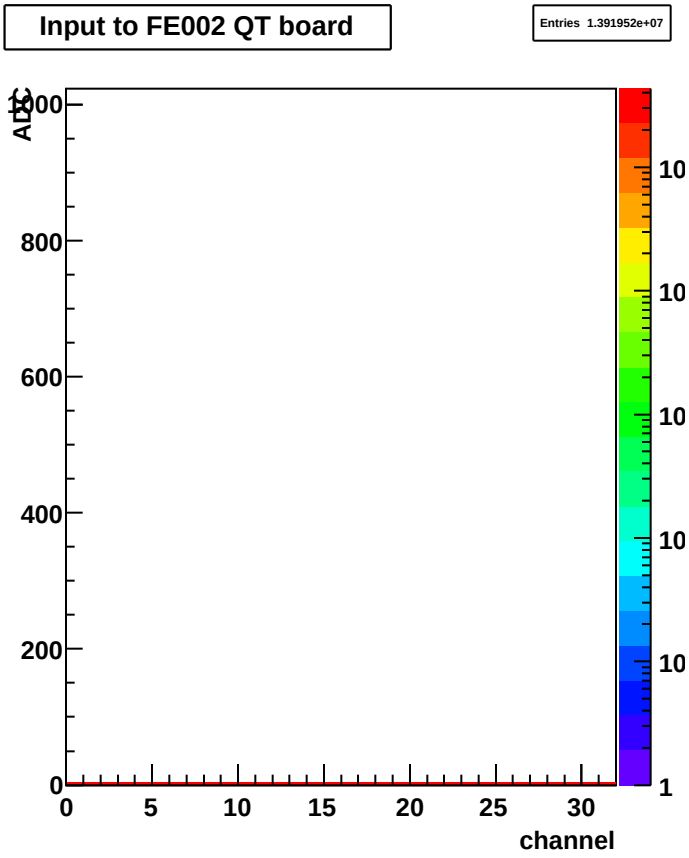

Input to FPD L2 DSM
Entries 1304955
CL bits - simulated
434985
434985
434985
SMALL
LARGE-S
LARGE-N

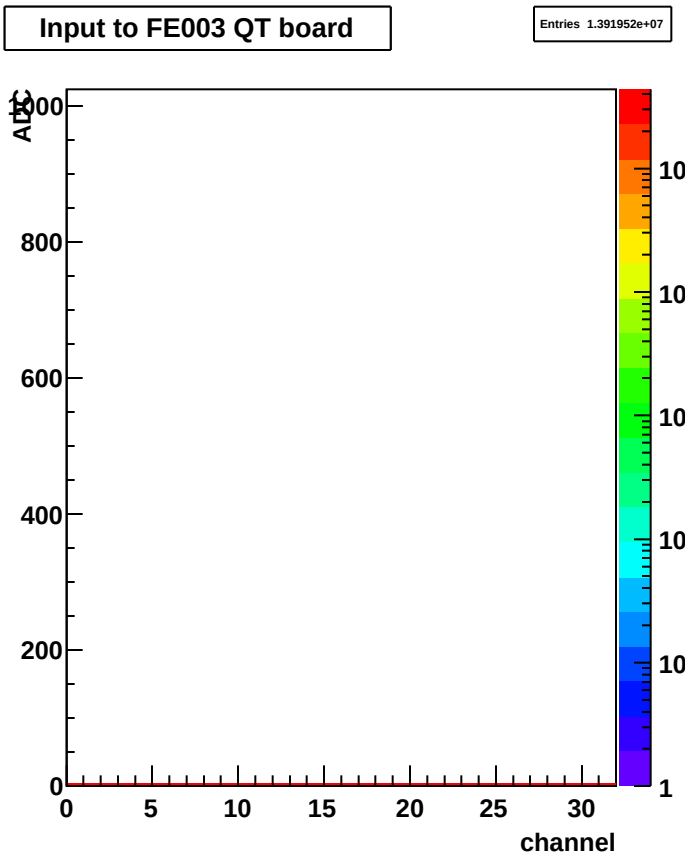

Input to FPD L2 DSM
Entries 1304955
HT bits
433553
434985
434985
1432
SMALL
LARGE-S
LARGE-N

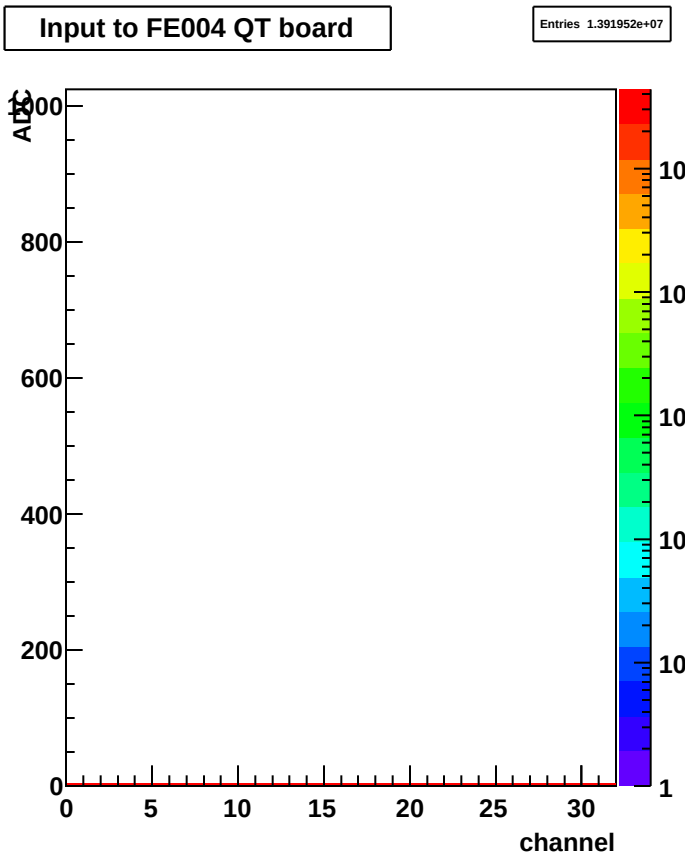

Input to FPD L2 DSM
Entries 1304955
HT bits - simulated
433553
434985
434985
1432
SMALL
LARGE-S
LARGE-N

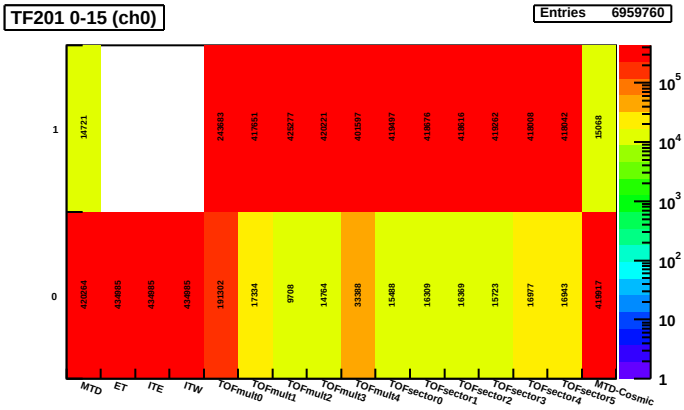

Input to FPD L2 DSM
Entries 1739940
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

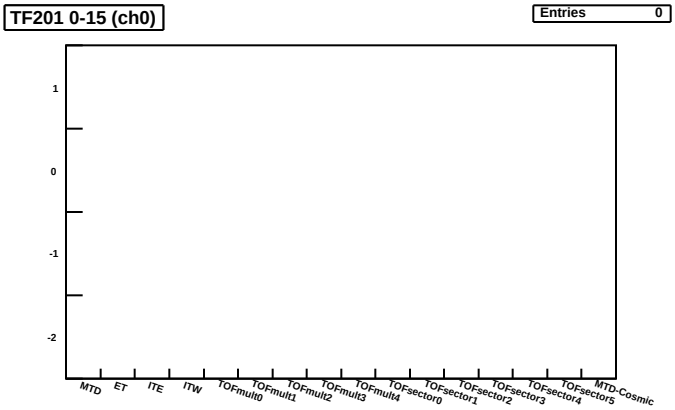

Input to FE001 QT board
Entries 1.391952e+07
ADC
channel

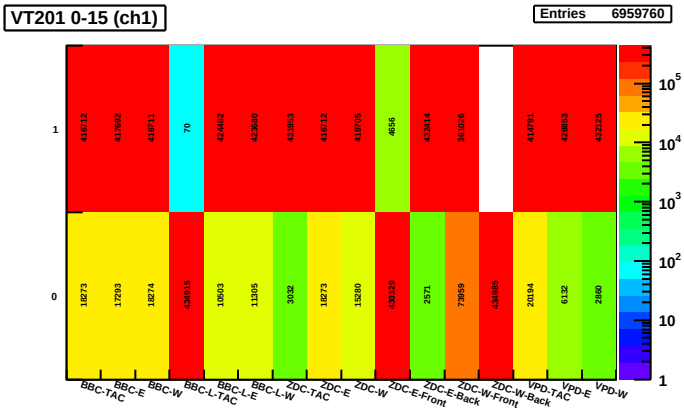

Input to FE002 QT board
Entries 1.391952e+07
ADC
channel

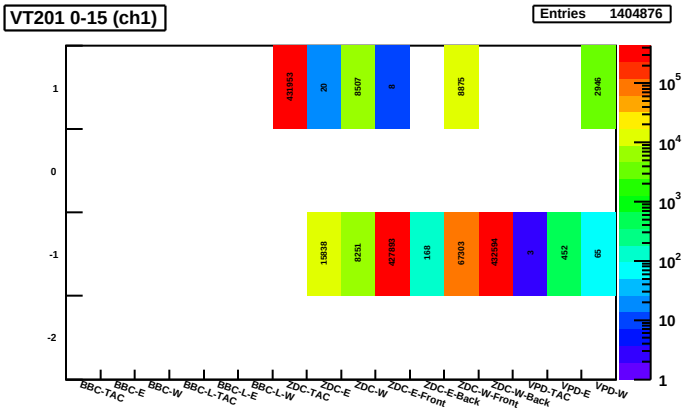

Input to FE003 QT board
Entries 1.391952e+07
ADC
channel

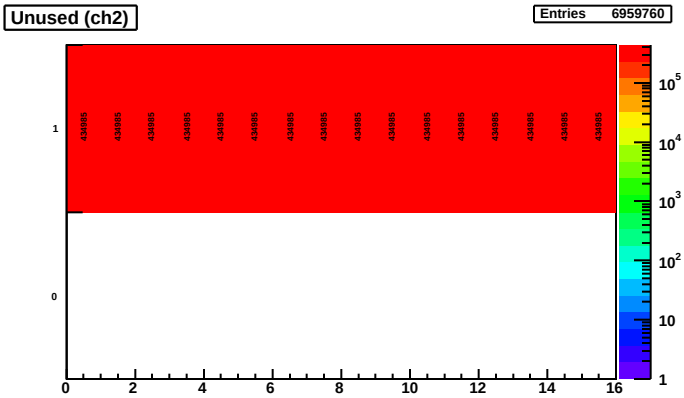

Input to FE004 QT board
Entries 1.391952e+07
ADC
channel

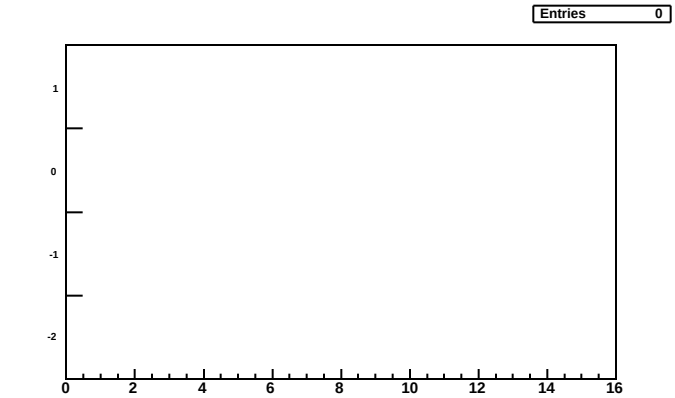

TF201 0-15 (ch0)
Entries 6959760
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

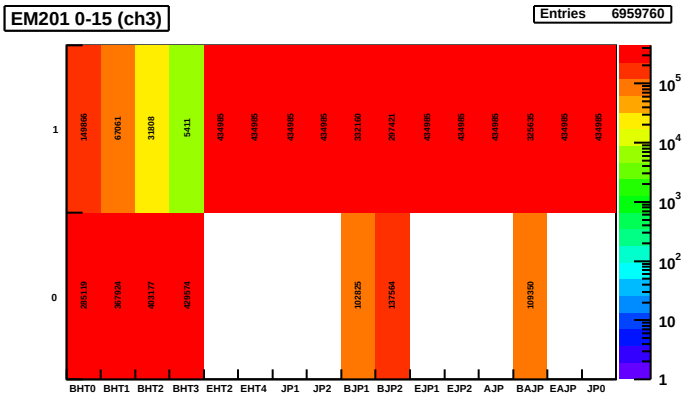

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

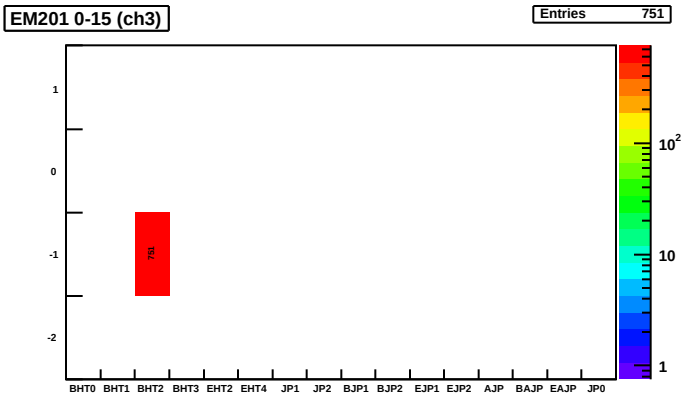

VT201 0-15 (ch1)
Entries 6959760
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

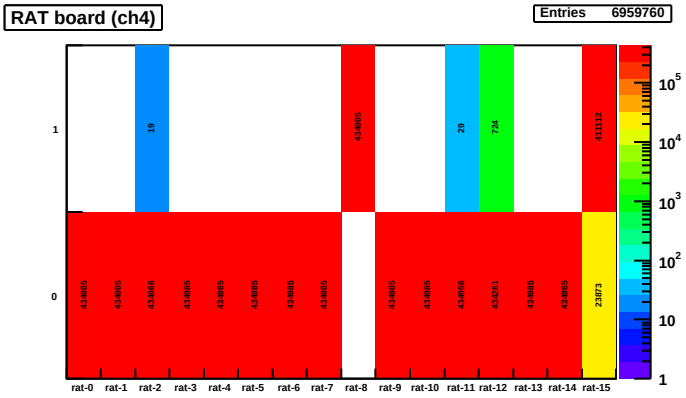

VT201 0-15 (ch1)
Entries 1404876
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

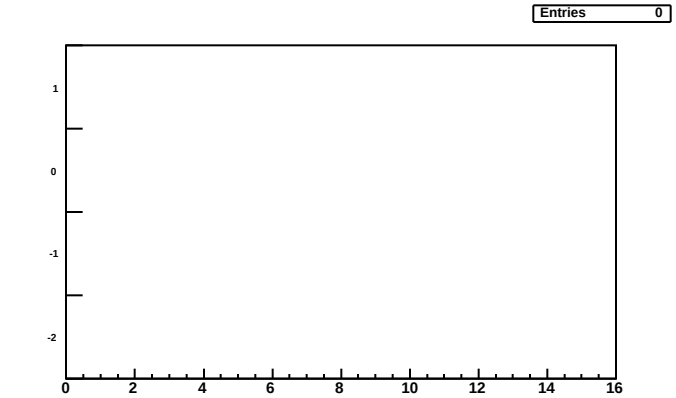

Unused (ch2)
Entries 6959760

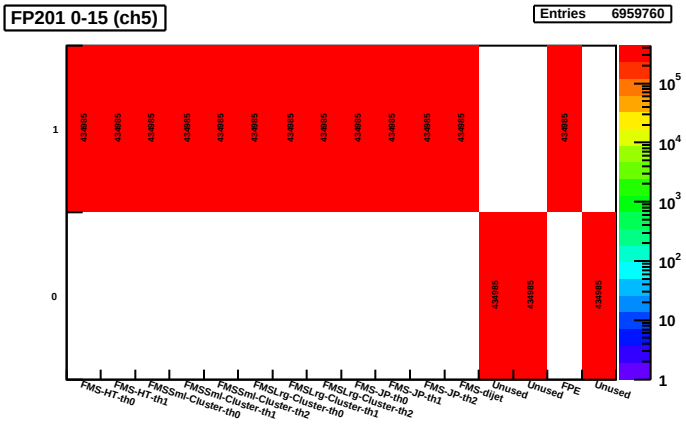

Entries 0

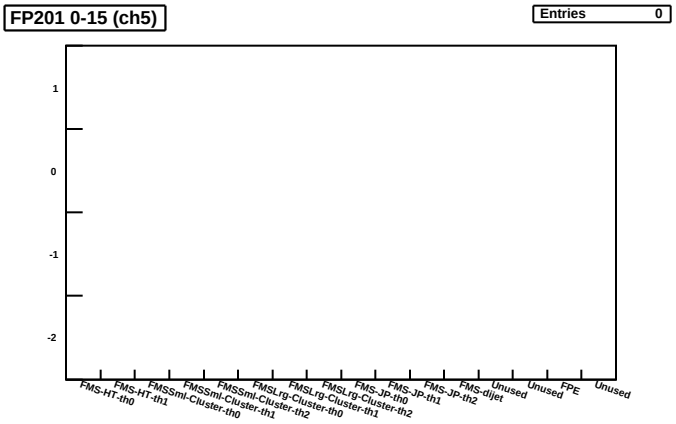

EM201 0-15 (ch3)
Entries 6959760
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

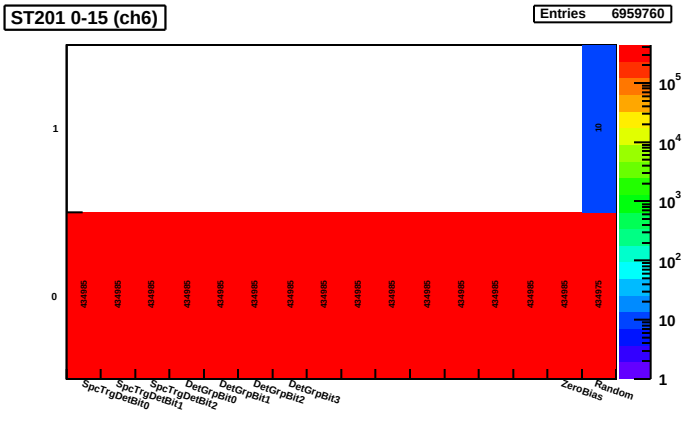

EM201 0-15 (ch3)
Entries 751
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

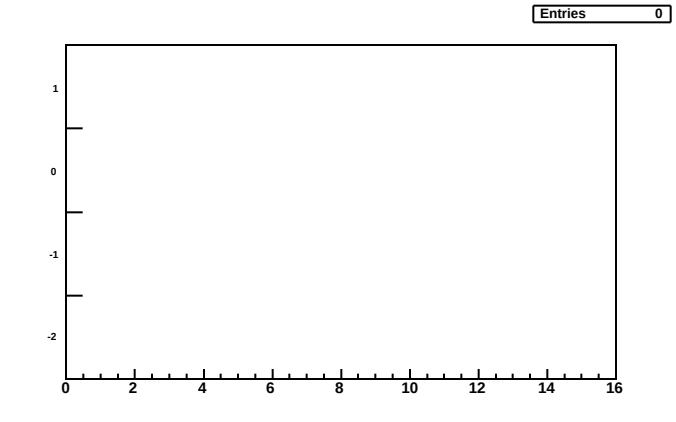

RAT board (ch4)
Entries 6959760
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

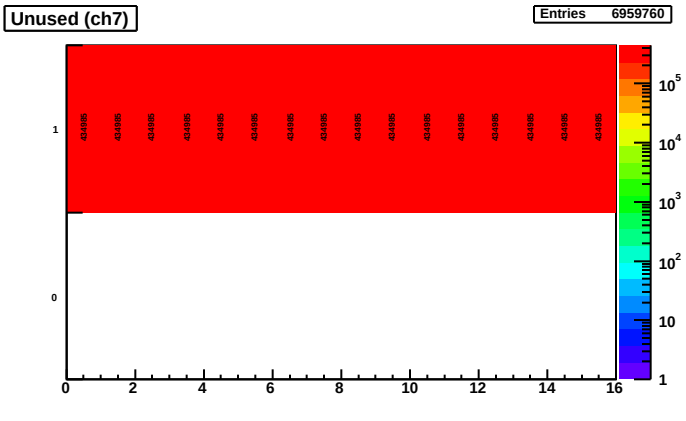

Entries 0

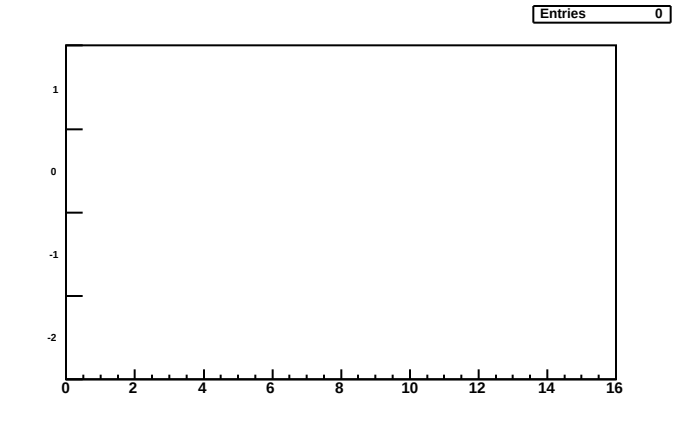

FP201 0-15 (ch5)
Entries 6959760
FMS-HT-th0
FMS-HT-th1
FMSSml-Cluster-th0
FMSSml-Cluster-th1
FMSSml-Cluster-th2
FMSLrg-Cluster-th0
FMSLrg-Cluster-th1
FMSLrg-Cluster-th2
FMS-JP-th0
FMS-JP-th1
FMS-JP-th2
FMS-diJet
Unused
Unused
FPE
Unused


FP201 0-15 (ch5)
Entries 0
FMS-HT-th0
FMS-HT-th1
FMSSml-Cluster-th0
FMSSml-Cluster-th1
FMSSml-Cluster-th2
FMSLrg-Cluster-th0
FMSLrg-Cluster-th1
FMSLrg-Cluster-th2
FMS-JP-th0
FMS-JP-th1
FMS-JP-th2
FMS-diJet
Unused
Unused
FPE
Unused


ST201 0-15 (ch6)
Entries 6959760
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetGrpBit0
DetGrpBit1
DetGrpBit2
DetGrpBit3
ZeroBias
Random


Entries 0


Unused (ch7)
Entries 6959760


Entries 0