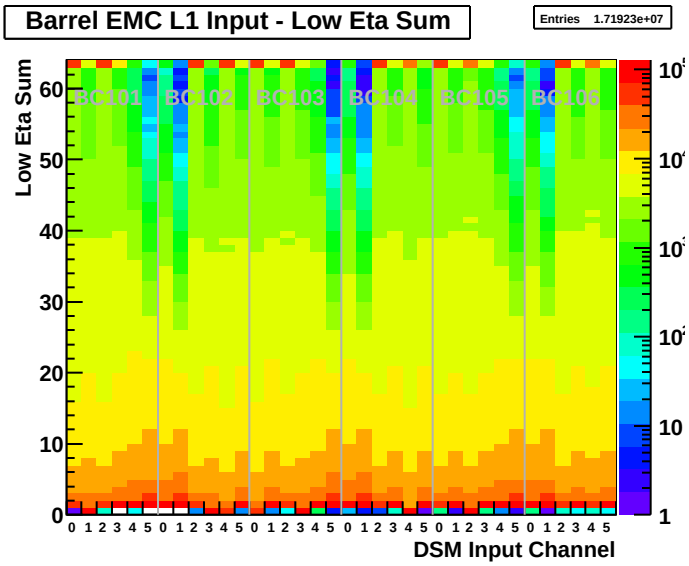
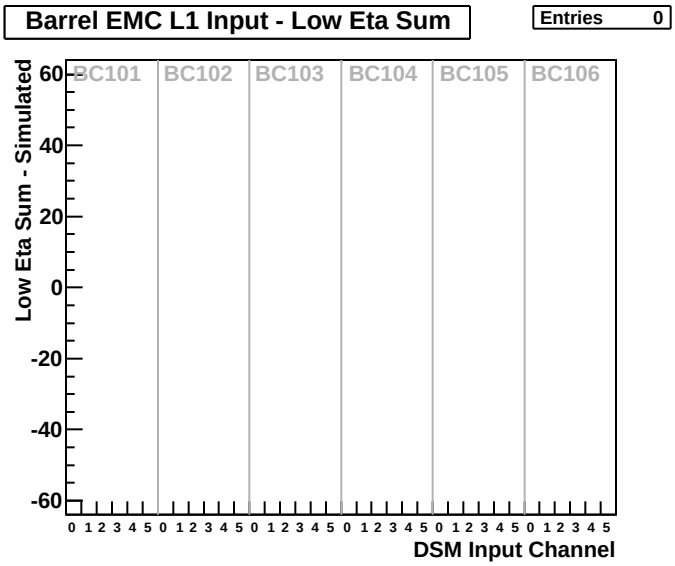
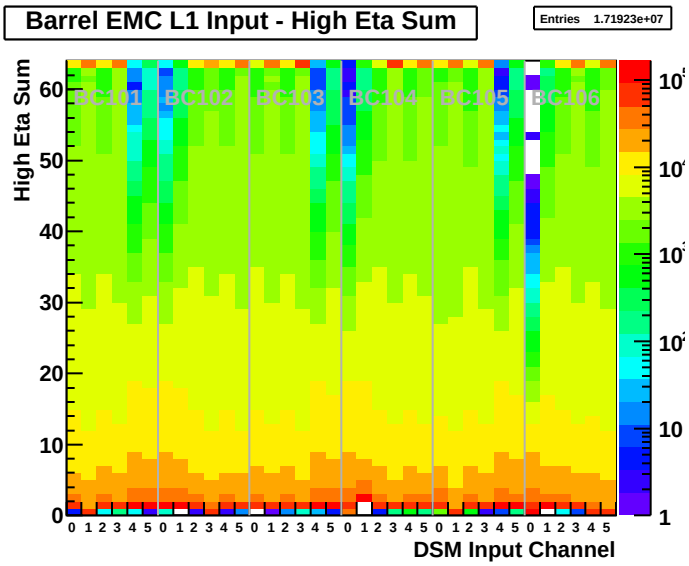
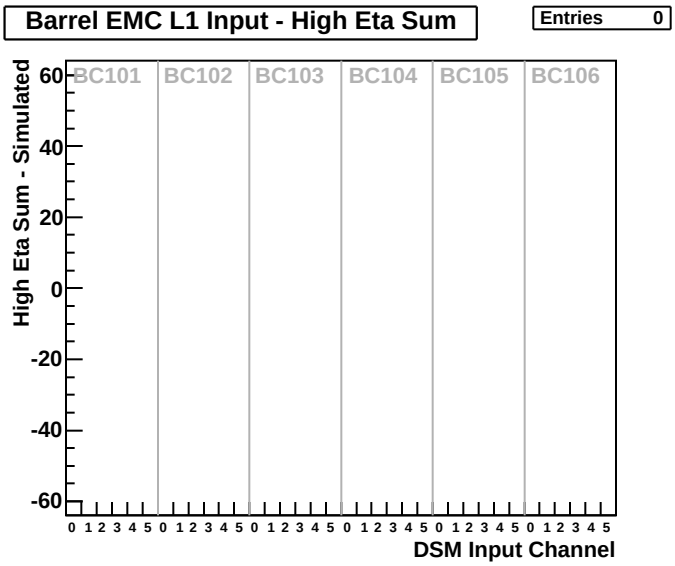
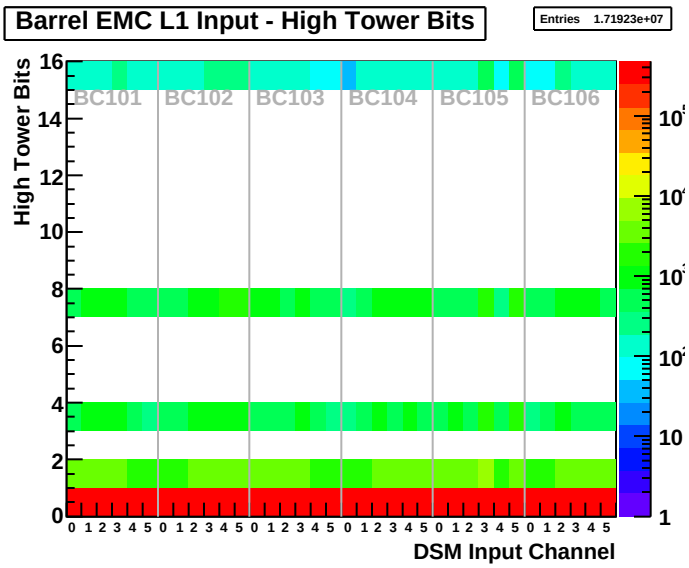
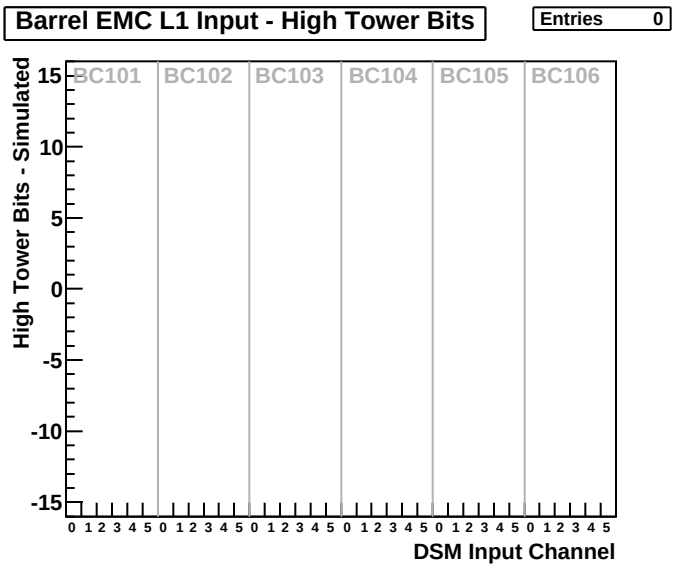

Barrel EMC L1 Input - Low Eta Sum
Entries 1.71923e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

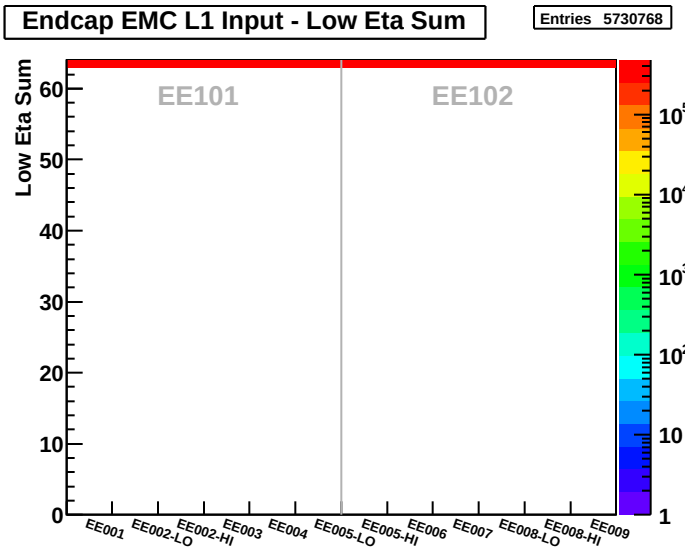

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

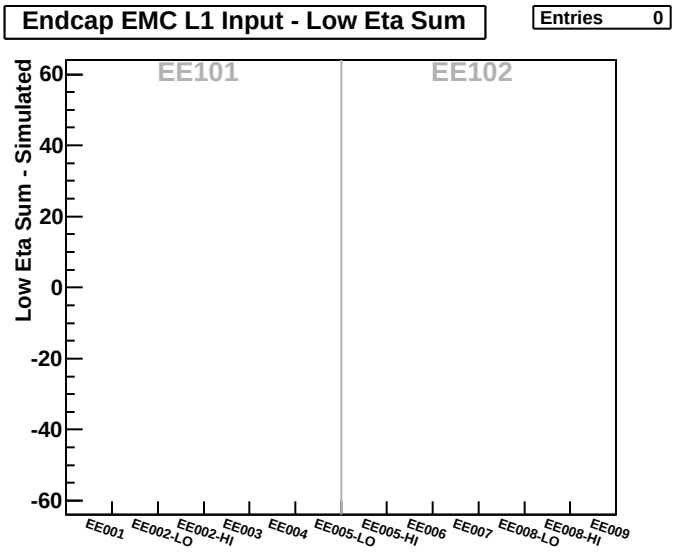

Barrel EMC L1 Input - High Eta Sum
Entries 1.71923e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

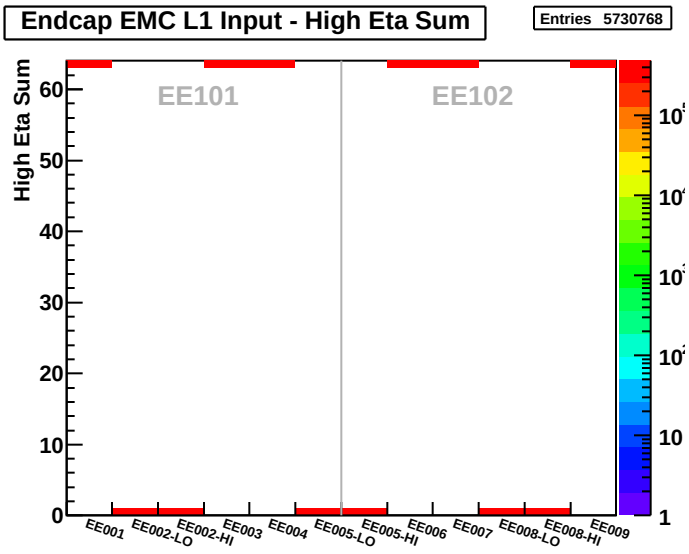

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

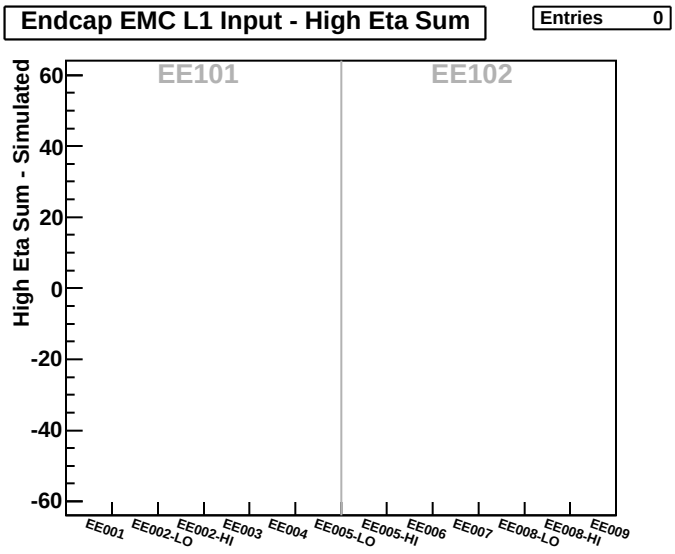

Barrel EMC L1 Input - High Tower Bits
Entries 1.71923e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

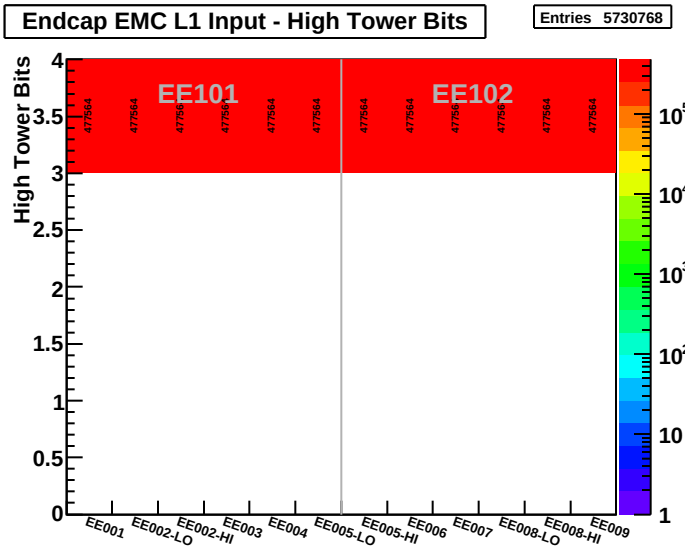

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

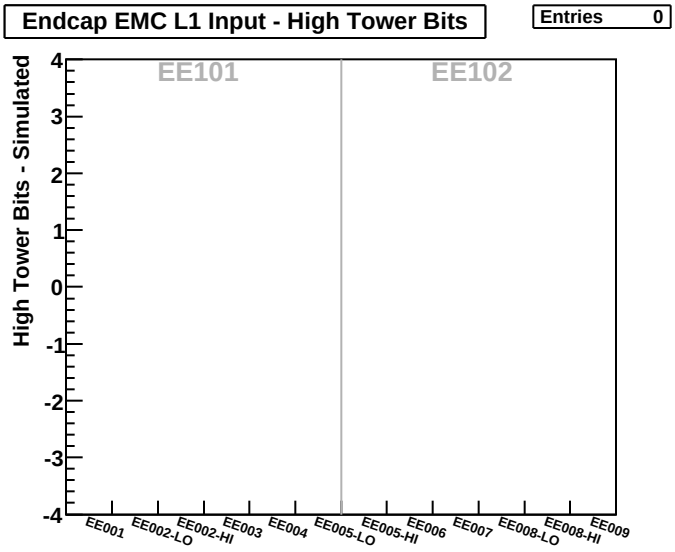

Endcap EMC L1 Input - Low Eta Sum
Entries 5730768
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

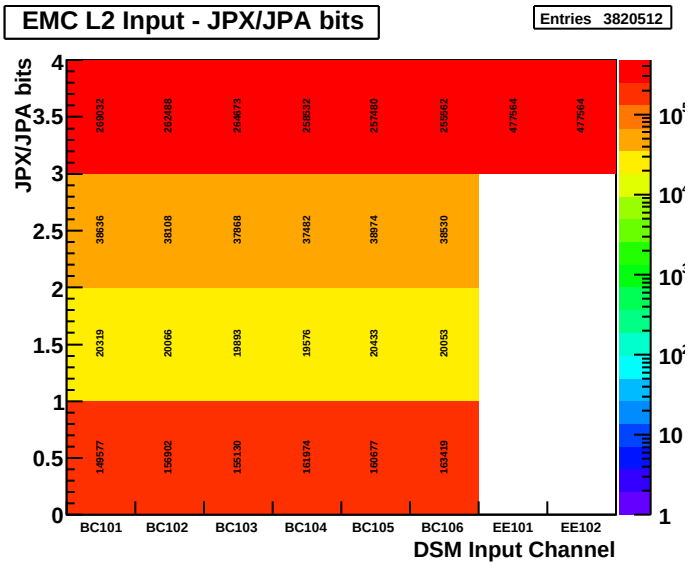

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

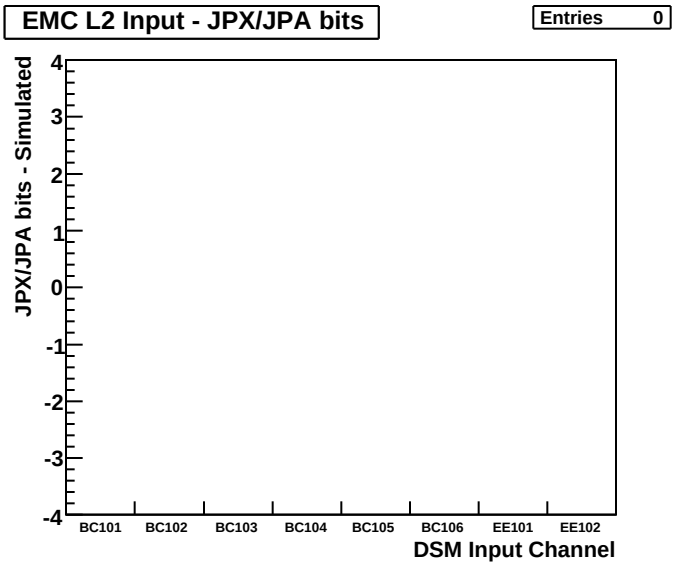

Endcap EMC L1 Input - High Eta Sum
Entries 5730768
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

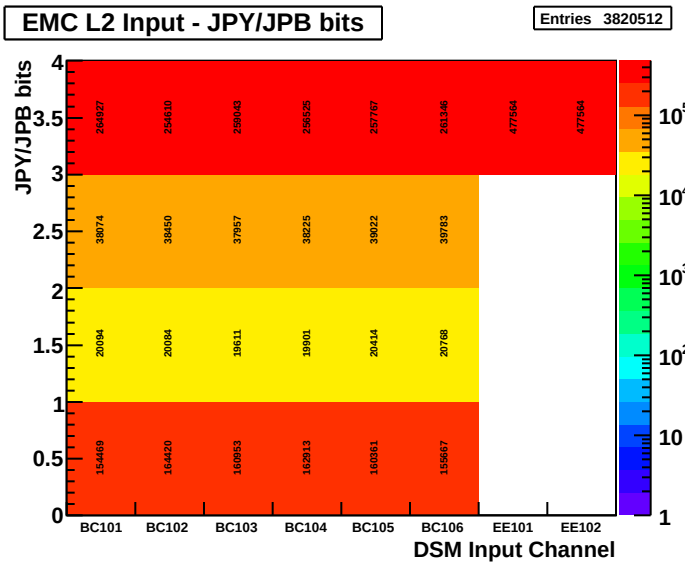

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

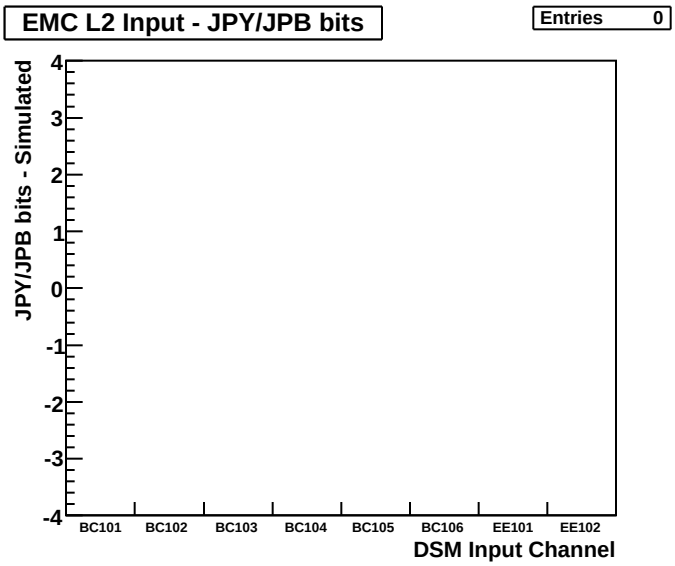

Endcap EMC L1 Input - High Tower Bits
Entries 5730768
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

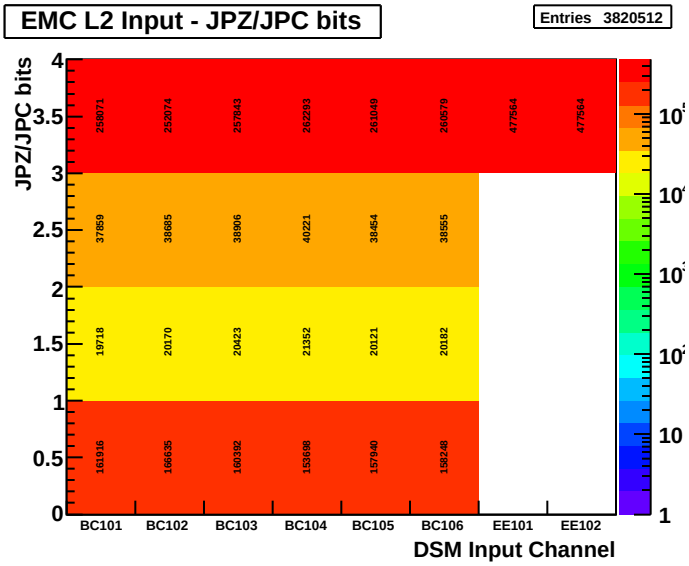

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

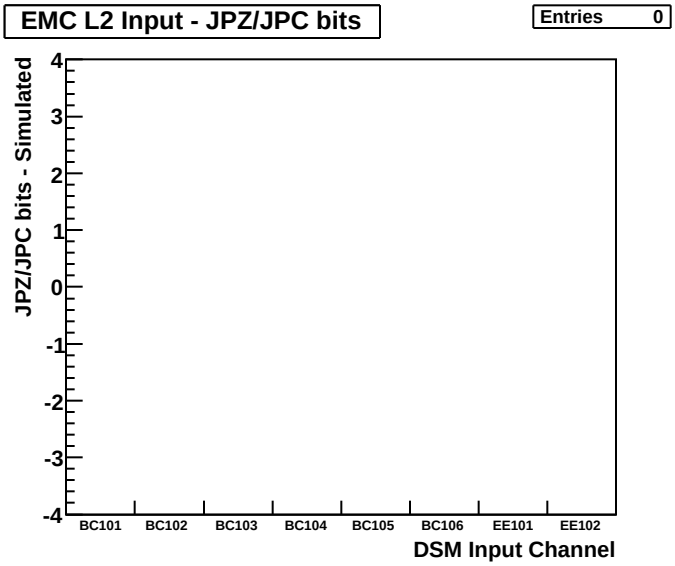

EMC L2 Input - JPX/JPA bits
Entries 3820512
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
259032 252488 254673 258532 257480 255862 477564 477564
38636 38108 37868 37482 38974 38530
20319 20066 19893 19576 20433 20053
149577 156902 155130 161974 160677 163419

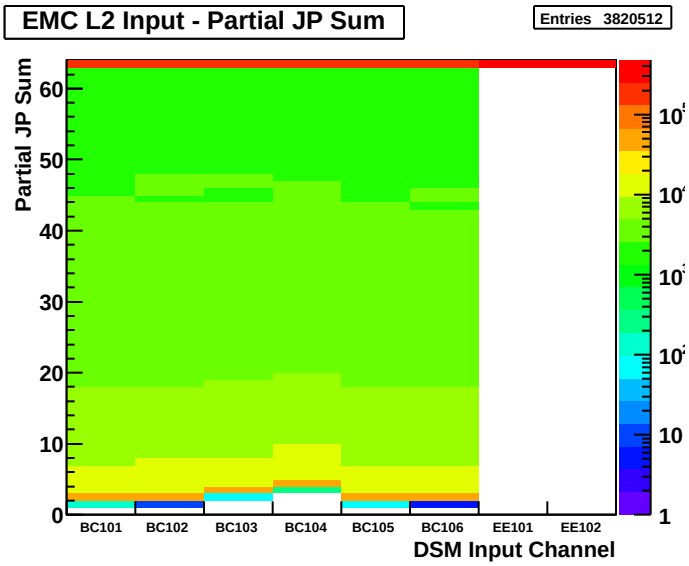

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

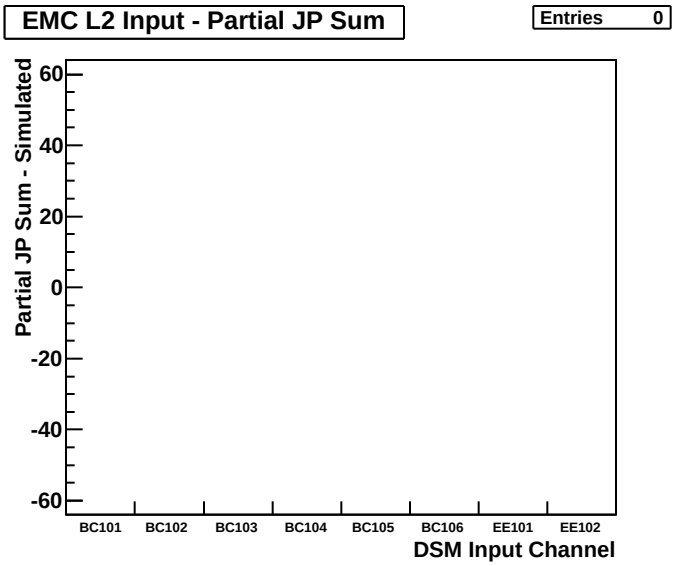

EMC L2 Input - JPY/JPB bits
Entries 3820512
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
264927 254610 258043 256525 257767 261346 477564 477564
38074 38450 37957 38225 39022 39783
20094 20084 19611 19901 20414 20768
154469 164420 160953 162913 160361 155667

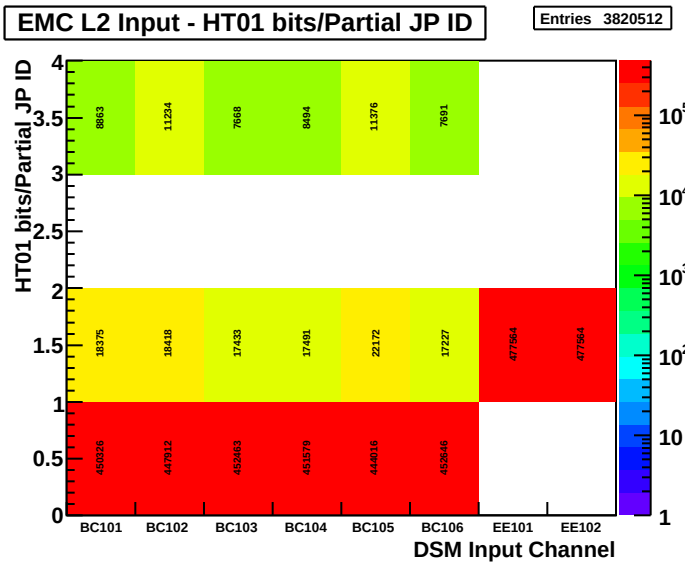

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

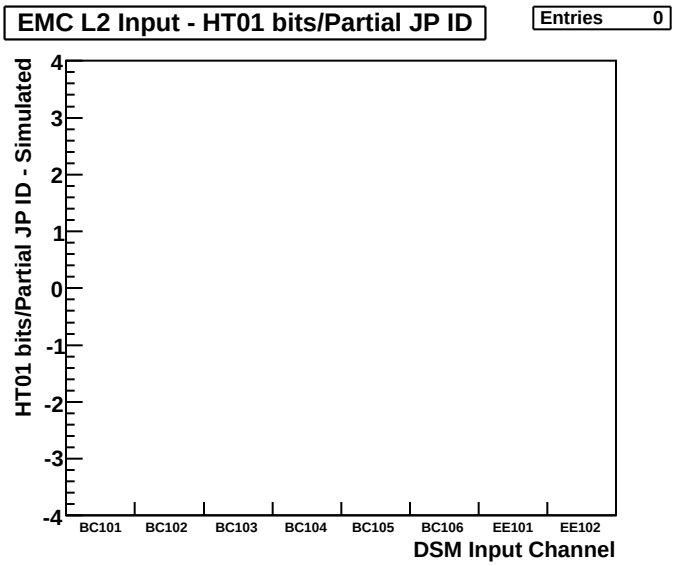

EMC L2 Input - JPZ/JPC bits
Entries 3820512
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
258071 252074 257043 262283 261049 260579 477564 477564
37859 38685 38906 40221 38454 38555
19718 20170 20423 21352 20121 20182
161916 166635 160392 153698 157940 158248

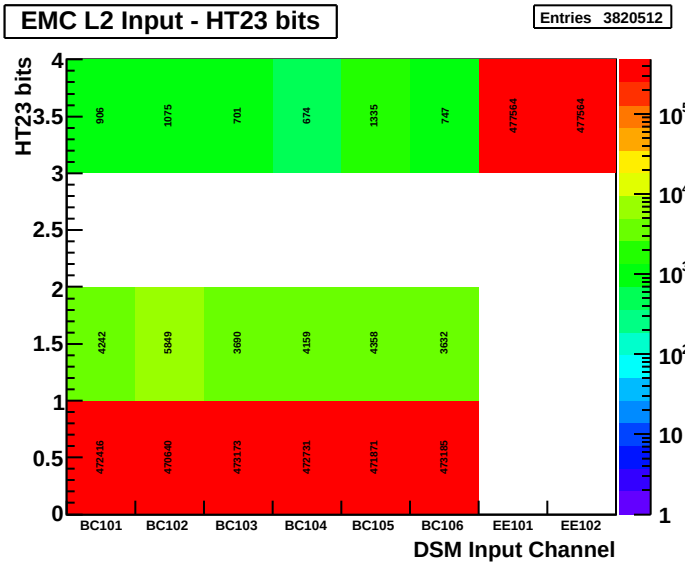

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

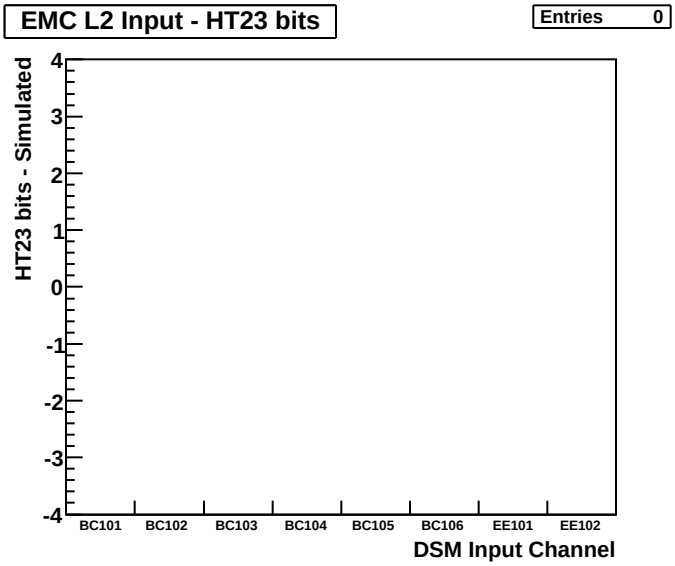

EMC L2 Input - Partial JP Sum
Entries 3820512
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

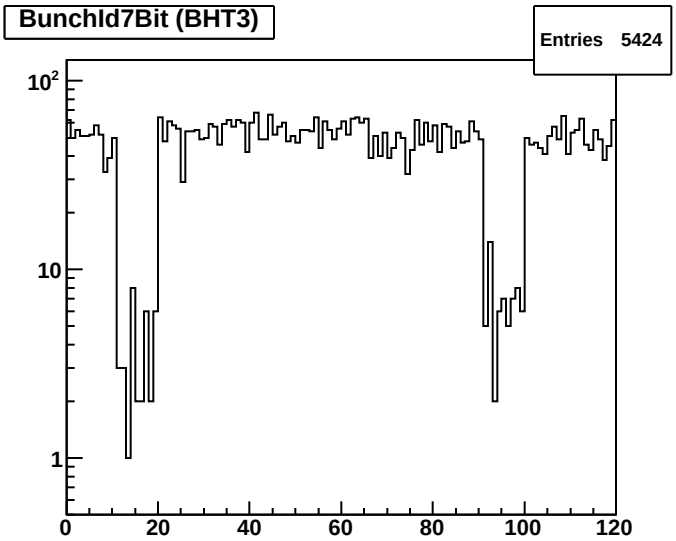

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

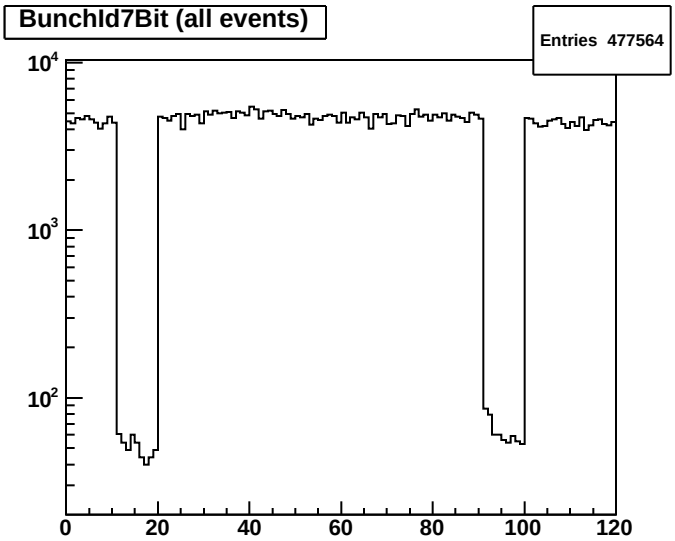

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3820512
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

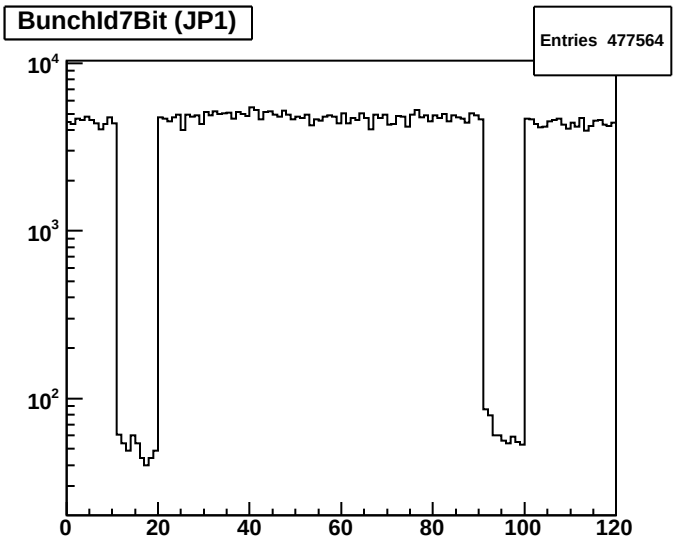

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

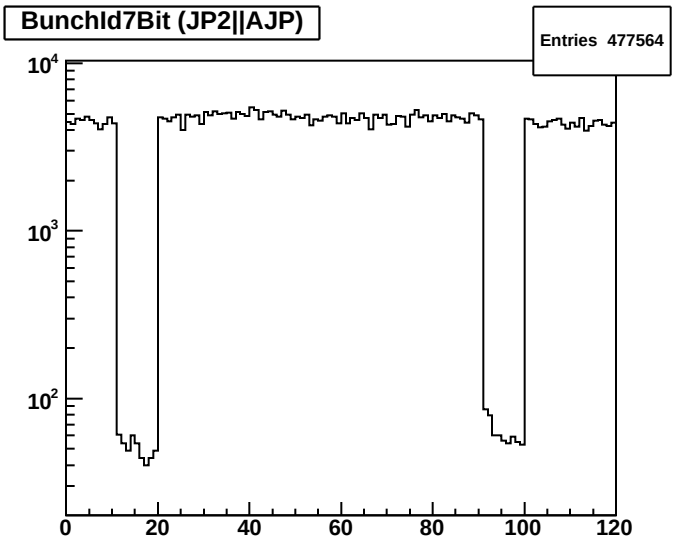

EMC L2 Input - HT23 bits
Entries 3820512
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

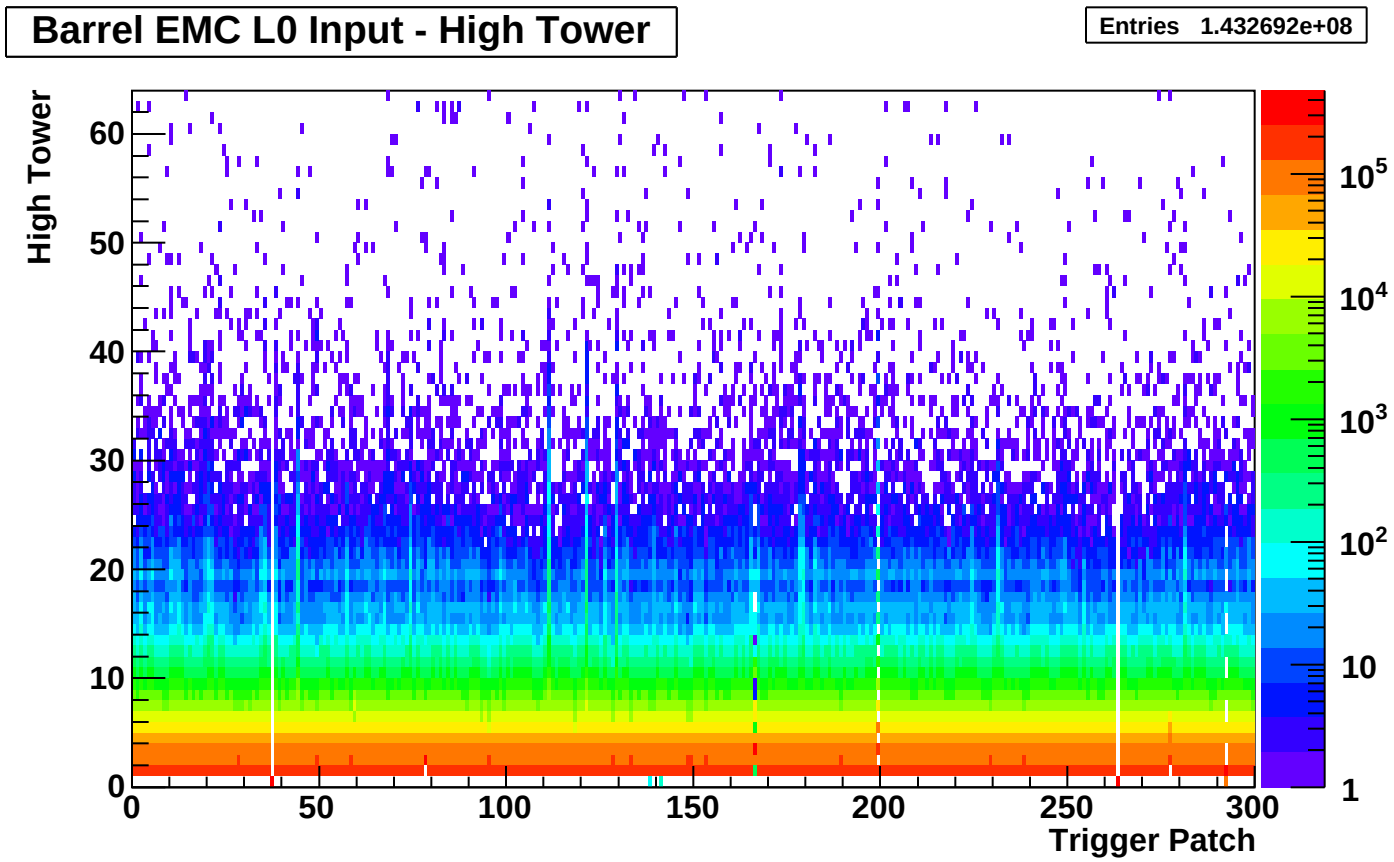

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

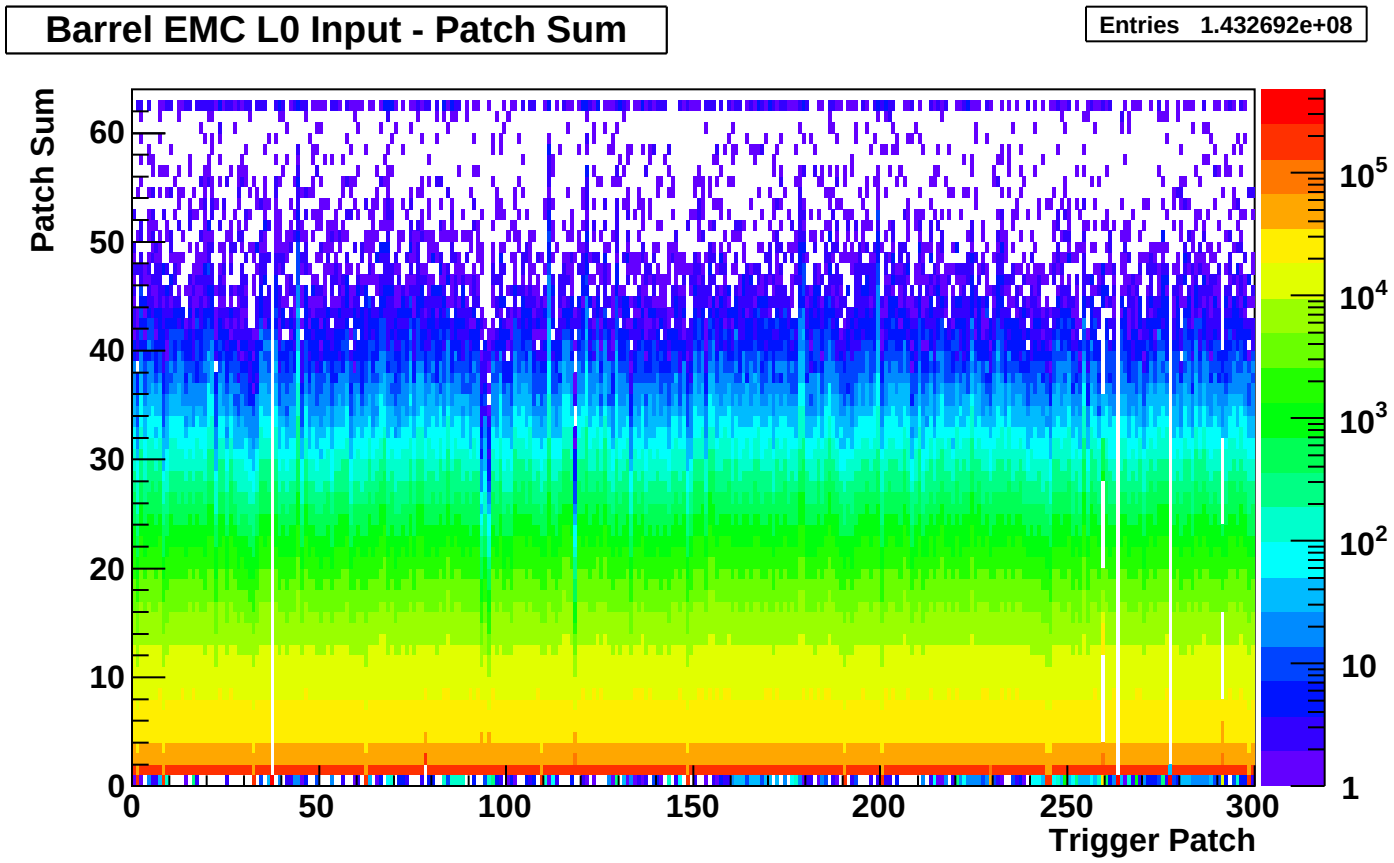

BunchId7Bit (BHT3)
Entries 5424
10^2
10
1
0
20
40
60
80
100
120

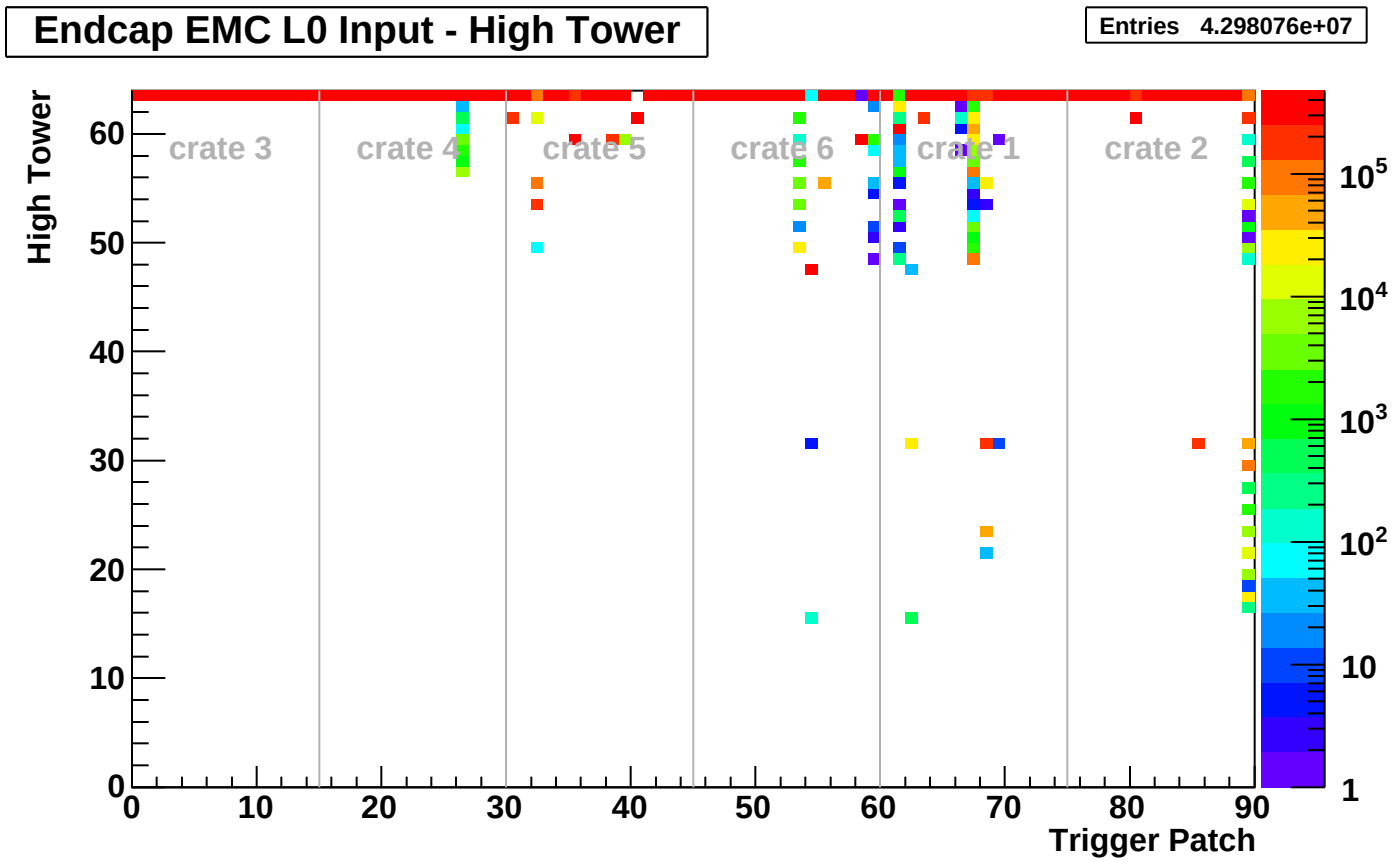

BunchId7Bit (all events)
Entries 477564
10^4
10^3
10^2
0
20
40
60
80
100
120

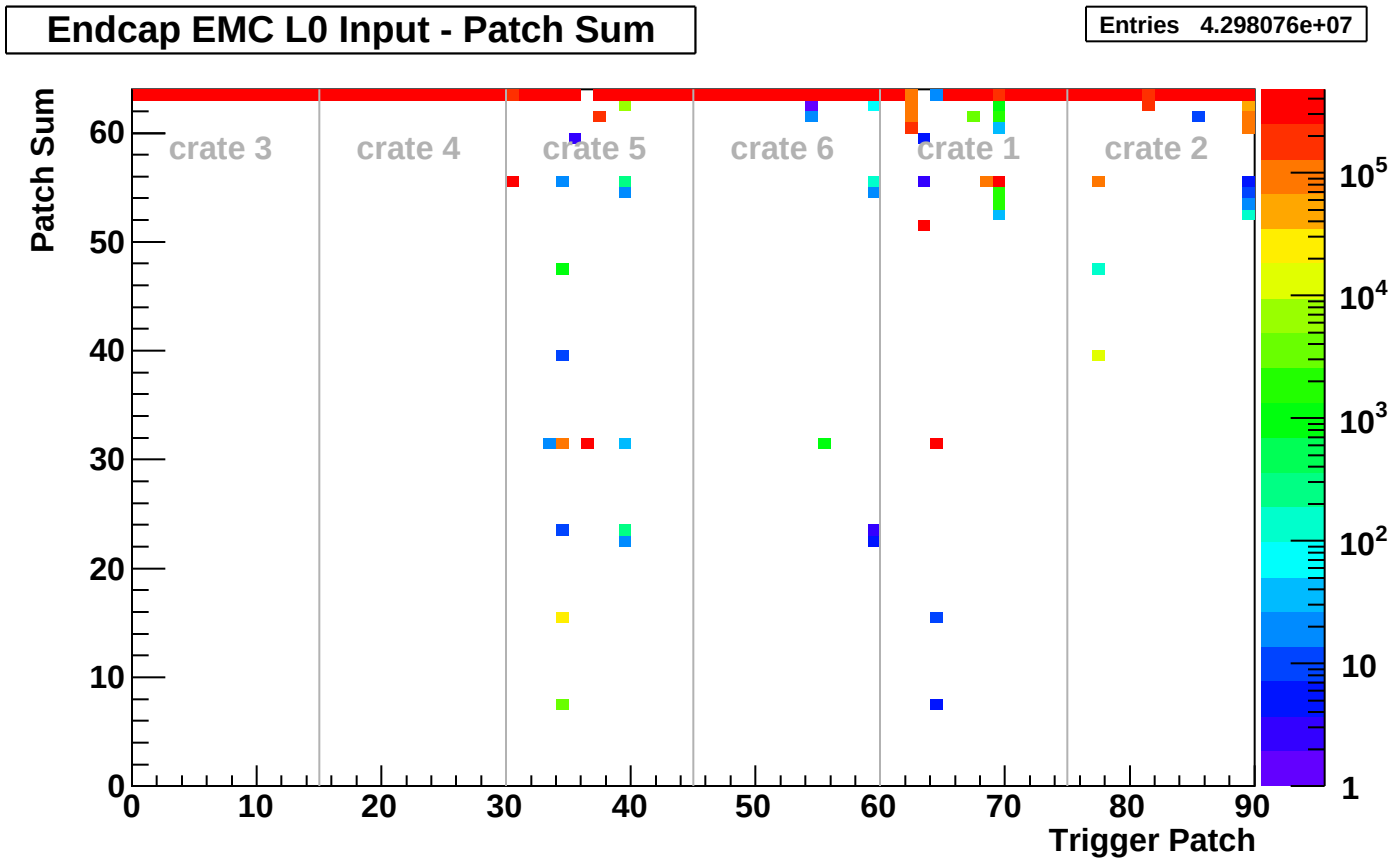

BunchId7Bit (JP1)
Entries 477564
10^4
10^3
10^2
0
20
40
60
80
100
120

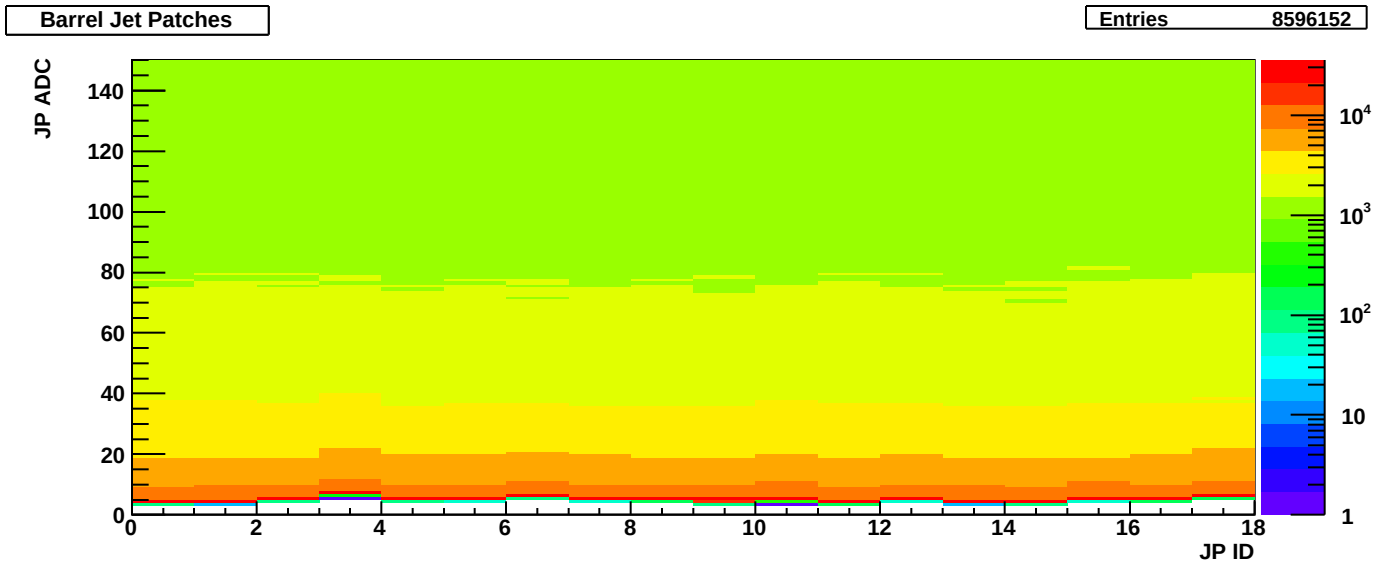

BunchId7Bit (JP2||AJP)
Entries 477564
10^4
10^3
10^2
0
20
40
60
80
100
120

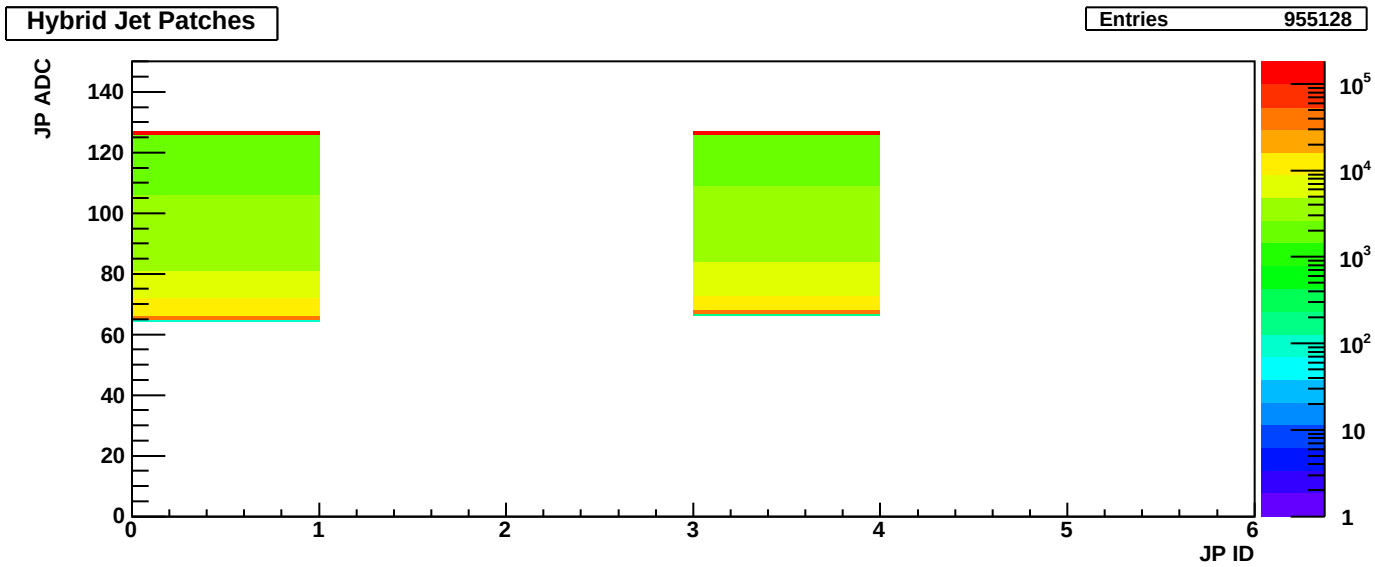

Barrel EMC L0 Input - High Tower
Entries 1.432692e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

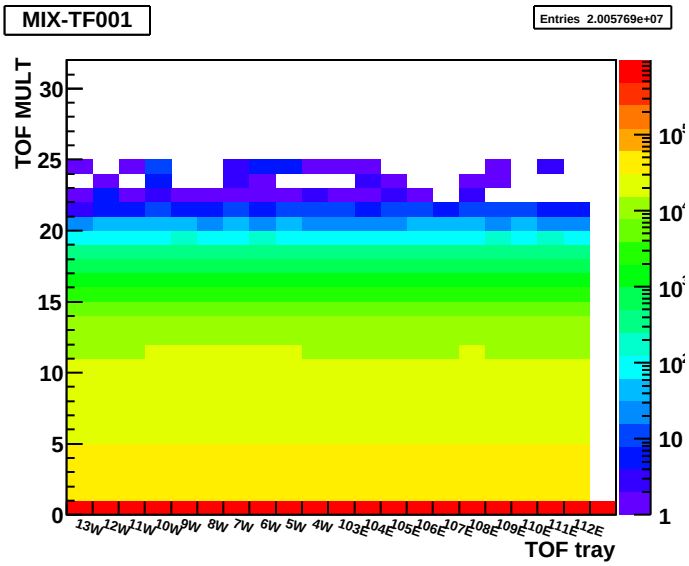

Barrel EMC L0 Input - Patch Sum
Entries 1.432692e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

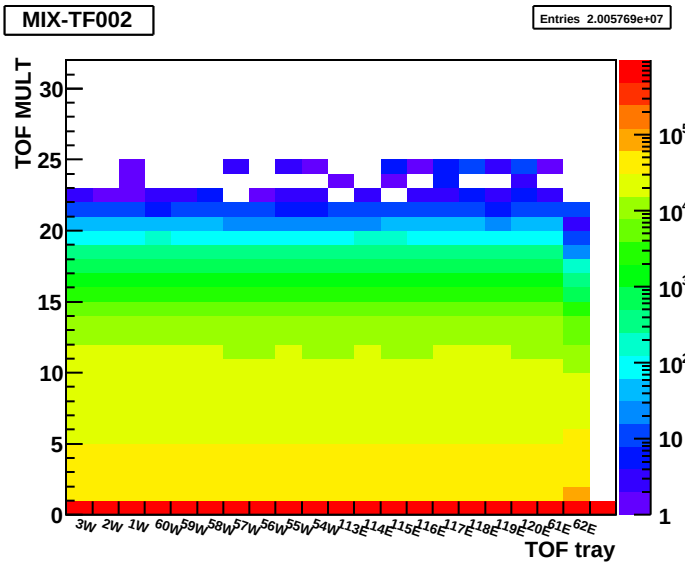

Endcap EMC L0 Input - High Tower
Entries 4.298076e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

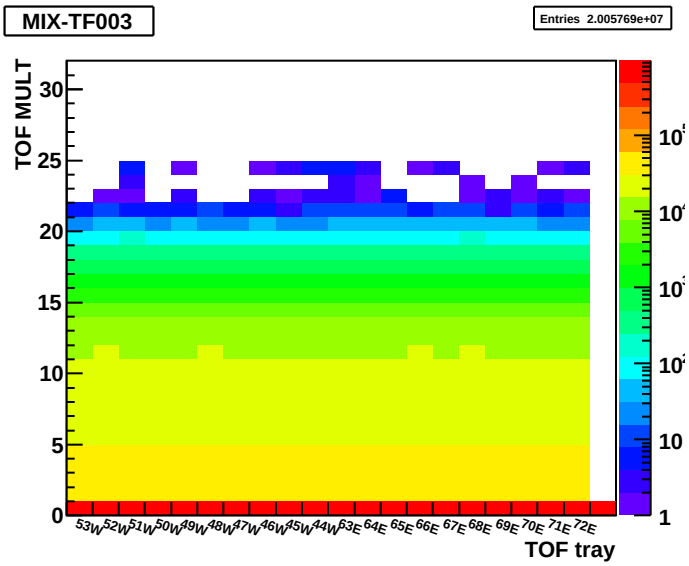

Endcap EMC L0 Input - Patch Sum
Entries 4.298076e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

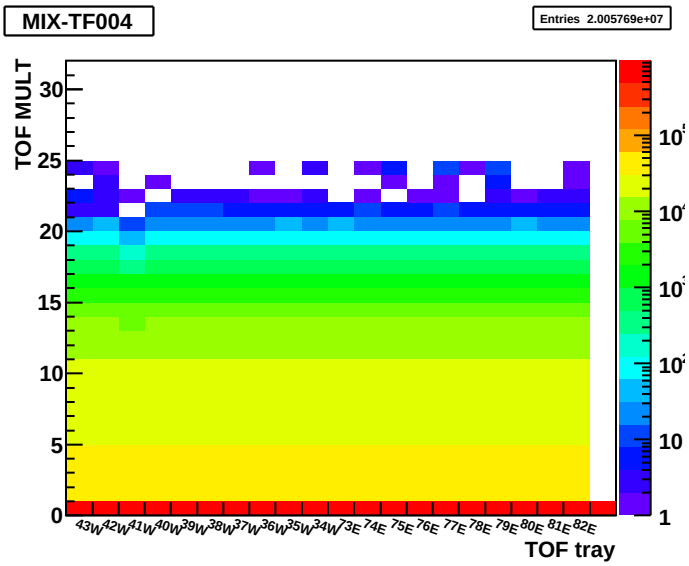

Barrel Jet Patches
Entries 8596152
JP ADC
JP ID

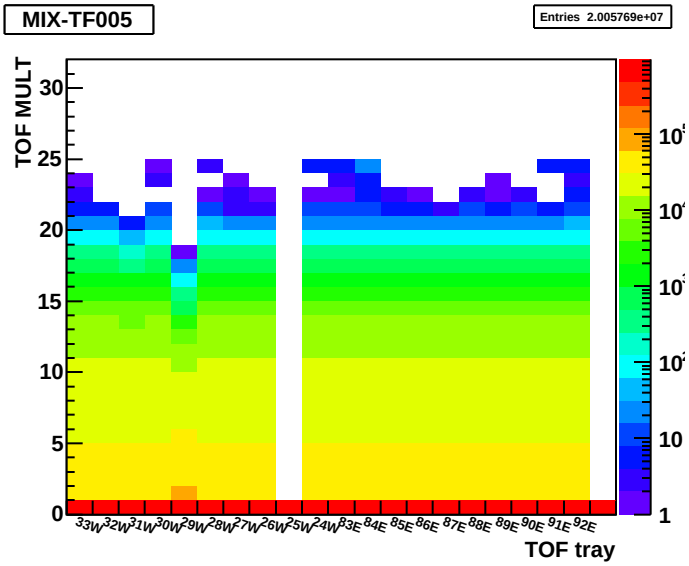

Hybrid Jet Patches
Entries 955128
JP ADC
JP ID

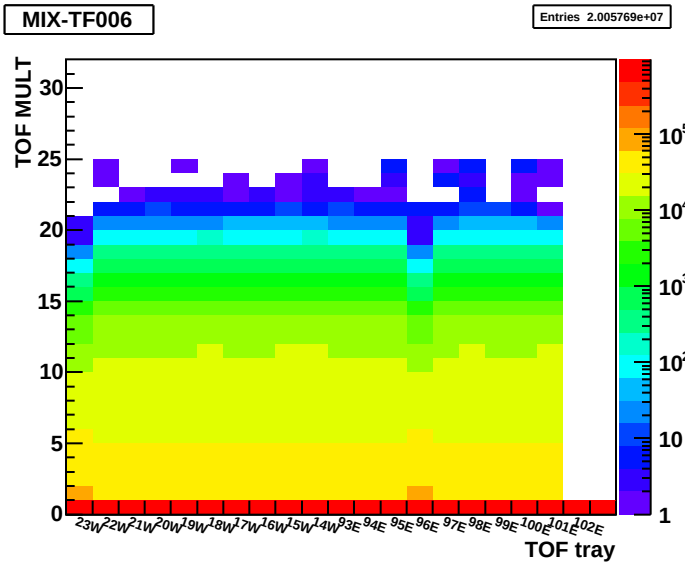

MIX-TF001
Entries 2.005769e+07
TOF MULT
TOF tray

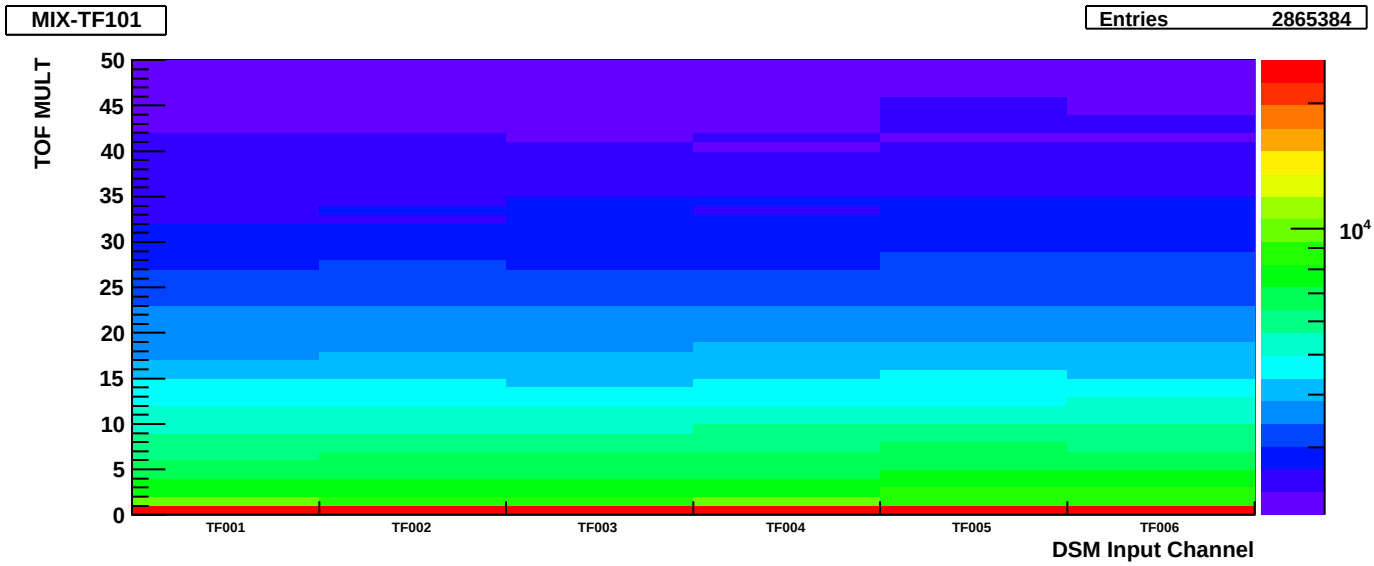

MIX-TF002
Entries 2.005769e+07
TOF MULT
TOF tray

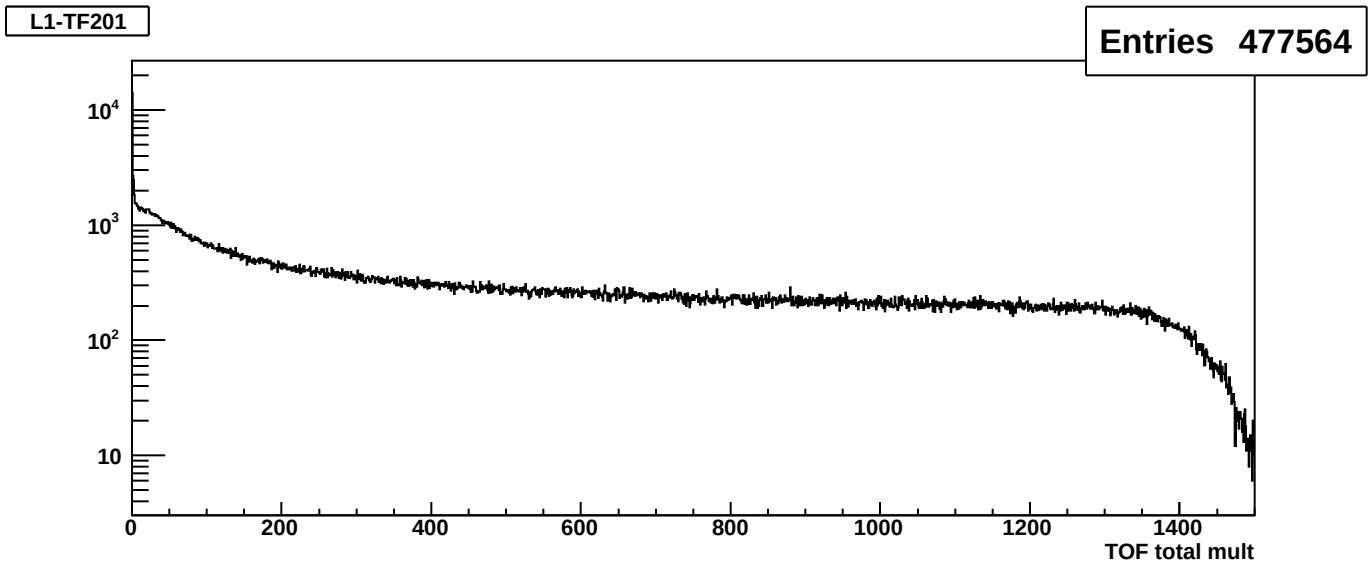

MIX-TF003
Entries 2.005769e+07
TOF MULT
TOF tray

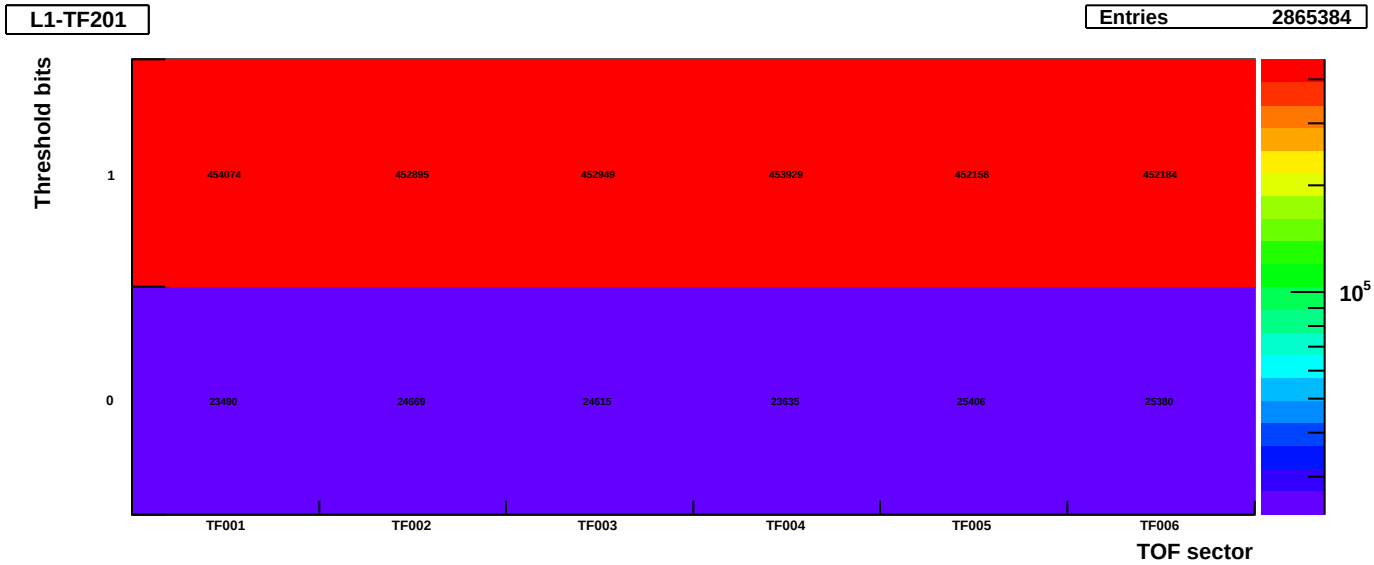

MIX-TF004
Entries 2.005769e+07
TOF MULT
TOF tray

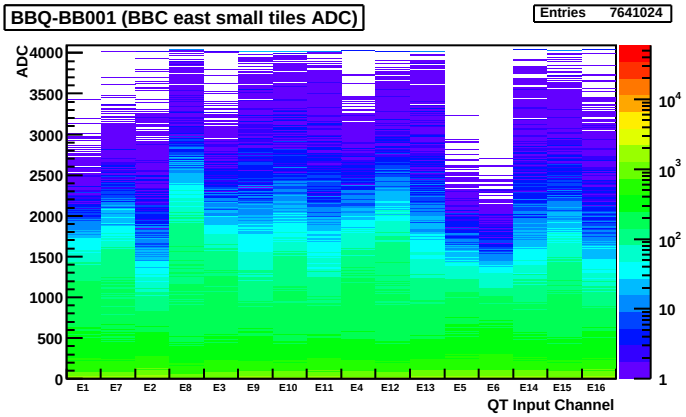

MIX-TF005
Entries 2.005769e+07
TOF MULT
TOF tray

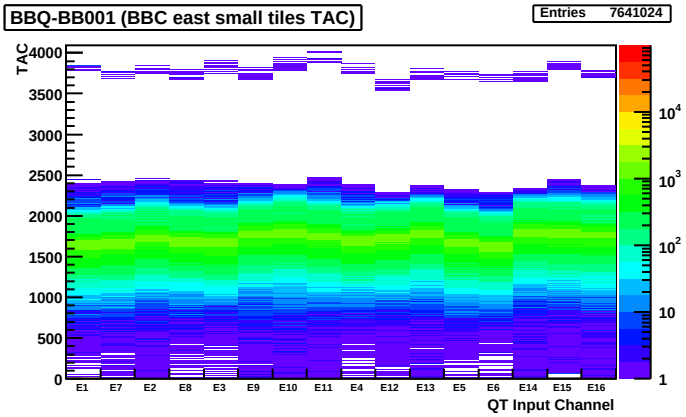

MIX-TF006
Entries 2.005769e+07
TOF MULT
TOF tray

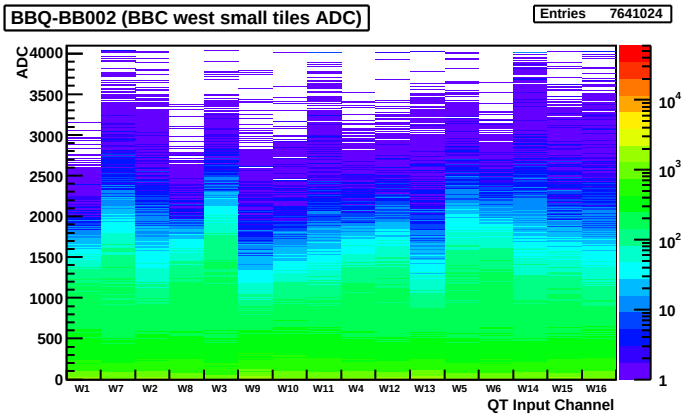

MIX-TF101
Entries 2865384
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

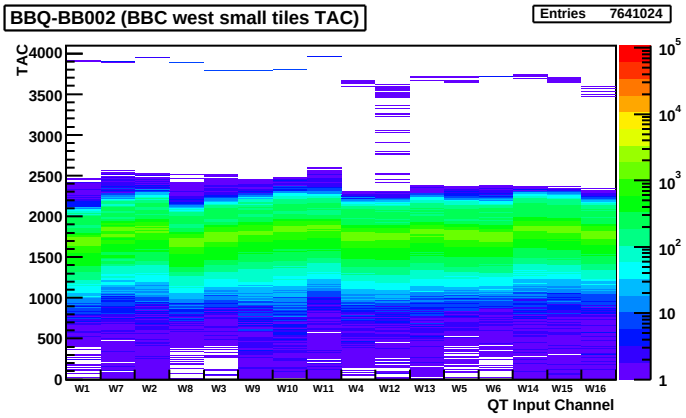

L1-TF201
Entries 477564
10^4
10^3
10^2
10
0
200
400
600
800
1000
1200
1400
TOF total mult

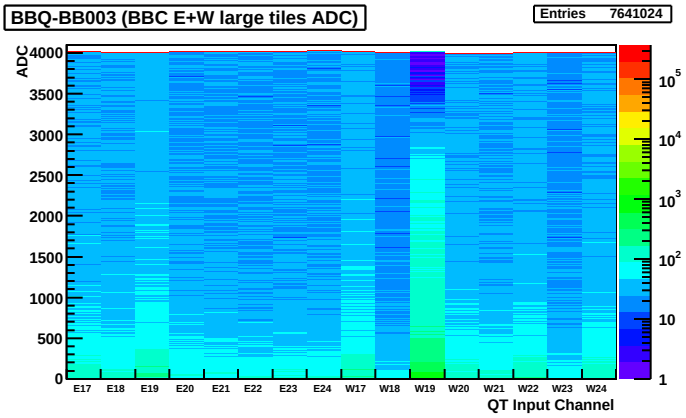

L1-TF201
Entries 2865384
Threshold bits
1
0
454074
452895
452949
453929
452158
452184
23490
24669
24615
23635
25406
25380
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

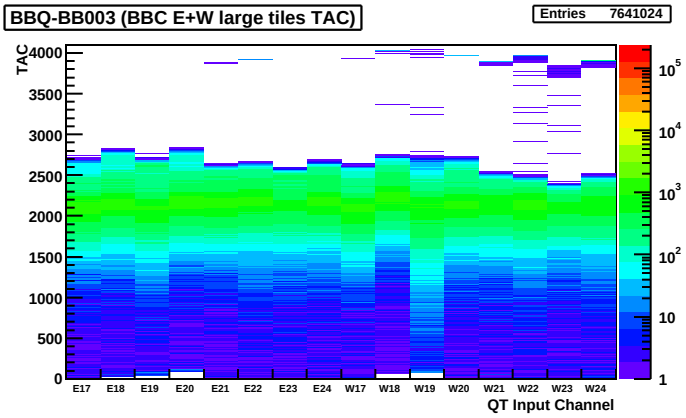

BBQ-BB001 (BBC east small tiles ADC)
Entries 7641024
ADC
QT Input Channel

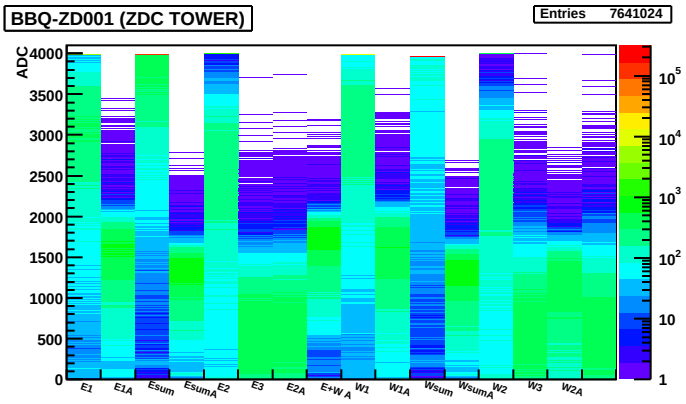

BBQ-BB001 (BBC east small tiles TAC)
Entries 7641024
TAC
QT Input Channel

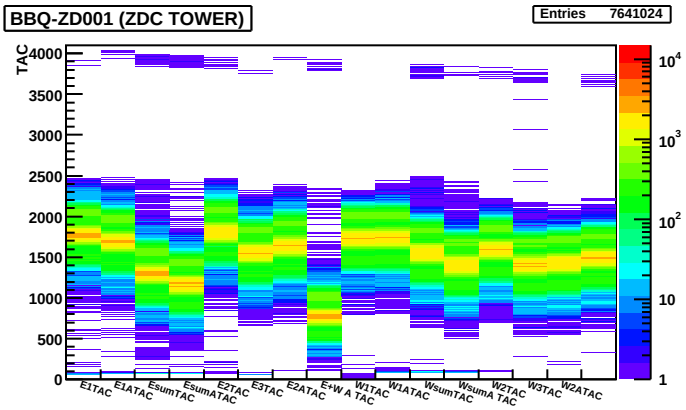

BBQ-BB002 (BBC west small tiles ADC)
Entries 7641024
ADC
QT Input Channel

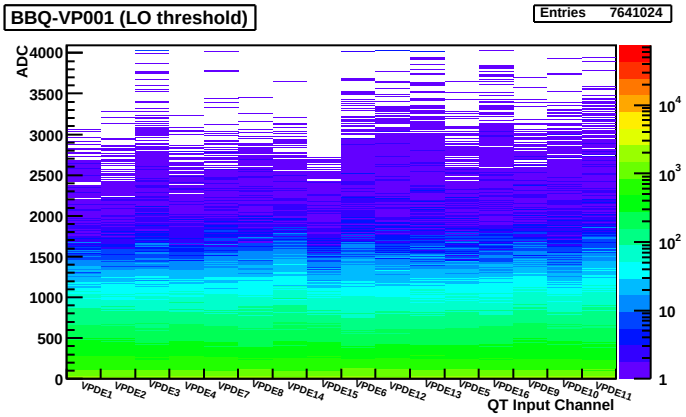

BBQ-BB002 (BBC west small tiles TAC)
Entries 7641024
TAC
QT Input Channel

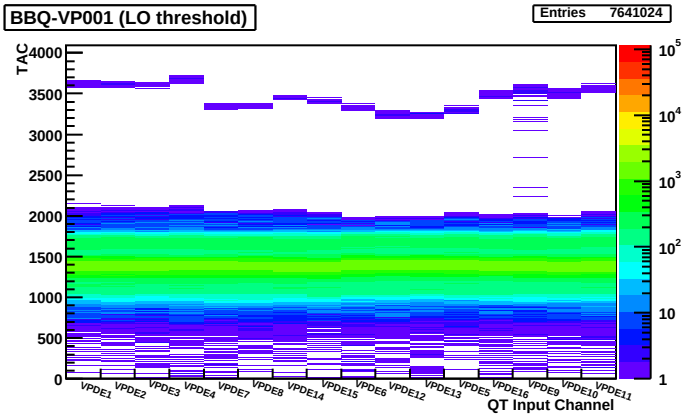

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 7641024
ADC
QT Input Channel

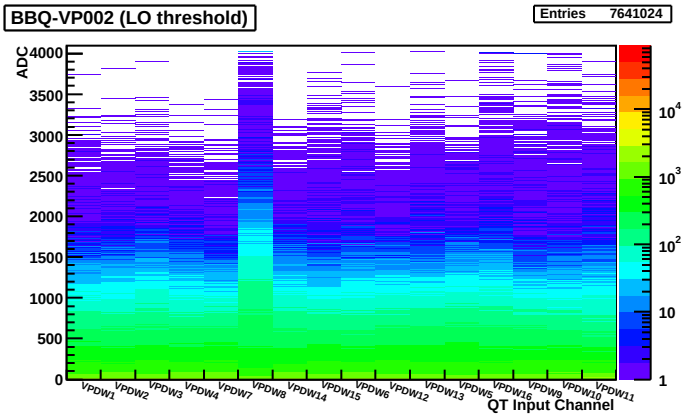

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 7641024
TAC
QT Input Channel

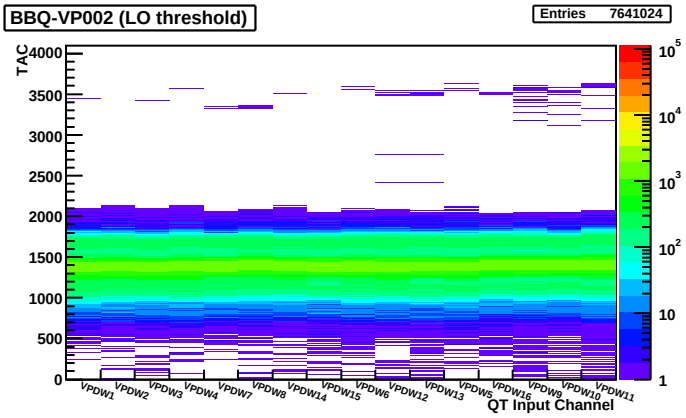

BBQ-ZD001 (ZDC TOWER)
Entries 7641024
ADC

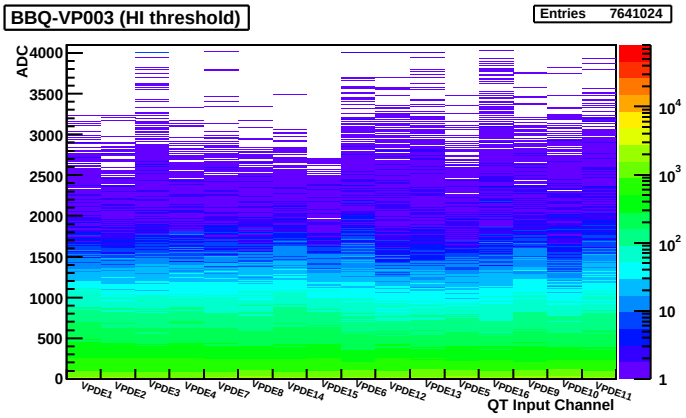

BBQ-ZD001 (ZDC TOWER)
Entries 7641024
TAC

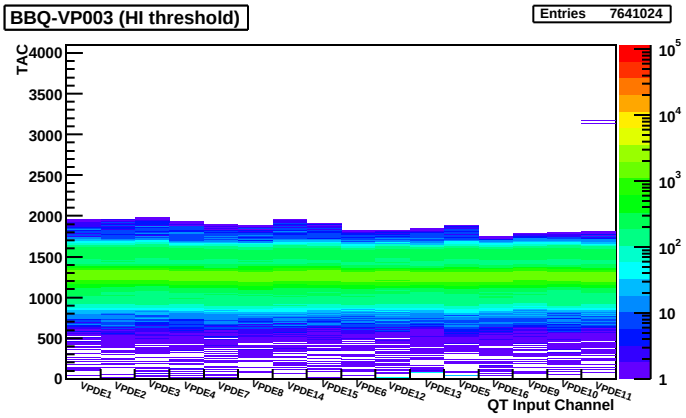

BBQ-VP001 (LO threshold)
Entries 7641024
ADC
QT Input Channel

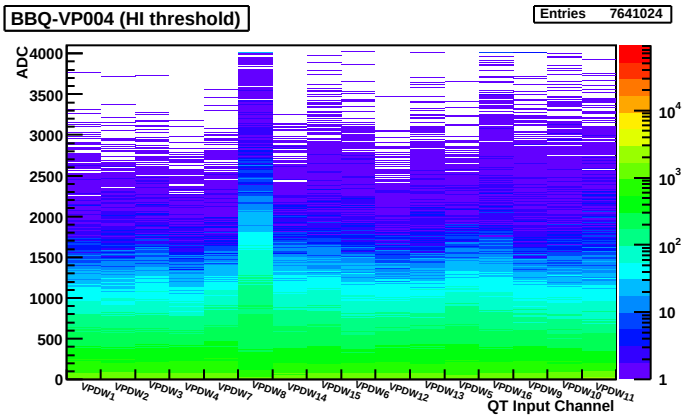

BBQ-VP001 (LO threshold)
Entries 7641024
TAC
QT Input Channel

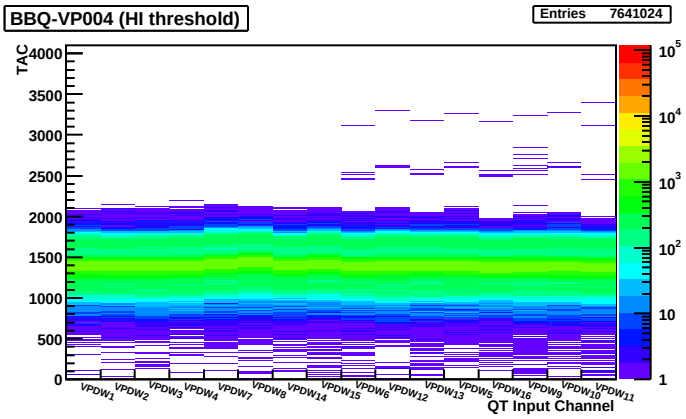

BBQ-VP002 (LO threshold)
Entries 7641024
ADC
QT Input Channel

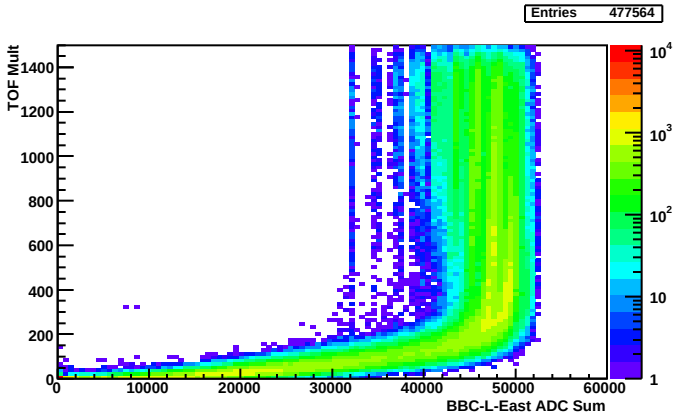

BBQ-VP002 (LO threshold)
Entries 7641024
TAC
QT Input Channel

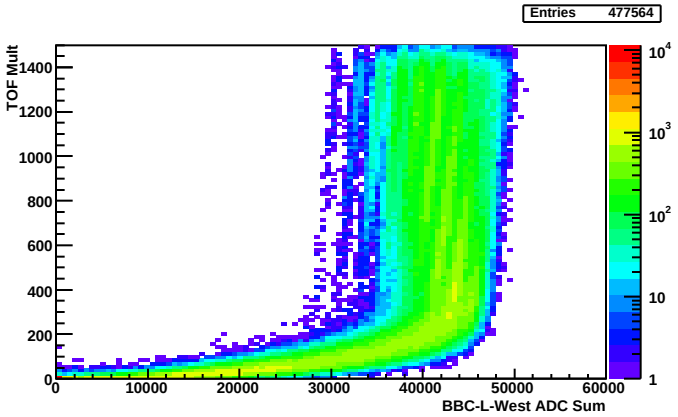

BBQ-VP003 (HI threshold)
Entries 7641024
ADC
QT Input Channel

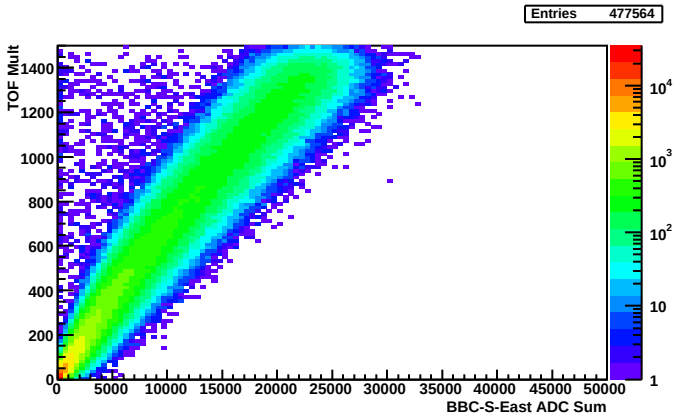

BBQ-VP003 (HI threshold)
Entries 7641024
TAC
QT Input Channel

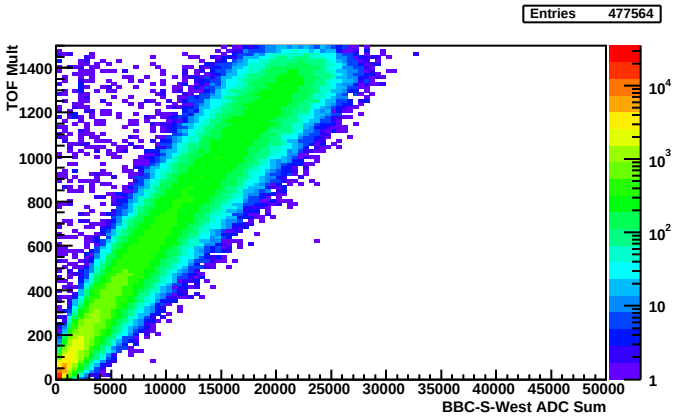

BBQ-VP004 (HI threshold)
Entries 7641024
ADC
QT Input Channel

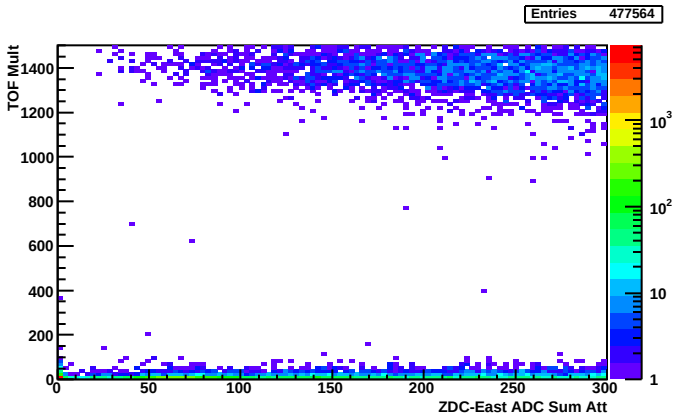

BBQ-VP004 (HI threshold)
Entries 7641024
TAC
QT Input Channel

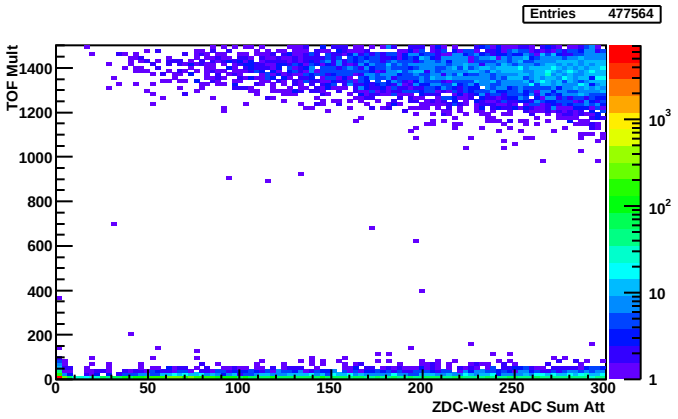

Entries 477564
TOF Mult
BBC-L-East ADC Sum

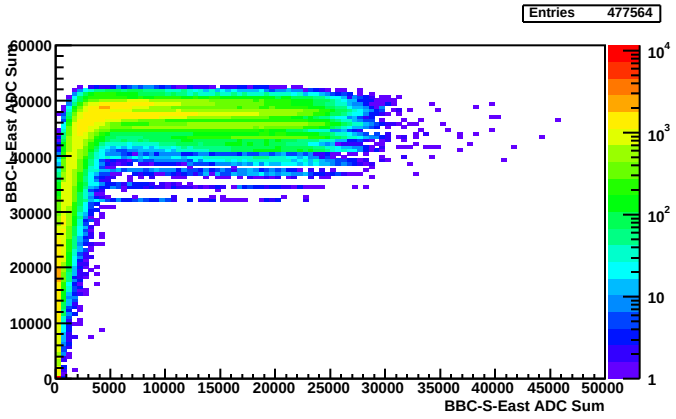

Entries 477564
TOF Mult
BBC-L-West ADC Sum

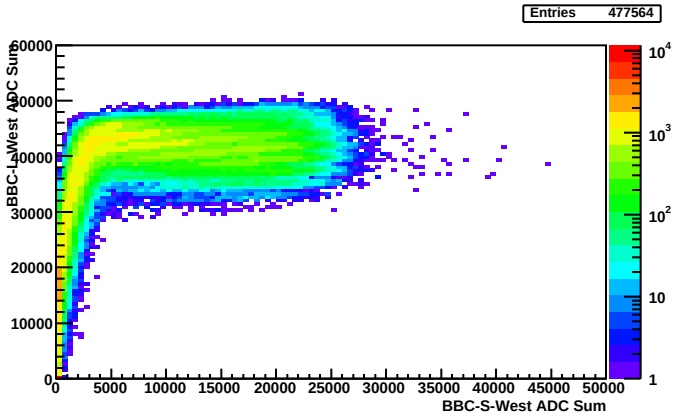

Entries 477564
TOF Mult
BBC-S-East ADC Sum

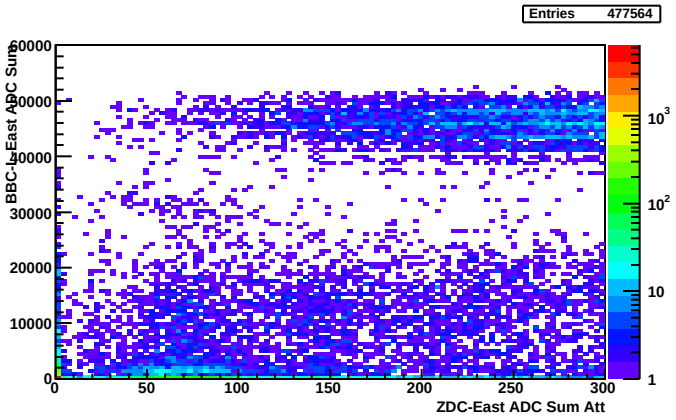

Entries 477564
TOF Mult
BBC-S-West ADC Sum

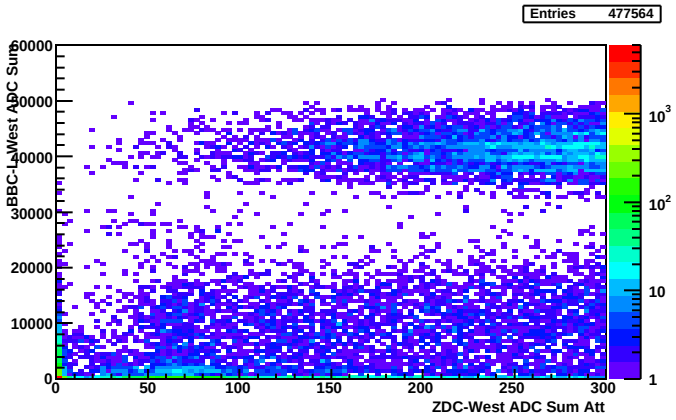

Entries 477564
TOF Mult
ZDC-East ADC Sum Att


Entries 477564
TOF Mult
ZDC-West ADC Sum Att


Entries 477564
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 477564
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 477564
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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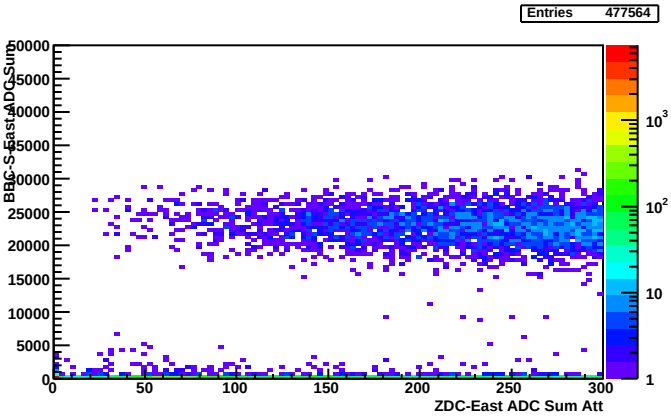
BBC-L-West ADC Sum
ZDC-West ADC Sum Att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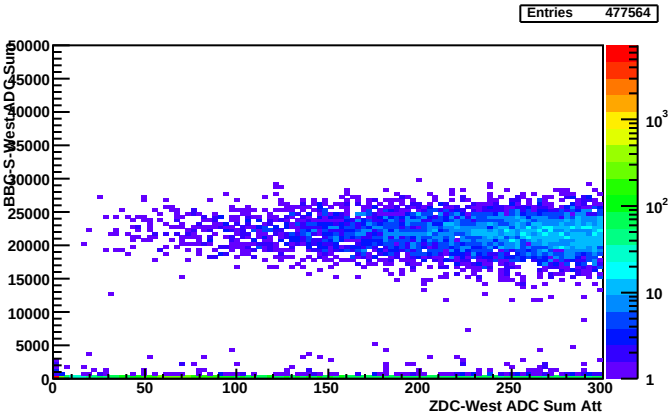
BBC-S-East ADC Sum
ZDC-East ADC Sum Att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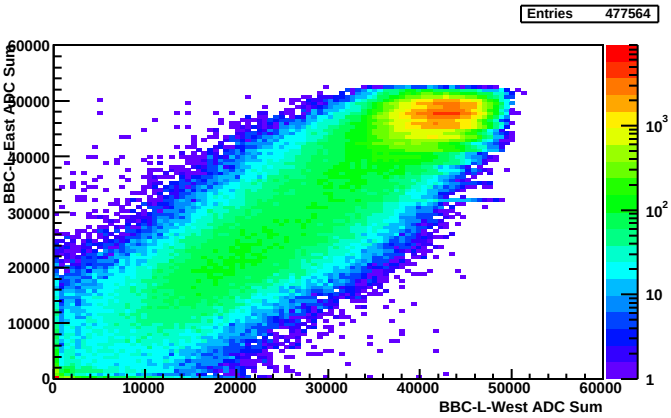
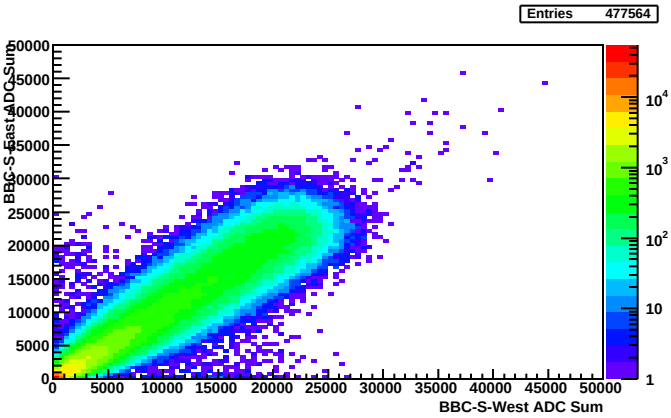
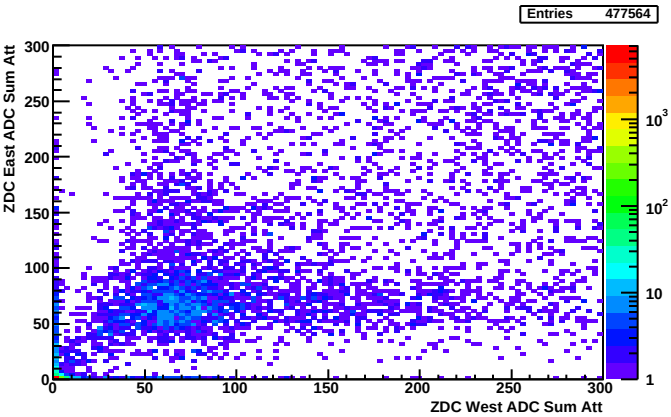
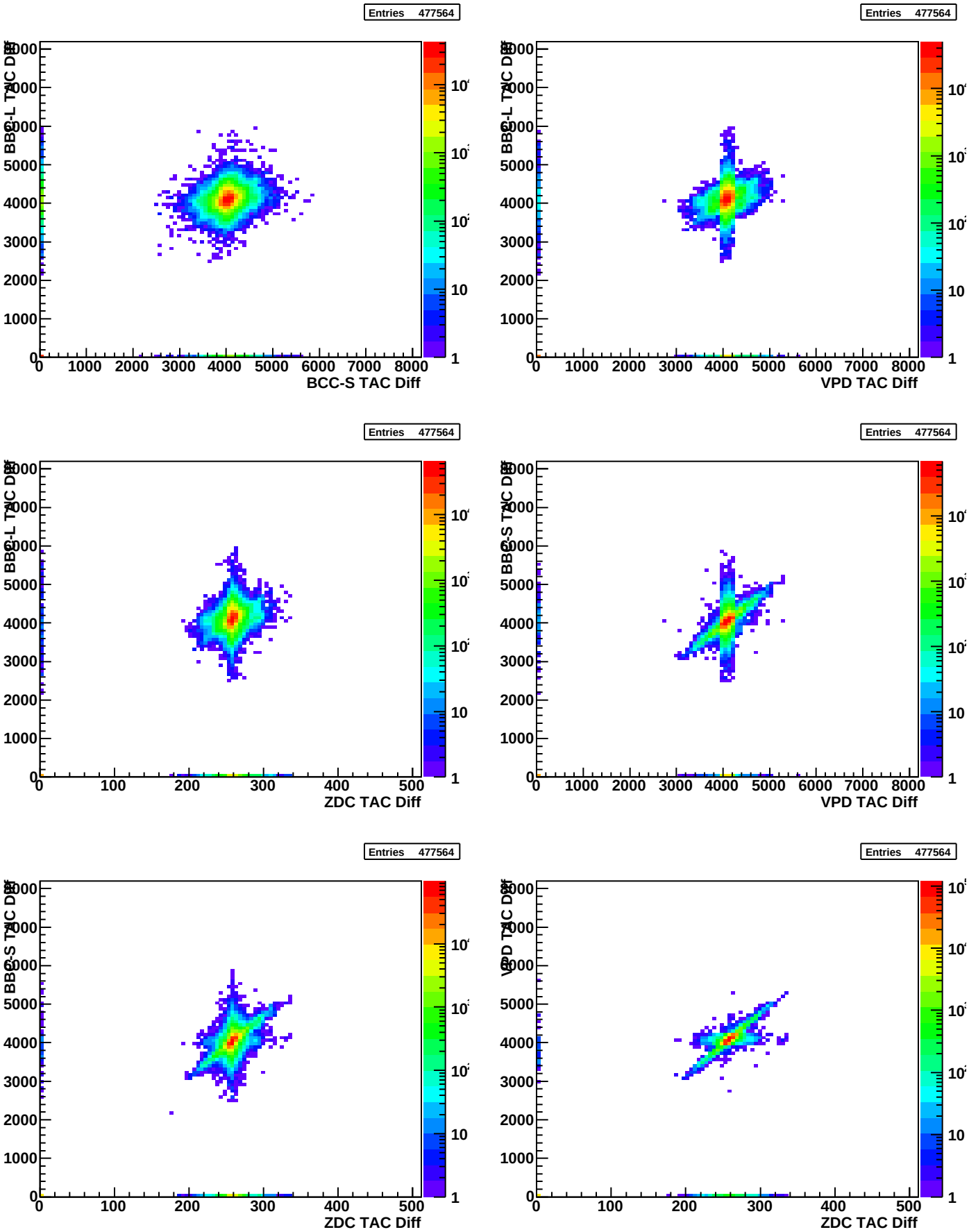
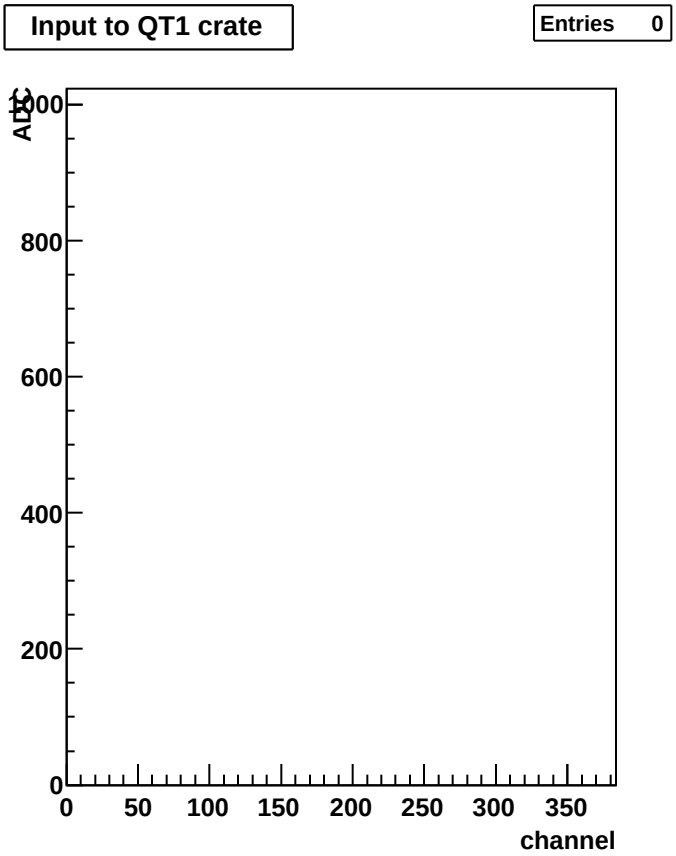
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

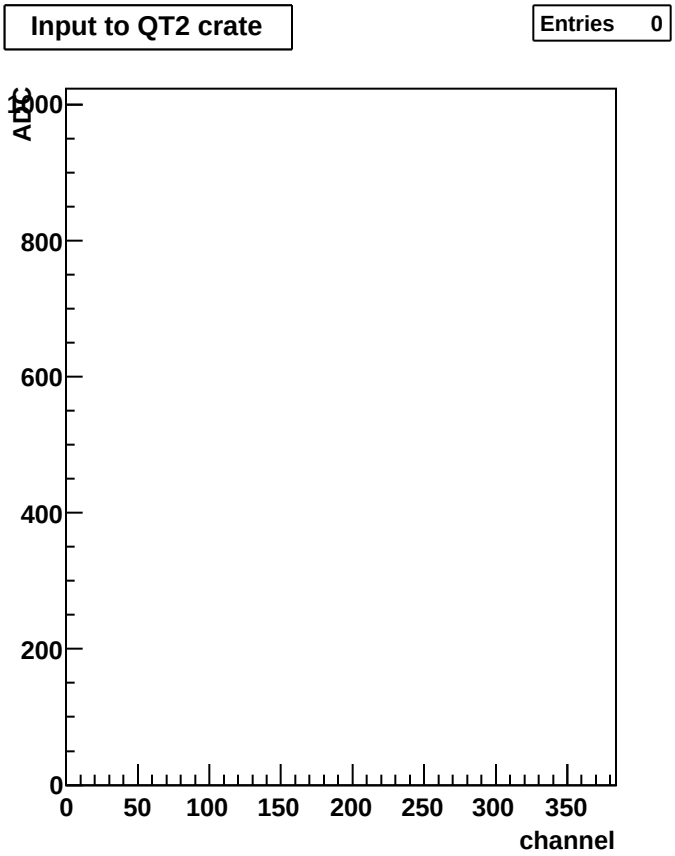

Entries 477564
BBC-L-East ADC Sum
BBC-L-West ADC Sum

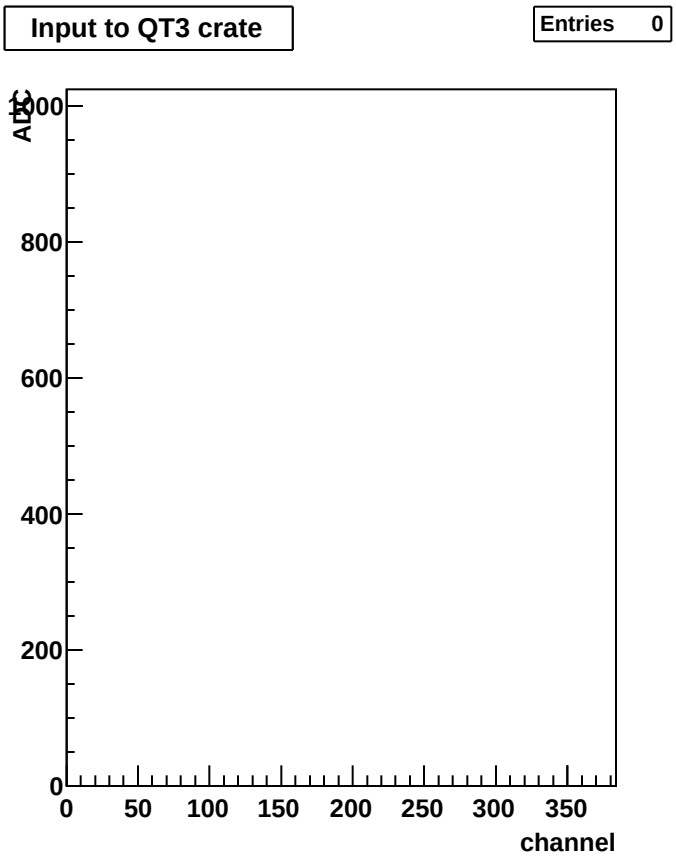

Entries 477564
BBC-S-East ADC Sum
BBC-S-West ADC Sum

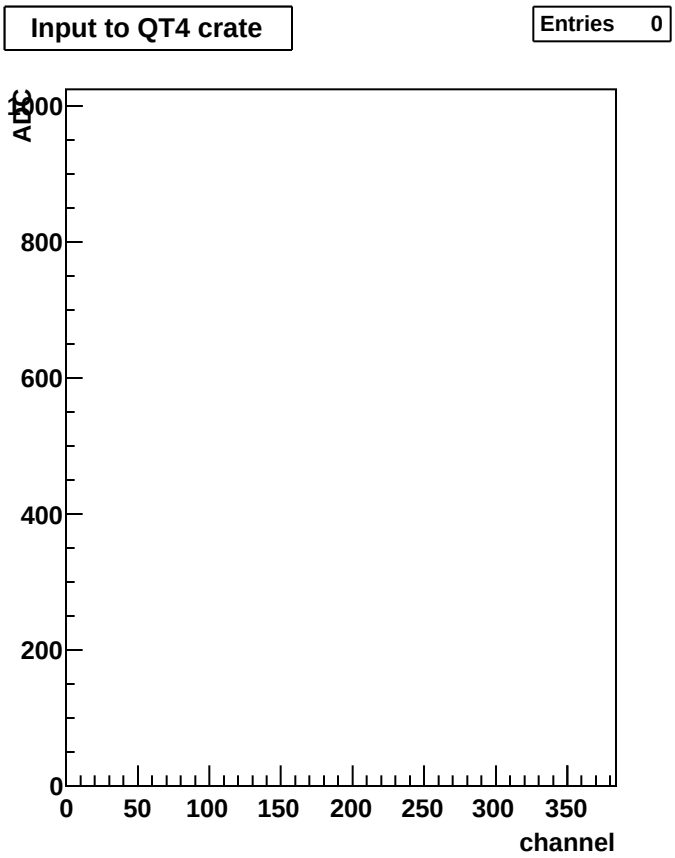

Entries 477564
ZDC East ADC Sum Att
ZDC West ADC Sum Att

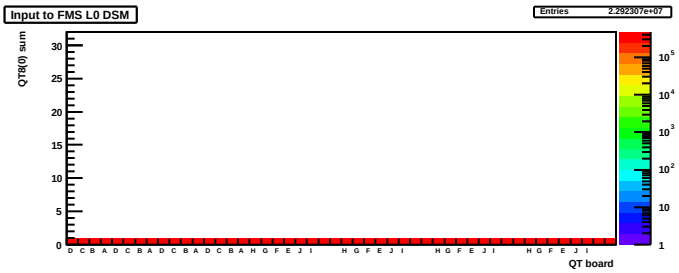

Entries 477564
BBC-L TAC Diff
BCC-S TAC Diff
Entries 477564
BBC-L TAC Diff
VPD TAC Diff
Entries 477564
BBC-L TAC Diff
ZDC TAC Diff
Entries 477564
BBC-S TAC Diff
VPD TAC Diff
Entries 477564
BBC-S TAC Diff
ZDC TAC Diff
Entries 477564
VPD TAC Diff
ZDC TAC Diff

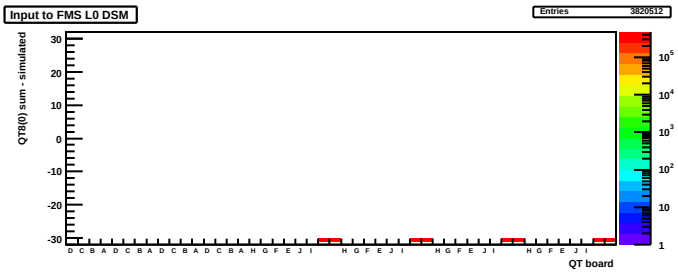

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

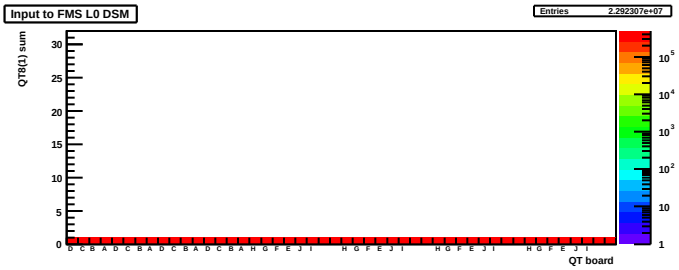

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

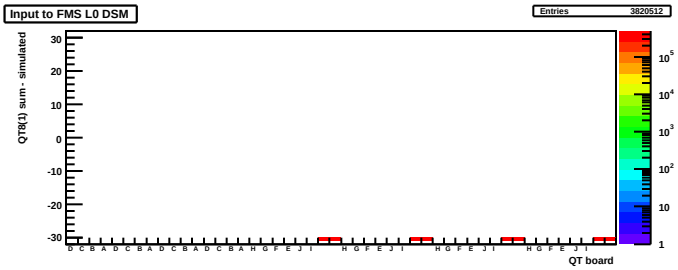

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

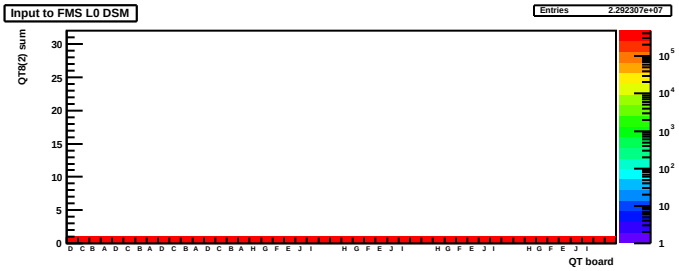

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

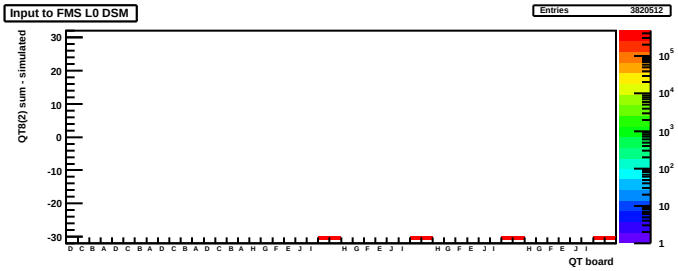

Input to FMS L0 DSM
Entries 2.292307e+07
QT8(0) sum
QT board

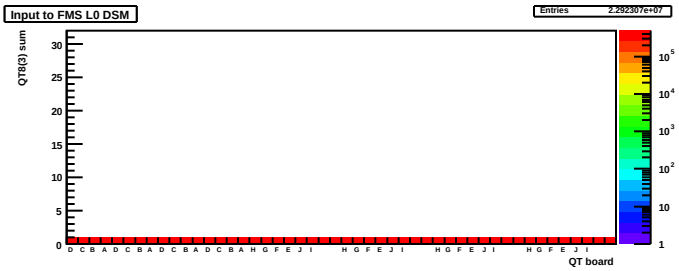

Input to FMS L0 DSM
Entries 3820512
QT8(0) sum - simulated
QT board

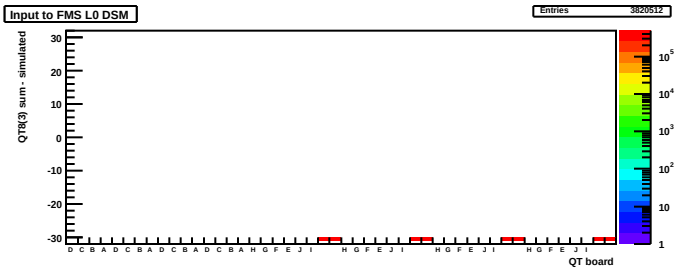

Input to FMS L0 DSM
Entries 2.292307e+07
QT8(1) sum
QT board

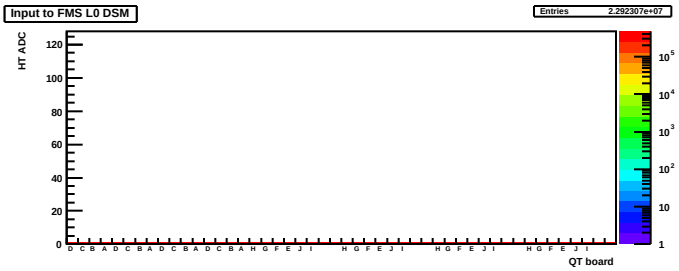

Input to FMS L0 DSM
Entries 3820512
QT8(1) sum - simulated
QT board

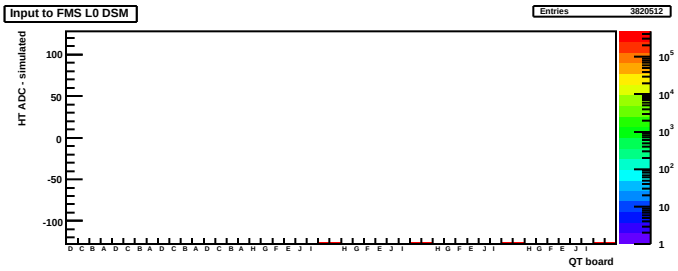

Input to FMS L0 DSM
Entries 2.292307e+07
QT8(2) sum
QT board

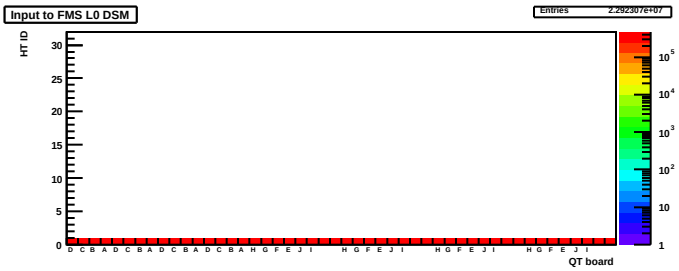

Input to FMS L0 DSM
Entries 3820512
QT8(2) sum - simulated
QT board

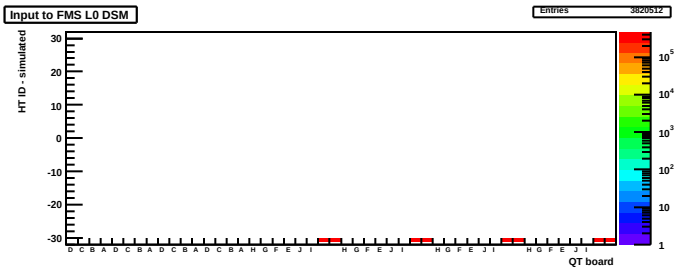

Input to FMS L0 DSM
Entries 2.292307e+07
QT8(3) sum
QT board

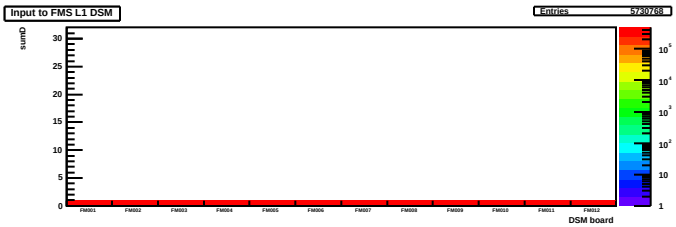

Input to FMS L0 DSM
Entries 3820512
QT8(3) sum - simulated
QT board

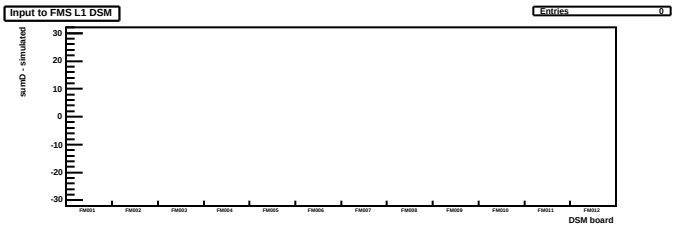

Input to FMS L0 DSM
Entries 2.292307e+07
HT ADC
QT board

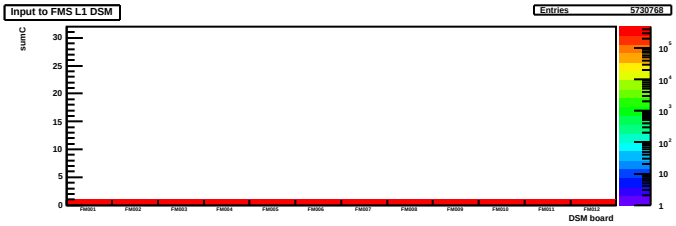

Input to FMS L0 DSM
Entries 3820512
HT ADC - simulated
QT board

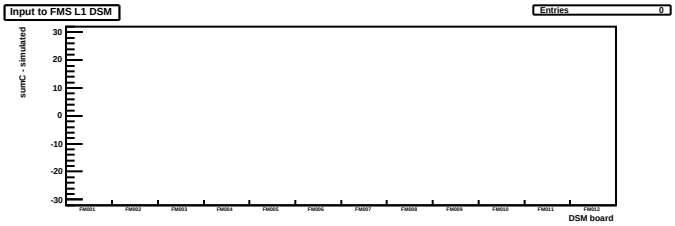

Input to FMS L0 DSM
Entries 2.292307e+07
HT ID
QT board

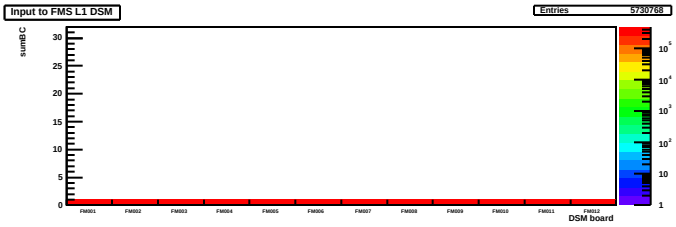

Input to FMS L0 DSM
Entries 3820512
HT ID - simulated
QT board

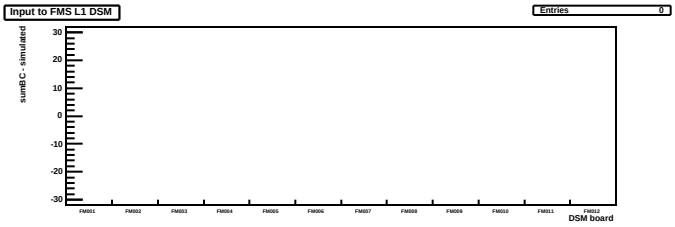

Input to FMS L1 DSM
Entries 5730768
sumD
DSM board

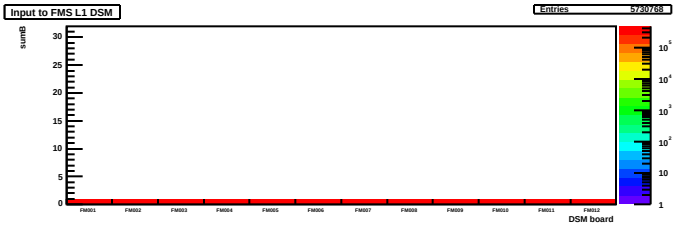

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

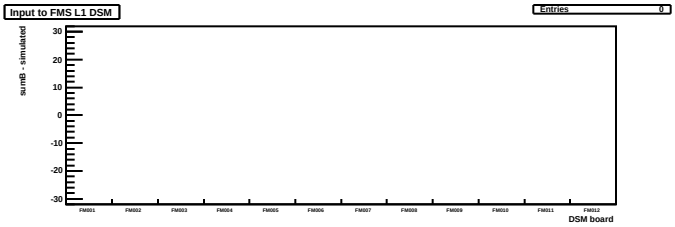

Input to FMS L1 DSM
Entries 5730768
sumC
DSM board

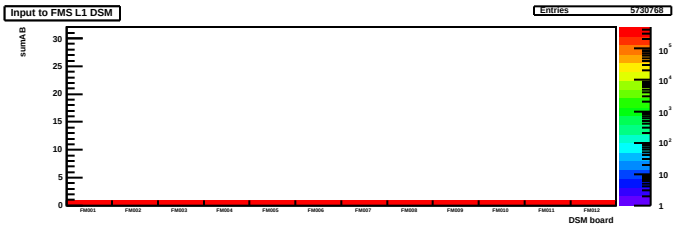

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

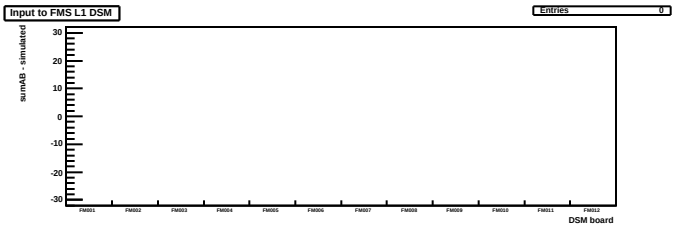

Input to FMS L1 DSM
Entries 5730768
sumBC
DSM board

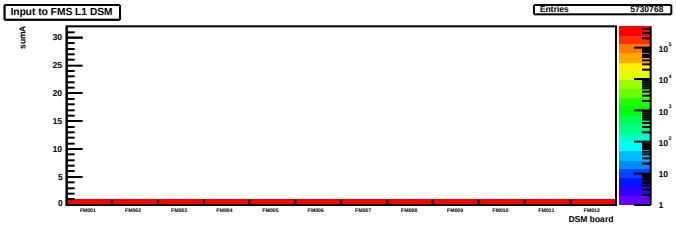

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

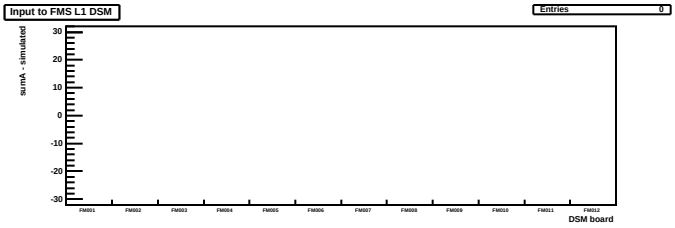

Input to FMS L1 DSM
Entries 5730768
sumB
DSM board

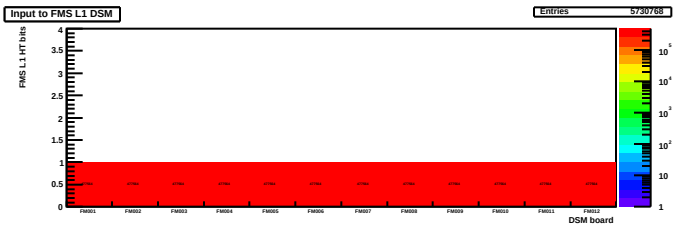

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

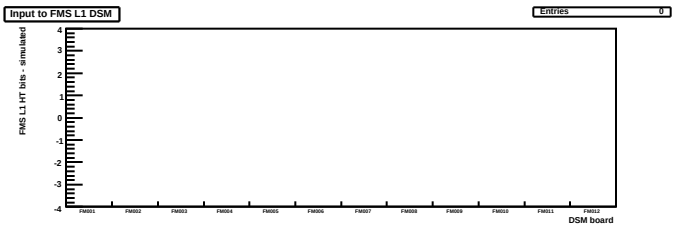

Input to FMS L1 DSM
Entries 5730768
sumAB
DSM board

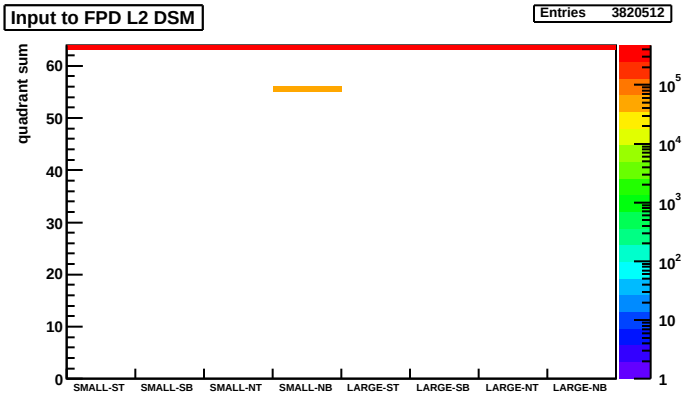

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

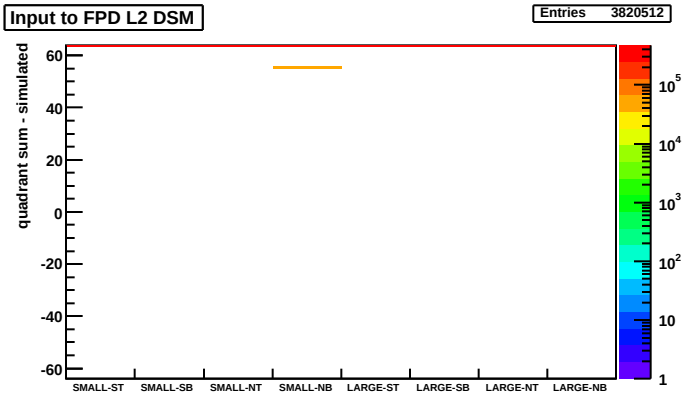

Input to FMS L1 DSM
Entries 5730768
sumA
DSM board

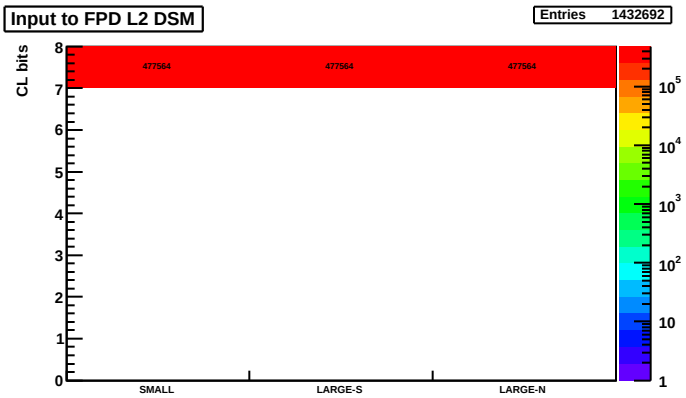

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

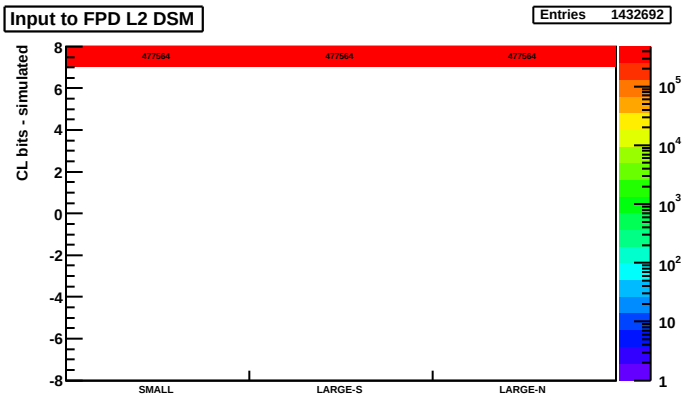

Input to FMS L1 DSM
Entries 5730768
FMS L1 HT bits
DSM board

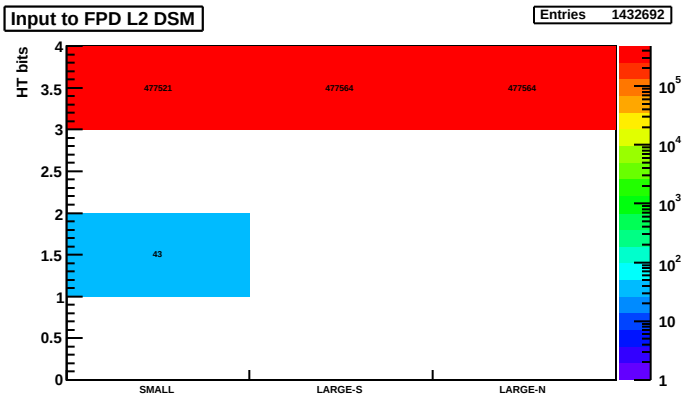

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

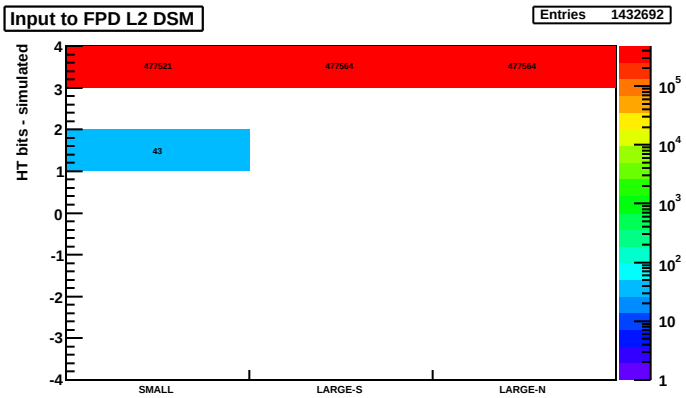

Input to FPD L2 DSM
Entries 3820512
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

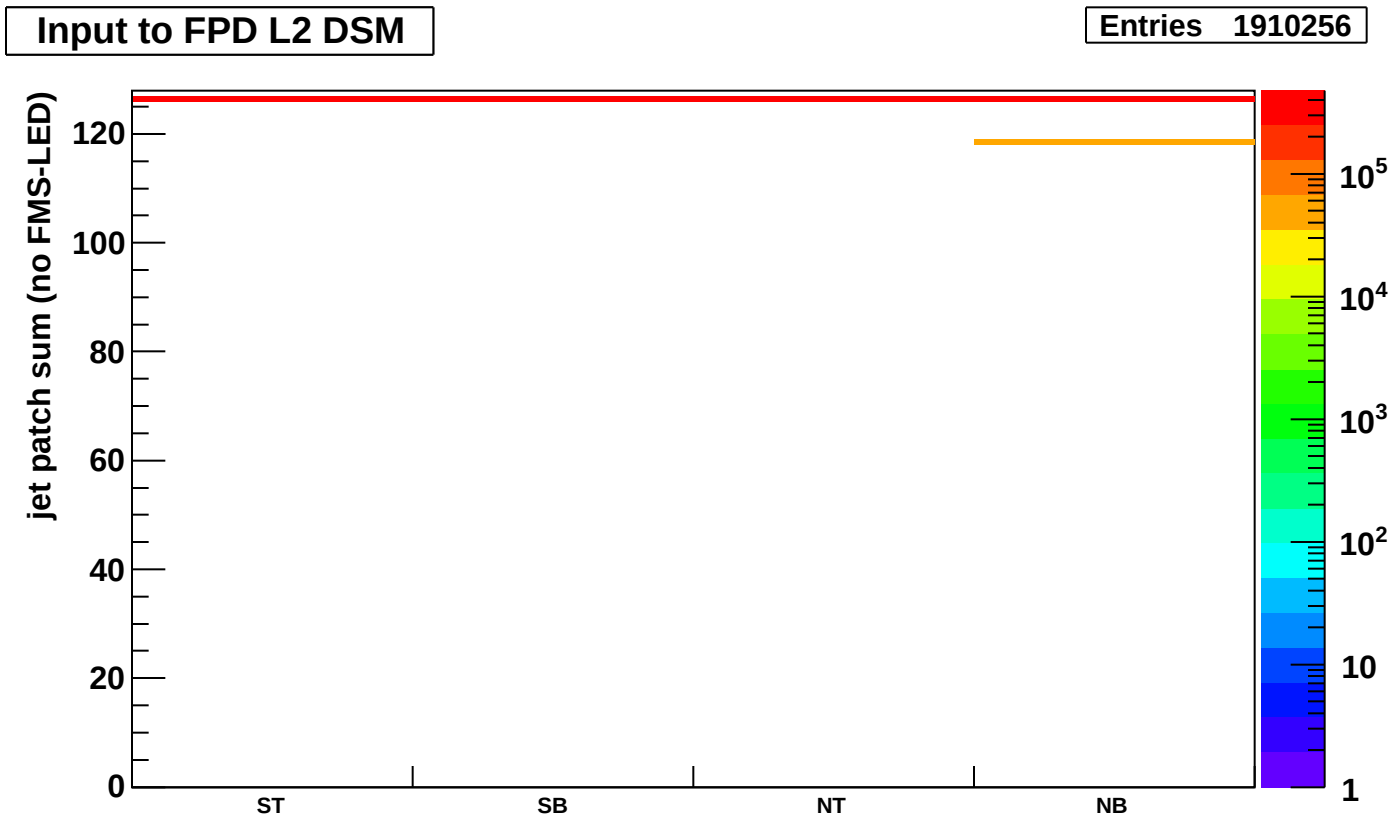

Input to FPD L2 DSM
Entries 3820512
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

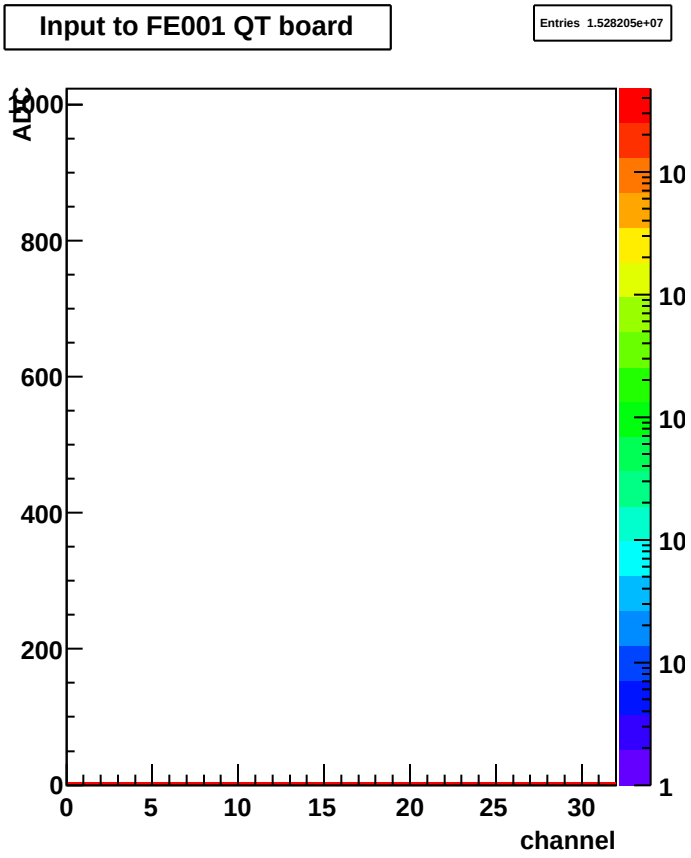

Input to FPD L2 DSM
Entries 1432692
CL bits
477564
477564
477564
SMALL
LARGE-S
LARGE-N

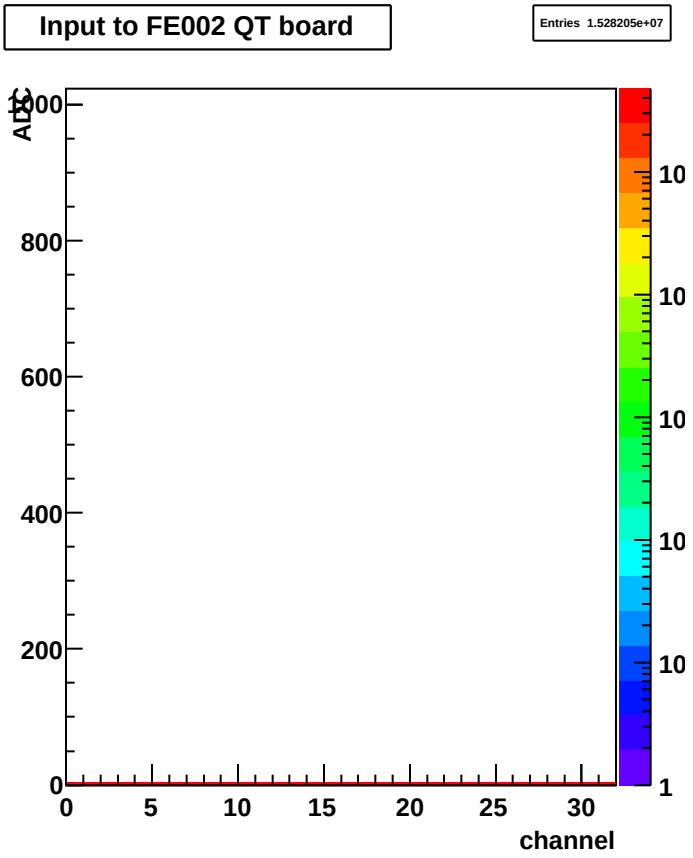

Input to FPD L2 DSM
Entries 1432692
CL bits - simulated
477564
477564
477564
SMALL
LARGE-S
LARGE-N

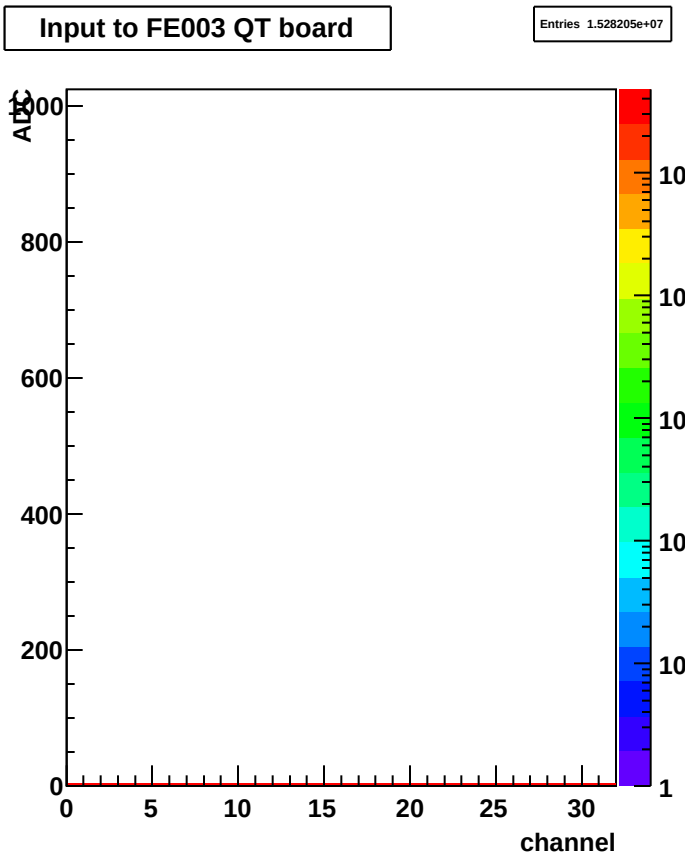

Input to FPD L2 DSM
Entries 1432692
HT bits
477521
477564
477564
43
SMALL
LARGE-S
LARGE-N

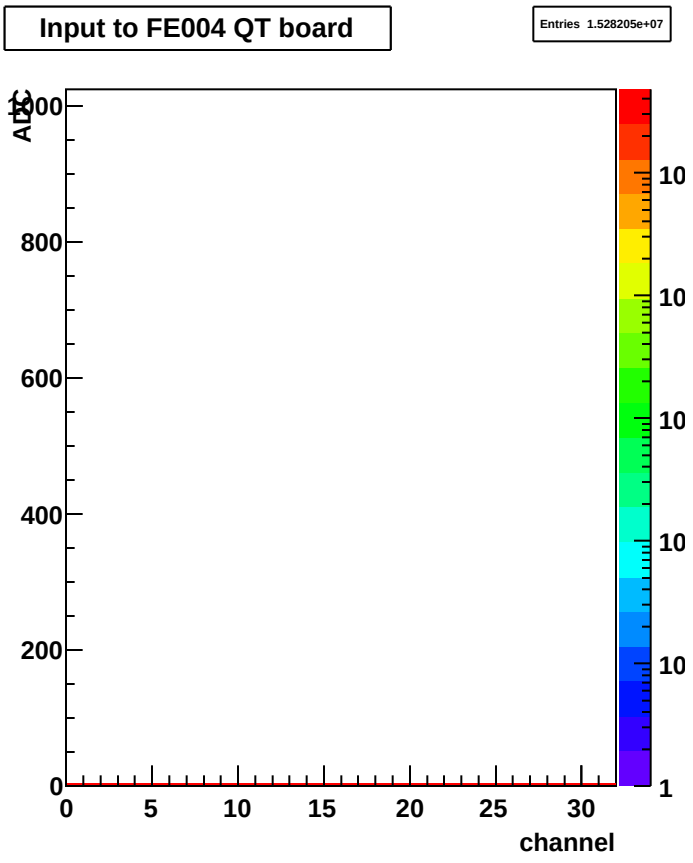

Input to FPD L2 DSM
Entries 1432692
HT bits - simulated
477521
477564
477564
43
SMALL
LARGE-S
LARGE-N

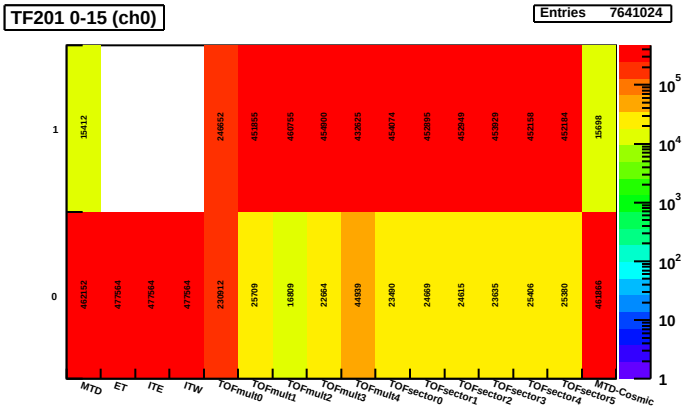

Input to FPD L2 DSM
Entries 1910256
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

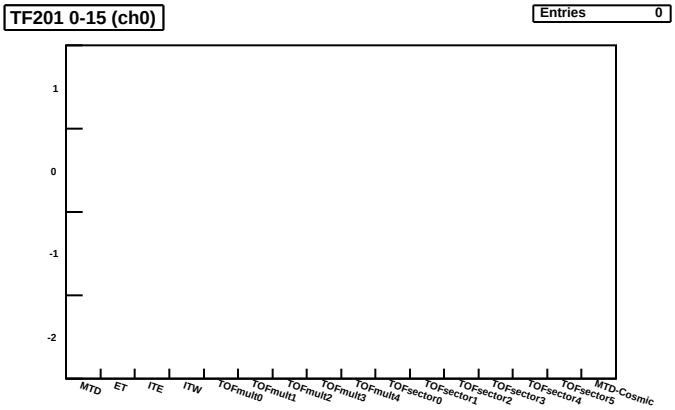

Input to FE001 QT board
Entries 1.528205e+07
ADC
channel

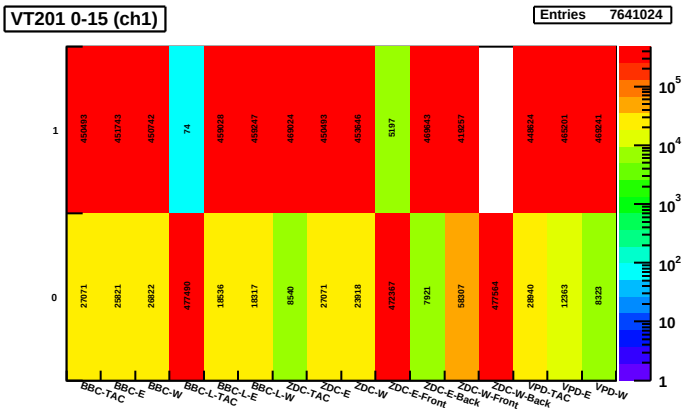

Input to FE002 QT board
Entries 1.528205e+07
ADC
channel

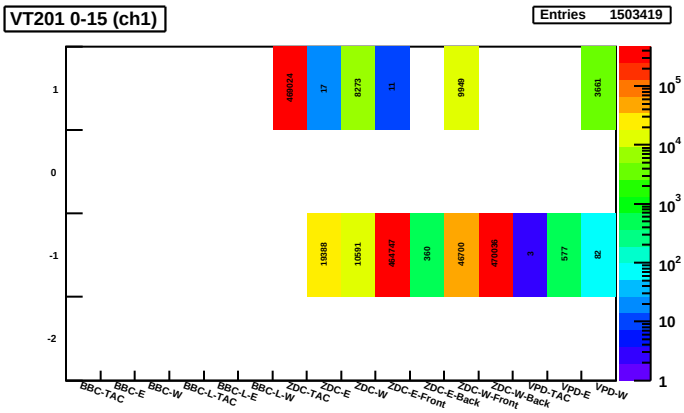

Input to FE003 QT board
Entries 1.528205e+07
ADC
channel

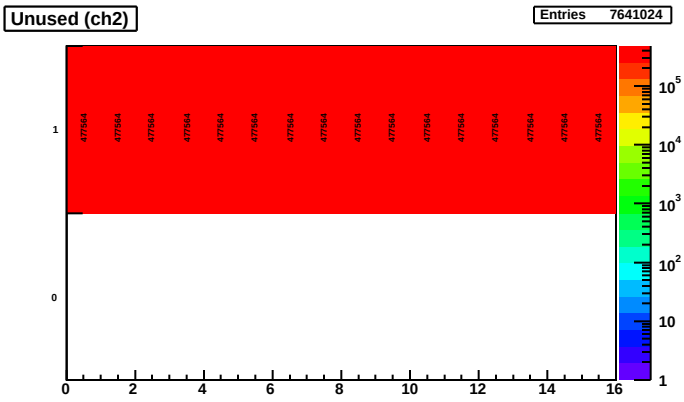

Input to FE004 QT board
Entries 1.528205e+07
ADC
channel

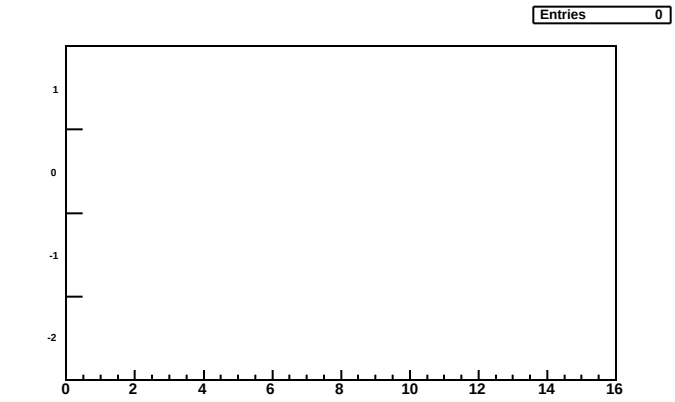

TF201 0-15 (ch0)
Entries 7641024
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

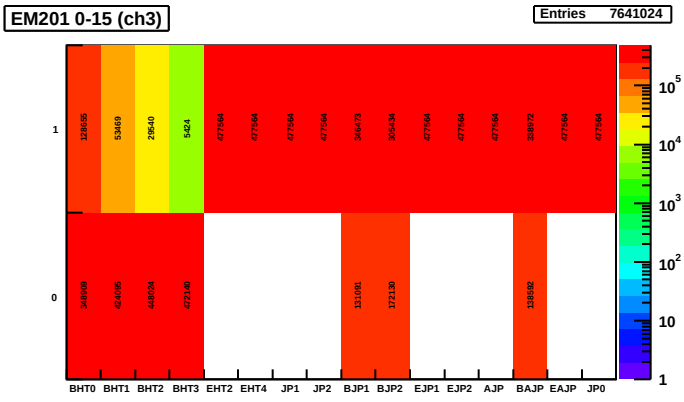

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

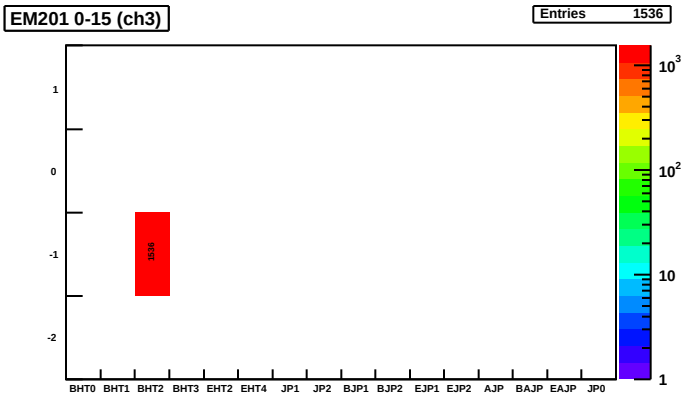

VT201 0-15 (ch1)
Entries 7641024
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 1503419
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

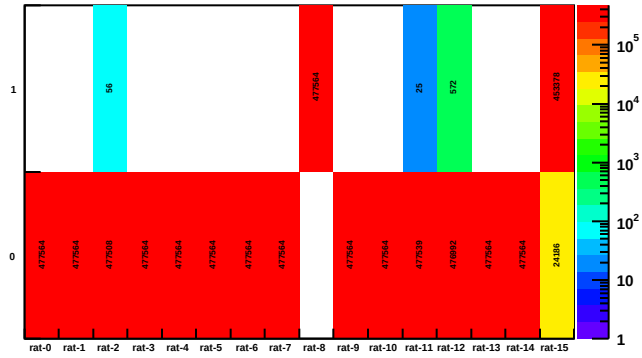

Unused (ch2)
Entries 7641024

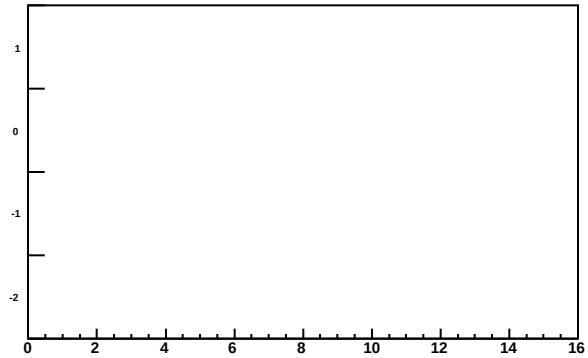

Entries 0


EM201 0-15 (ch3)
Entries 7641024
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

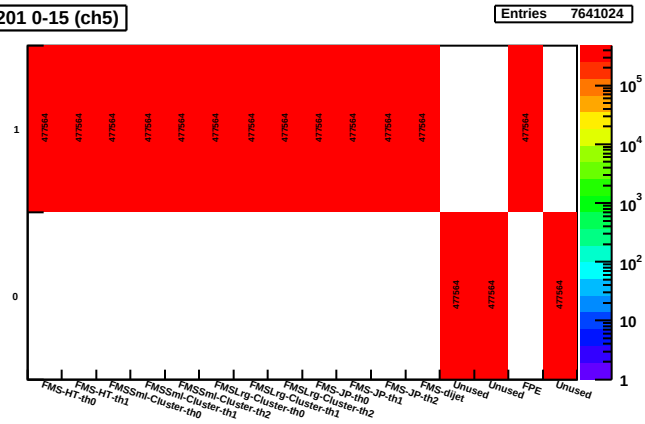

EM201 0-15 (ch3)
Entries 1536
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

RAT board (ch4)

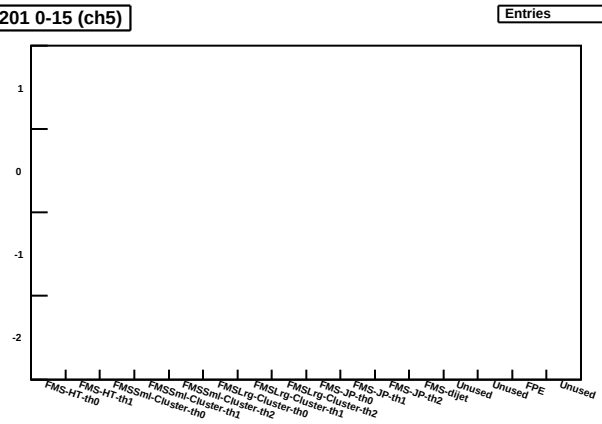




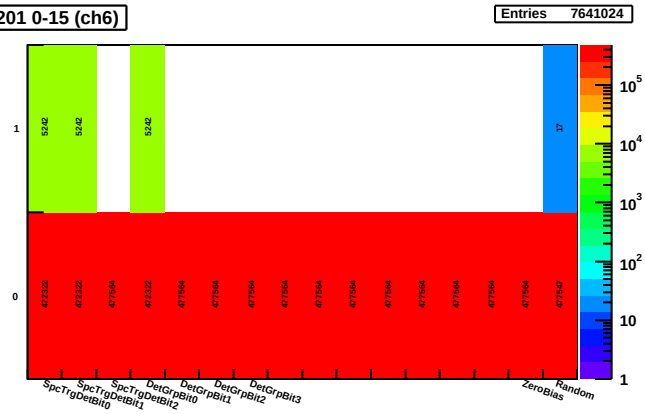
FP201 0-15 (ch5)

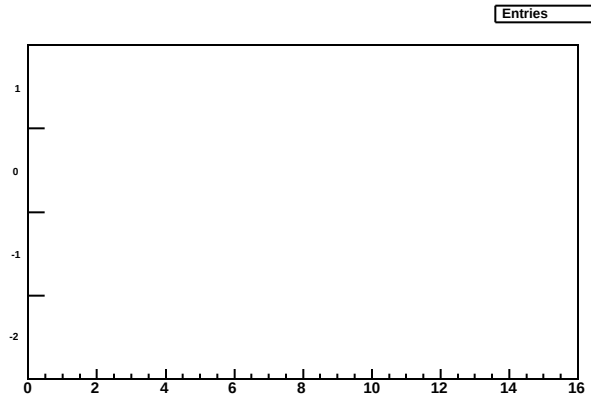
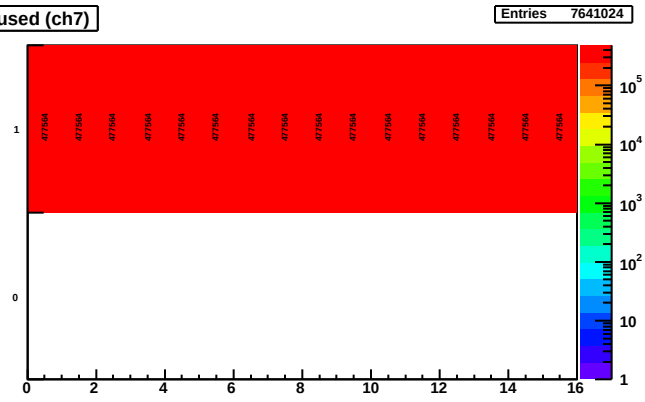
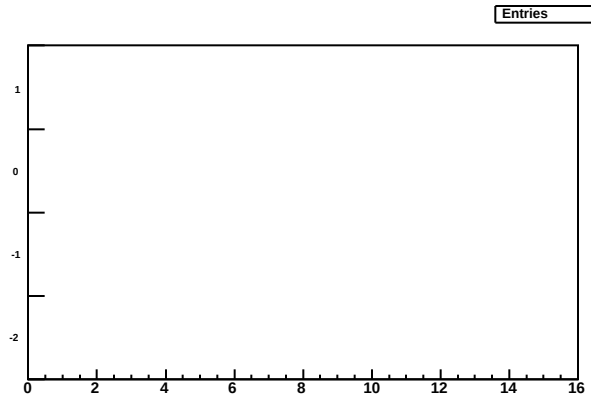


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)