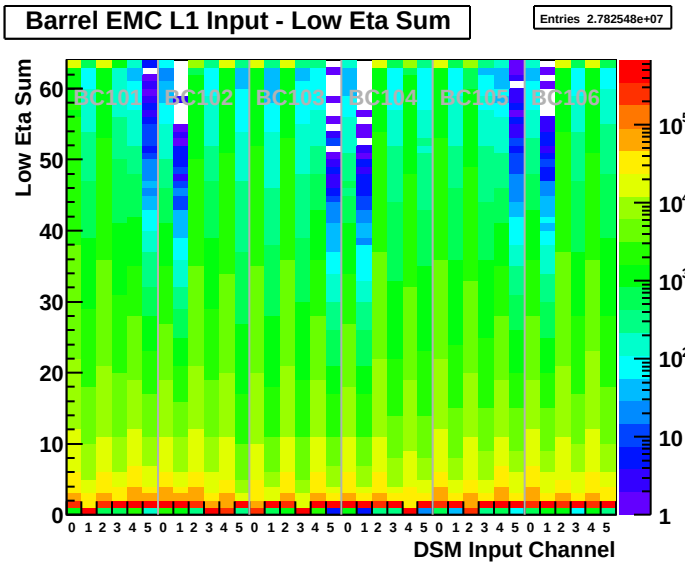
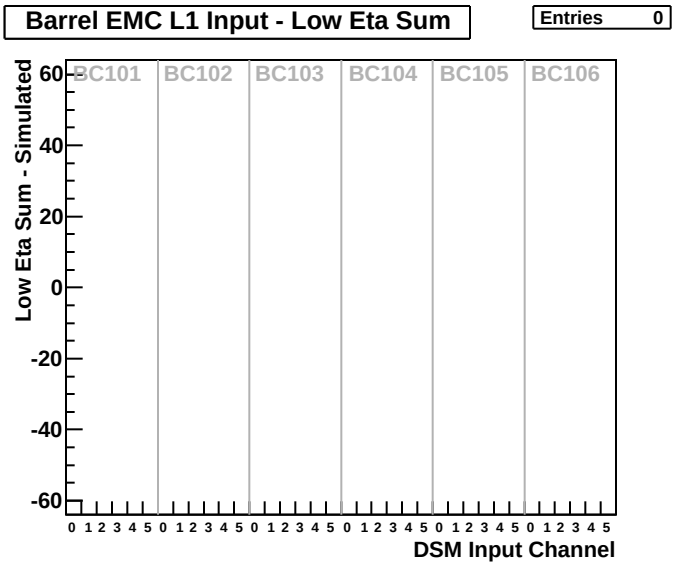
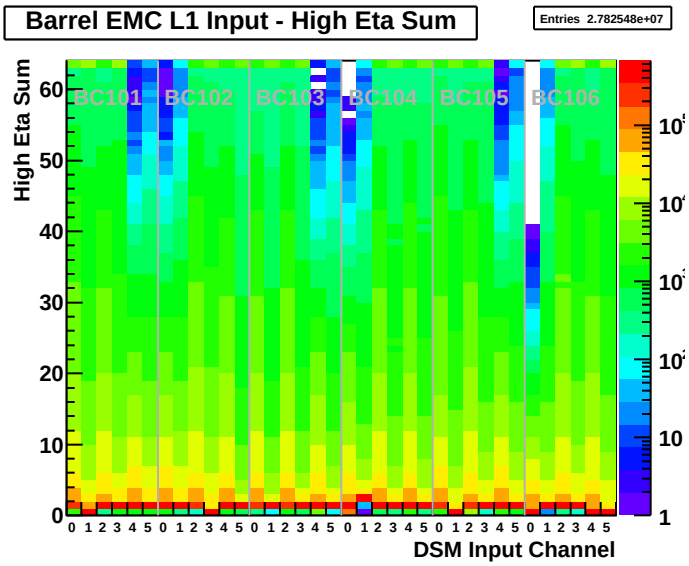
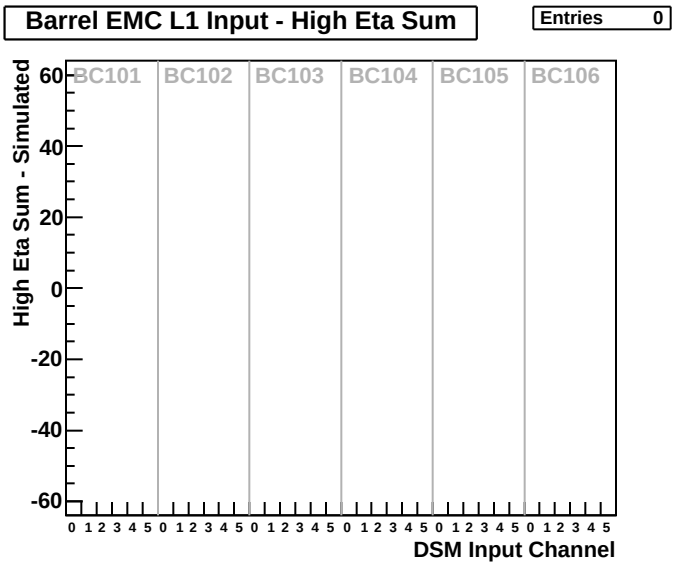
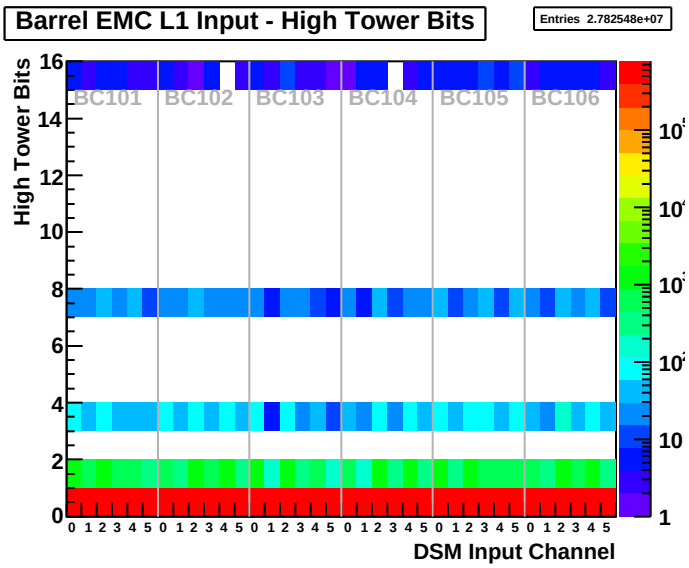
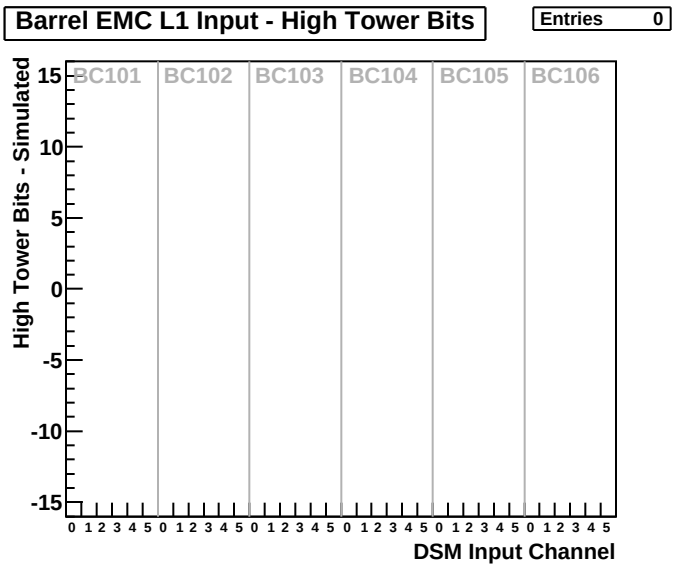

Barrel EMC L1 Input - Low Eta Sum
Entries 2.782548e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

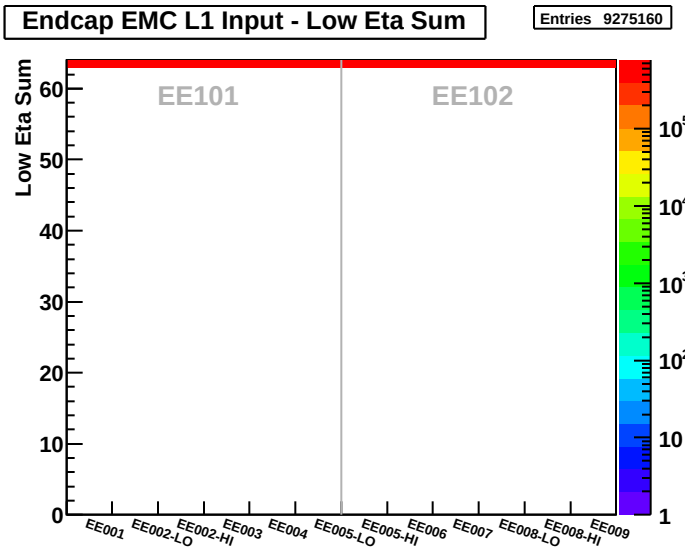

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

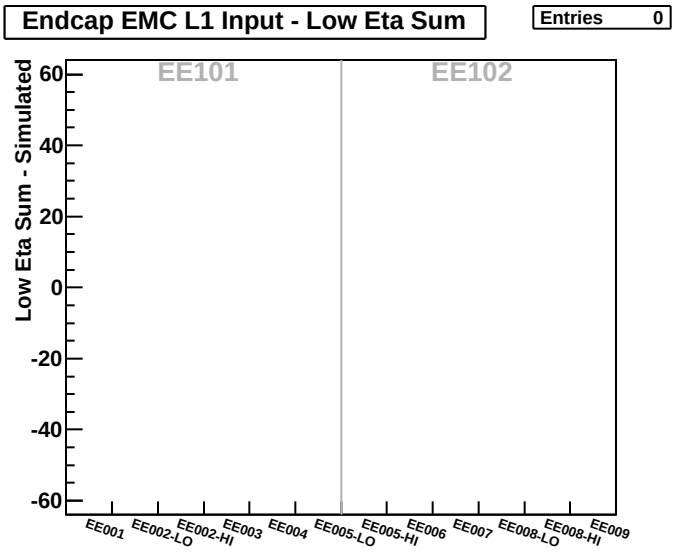

Barrel EMC L1 Input - High Eta Sum
Entries 2.782548e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

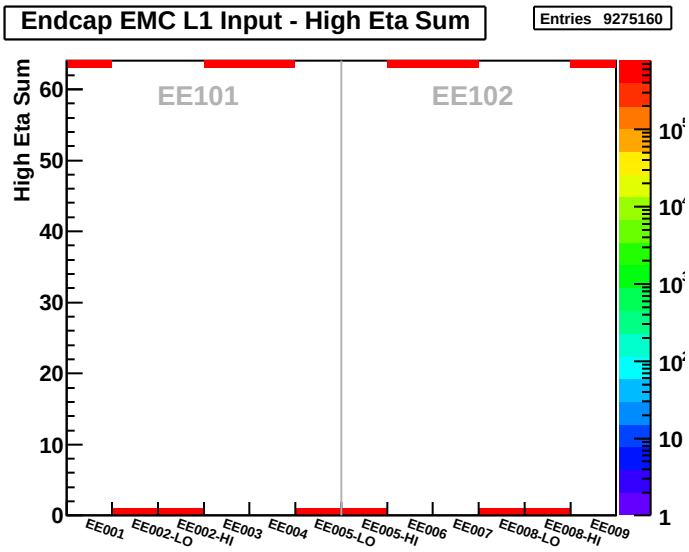

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

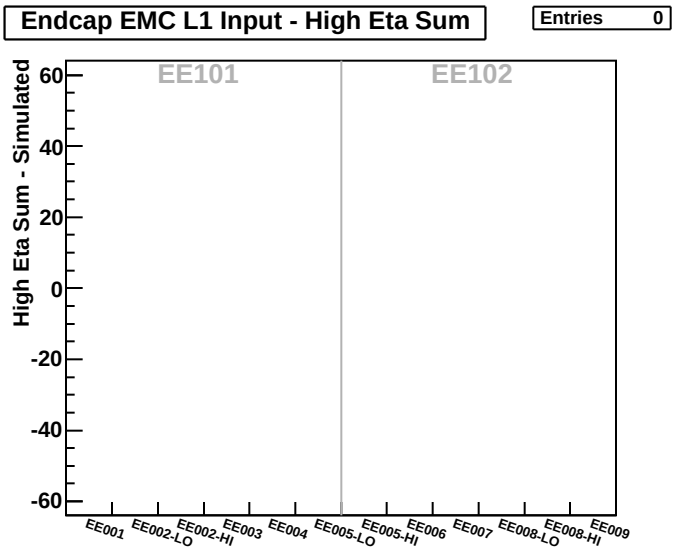

Barrel EMC L1 Input - High Tower Bits
Entries 2.782548e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

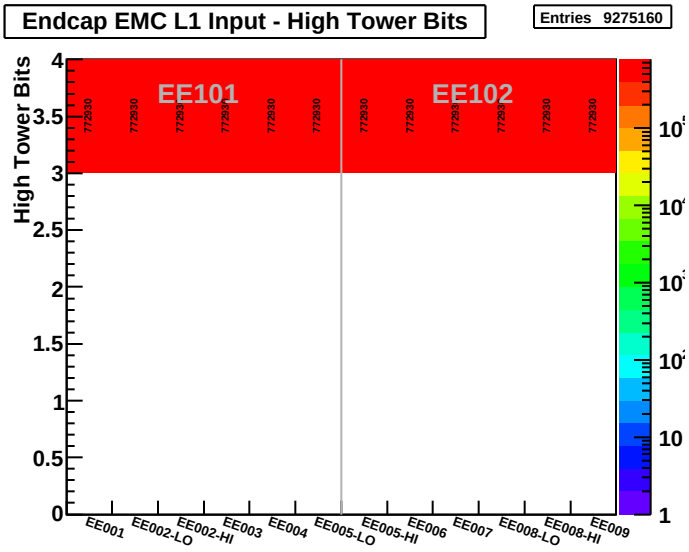

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

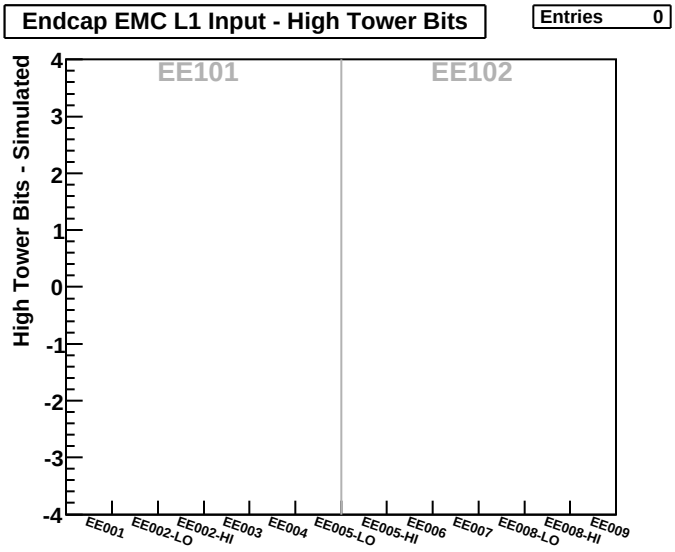

Endcap EMC L1 Input - Low Eta Sum
Entries 9275160
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

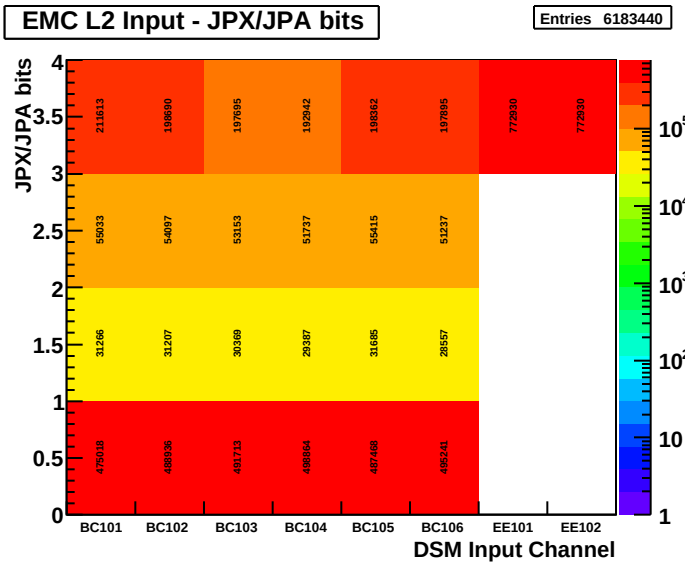

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

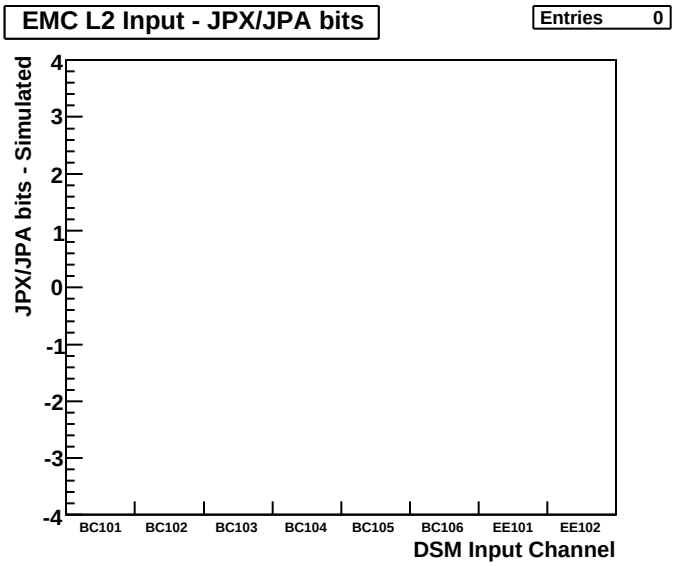

Endcap EMC L1 Input - High Eta Sum
Entries 9275160
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

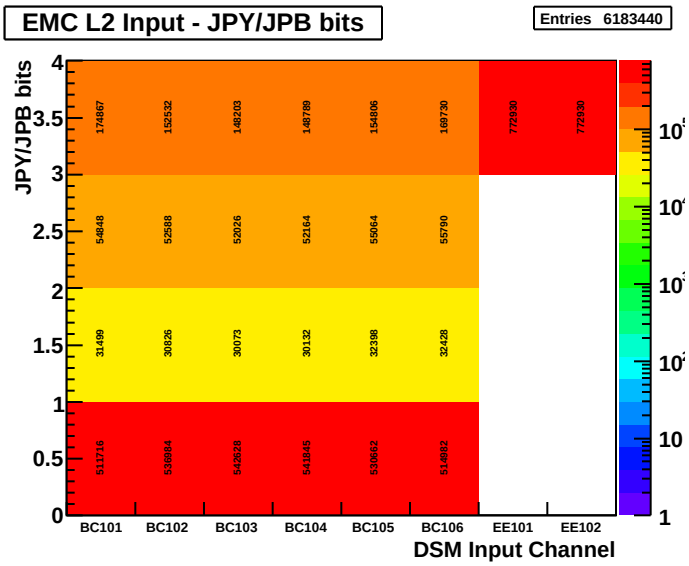

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

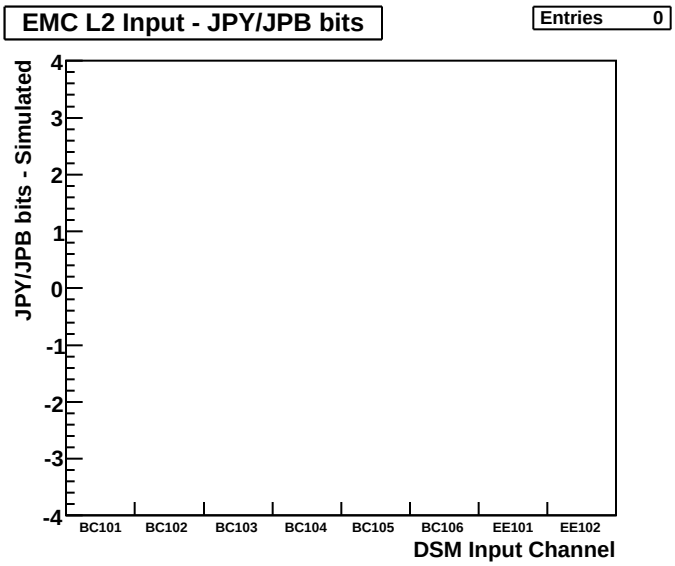

Endcap EMC L1 Input - High Tower Bits
Entries 9275160
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

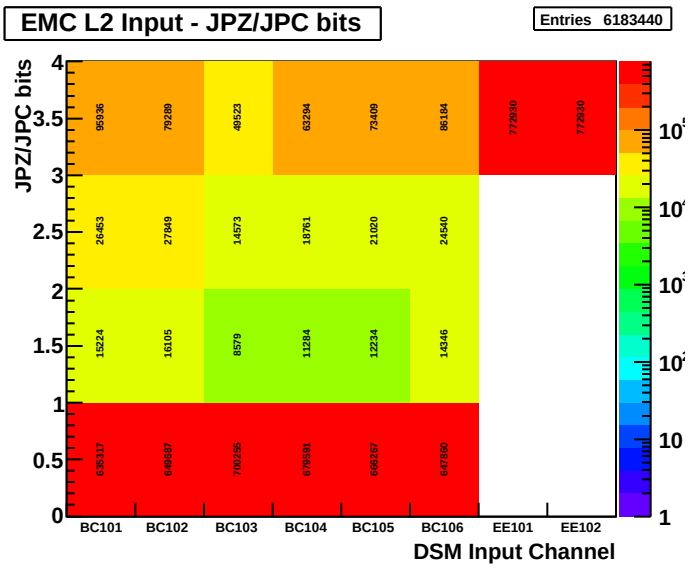

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

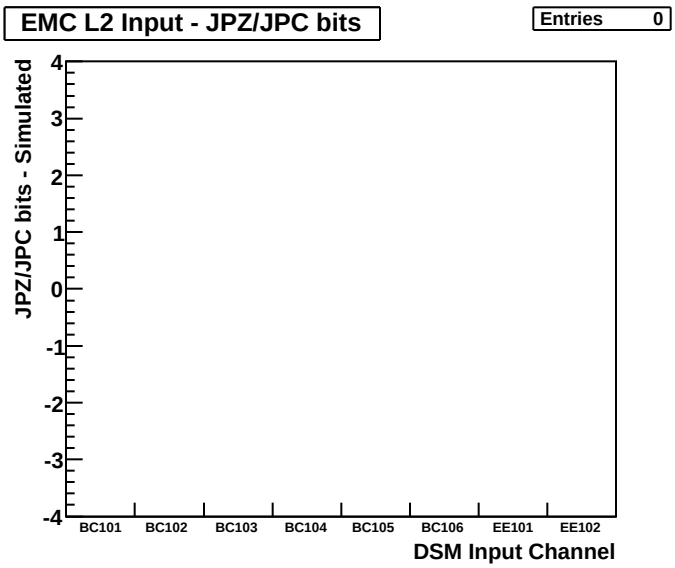

EMC L2 Input - JPX/JPA bits
Entries 6183440
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
211613 198690 197695 192942 198362 197895 772930 772930
55033 54097 53153 51737 55415 51237
31266 31207 30369 29367 31685 29557
475018 489936 491713 498864 497468 495241

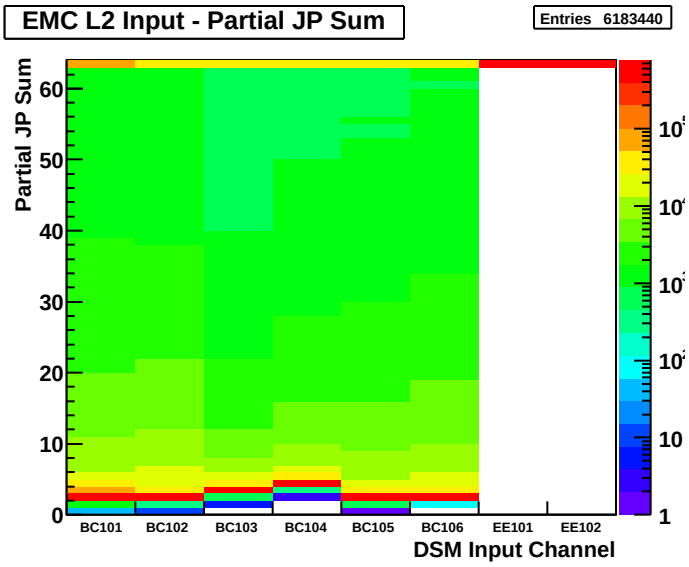

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

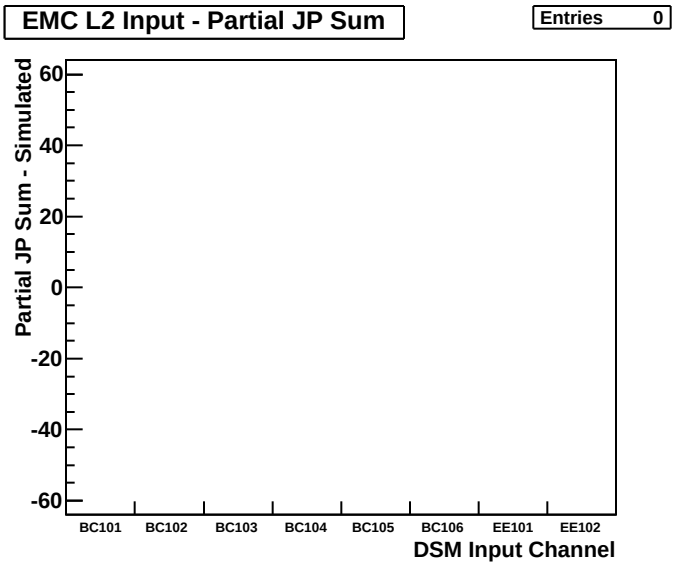

EMC L2 Input - JPY/JPB bits
Entries 6183440
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
174867 152532 148203 148789 154806 169730 772930 772930
54848 52568 52026 52164 55064 55790
31499 30826 30073 30132 32398 32428
511716 530884 542828 541845 530662 514982

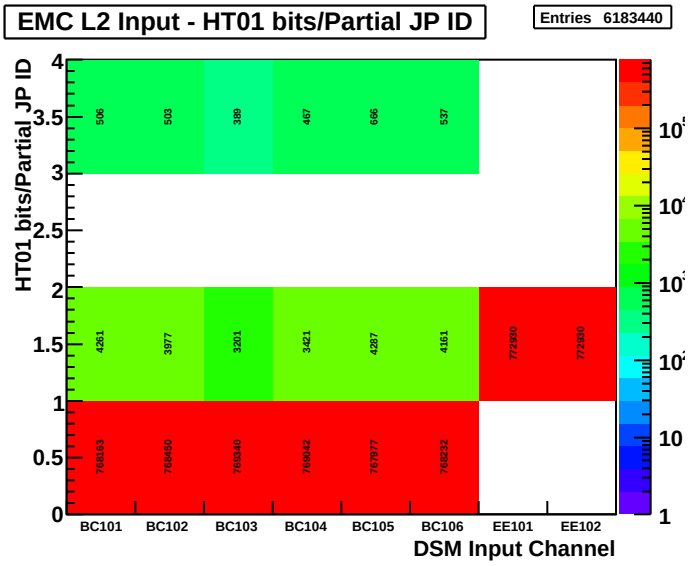

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

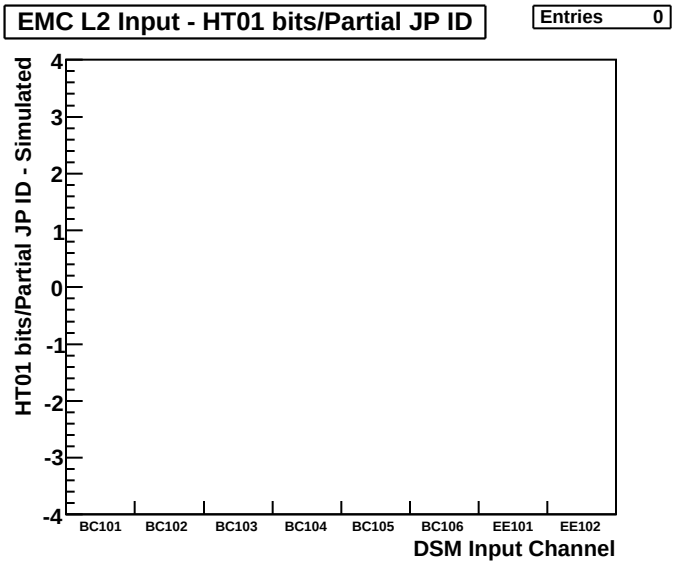

EMC L2 Input - JPZ/JPC bits
Entries 6183440
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
95936 79289 49523 63294 73409 86184 772930 772930
26453 27849 14573 18761 21020 24540
15224 16105 8579 11284 12234 14346
635317 649687 700259 679591 666267 647860

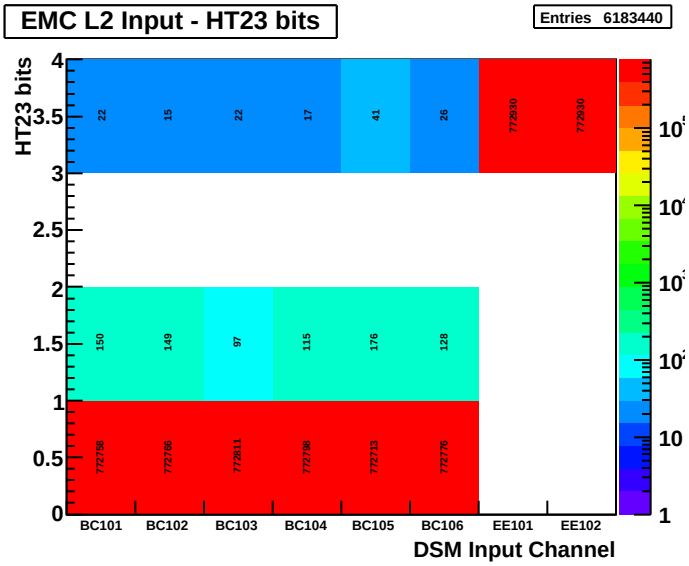

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

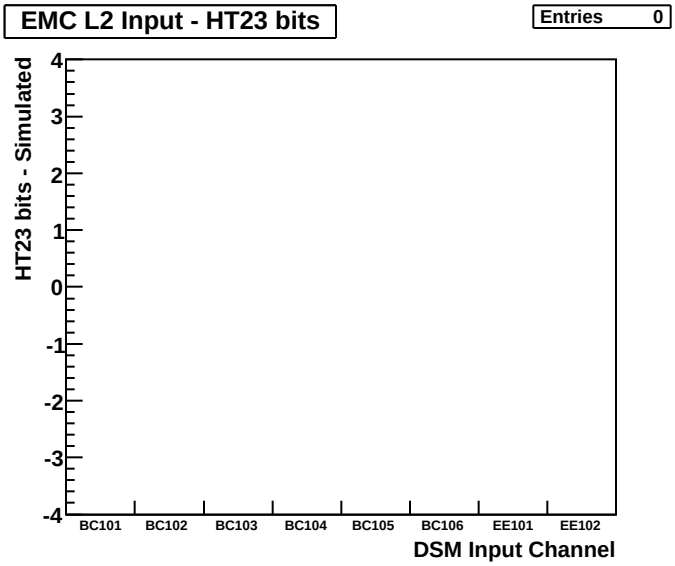

EMC L2 Input - Partial JP Sum
Entries 6183440
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

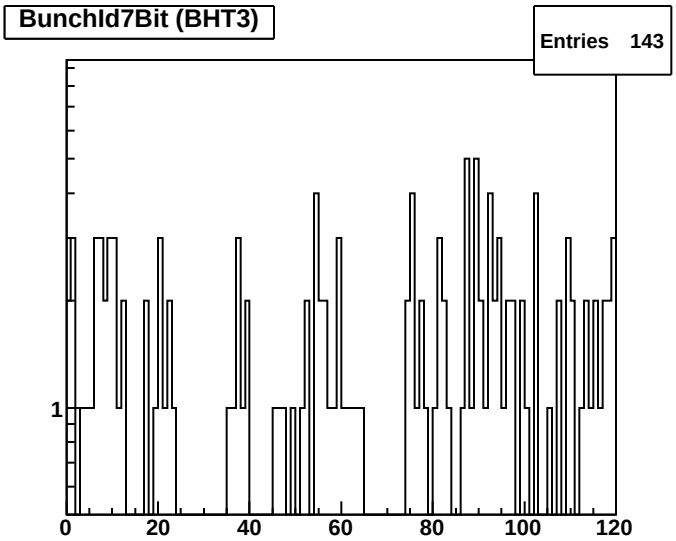

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

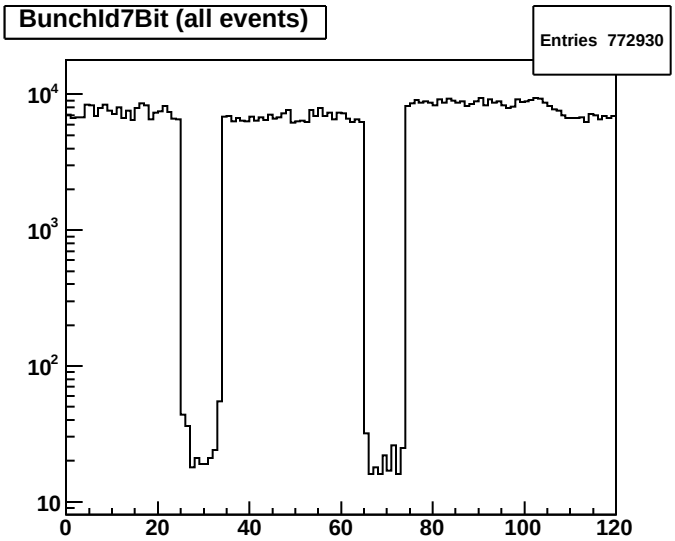

EMC L2 Input - HT01 bits/Partial JP ID
Entries 6183440
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

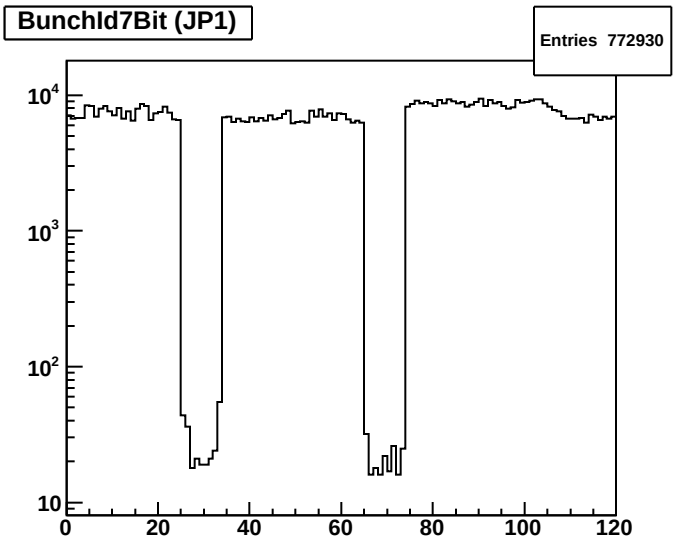

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

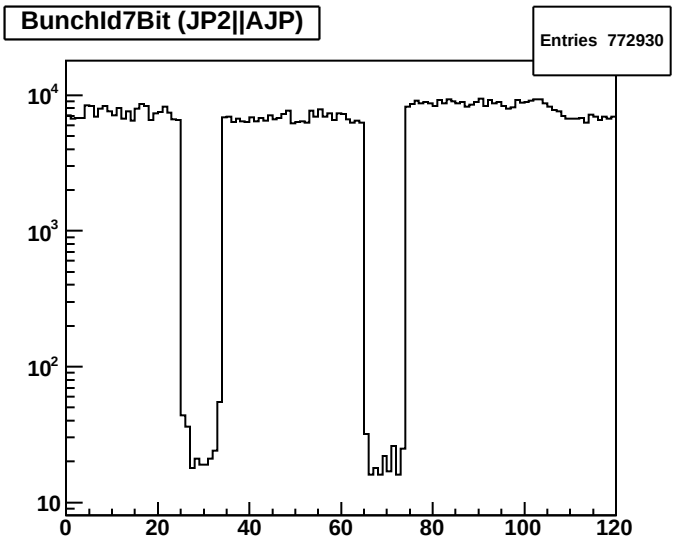

EMC L2 Input - HT23 bits
Entries 6183440
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

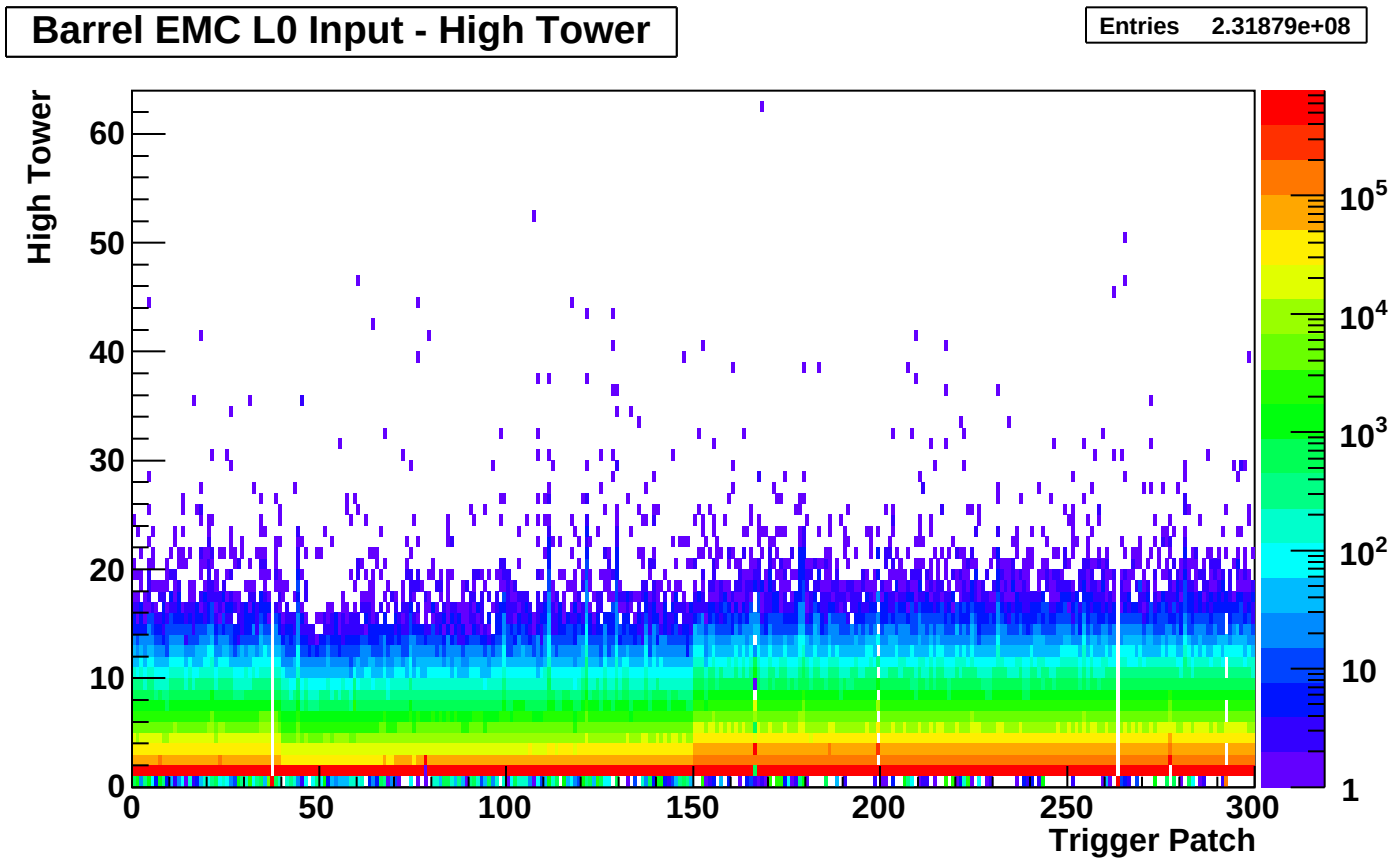

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

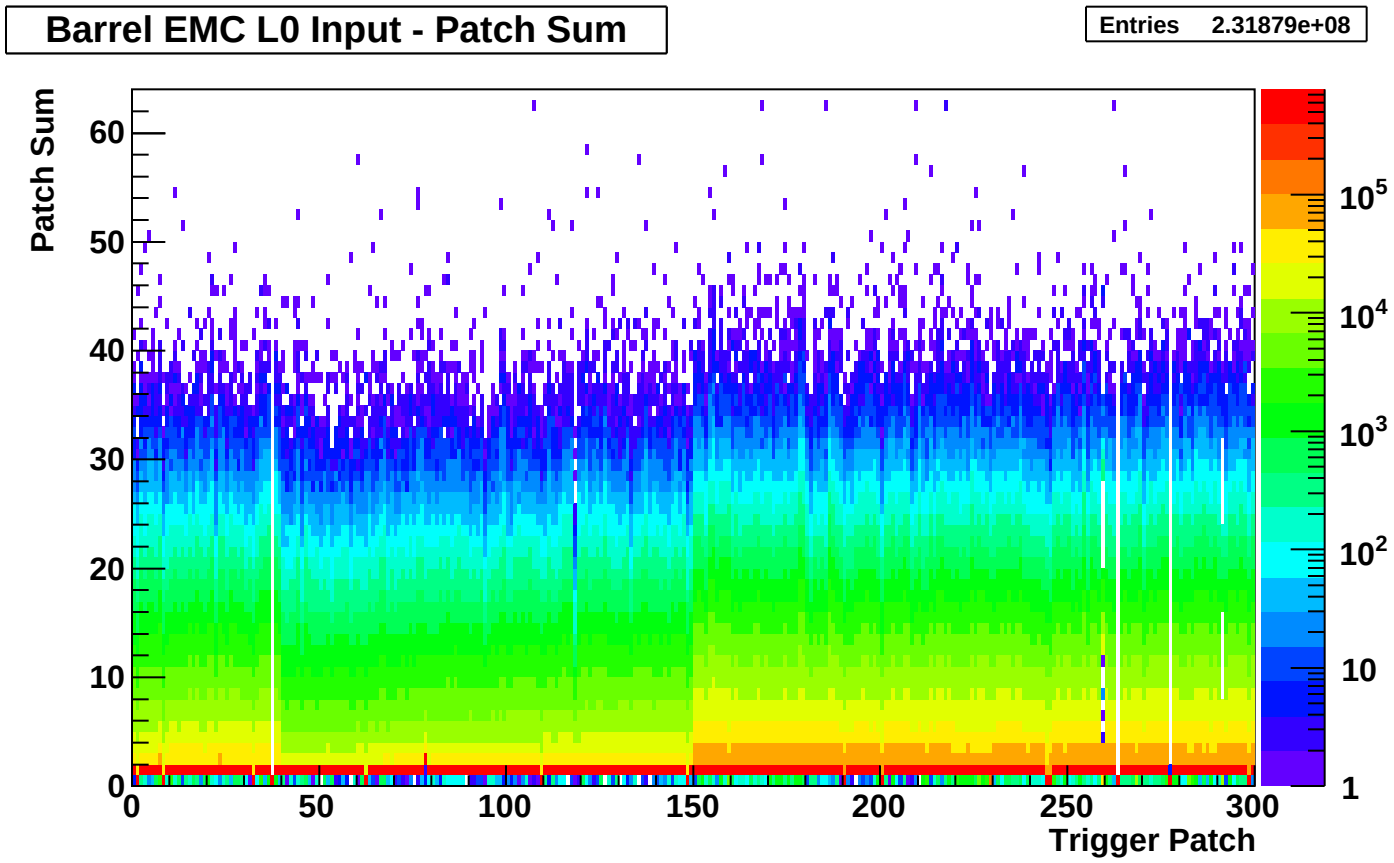

BunchId7Bit (BHT3)
Entries 143
1
0
20
40
60
80
100
120

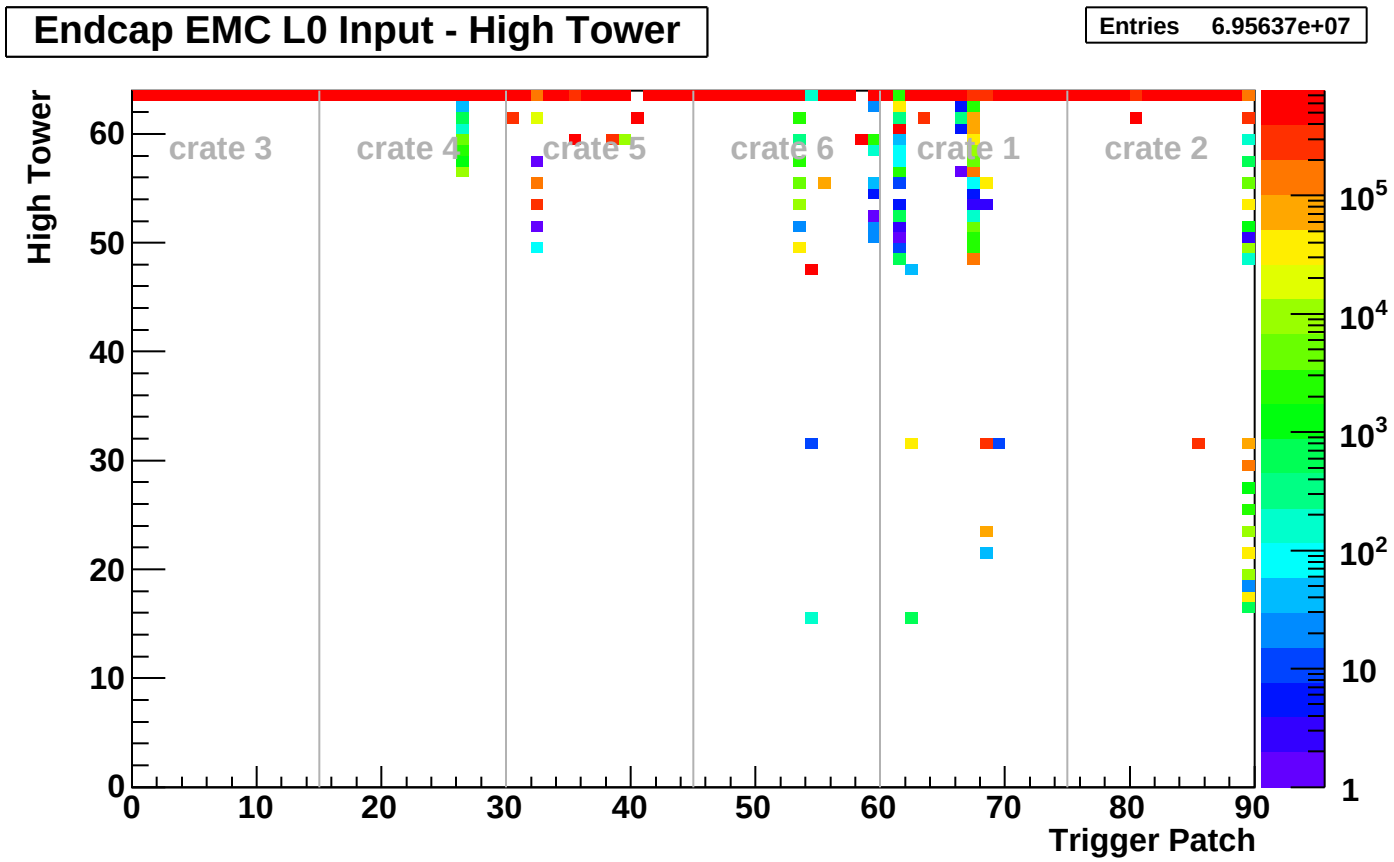

BunchId7Bit (all events)
Entries 772930
10^4
10^3
10^2
10
0
20
40
60
80
100
120

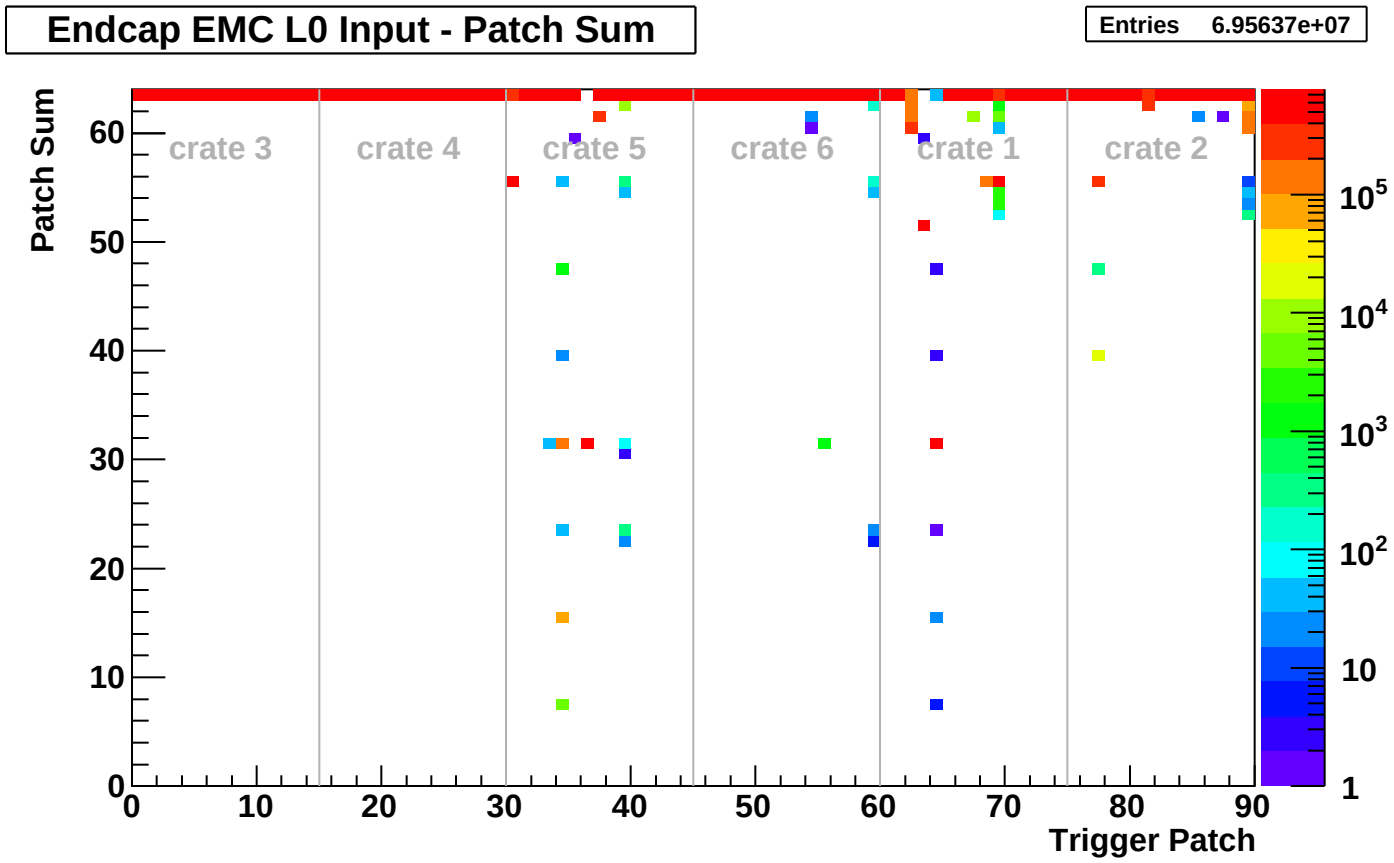

BunchId7Bit (JP1)
Entries 772930
10^4
10^3
10^2
10
0
20
40
60
80
100
120

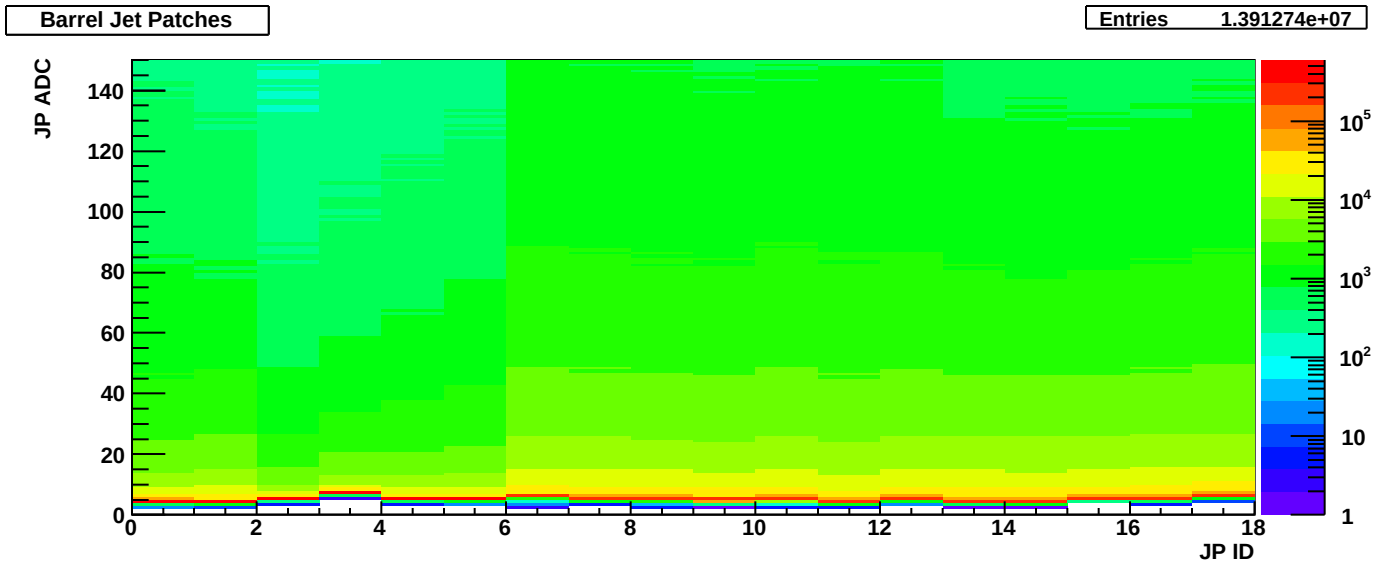

BunchId7Bit (JP2||AJP)
Entries 772930
10^4
10^3
10^2
10
0
20
40
60
80
100
120

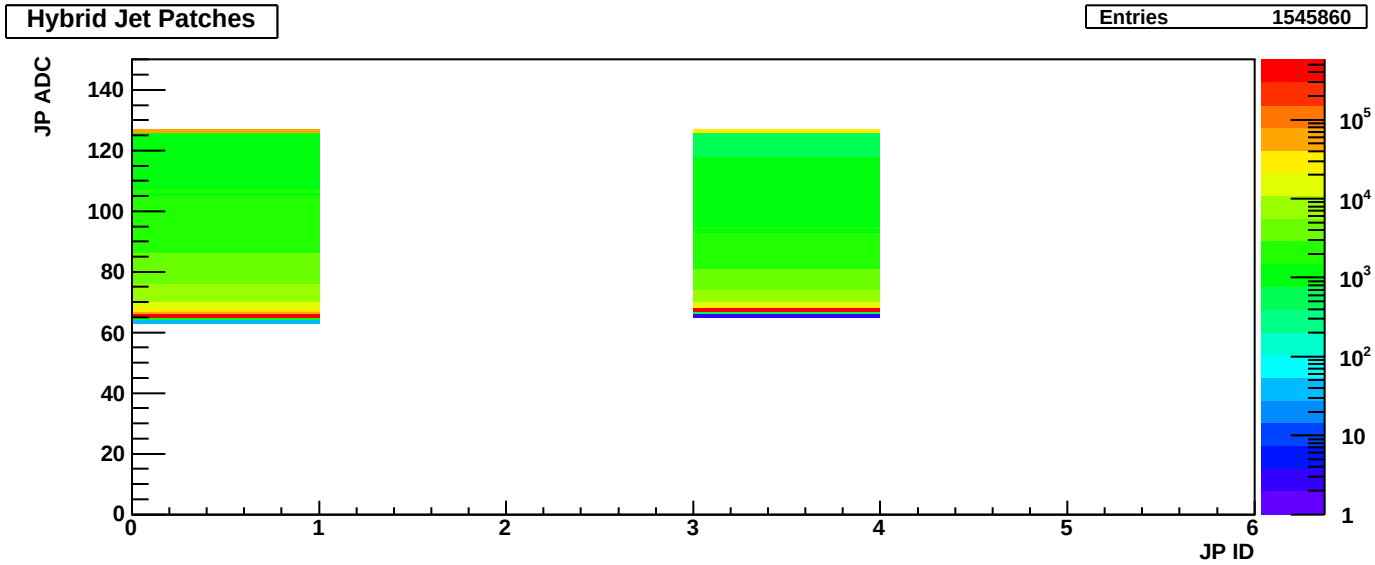

Barrel EMC L0 Input - High Tower
Entries 2.31879e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

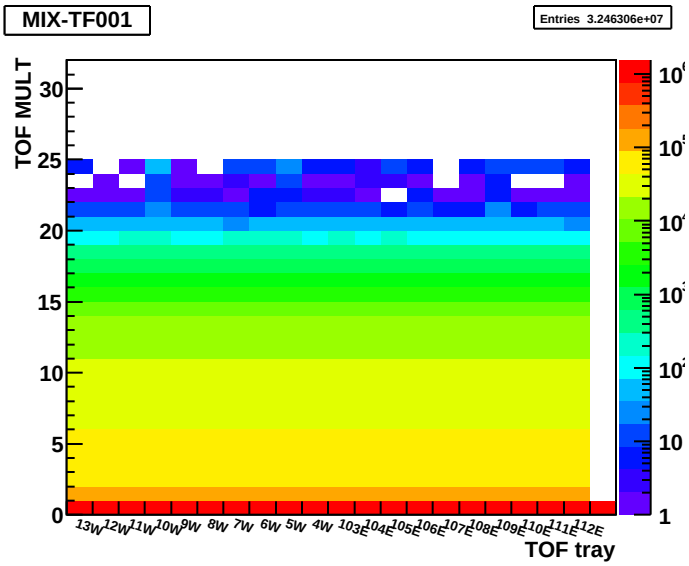

Barrel EMC L0 Input - Patch Sum
Entries 2.31879e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

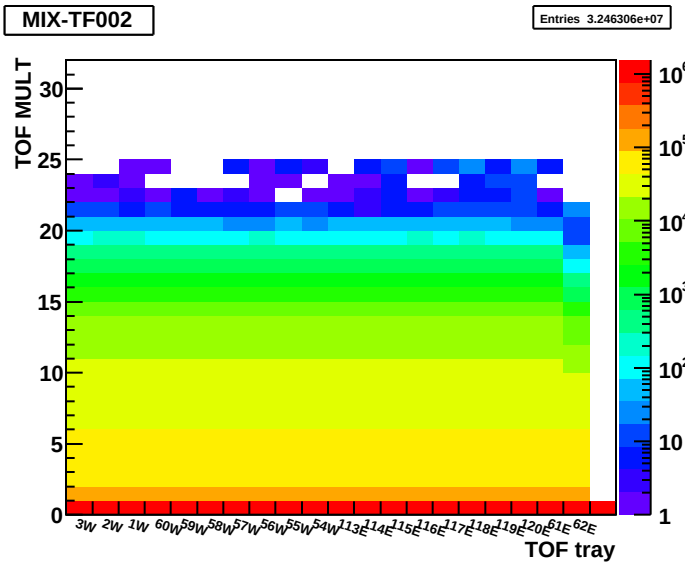

Endcap EMC L0 Input - High Tower
Entries 6.95637e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

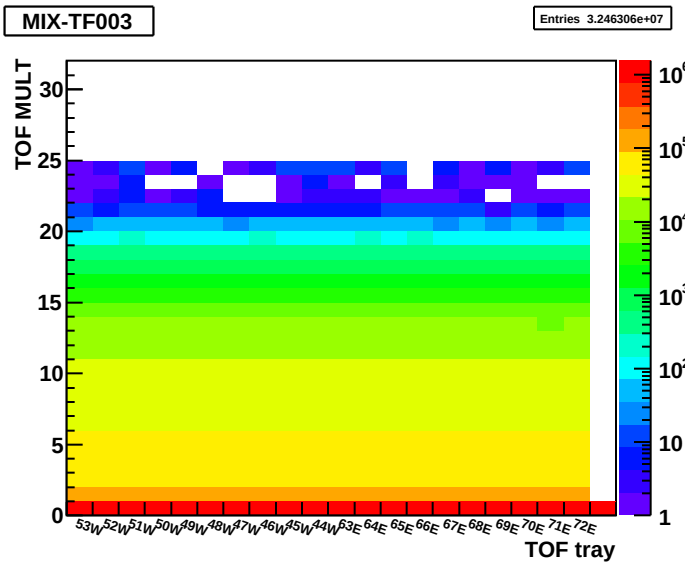

Endcap EMC L0 Input - Patch Sum
Entries 6.95637e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

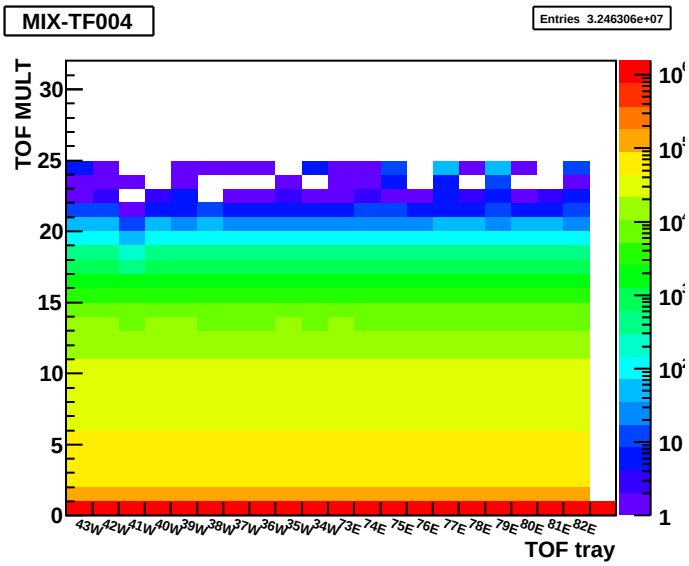

Barrel Jet Patches
Entries 1.391274e+07
JP ADC
JP ID

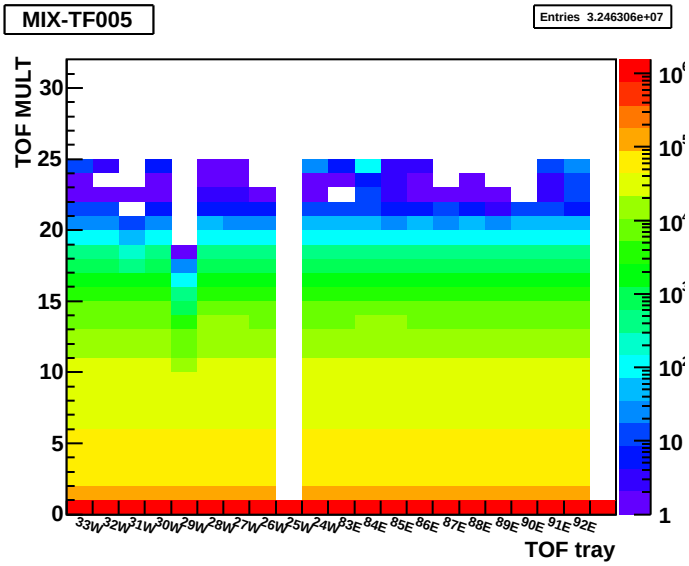

Hybrid Jet Patches
Entries 1545860
JP ADC
JP ID

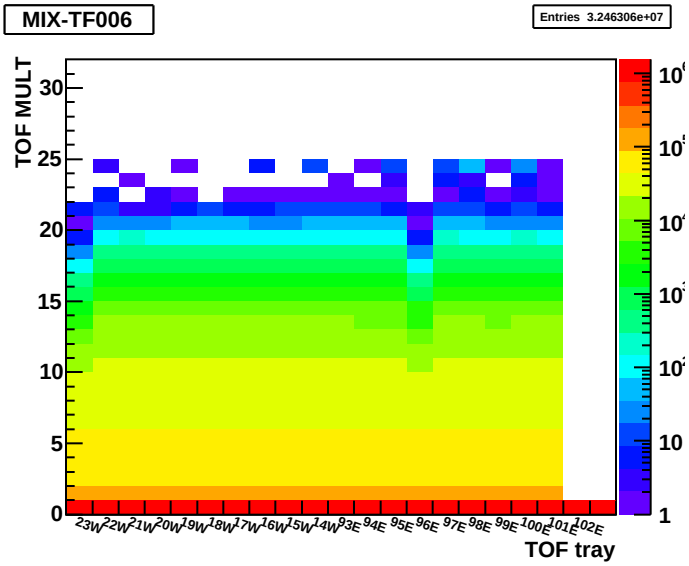

MIX-TF001
Entries 3.246306e+07
TOF MULT
TOF tray

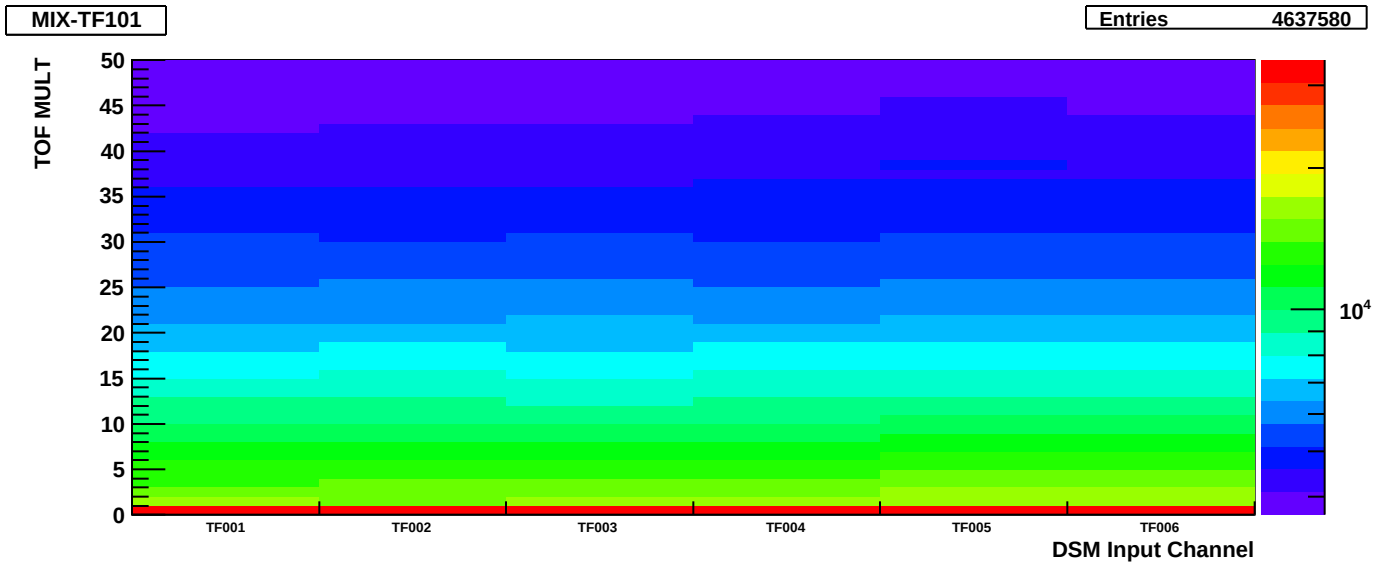

MIX-TF002
Entries 3.246306e+07
TOF MULT
TOF tray

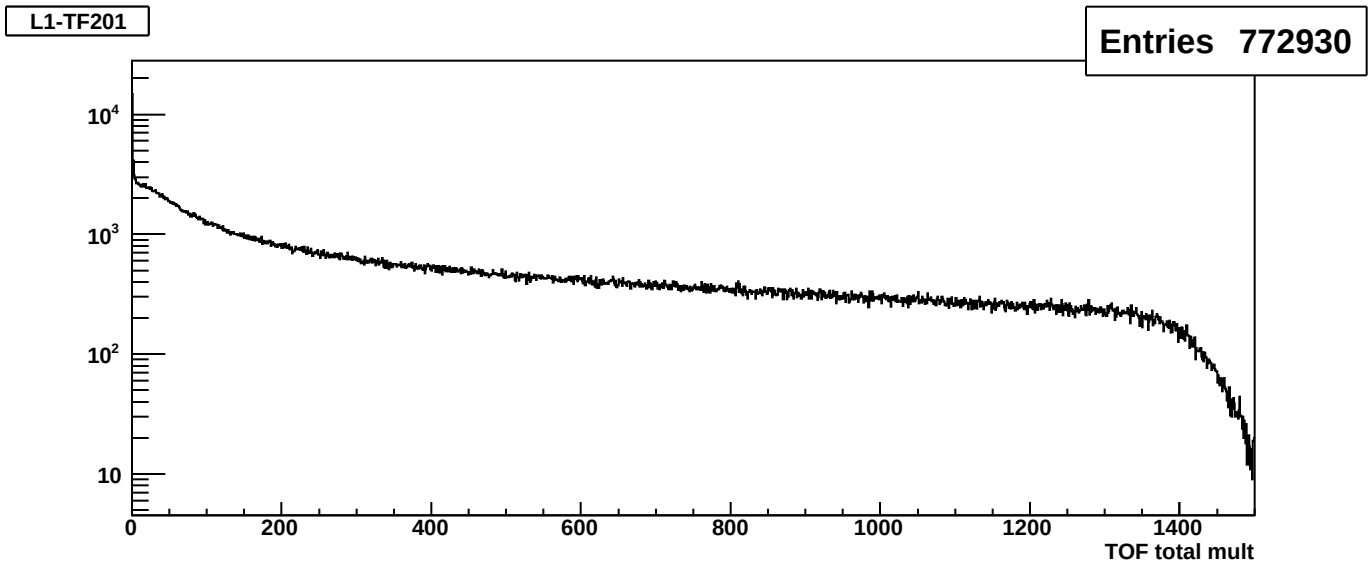

MIX-TF003
Entries 3.246306e+07
TOF MULT
TOF tray

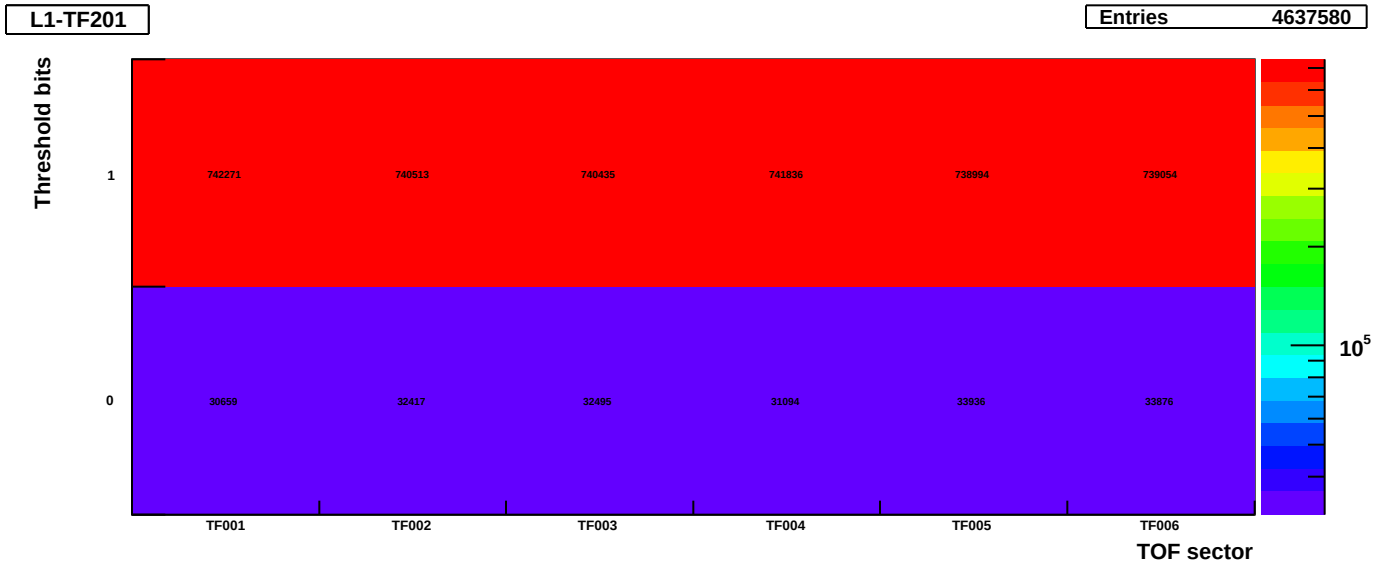

MIX-TF004
Entries 3.246306e+07
TOF MULT
TOF tray

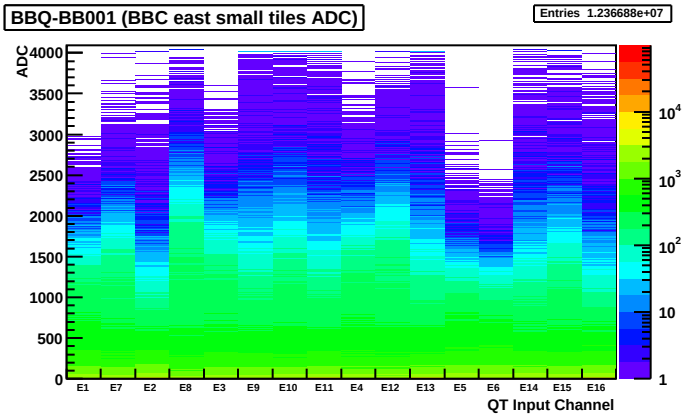

MIX-TF005
Entries 3.246306e+07
TOF MULT
TOF tray

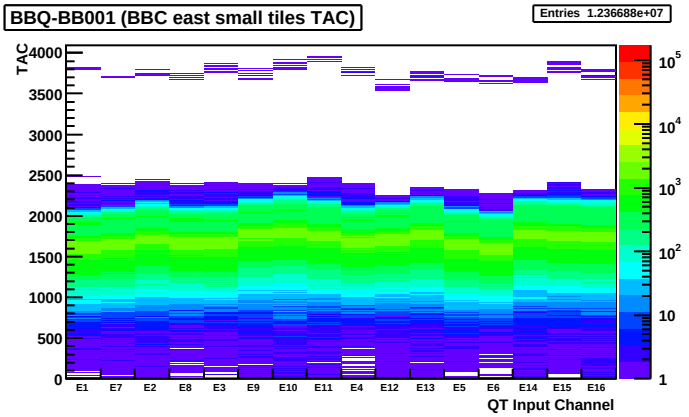

MIX-TF006
Entries 3.246306e+07
TOF MULT
TOF tray

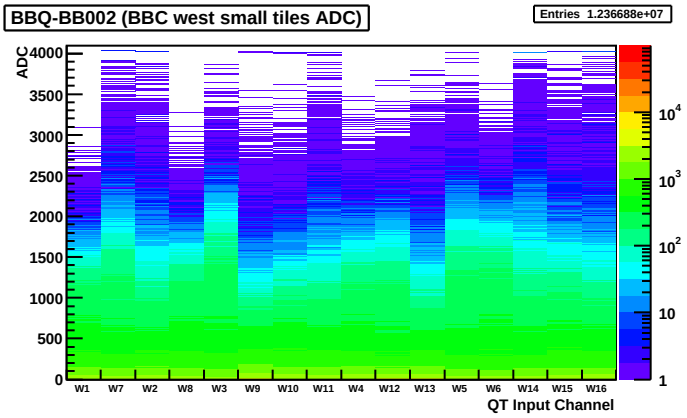

MIX-TF101
Entries 4637580
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

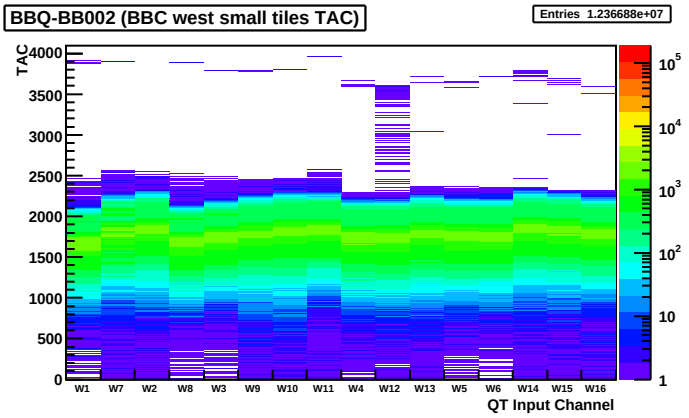

L1-TF201
Entries 772930
10^4
10^3
10^2
10
0
200
400
600
800
1000
1200
1400
TOF total mult

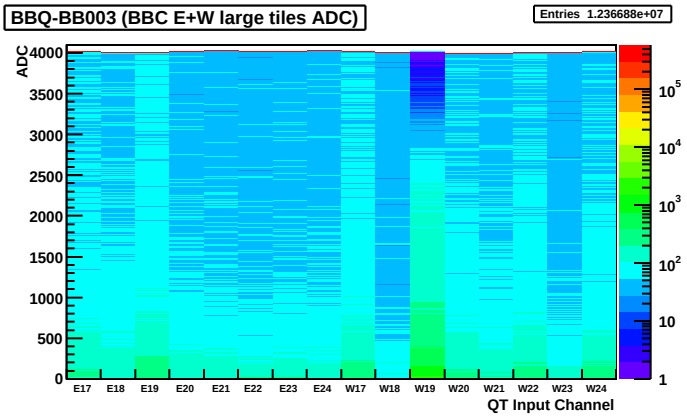

L1-TF201
Entries 4637580
Threshold bits
1
0
742271
740513
740435
741836
738994
739054
30659
32417
32495
31094
33936
33876
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

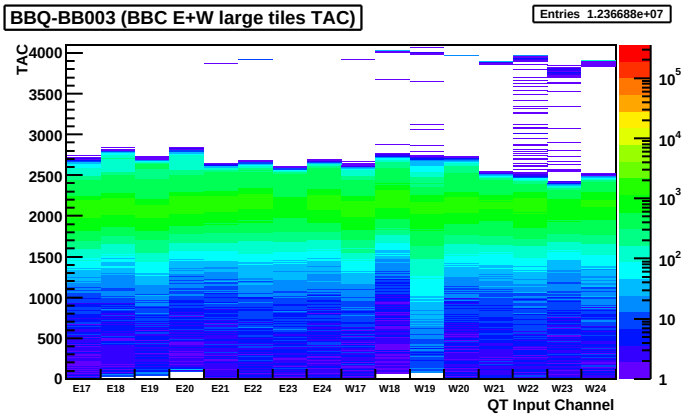

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.236688e+07
ADC
QT Input Channel

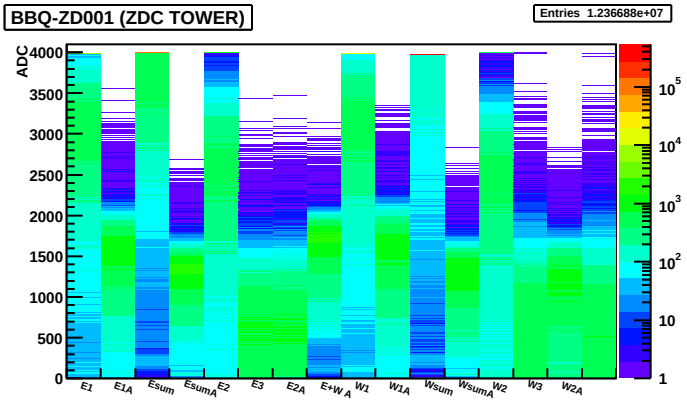

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.236688e+07
TAC
QT Input Channel

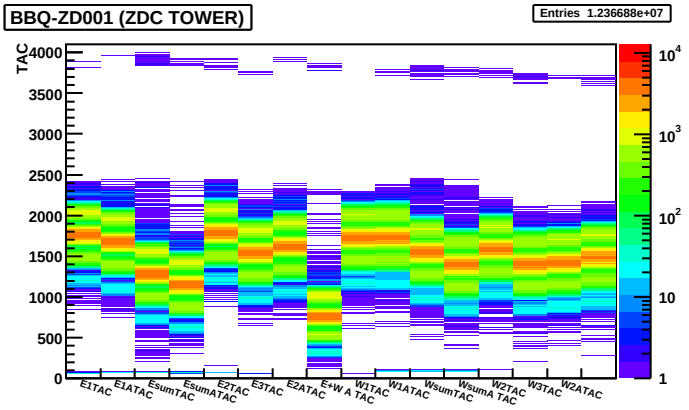

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.236688e+07
ADC
QT Input Channel

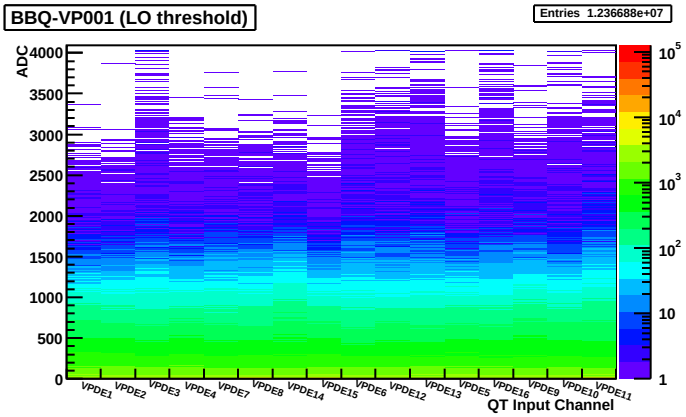

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.236688e+07
TAC
QT Input Channel

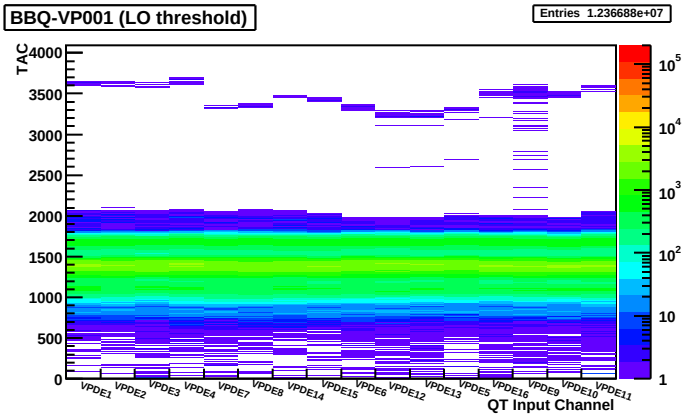

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.236688e+07
ADC
QT Input Channel

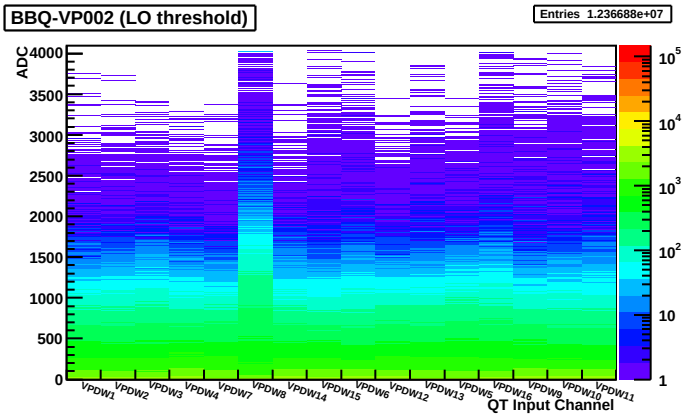

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.236688e+07
TAC
QT Input Channel

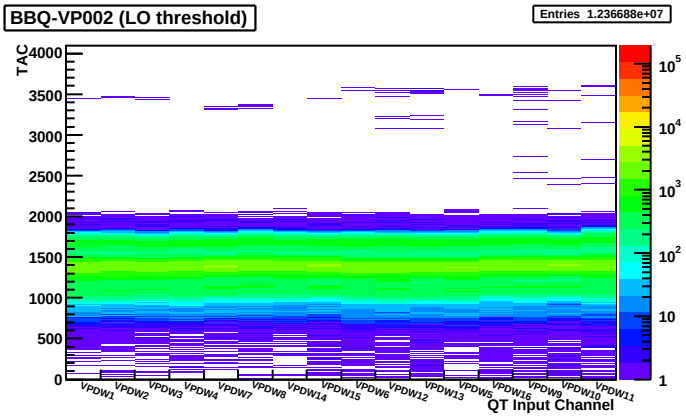

BBQ-ZD001 (ZDC TOWER)
Entries 1.236688e+07
ADC

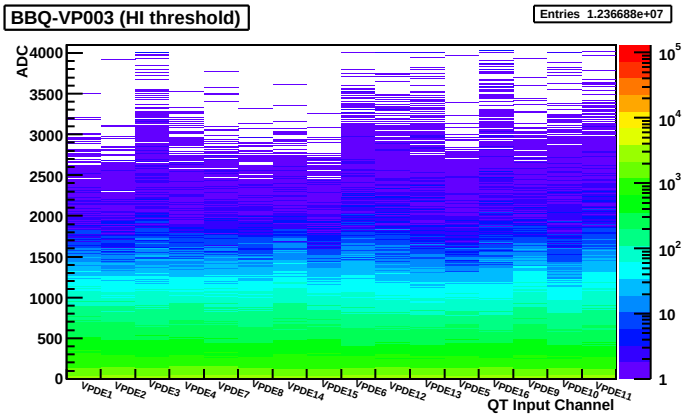

BBQ-ZD001 (ZDC TOWER)
Entries 1.236688e+07
TAC

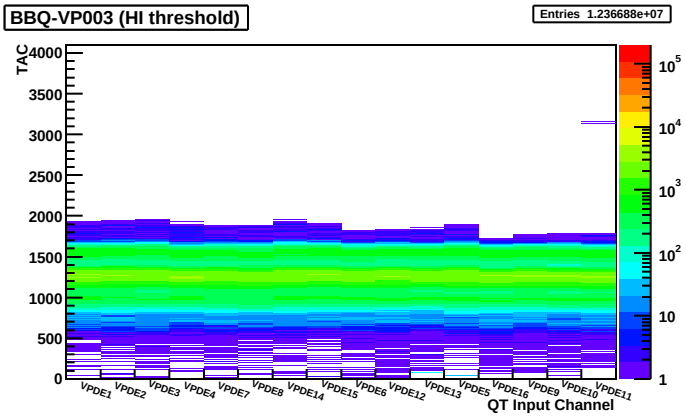

BBQ-VP001 (LO threshold)
Entries 1.236688e+07
ADC
QT Input Channel

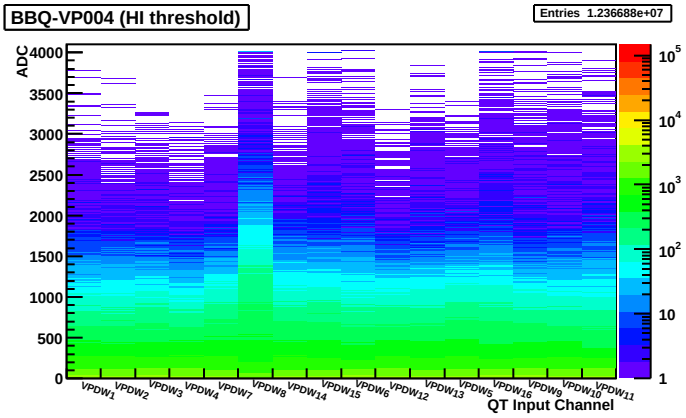

BBQ-VP001 (LO threshold)
Entries 1.236688e+07
TAC
QT Input Channel

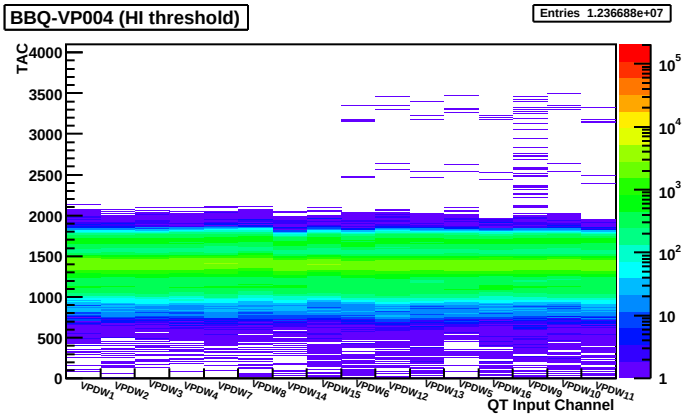

BBQ-VP002 (LO threshold)
Entries 1.236688e+07
ADC
QT Input Channel

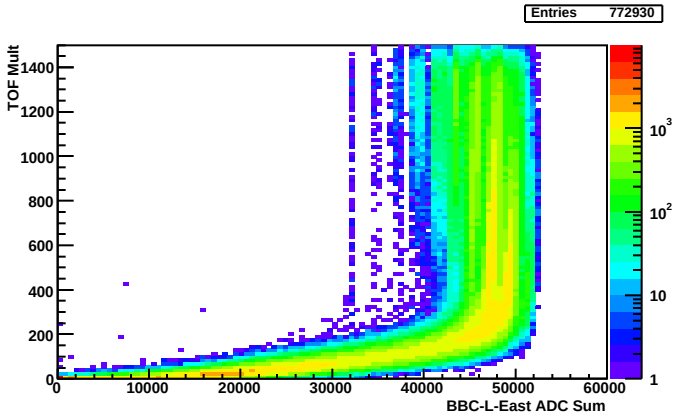

BBQ-VP002 (LO threshold)
Entries 1.236688e+07
TAC
QT Input Channel

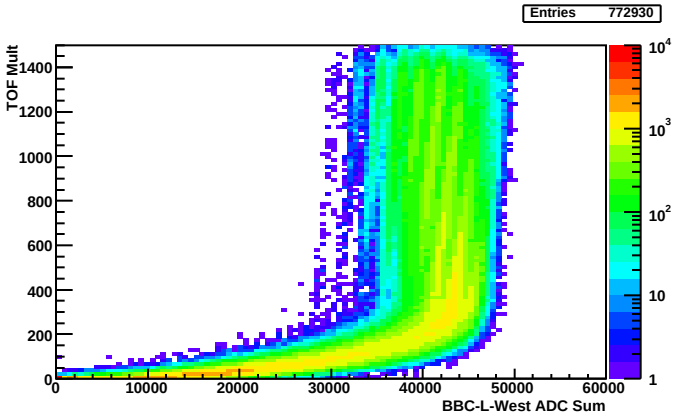

BBQ-VP003 (HI threshold)
Entries 1.236688e+07
ADC
QT Input Channel

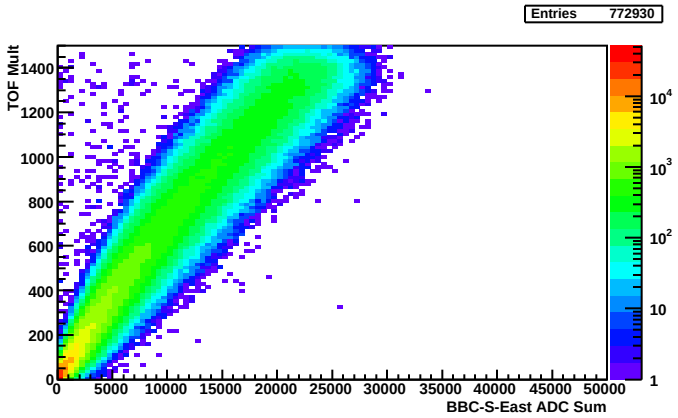

BBQ-VP003 (HI threshold)
Entries 1.236688e+07
TAC
QT Input Channel

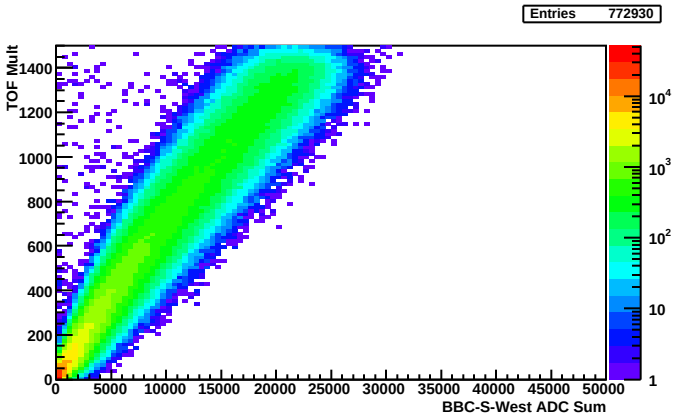

BBQ-VP004 (HI threshold)
Entries 1.236688e+07
ADC
QT Input Channel

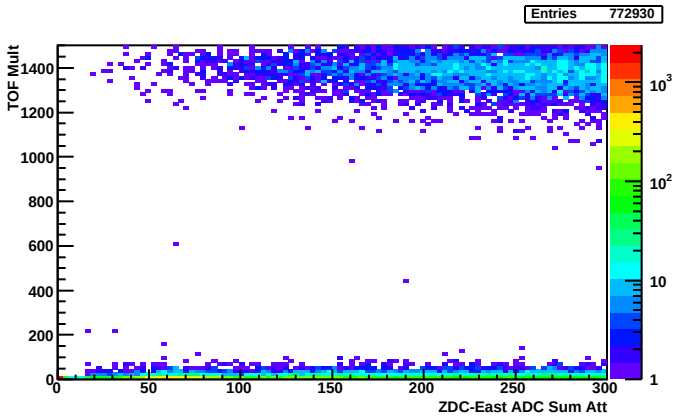

BBQ-VP004 (HI threshold)
Entries 1.236688e+07
TAC
QT Input Channel

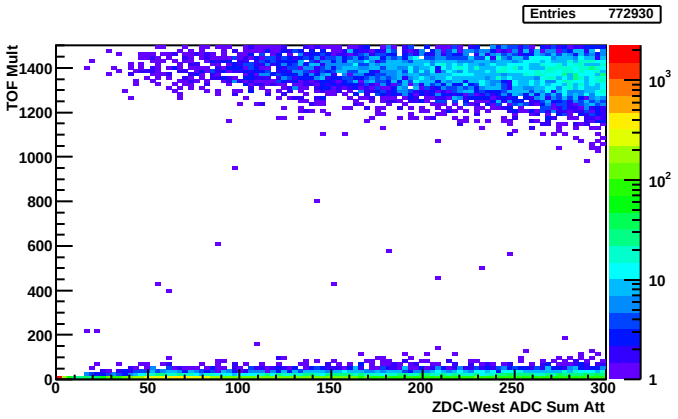

Entries 772930
TOF Mult
BBC-L-East ADC Sum

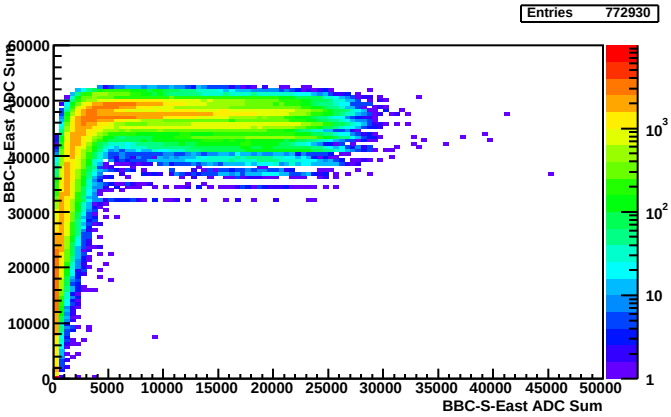

Entries 772930
TOF Mult
BBC-L-West ADC Sum

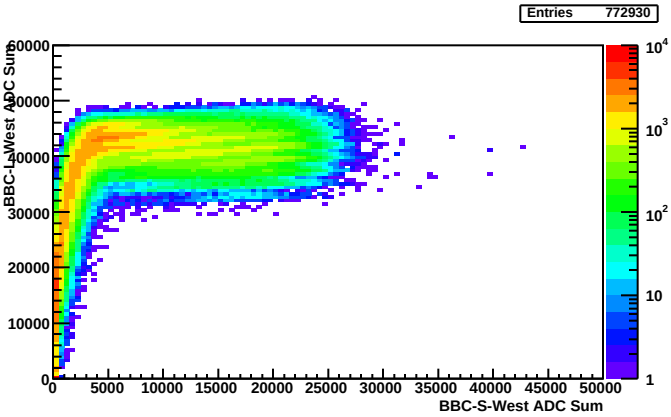

Entries 772930
TOF Mult
BBC-S-East ADC Sum

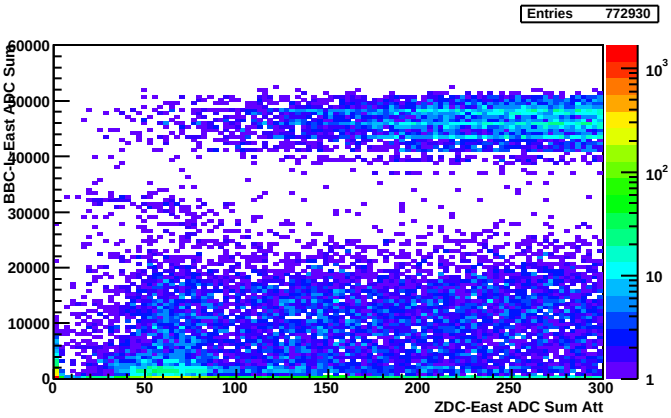

Entries 772930
TOF Mult
BBC-S-West ADC Sum

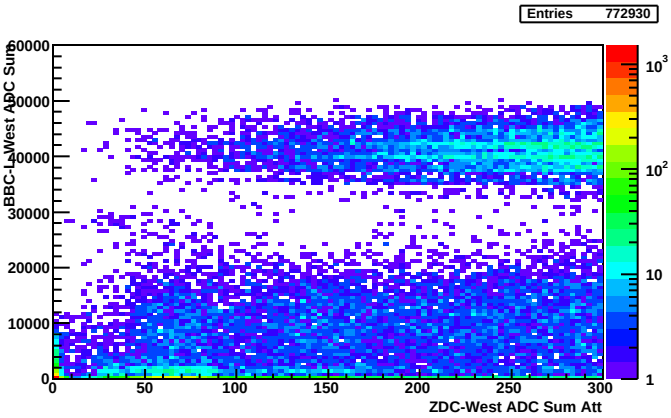

Entries 772930
TOF Mult
ZDC-East ADC Sum Att

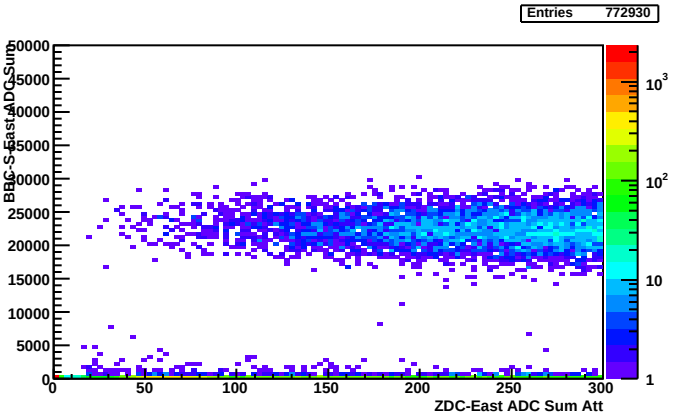

Entries 772930
TOF Mult
ZDC-West ADC Sum Att

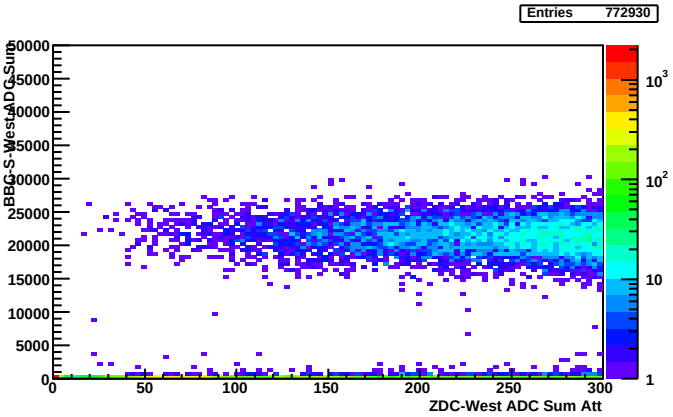

Entries 772930
BBC-L-East ADC Sum
BBC-S-East ADC Sum

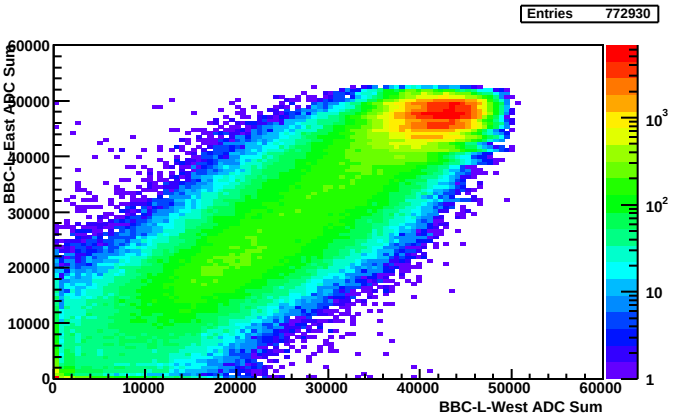

Entries 772930
BBC-L-West ADC Sum
BBC-S-West ADC Sum

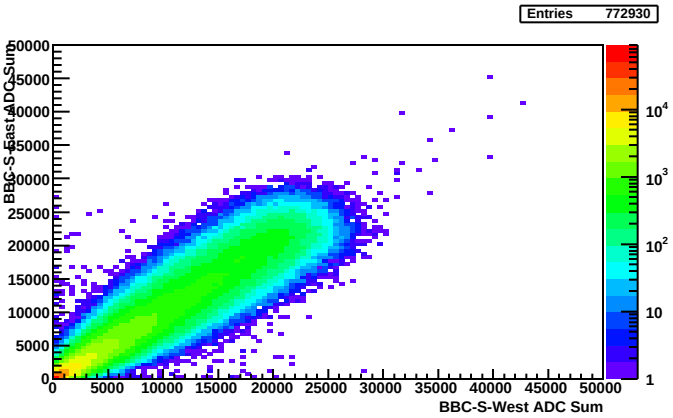

Entries 772930
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

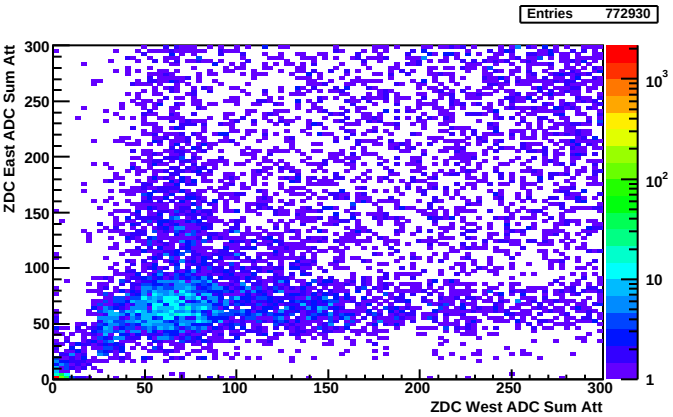

Entries 772930
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

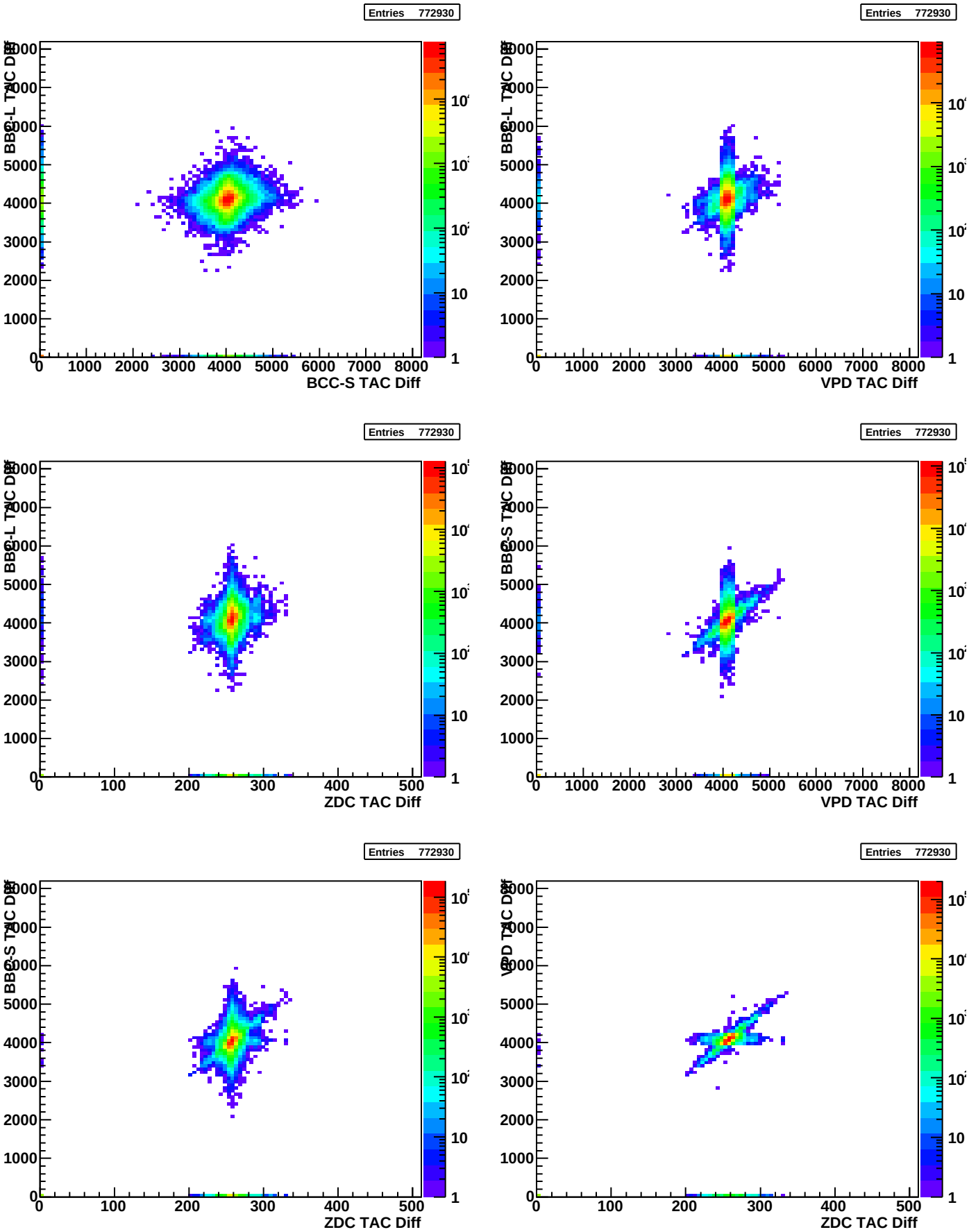

Entries 772930
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

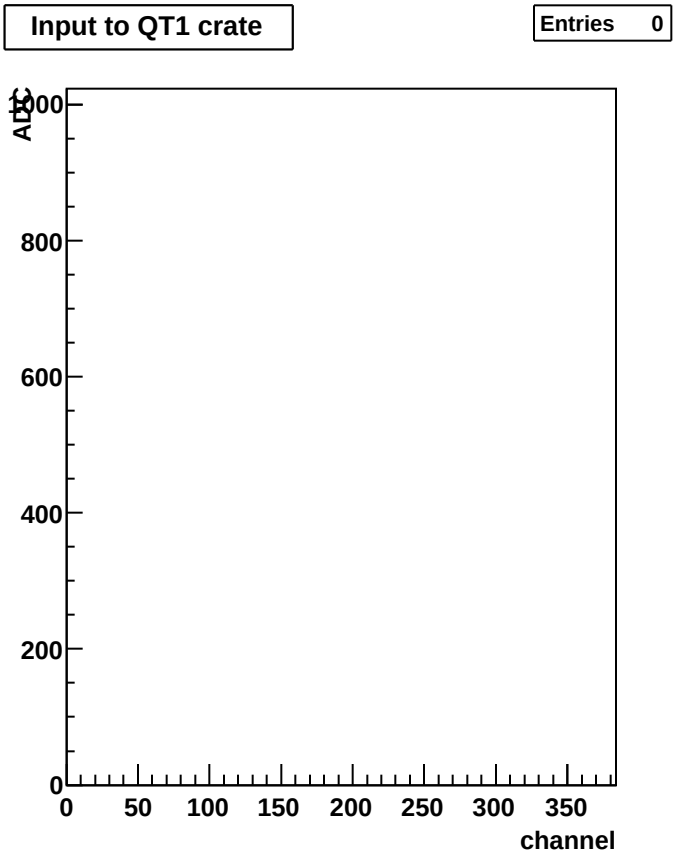

Entries 772930
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

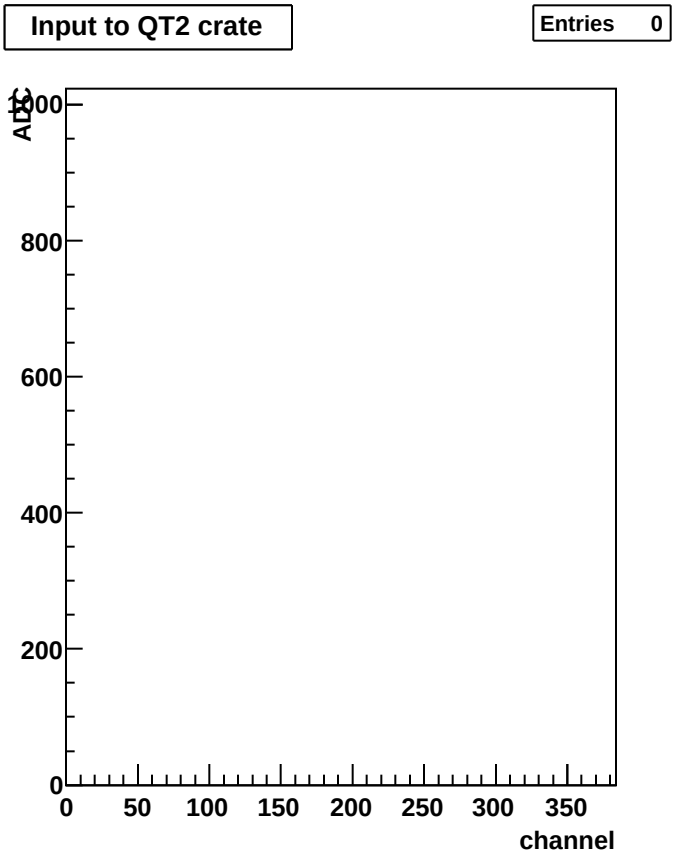

Entries 772930
BBC-L-East ADC Sum
BBC-L-West ADC Sum

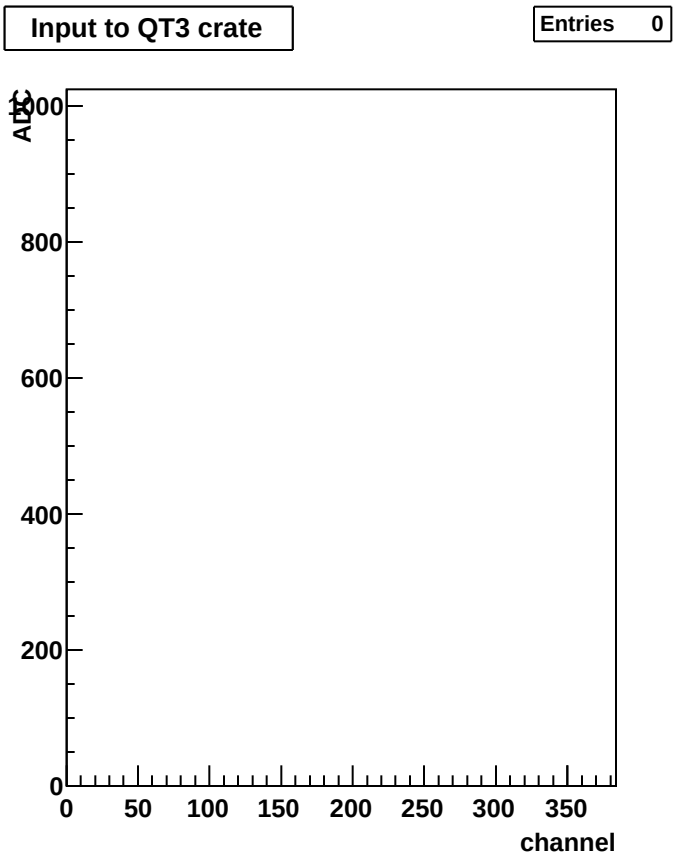

Entries 772930
BBC-S-East ADC Sum
BBC-S-West ADC Sum

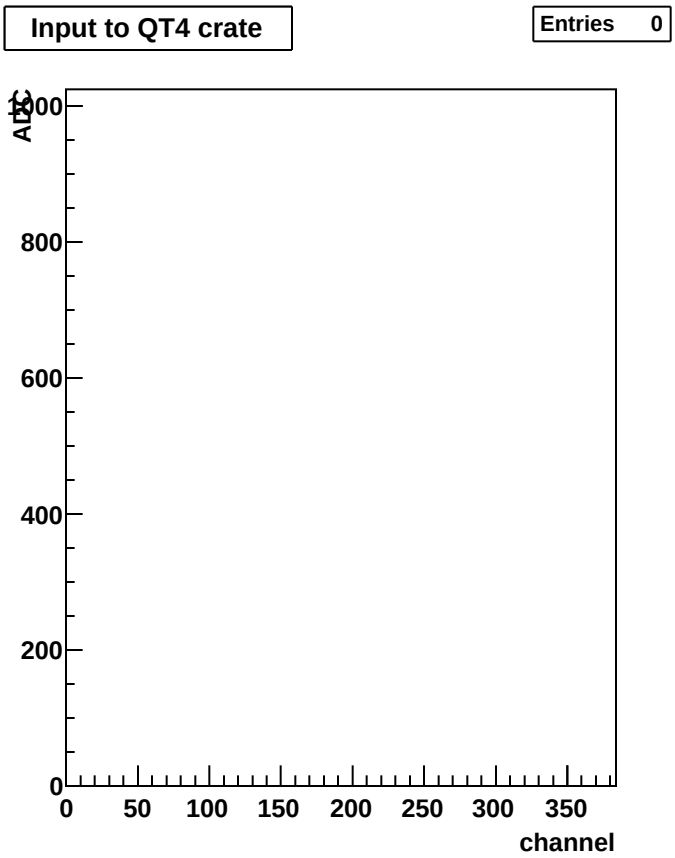

Entries 772930
ZDC East ADC Sum Att
ZDC West ADC Sum Att

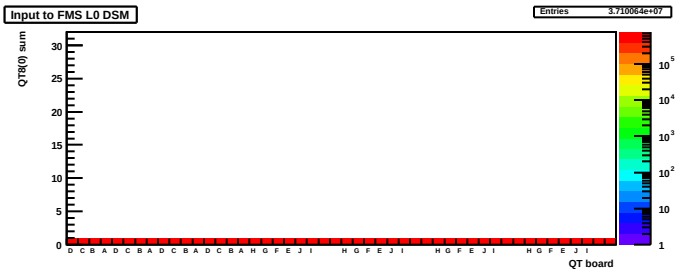

Entries 772930
BBC-L TAC Diff
BCC-S TAC Diff
Entries 772930
BBC-L TAC Diff
VPD TAC Diff
Entries 772930
BBC-L TAC Diff
ZDC TAC Diff
Entries 772930
BBC-S TAC Diff
VPD TAC Diff
Entries 772930
BBC-S TAC Diff
ZDC TAC Diff
Entries 772930
VPD TAC Diff
ZDC TAC Diff

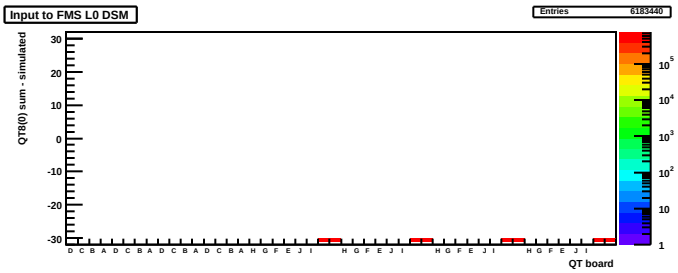

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

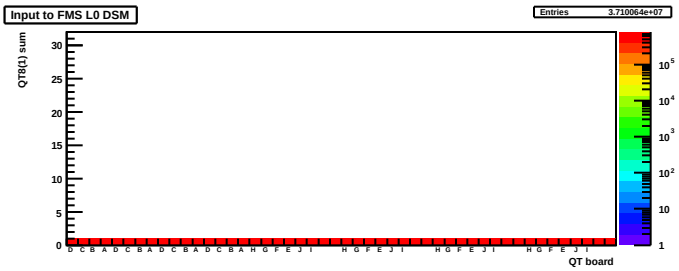

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

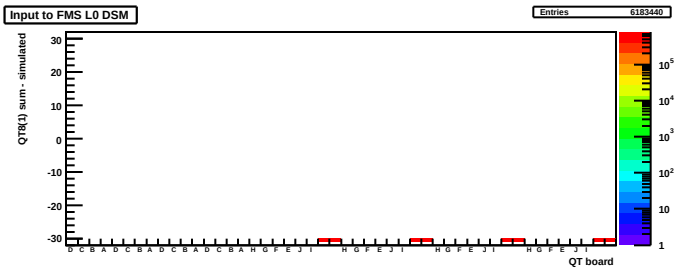

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

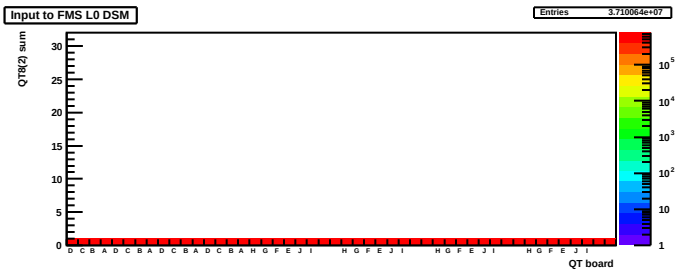

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

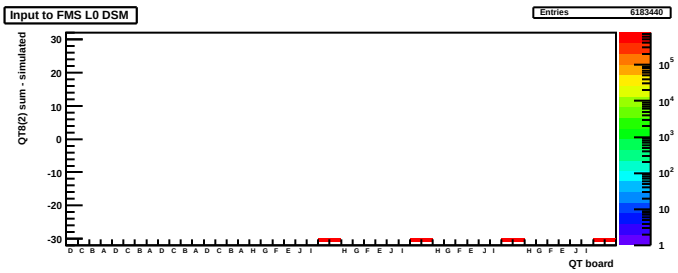

Input to FMS L0 DSM
Entries 3.710064e+07
QT8(0) sum
QT board

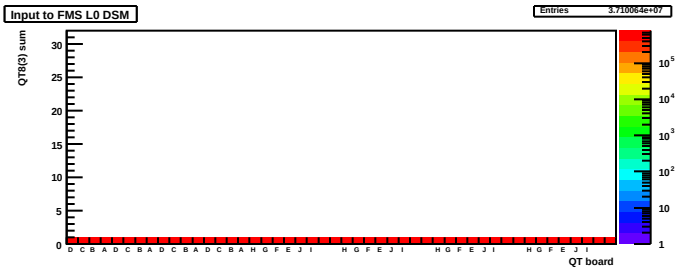

Input to FMS L0 DSM
Entries 6183440
QT8(0) sum - simulated
QT board

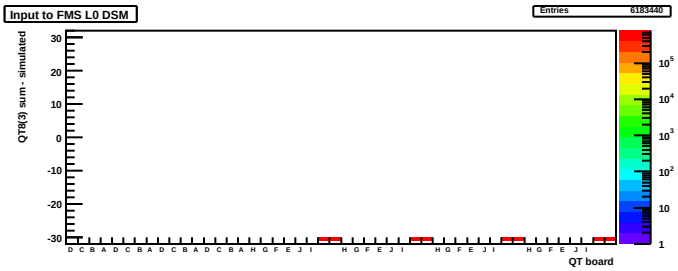

Input to FMS L0 DSM
Entries 3.710064e+07
QT8(1) sum
QT board

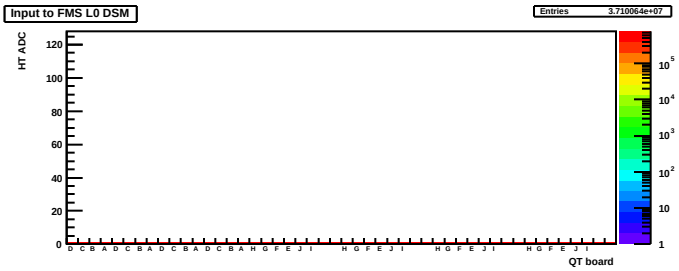

Input to FMS L0 DSM
Entries 6183440
QT8(1) sum - simulated
QT board

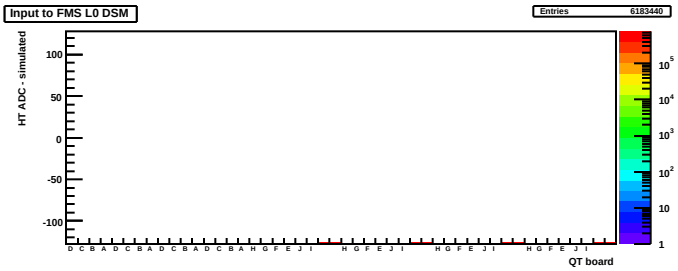

Input to FMS L0 DSM
Entries 3.710064e+07
QT8(2) sum
QT board

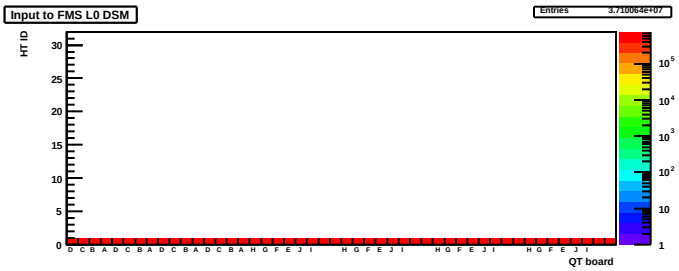

Input to FMS L0 DSM
Entries 6183440
QT8(2) sum - simulated
QT board

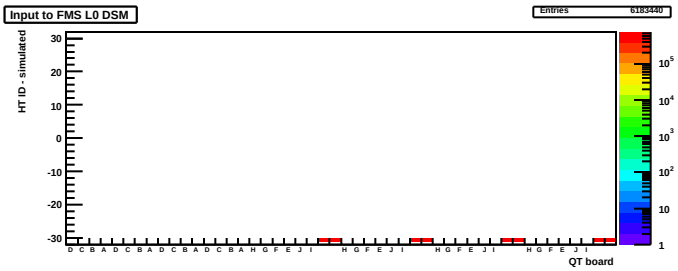

Input to FMS L0 DSM
Entries 3.710064e+07
QT8(3) sum
QT board

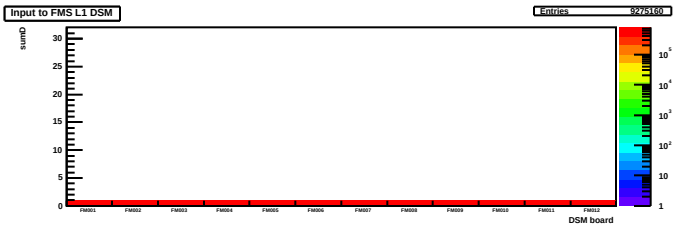

Input to FMS L0 DSM
Entries 6183440
QT8(3) sum - simulated
QT board

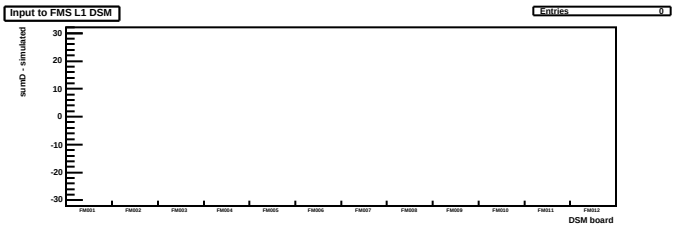

Input to FMS L0 DSM
Entries 3.710064e+07
HT ADC
QT board

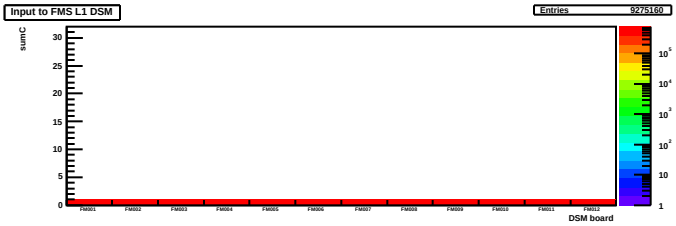

Input to FMS L0 DSM
Entries 6183440
HT ADC - simulated
QT board

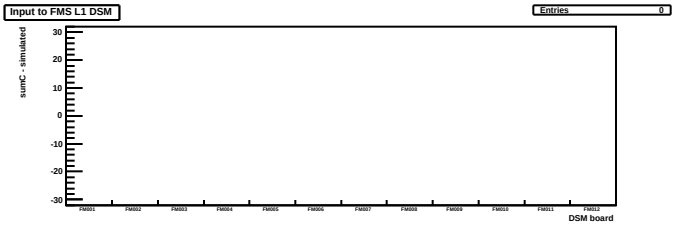

Input to FMS L0 DSM
Entries 3.710064e+07
HT ID
QT board

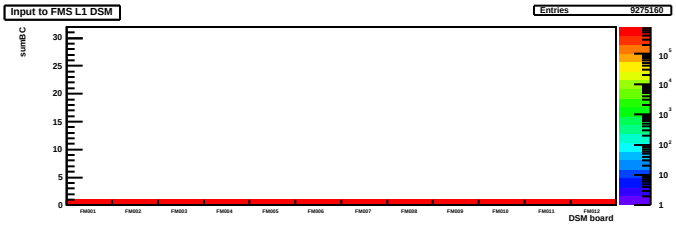

Input to FMS L0 DSM
Entries 6183440
HT ID - simulated
QT board

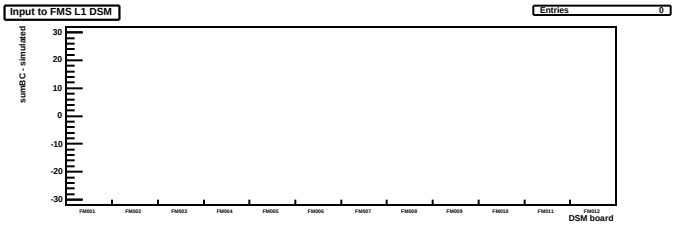

Input to FMS L1 DSM
Entries 9275160
sumD
DSM board

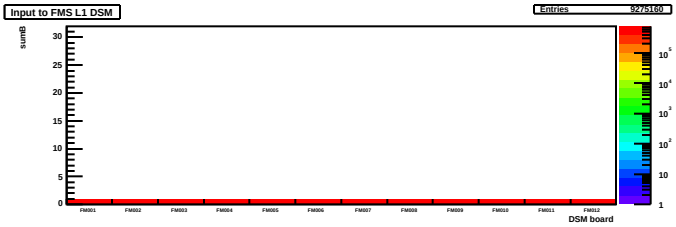

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

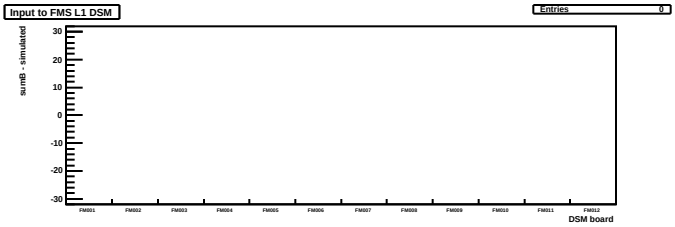

Input to FMS L1 DSM
Entries 9275160
sumC
DSM board

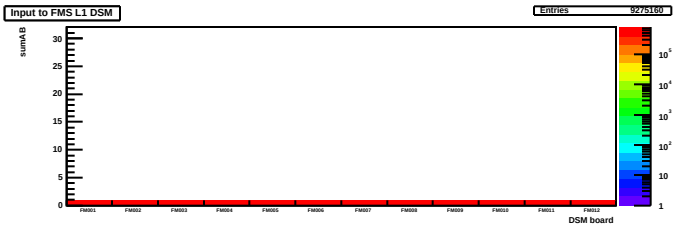

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

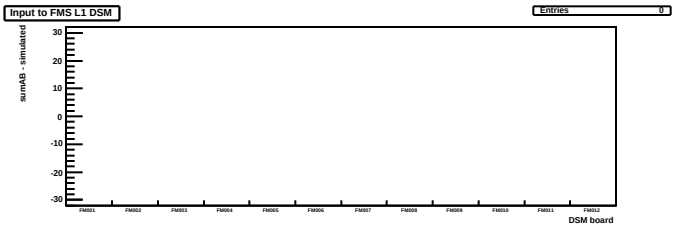

Input to FMS L1 DSM
Entries 9275160
sumBC
DSM board

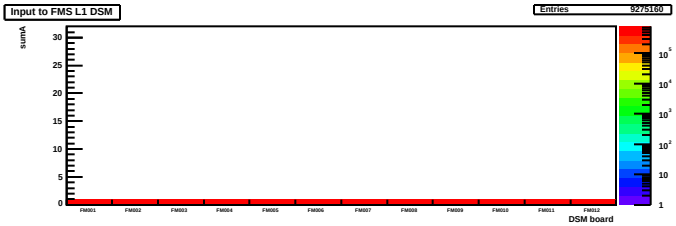

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

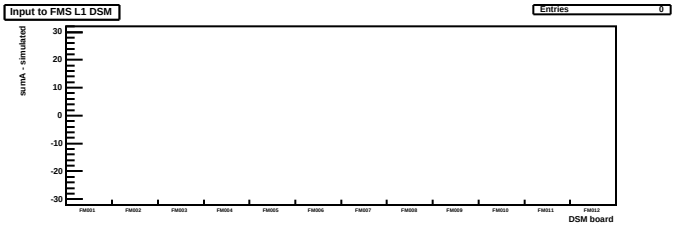

Input to FMS L1 DSM
Entries 9275160
sumB
DSM board

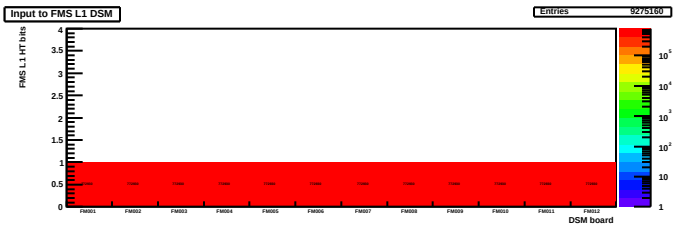

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

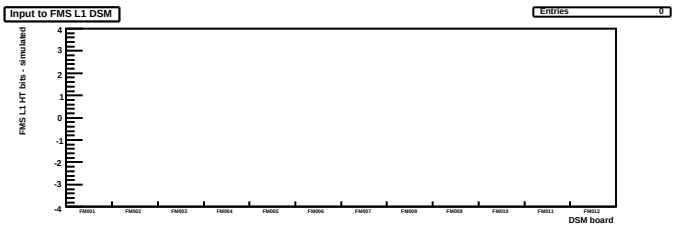

Input to FMS L1 DSM
Entries 9275160
sumAB
DSM board

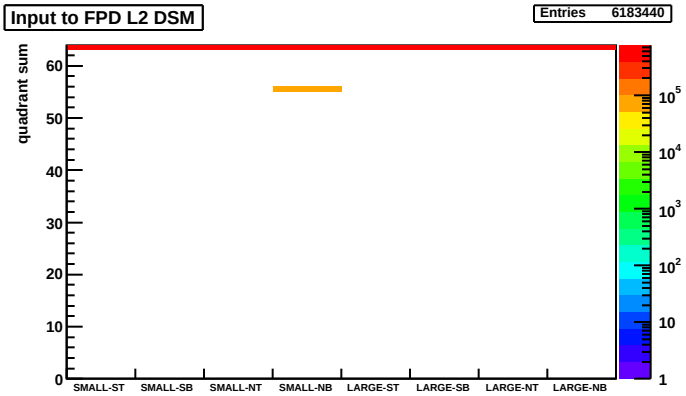

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

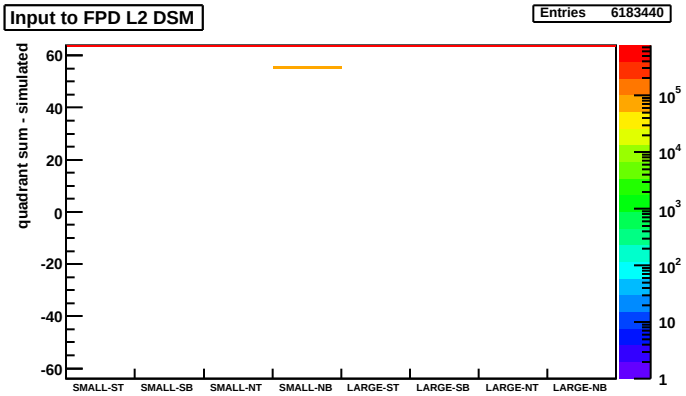

Input to FMS L1 DSM
Entries 9275160
sumA
DSM board

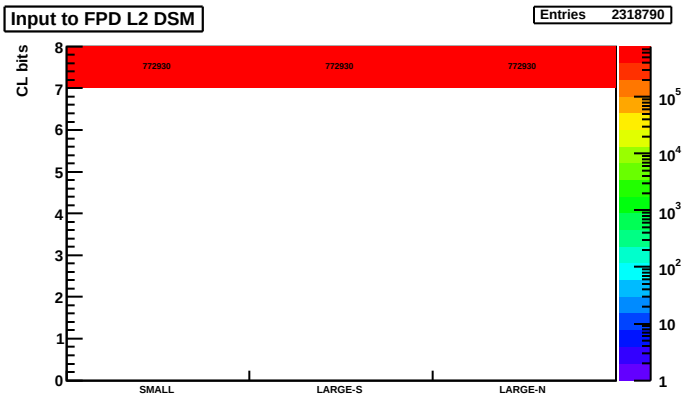

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

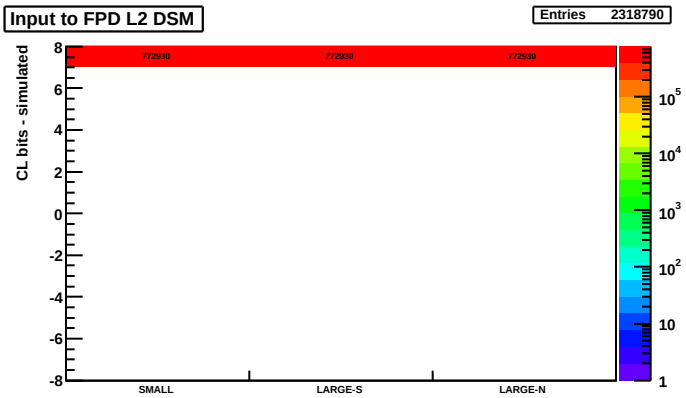

Input to FMS L1 DSM
Entries 9275160
FMS L1 HT bits
DSM board

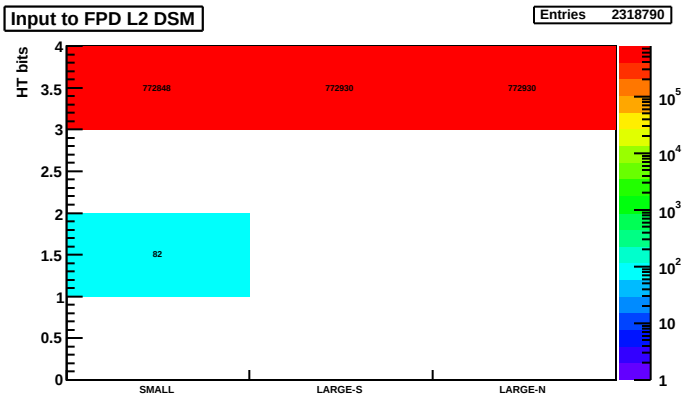

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

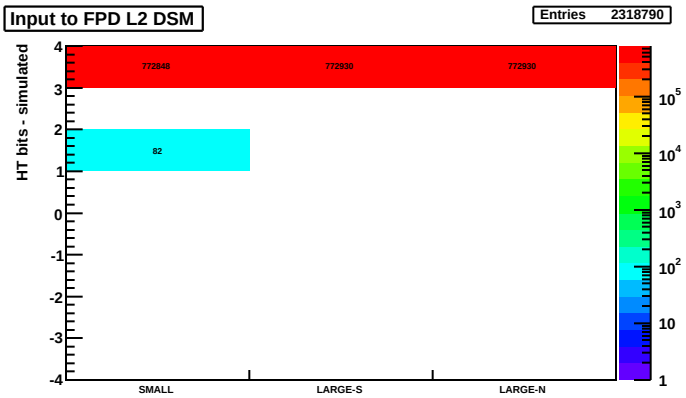

Input to FPD L2 DSM
Entries 6183440
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

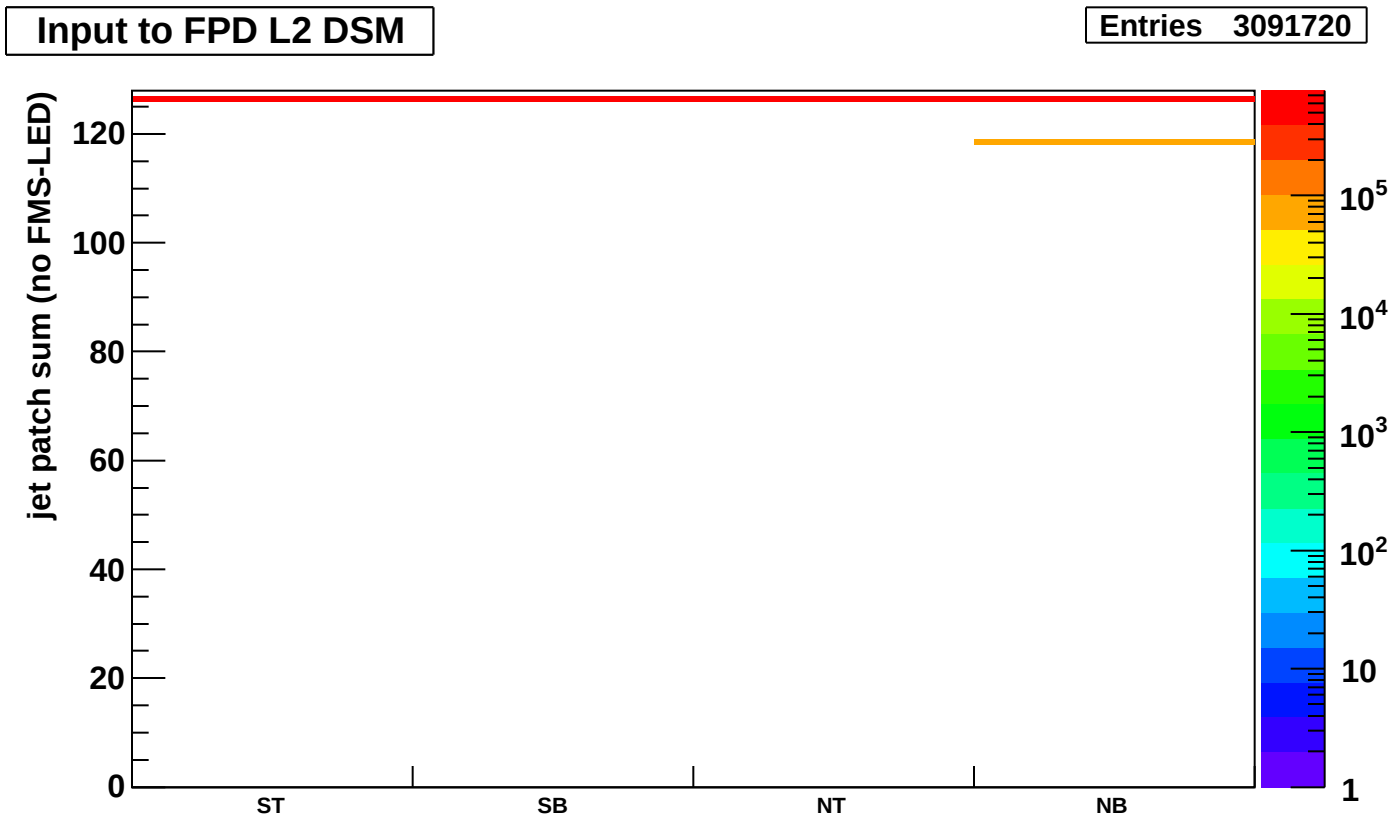

Input to FPD L2 DSM
Entries 6183440
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

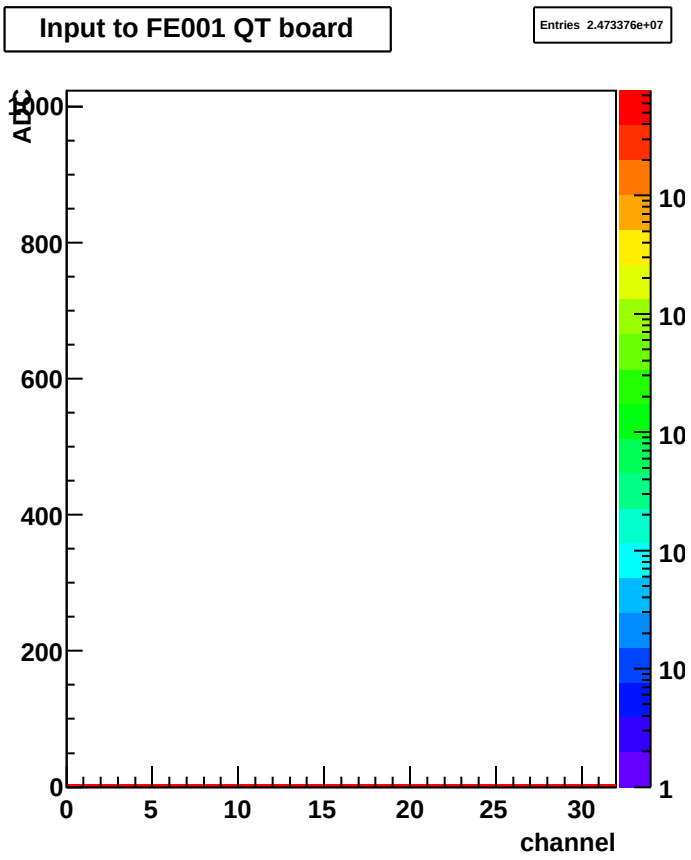

Input to FPD L2 DSM
Entries 2318790
CL bits
772930
772930
772930
SMALL
LARGE-S
LARGE-N

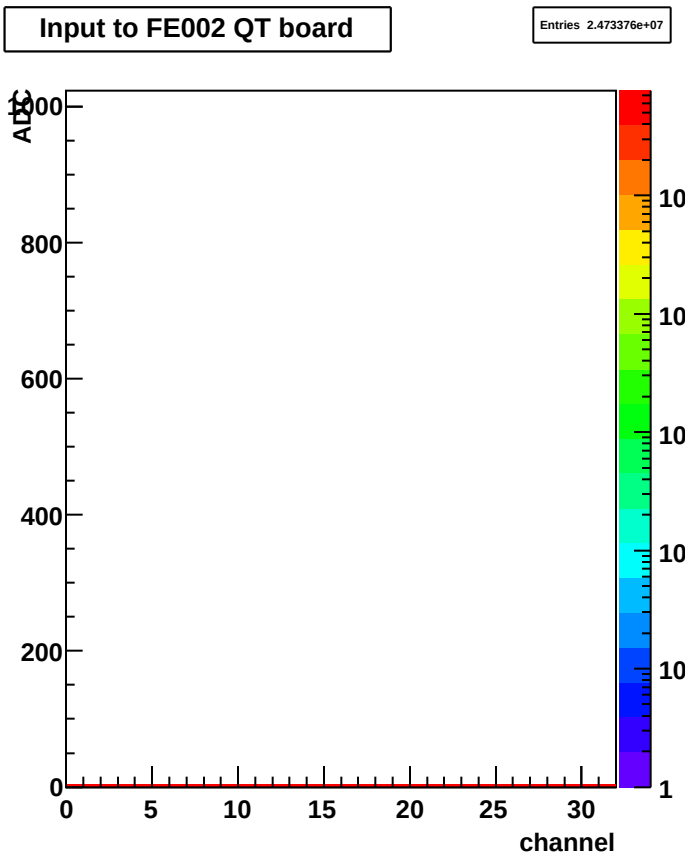

Input to FPD L2 DSM
Entries 2318790
CL bits - simulated
772930
772930
772930
SMALL
LARGE-S
LARGE-N

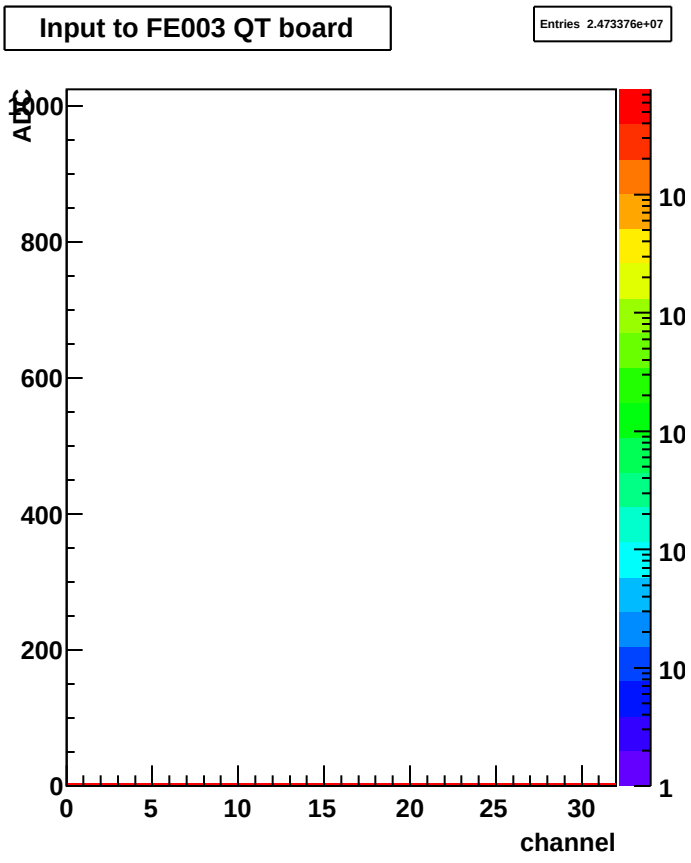

Input to FPD L2 DSM
Entries 2318790
HT bits
772848
772930
772930
82
SMALL
LARGE-S
LARGE-N

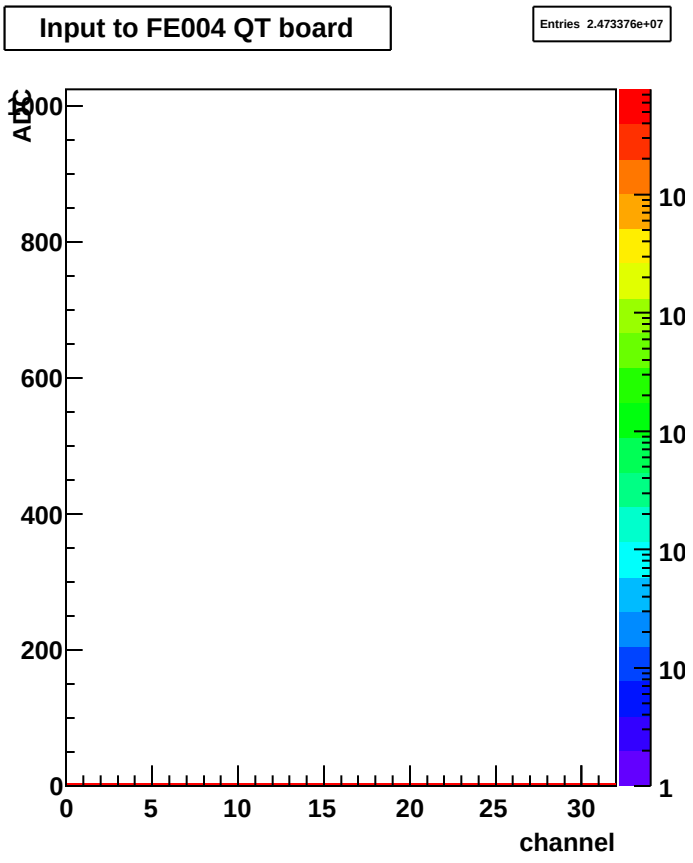

Input to FPD L2 DSM
Entries 2318790
HT bits - simulated
772848
772930
772930
82
SMALL
LARGE-S
LARGE-N


Input to FPD L2 DSM
Entries 3091720
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

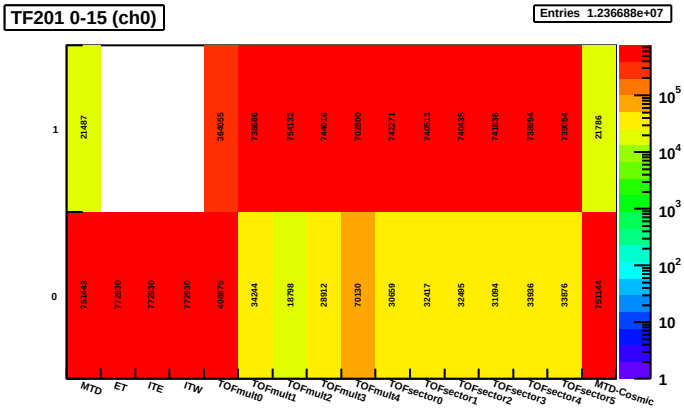

Input to FE001 QT board
Entries 2.473376e+07
ADC
channel


Input to FE002 QT board
Entries 2.473376e+07
ADC
channel

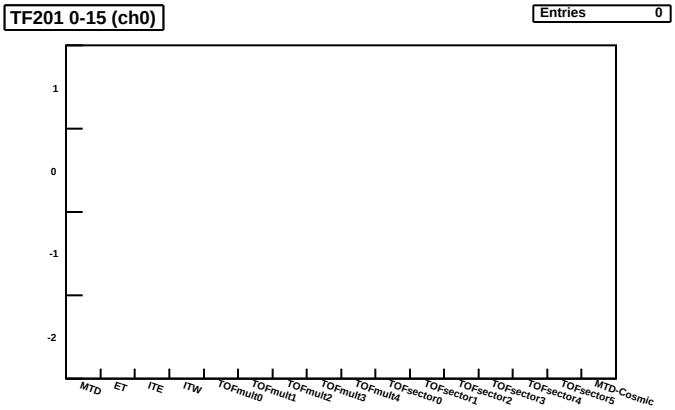

Input to FE003 QT board
Entries 2.473376e+07
ADC
channel


Input to FE004 QT board
Entries 2.473376e+07
ADC
channel

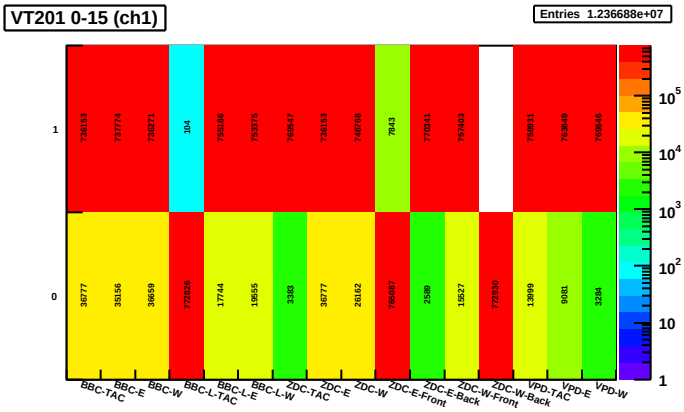
TF201 0-15 (ch0)



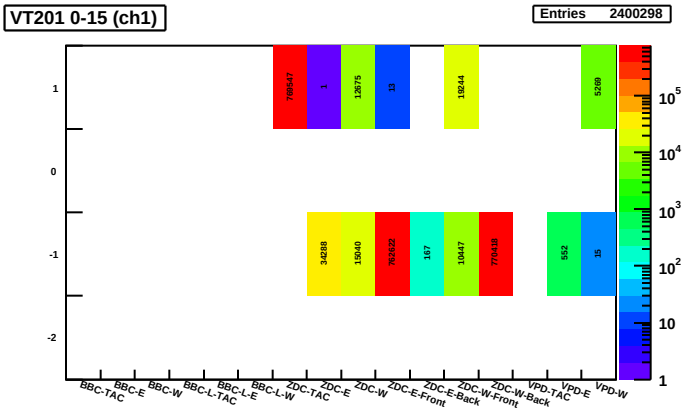
TF201 0-15 (ch0)



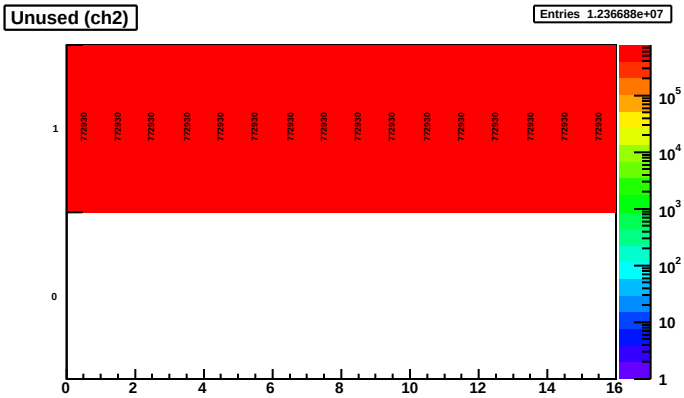
VT201 0-15 (ch1)

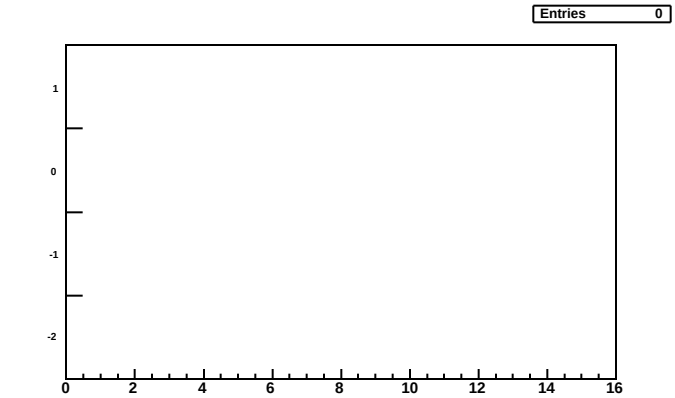


VT201 0-15 (ch1)

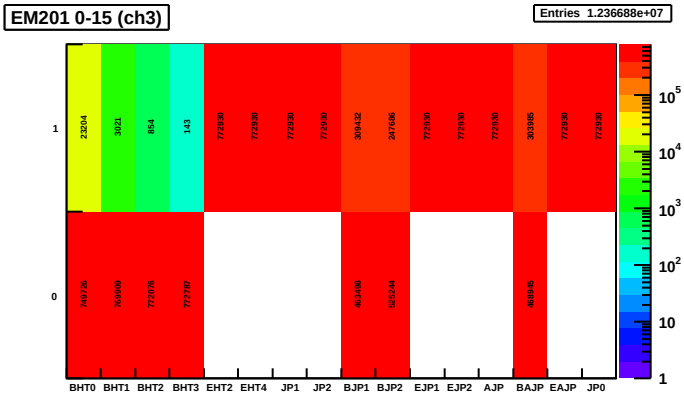


Unused (ch2)

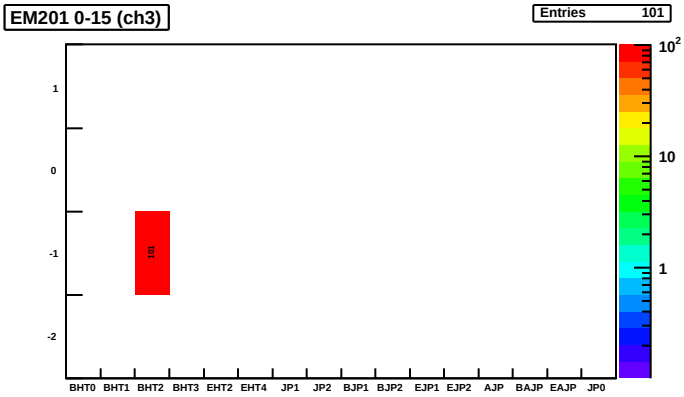




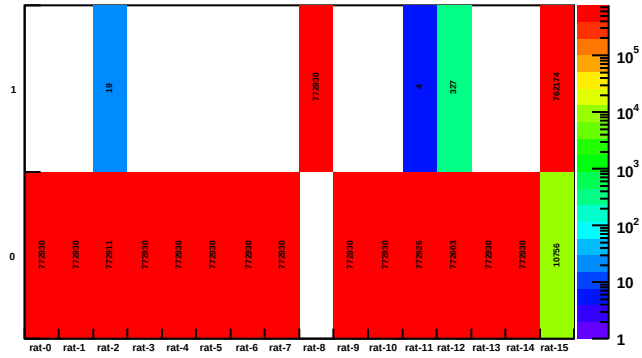
EM201 0-15 (ch3)

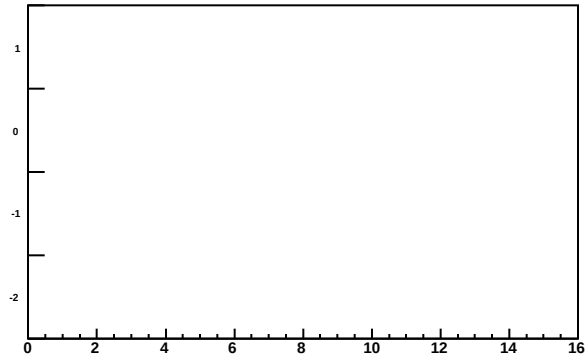


EM201 0-15 (ch3)

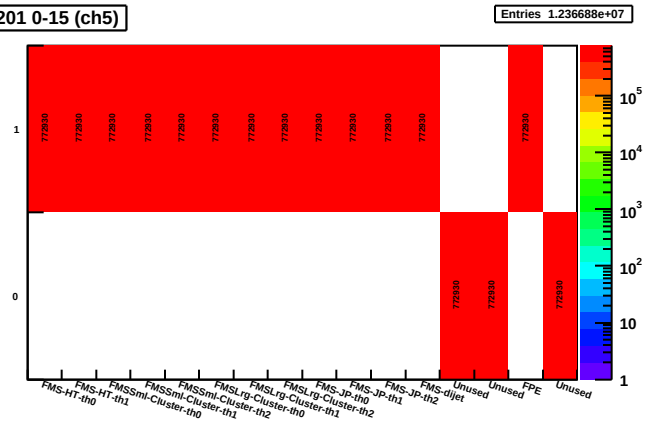


RAT board (ch4)

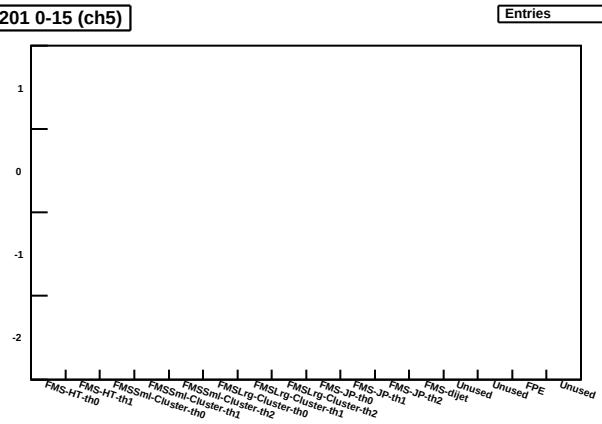




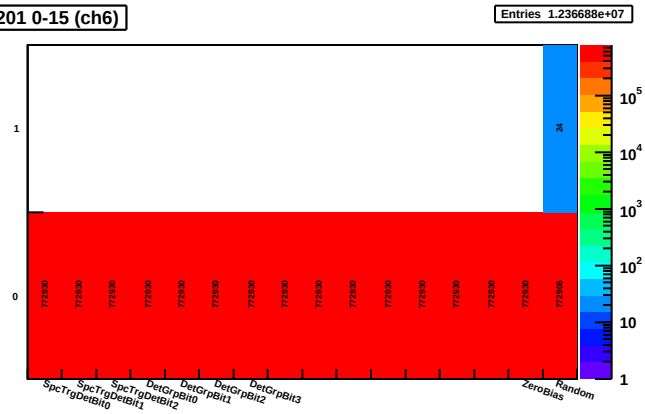
FP201 0-15 (ch5)

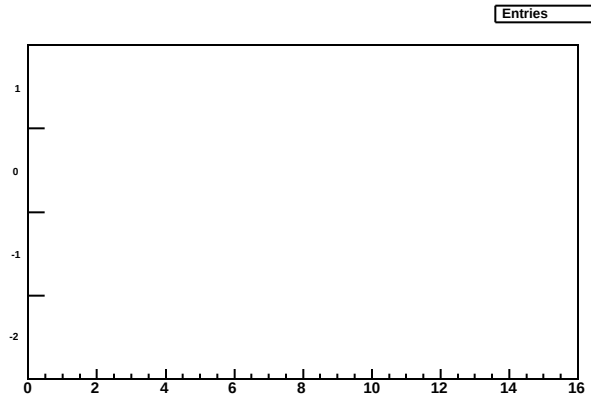
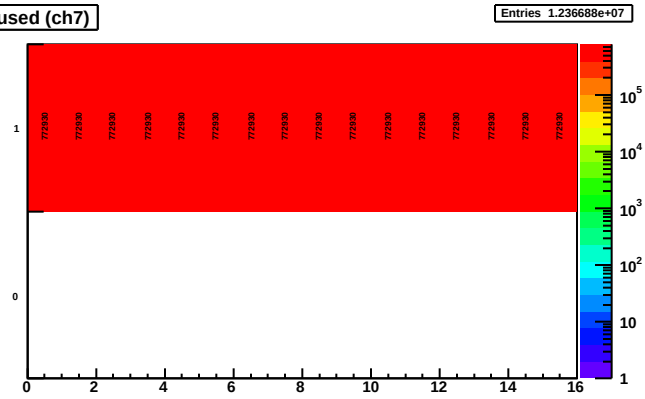
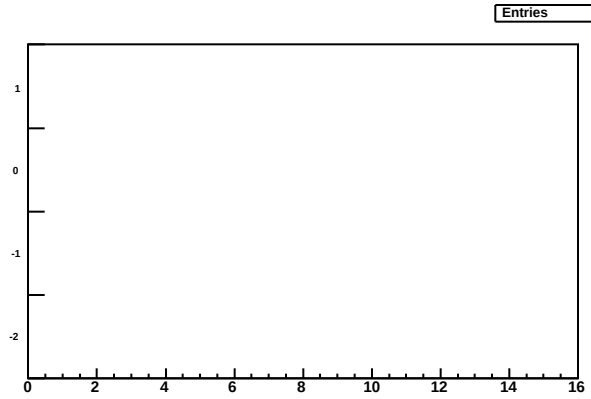


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)