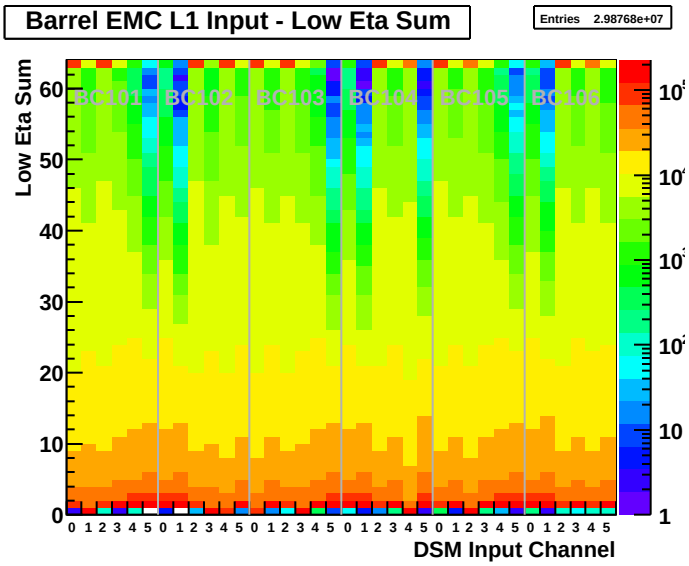
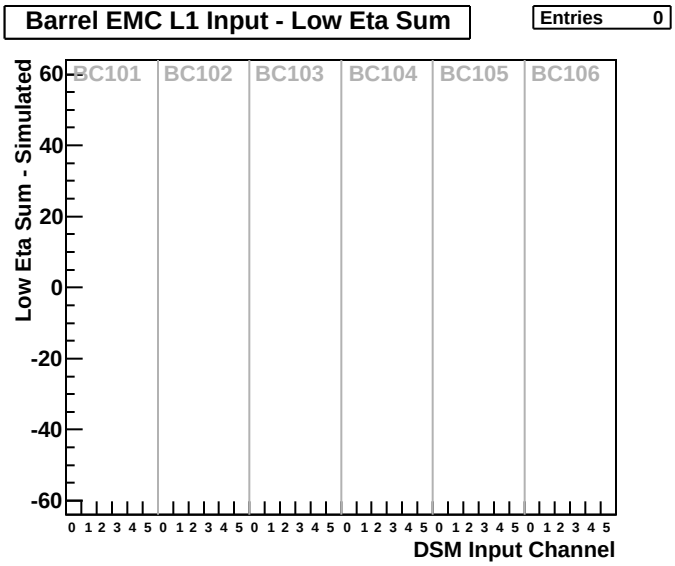
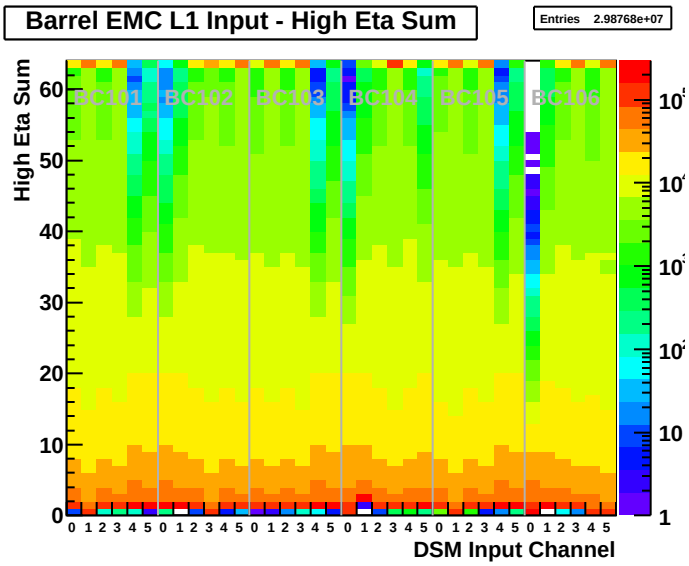
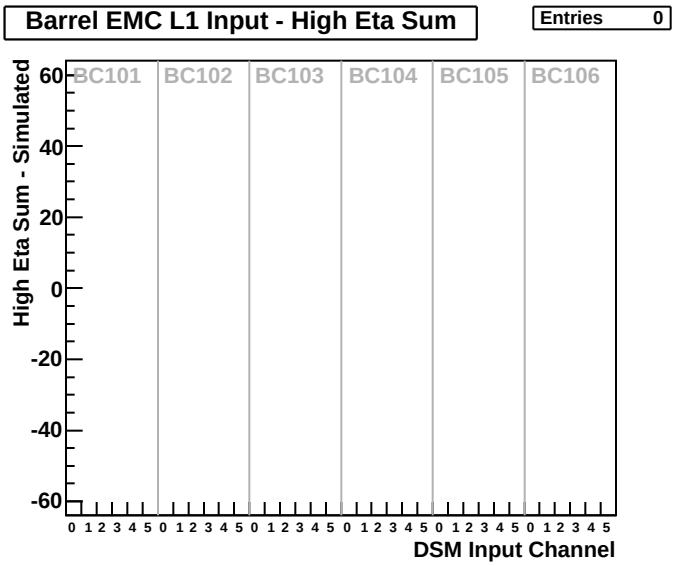
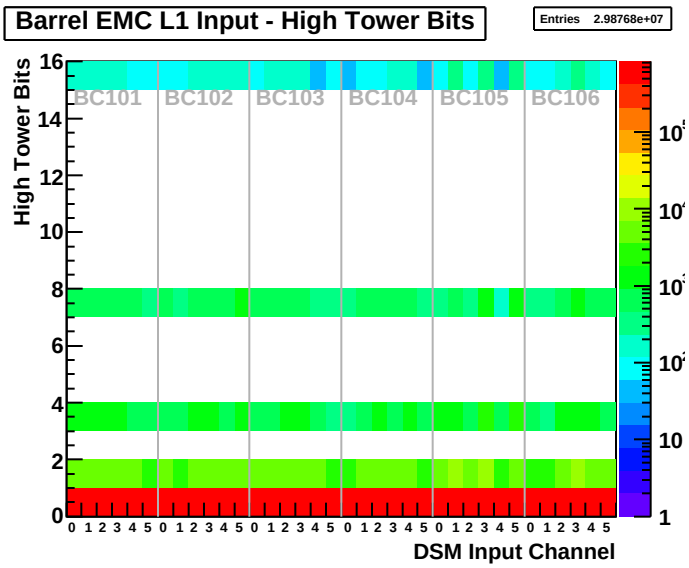
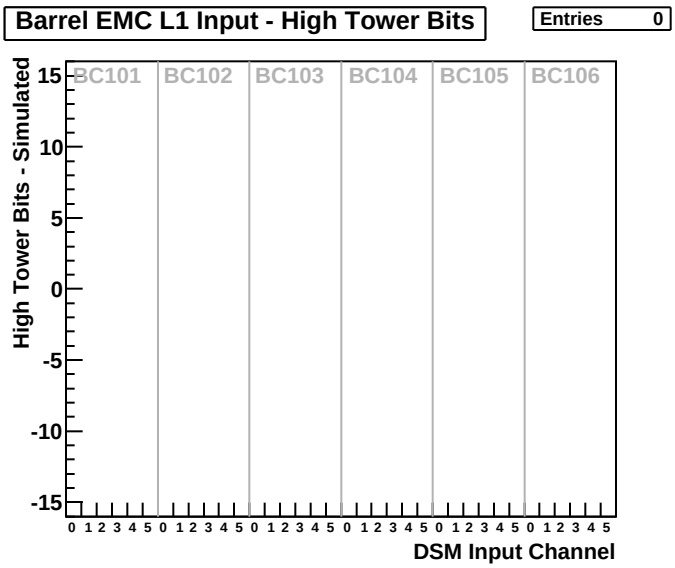

Barrel EMC L1 Input - Low Eta Sum
Entries 2.98768e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

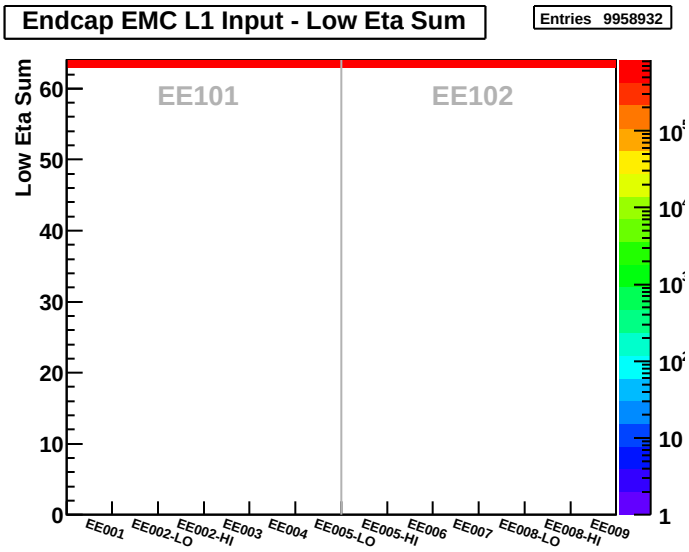

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

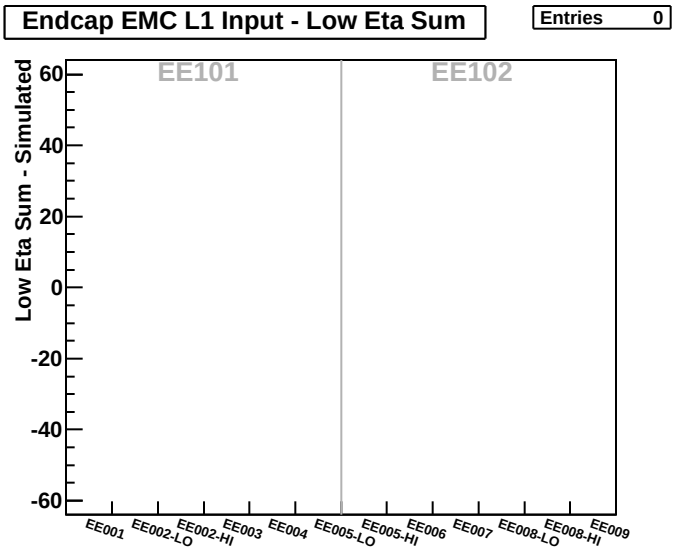

Barrel EMC L1 Input - High Eta Sum
Entries 2.98768e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

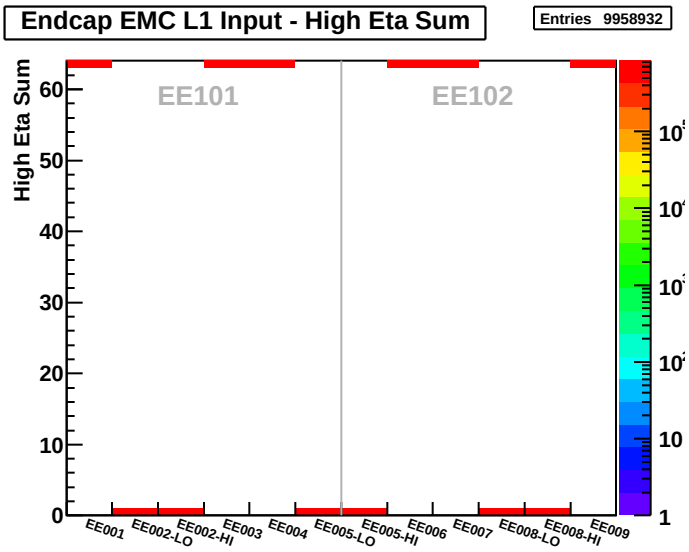

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

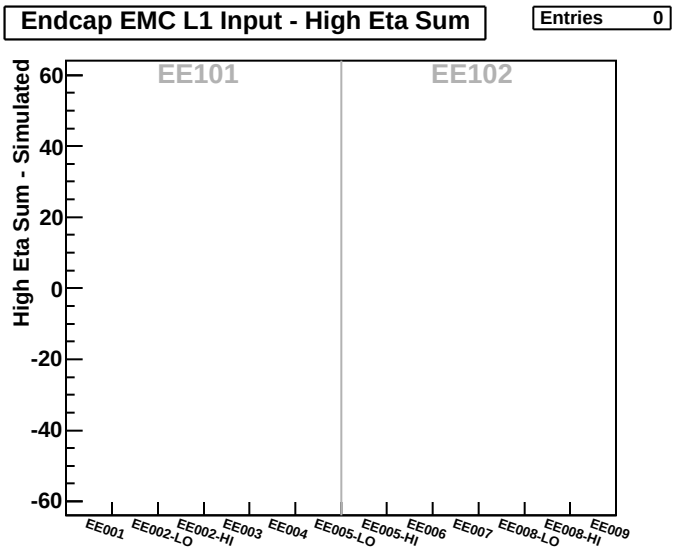

Barrel EMC L1 Input - High Tower Bits
Entries 2.98768e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

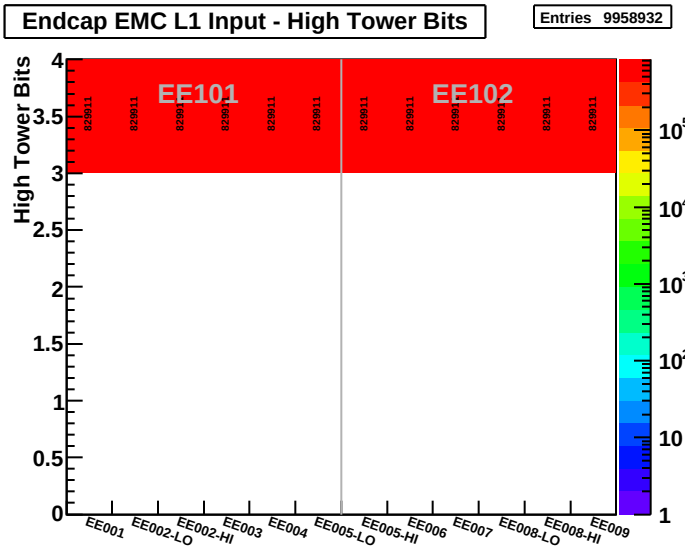

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

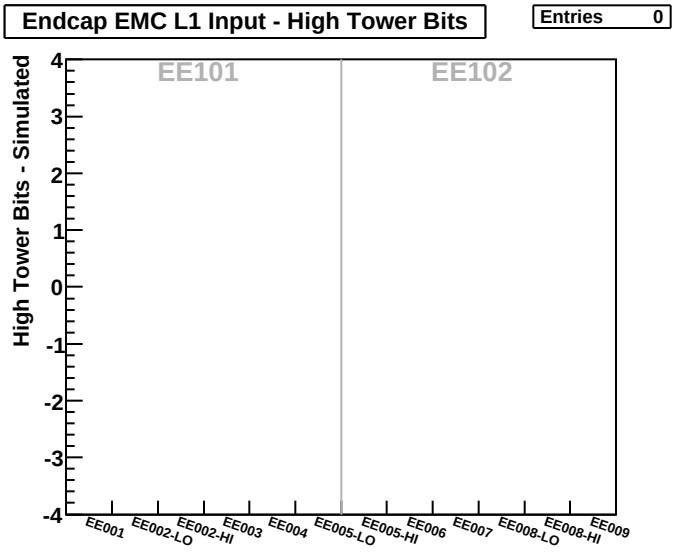

Endcap EMC L1 Input - Low Eta Sum
Entries 9958932
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

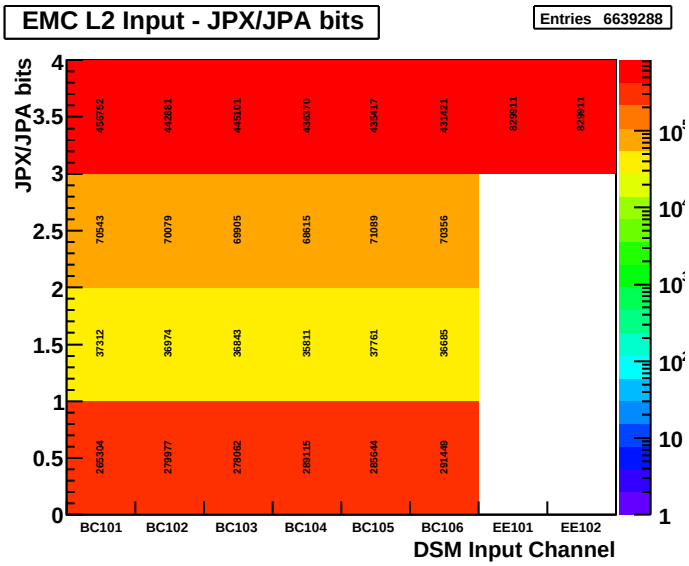

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

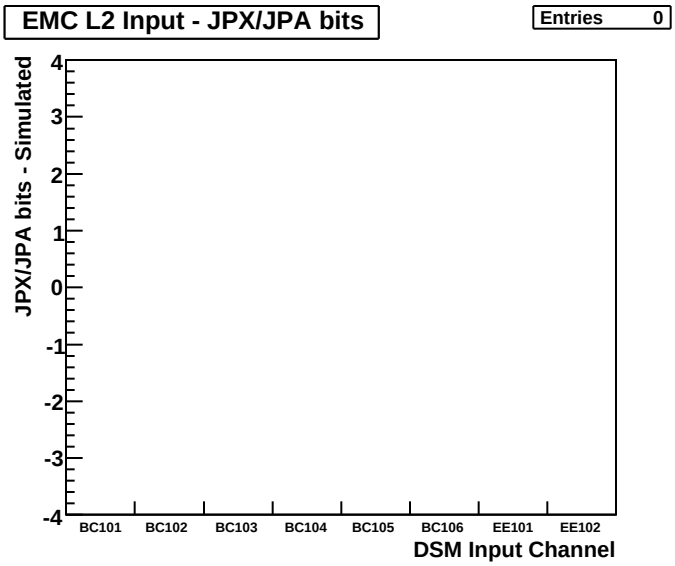

Endcap EMC L1 Input - High Eta Sum
Entries 9958932
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

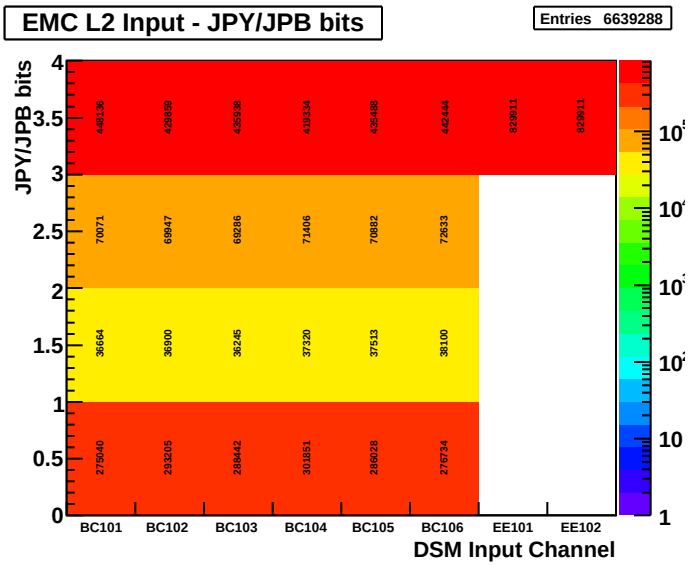

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

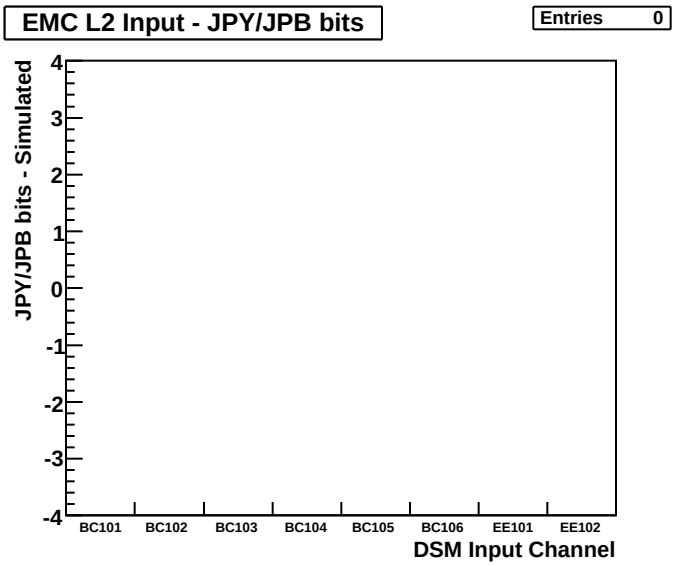

Endcap EMC L1 Input - High Tower Bits
Entries 9958932
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

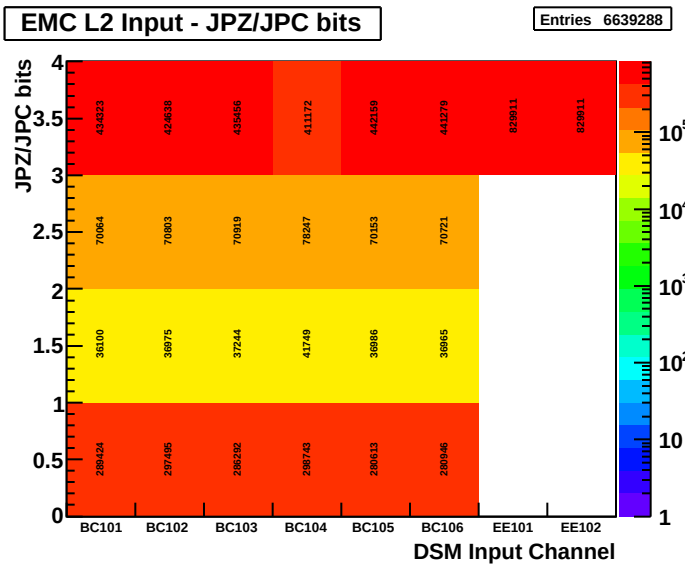

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

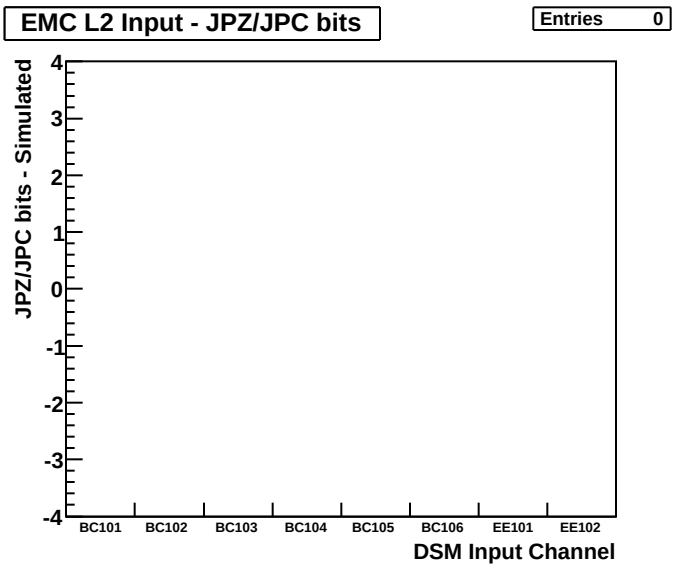

EMC L2 Input - JPX/JPA bits
Entries 6639288
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
456752 442881 445101 436370 435417 431421 829911 829911
70543 70079 69905 68615 71089 70356
37312 36974 36843 35811 37761 36685
265304 279977 278062 289115 285644 291449

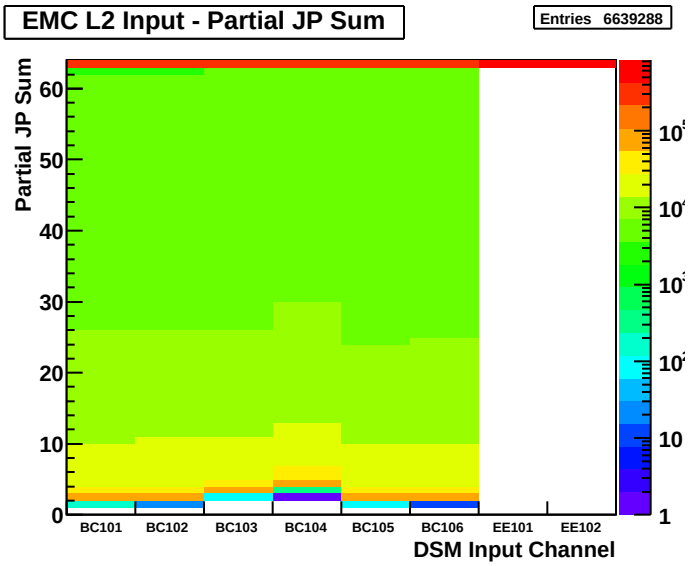

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

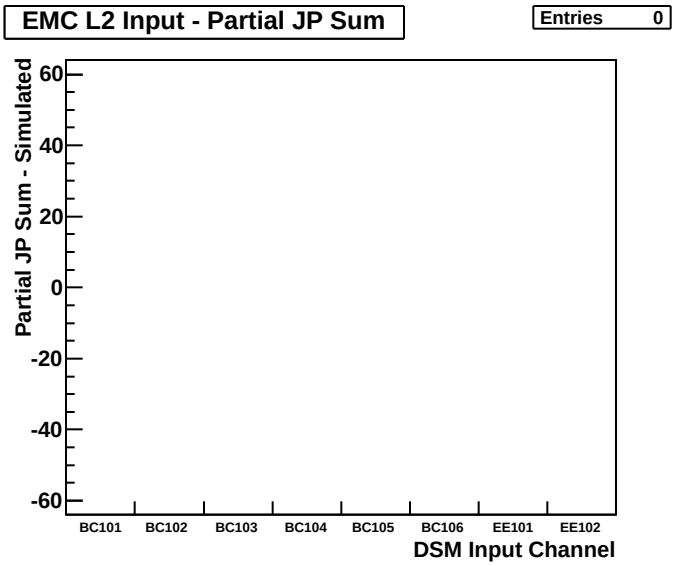

EMC L2 Input - JPY/JPB bits
Entries 6639288
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
448136 429859 435938 419334 435488 442444 829911 829911
70071 69947 69286 73406 70882 72633
36664 36900 36245 37320 37513 38100
275040 293205 288442 301851 286028 276734

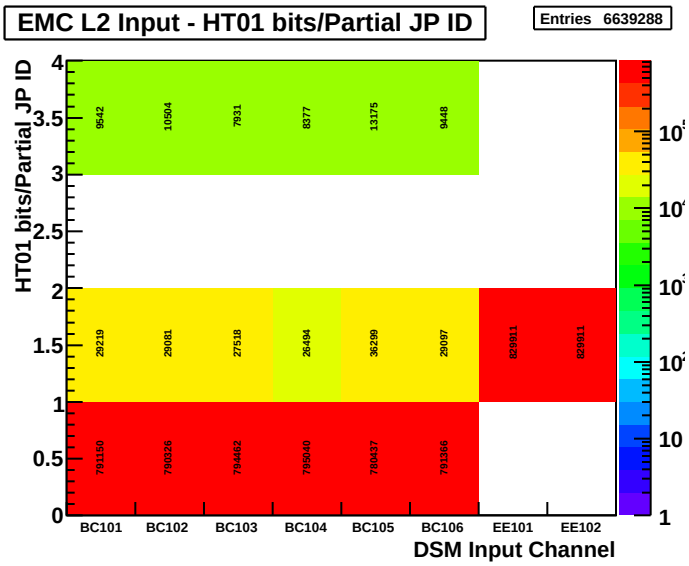

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

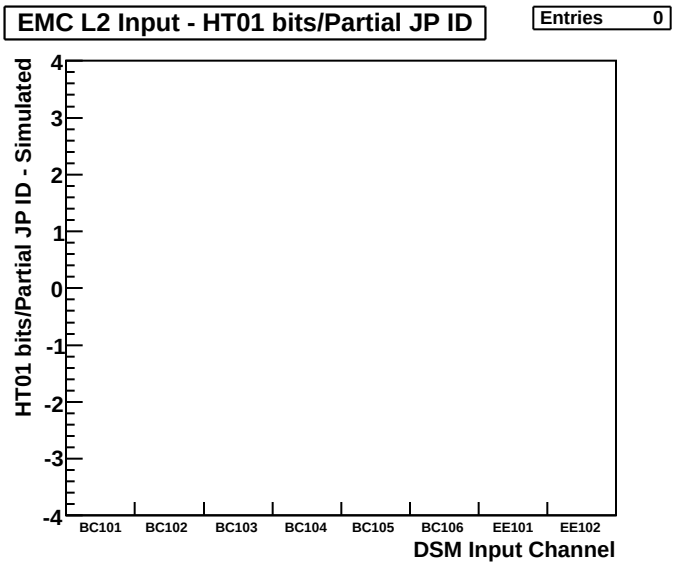

EMC L2 Input - JPZ/JPC bits
Entries 6639288
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
434323 424638 435456 411172 442159 441279 829911 829911
70064 70803 70919 78247 70153 70721
36100 36975 37244 41749 36986 36965
289424 297495 286292 298743 280613 280946

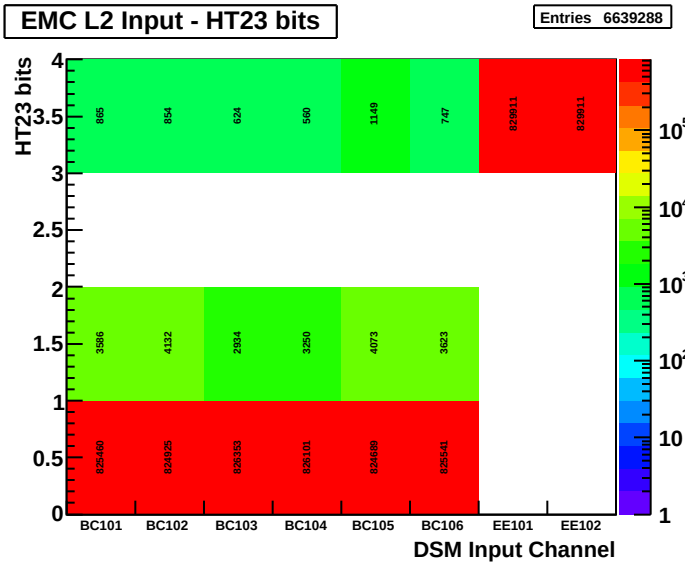

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

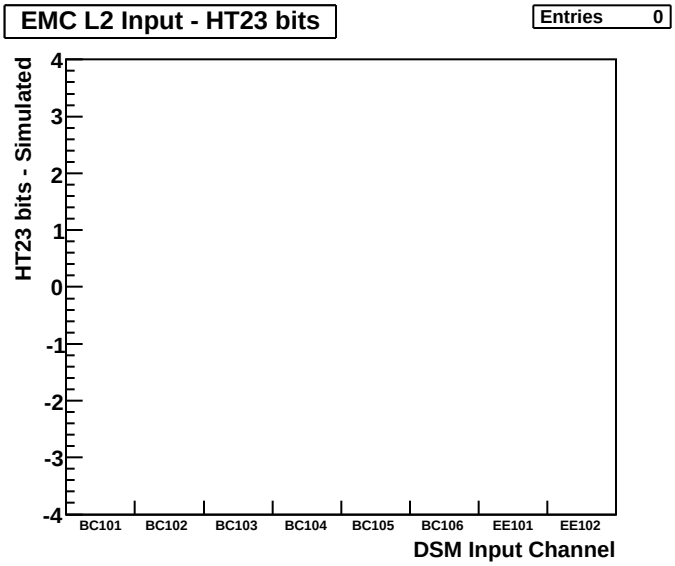

EMC L2 Input - Partial JP Sum
Entries 6639288
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

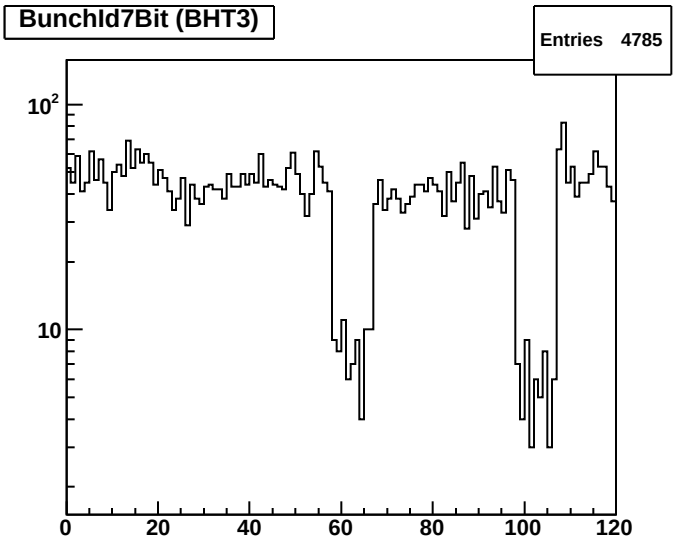

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

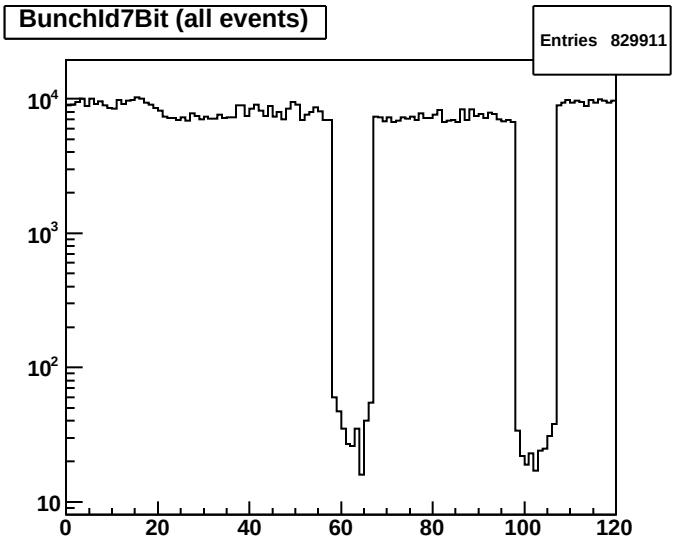

EMC L2 Input - HT01 bits/Partial JP ID
Entries 6639288
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

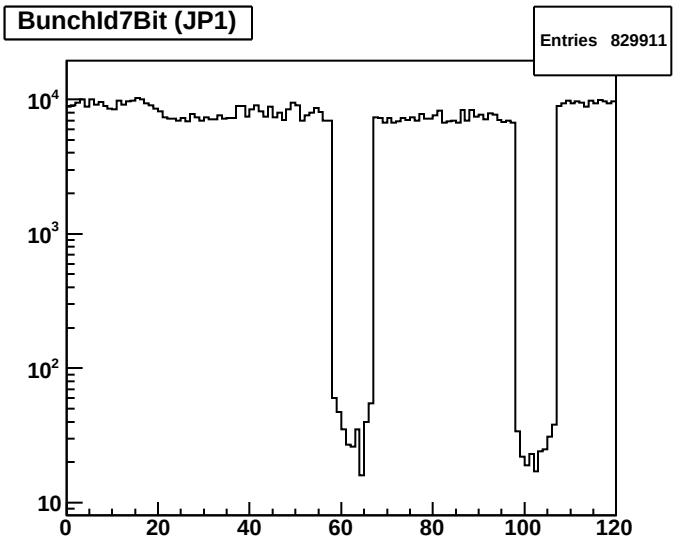

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

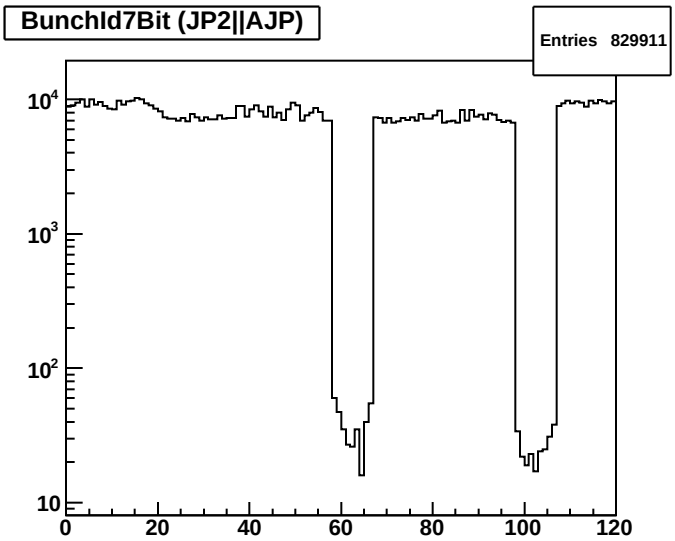

EMC L2 Input - HT23 bits
Entries 6639288
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

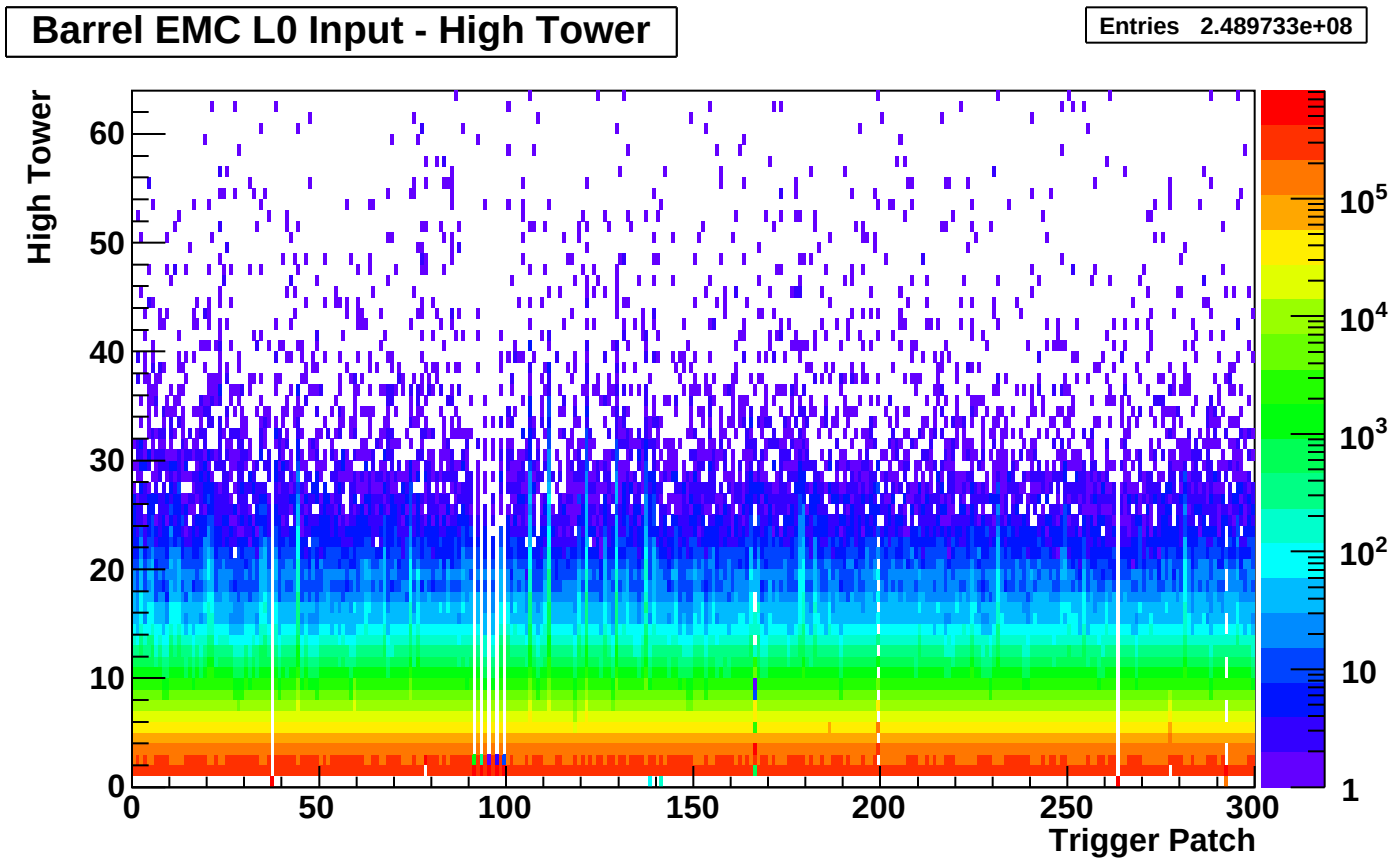

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

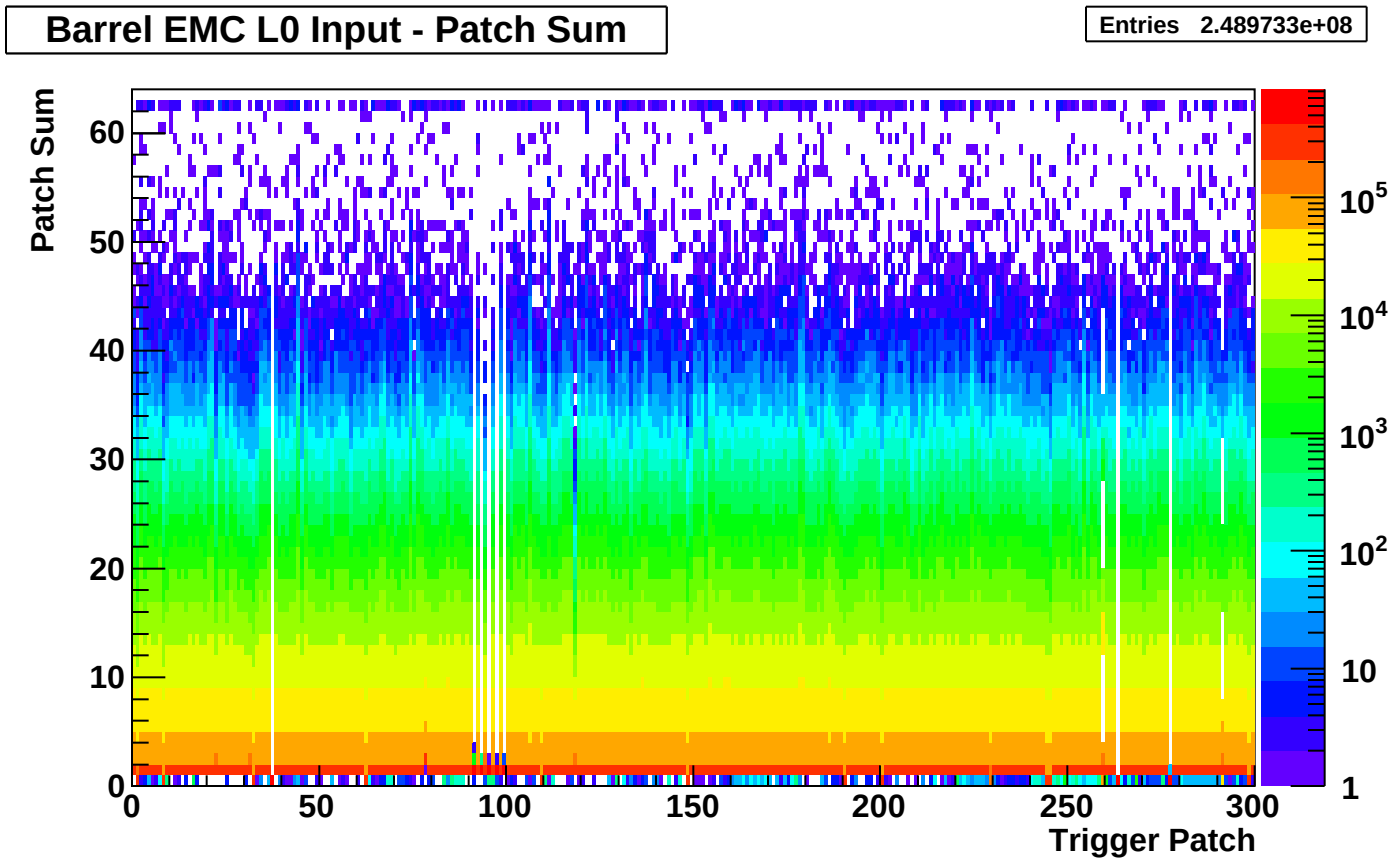

BunchId7Bit (BHT3)
Entries 4785
10^2
10
0
20
40
60
80
100
120

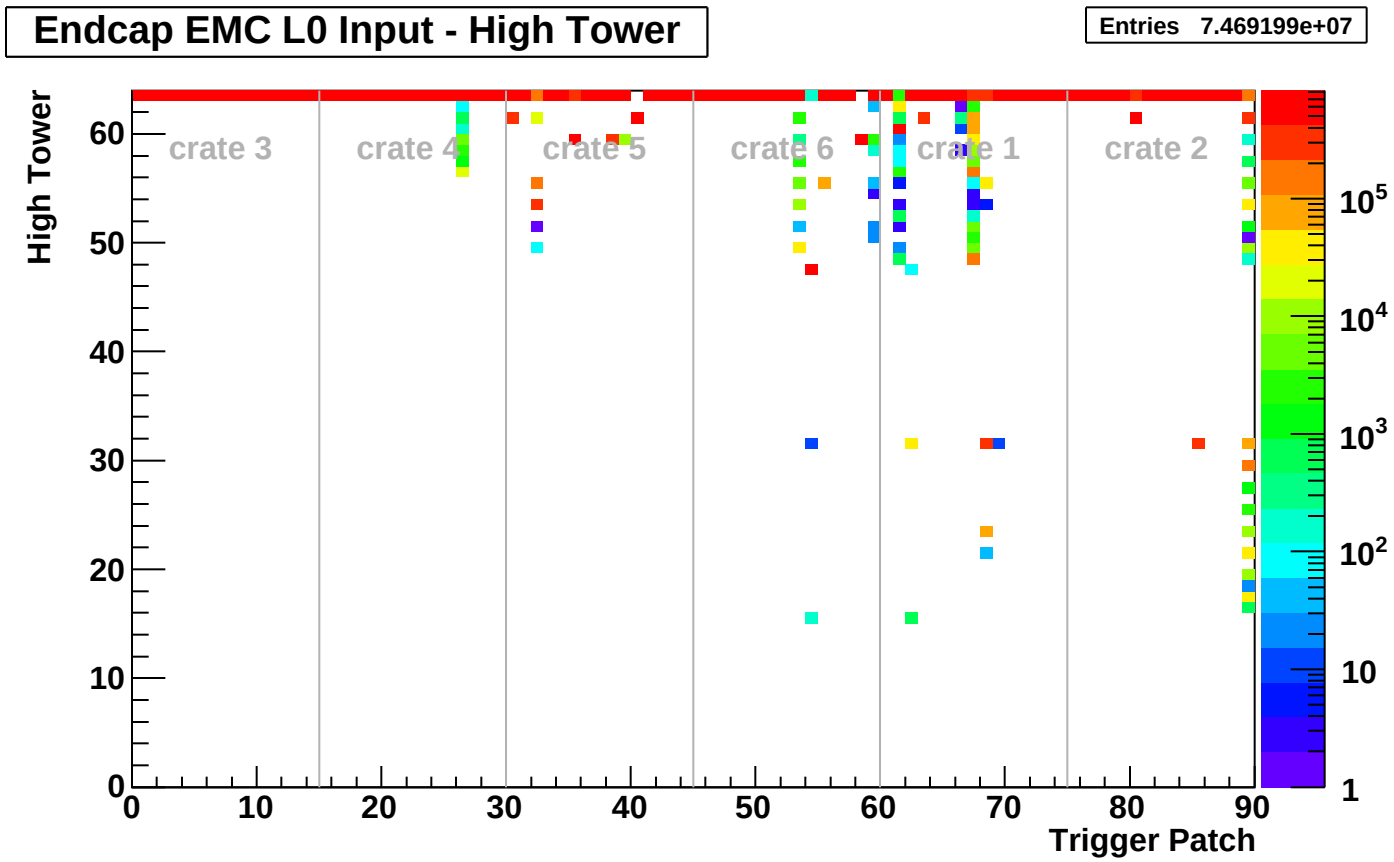

BunchId7Bit (all events)
Entries 829911
10^4
10^3
10^2
10
0
20
40
60
80
100
120

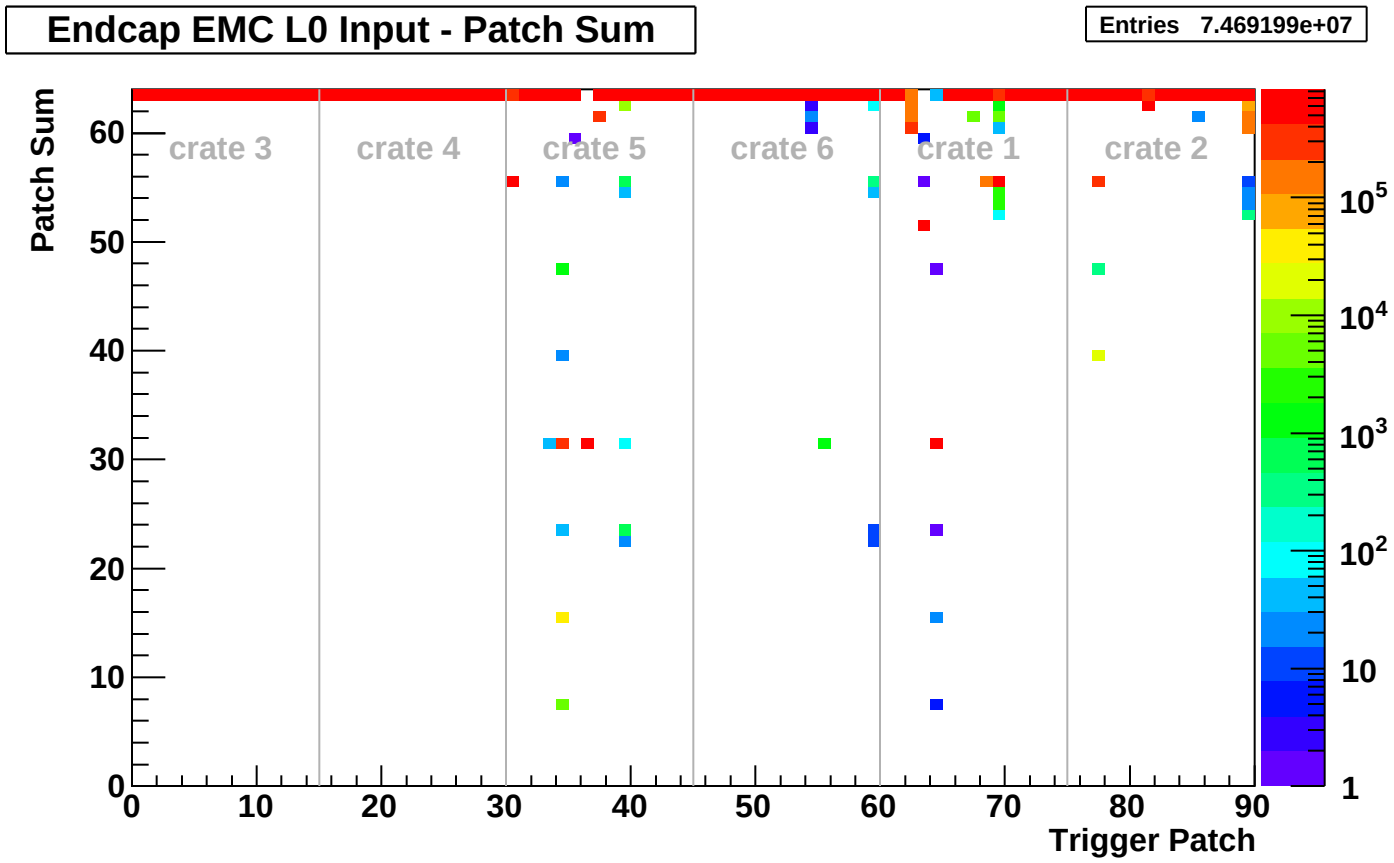

BunchId7Bit (JP1)
Entries 829911
10^4
10^3
10^2
10
0
20
40
60
80
100
120

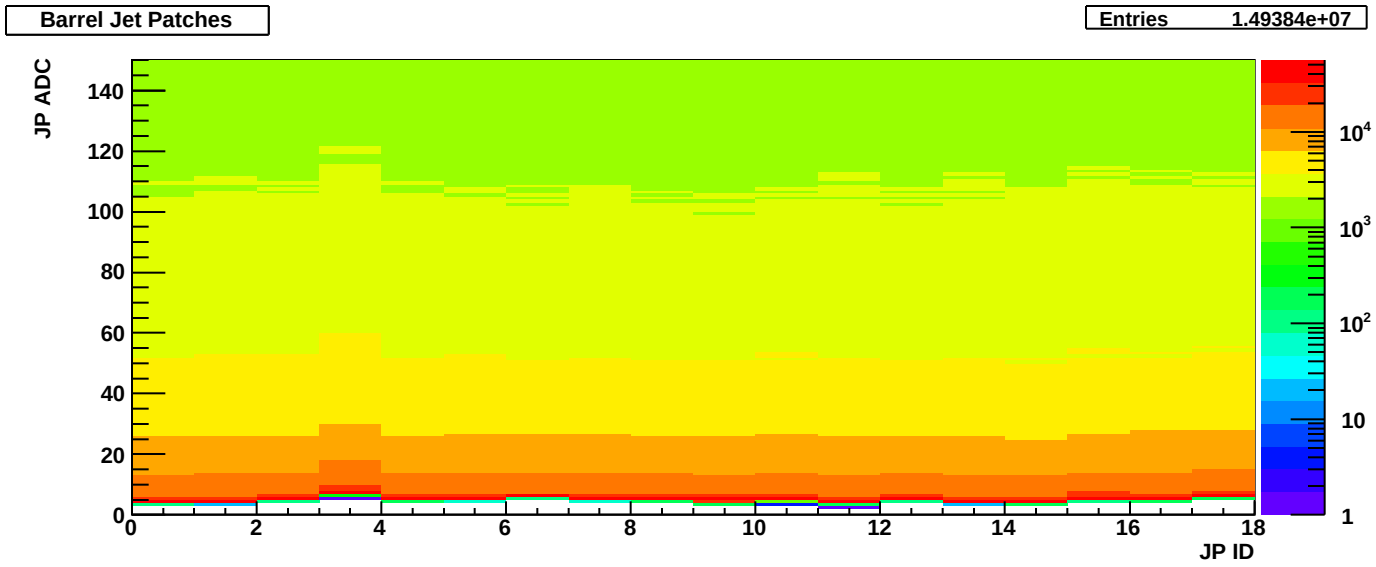

BunchId7Bit (JP2||AJP)
Entries 829911
10^4
10^3
10^2
10
0
20
40
60
80
100
120

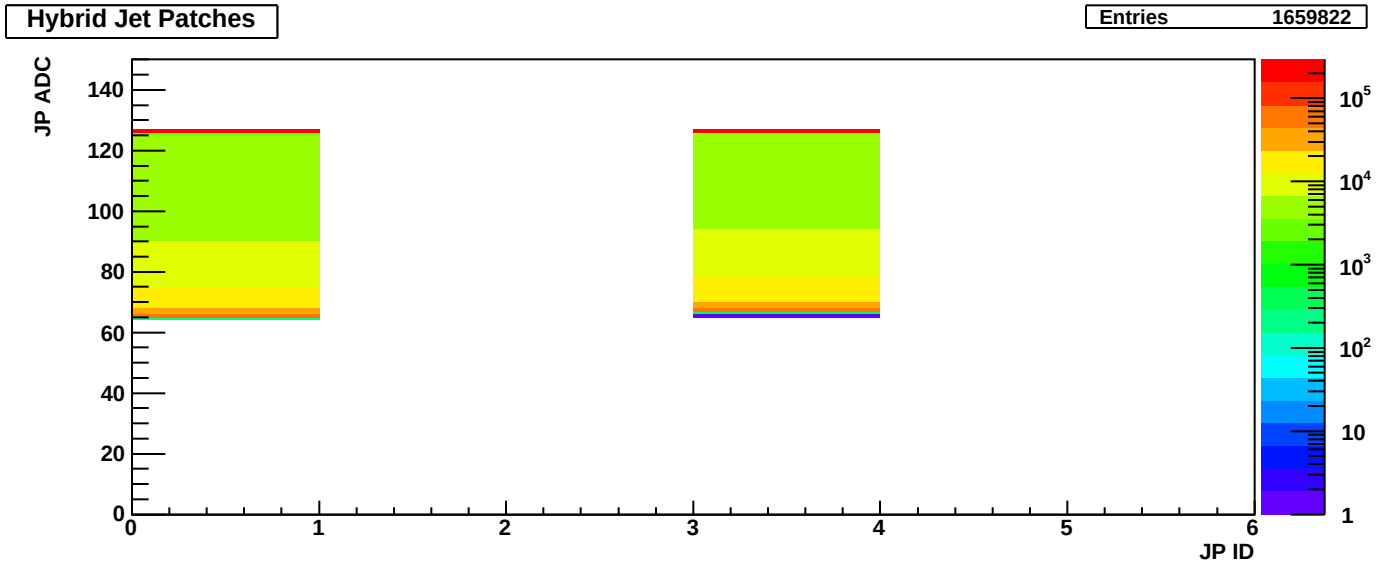

Barrel EMC L0 Input - High Tower
Entries 2.489733e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

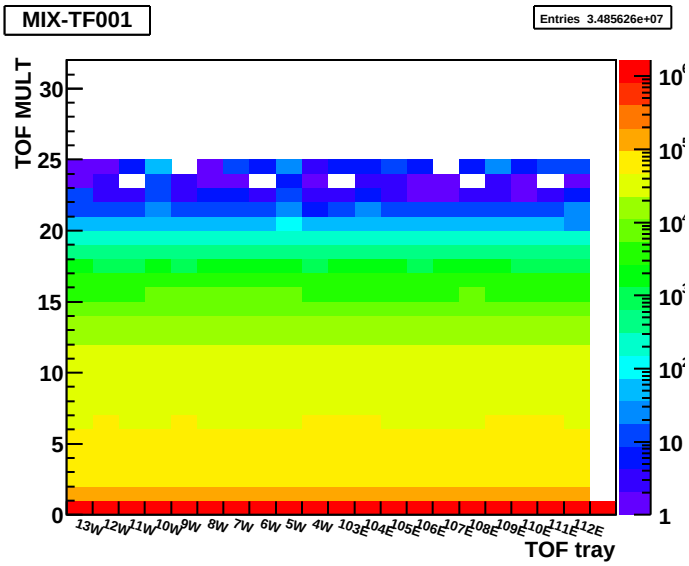

Barrel EMC L0 Input - Patch Sum
Entries 2.489733e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

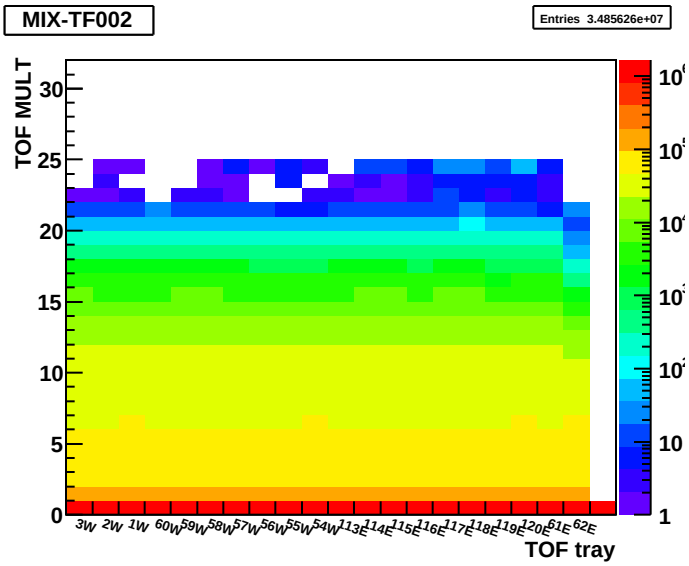

Endcap EMC L0 Input - High Tower
Entries 7.469199e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

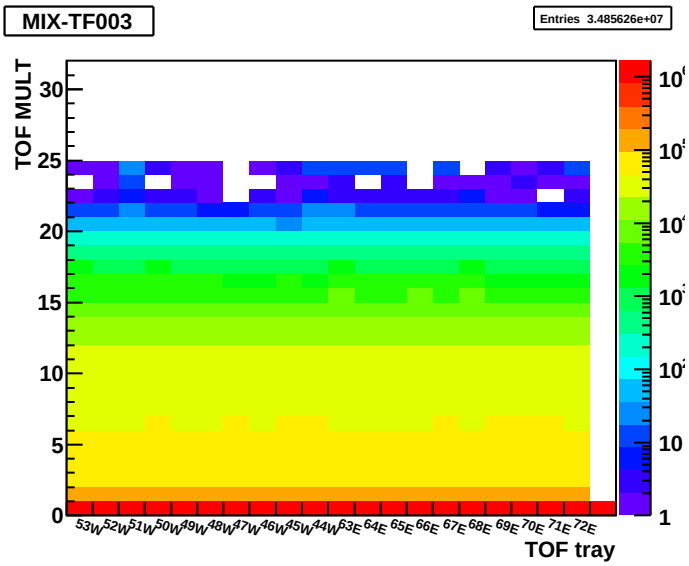

Endcap EMC L0 Input - Patch Sum
Entries 7.469199e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

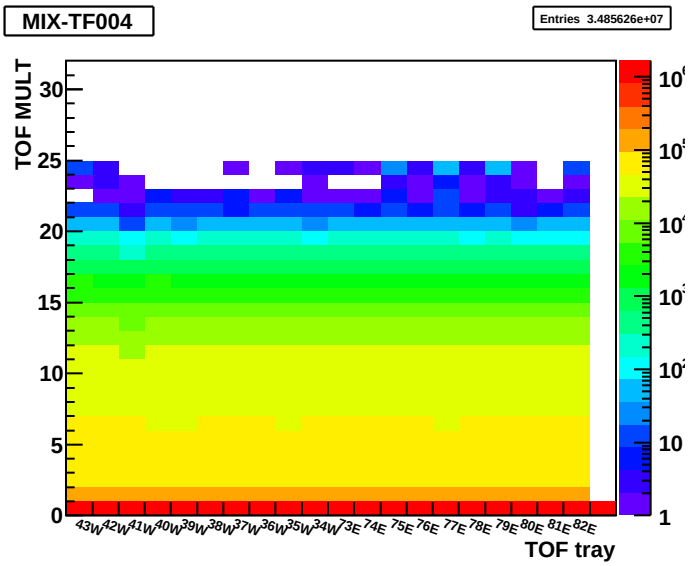

Barrel Jet Patches
Entries 1.49384e+07
JP ADC
JP ID

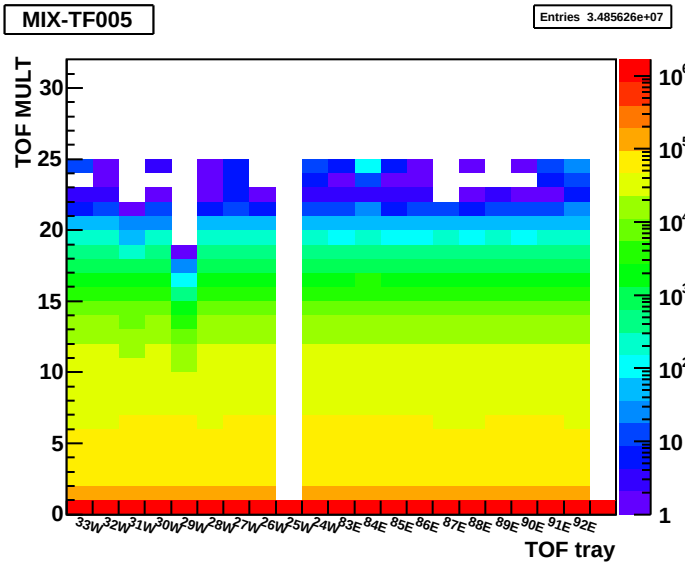

Hybrid Jet Patches
Entries 1659822
JP ADC
JP ID

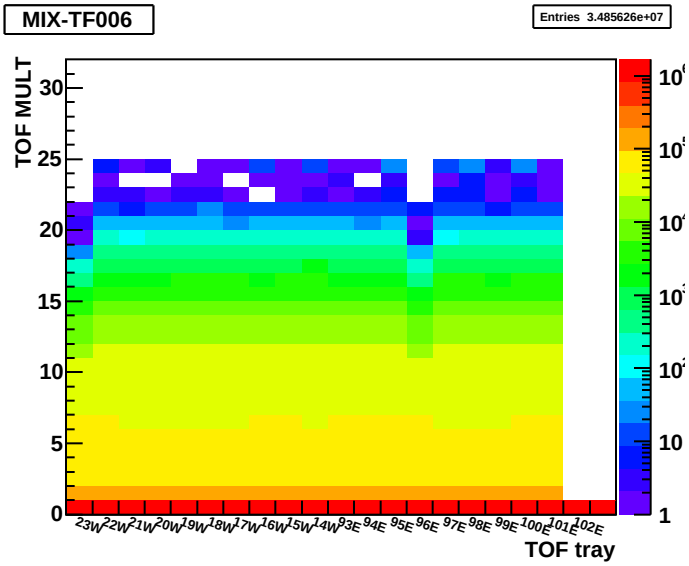

MIX-TF001
Entries 3.485626e+07
TOF MULT
TOF tray

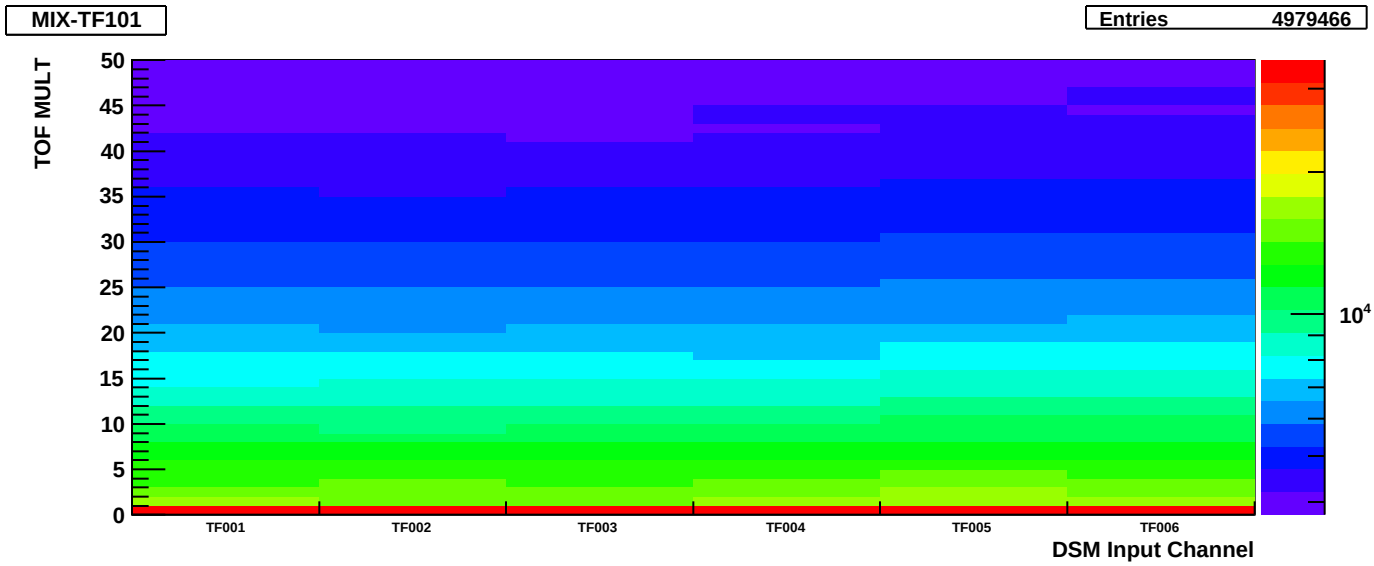

MIX-TF002
Entries 3.485626e+07
TOF MULT
TOF tray

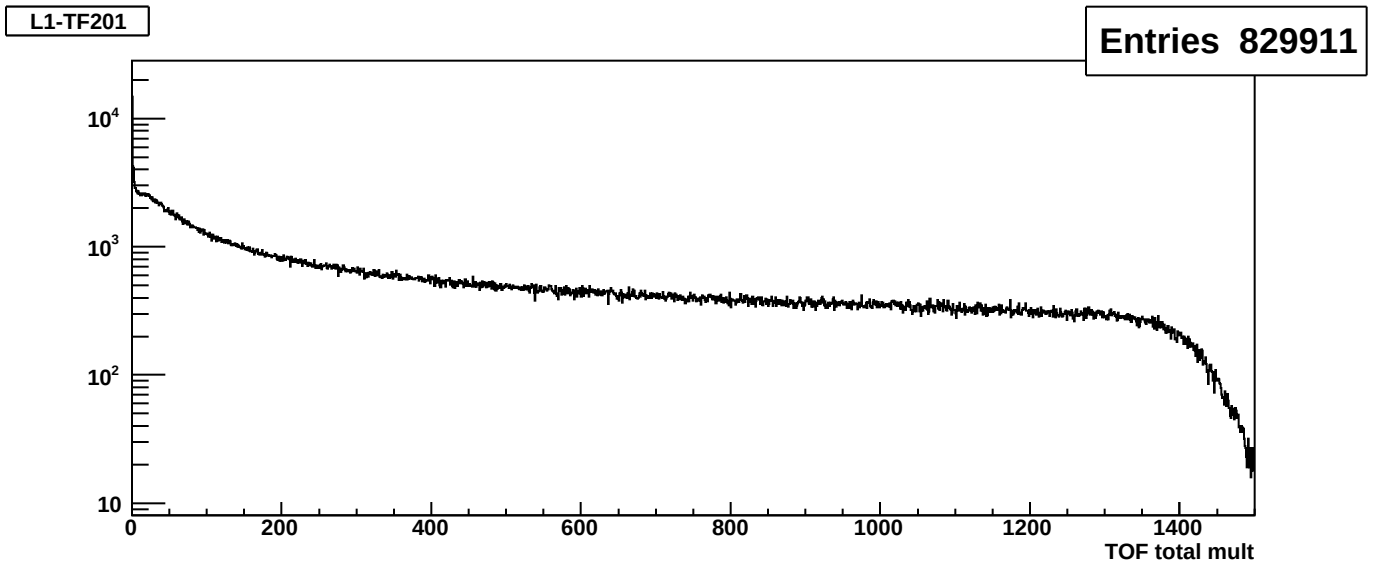

MIX-TF003
Entries 3.485626e+07
TOF MULT
TOF tray

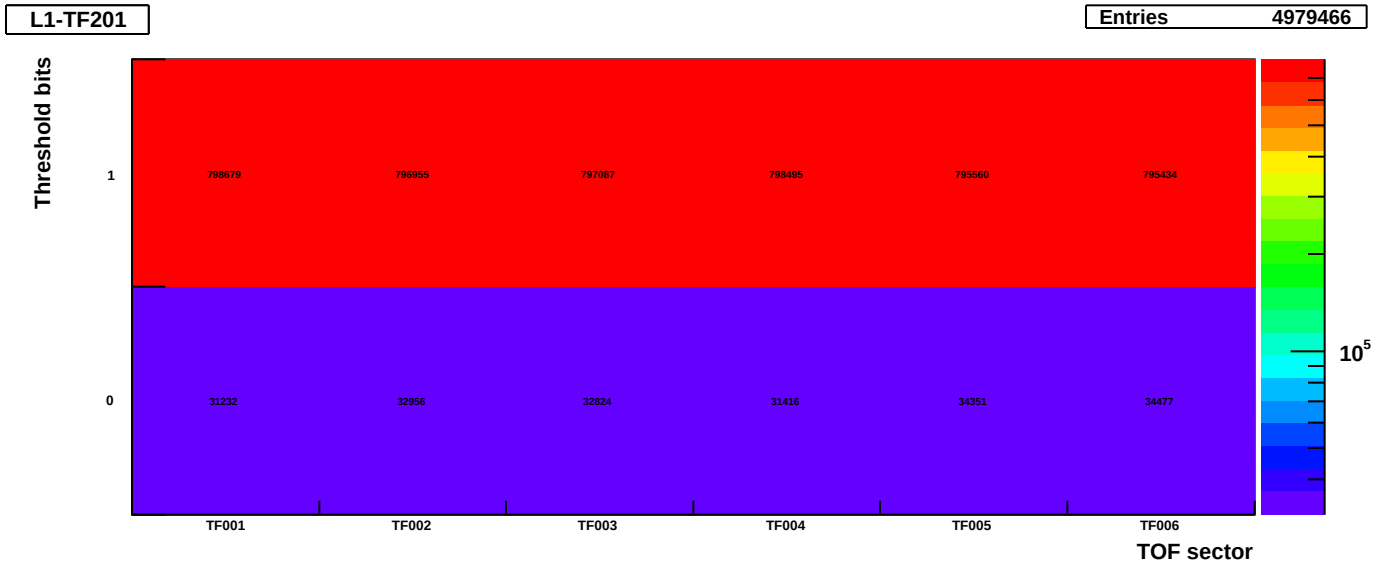

MIX-TF004
Entries 3.485626e+07
TOF MULT
TOF tray

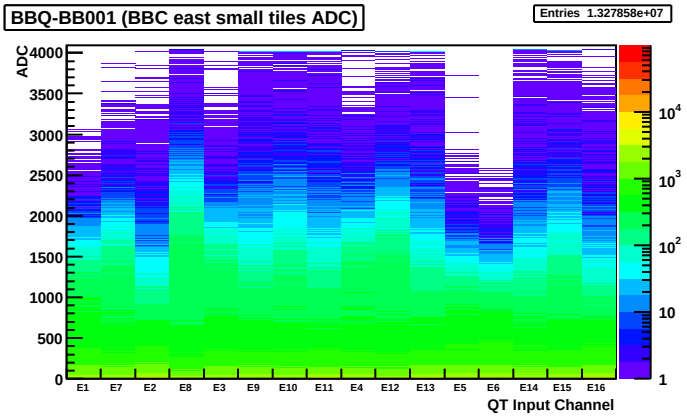

MIX-TF005
Entries 3.485626e+07
TOF MULT
TOF tray

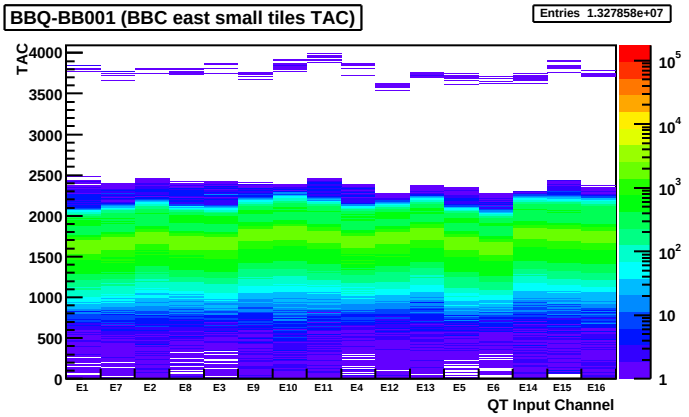

MIX-TF006
Entries 3.485626e+07
TOF MULT
TOF tray

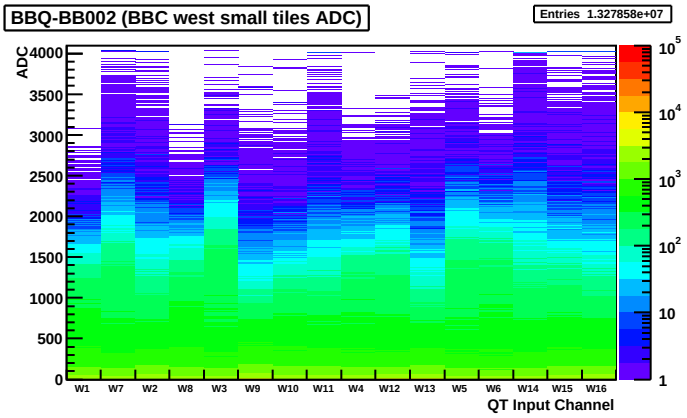

MIX-TF101
Entries 4979466
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

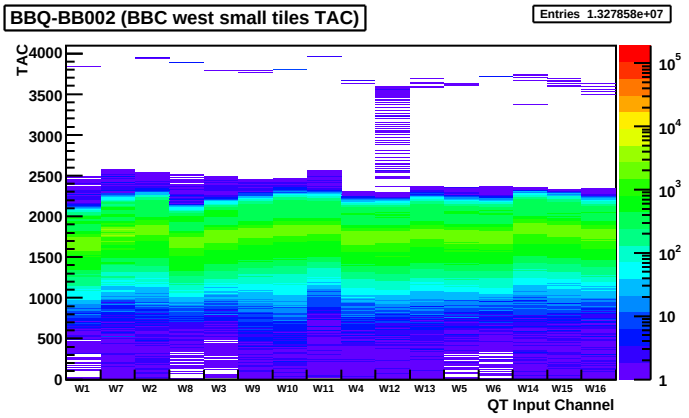

L1-TF201
Entries 829911
10^4
10^3
10^2
10
0
200
400
600
800
1000
1200
1400
TOF total mult

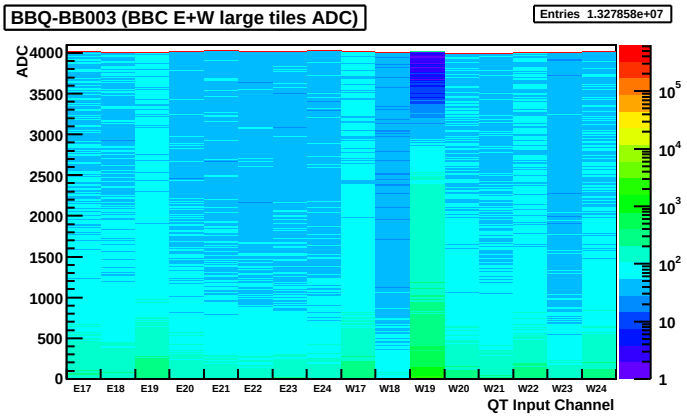

L1-TF201
Entries 4979466
Threshold bits
1
0
798679
796955
797087
798495
795560
795434
31232
32956
32824
31416
34351
34477
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

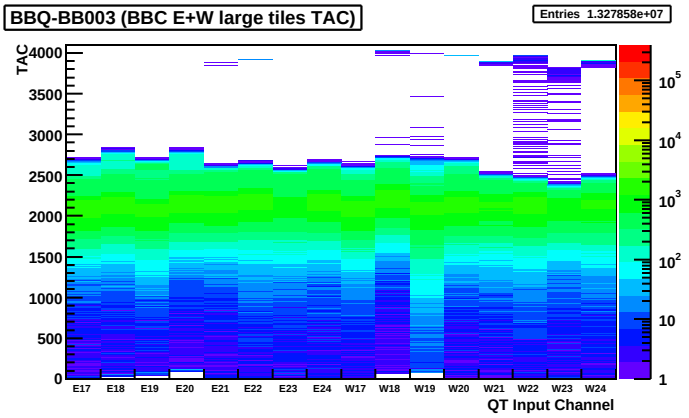

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.327858e+07
ADC
QT Input Channel

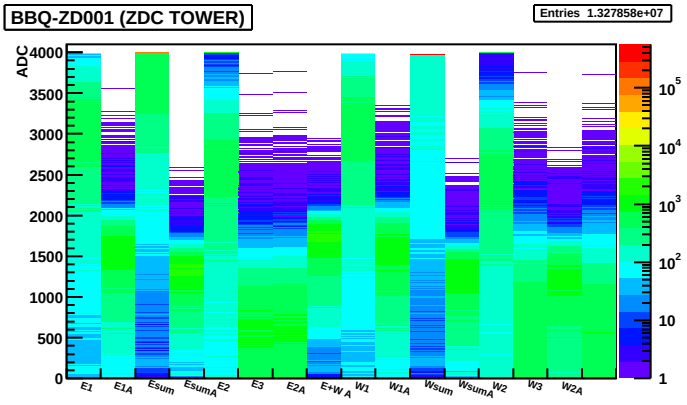

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.327858e+07
TAC
QT Input Channel

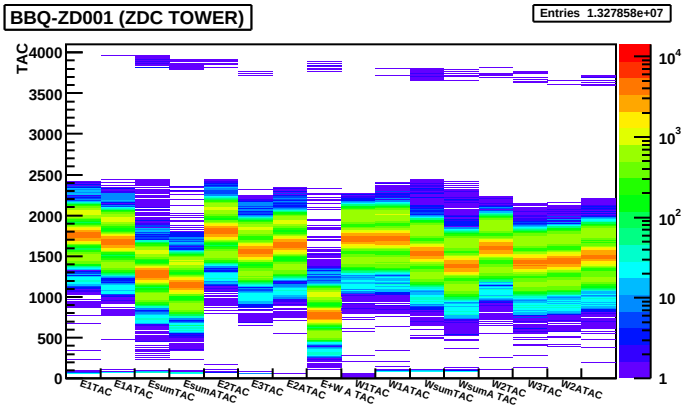

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.327858e+07
ADC
QT Input Channel

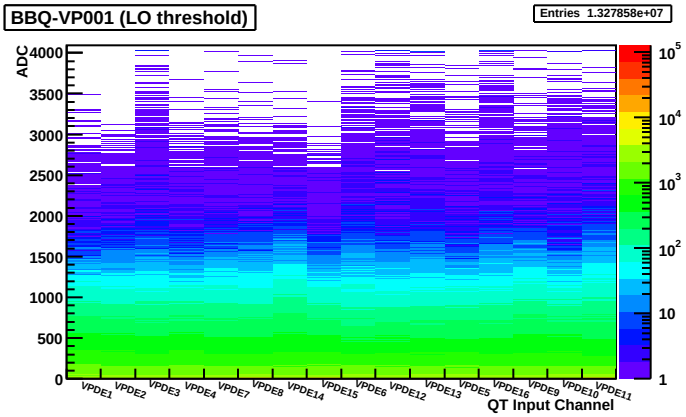

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.327858e+07
TAC
QT Input Channel

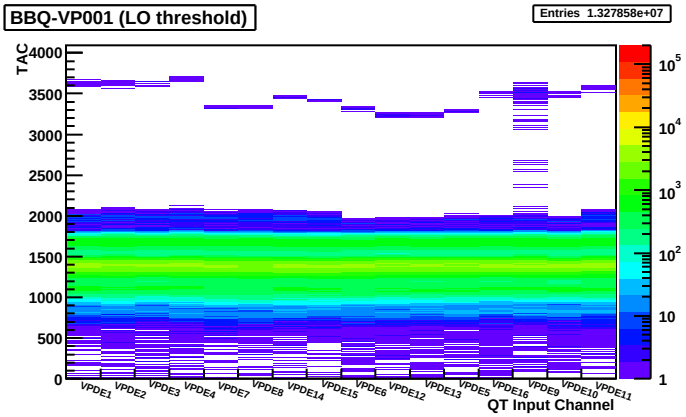

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.327858e+07
ADC
QT Input Channel

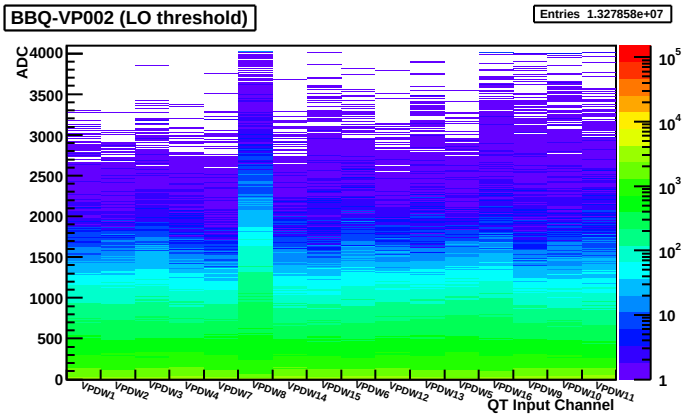

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.327858e+07
TAC
QT Input Channel

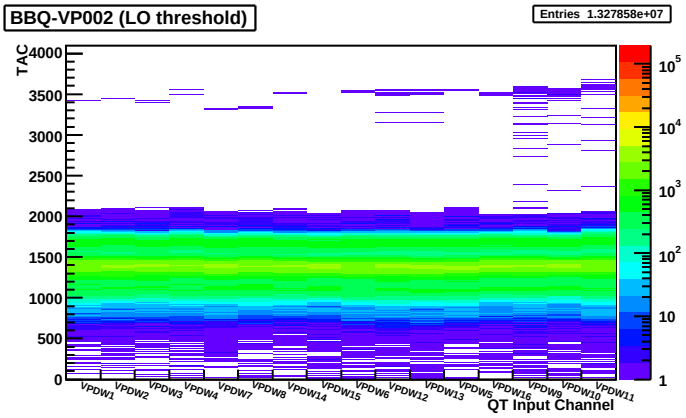

BBQ-ZD001 (ZDC TOWER)
Entries 1.327858e+07
ADC

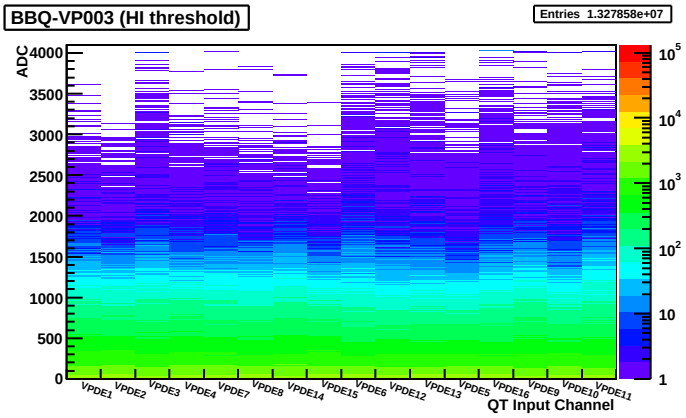

BBQ-ZD001 (ZDC TOWER)
Entries 1.327858e+07
TAC

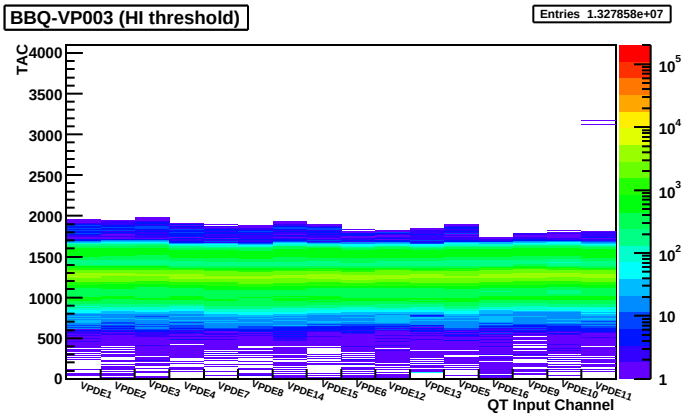

BBQ-VP001 (LO threshold)
Entries 1.327858e+07
ADC
QT Input Channel

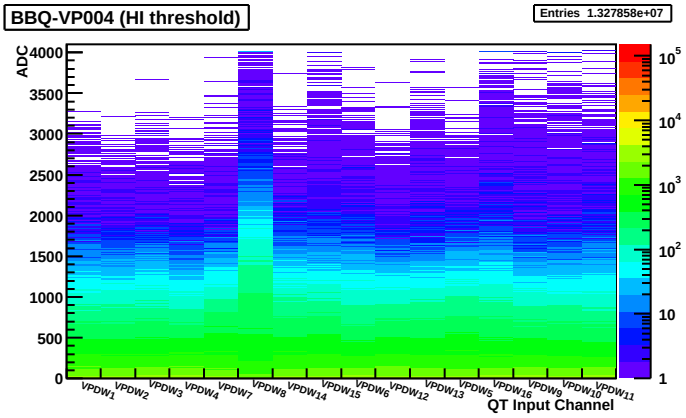

BBQ-VP001 (LO threshold)
Entries 1.327858e+07
TAC
QT Input Channel

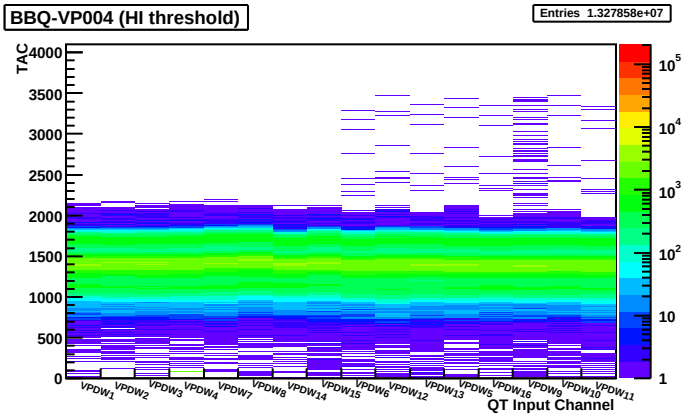

BBQ-VP002 (LO threshold)
Entries 1.327858e+07
ADC
QT Input Channel

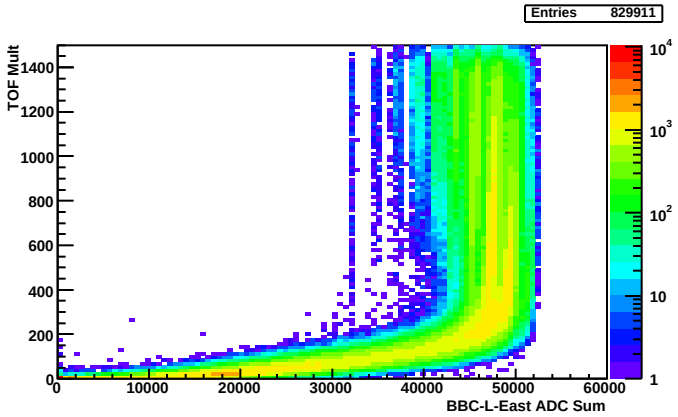

BBQ-VP002 (LO threshold)
Entries 1.327858e+07
TAC
QT Input Channel

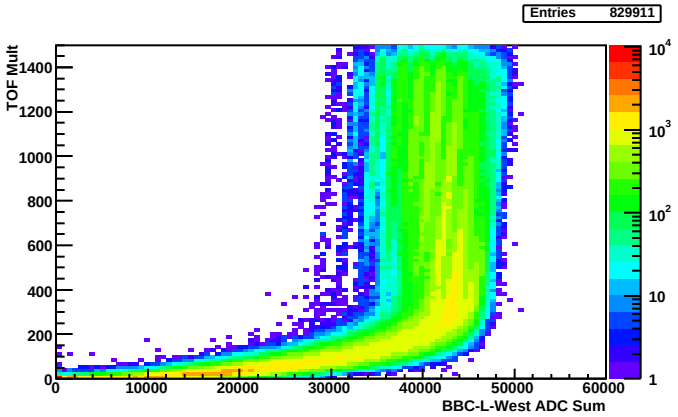

BBQ-VP003 (HI threshold)
Entries 1.327858e+07
ADC
QT Input Channel

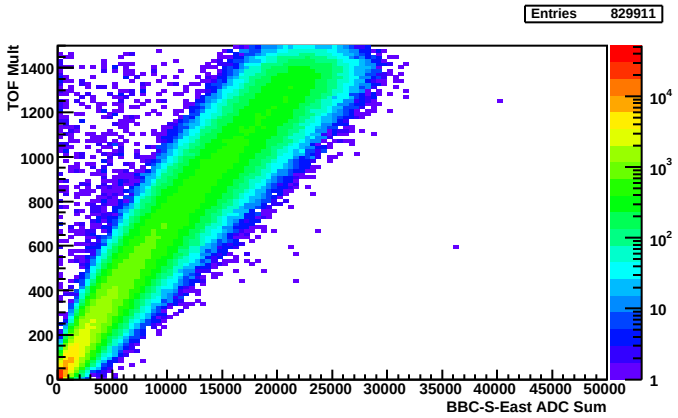

BBQ-VP003 (HI threshold)
Entries 1.327858e+07
TAC
QT Input Channel

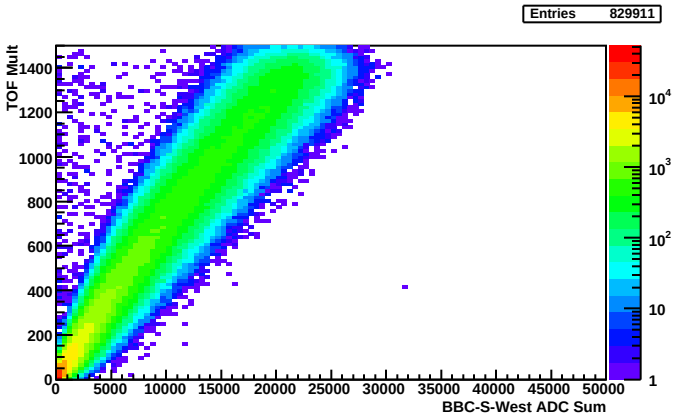

BBQ-VP004 (HI threshold)
Entries 1.327858e+07
ADC
QT Input Channel

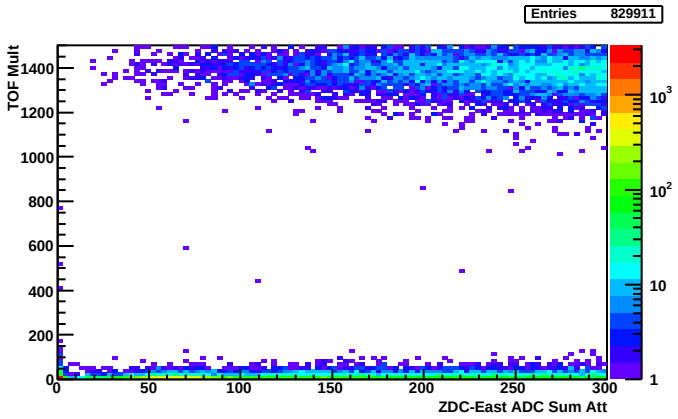

BBQ-VP004 (HI threshold)
Entries 1.327858e+07
TAC
QT Input Channel

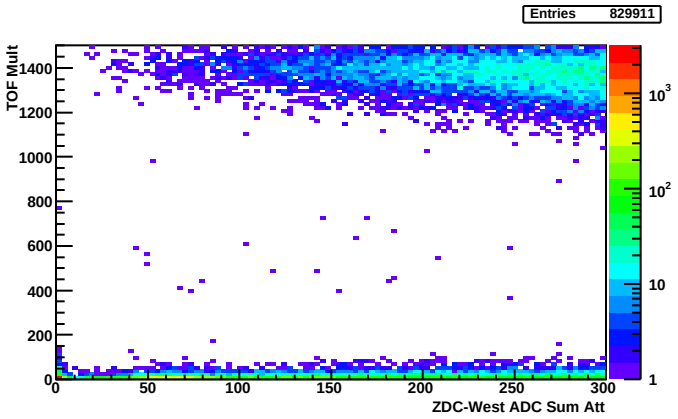

Entries 829911
TOF Mult
BBC-L-East ADC Sum

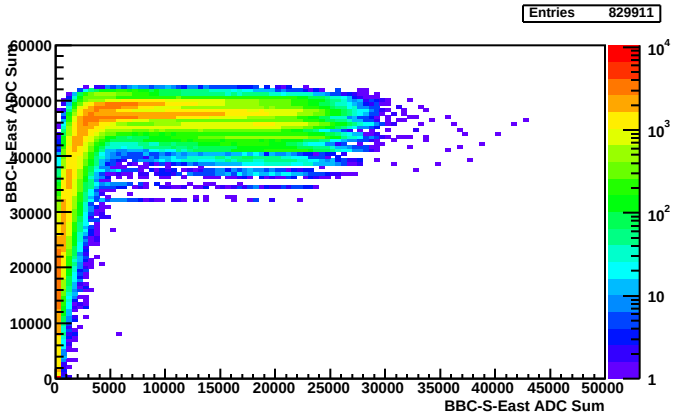

Entries 829911
TOF Mult
BBC-L-West ADC Sum

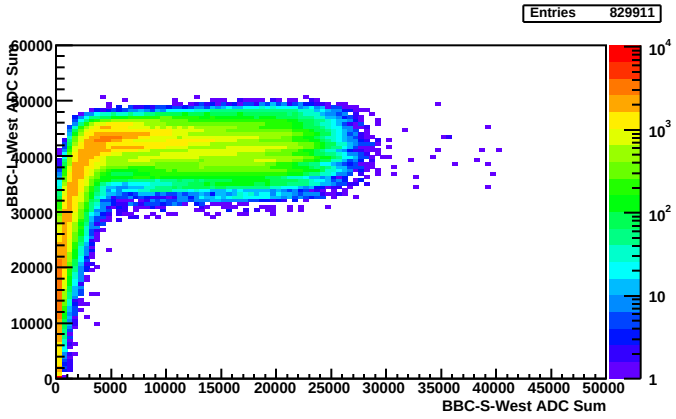

Entries 829911
TOF Mult
BBC-S-East ADC Sum

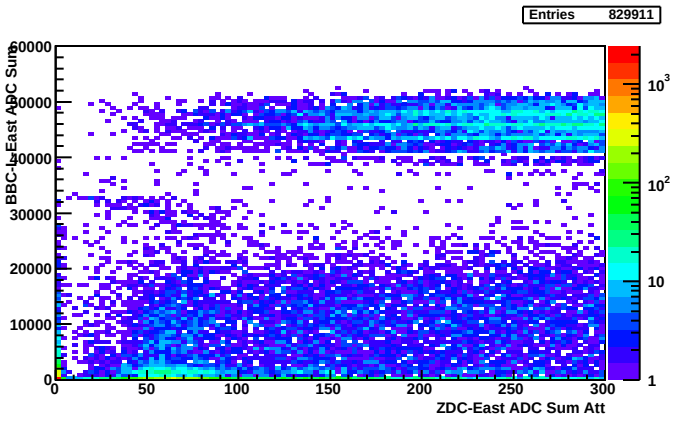

Entries 829911
TOF Mult
BBC-S-West ADC Sum

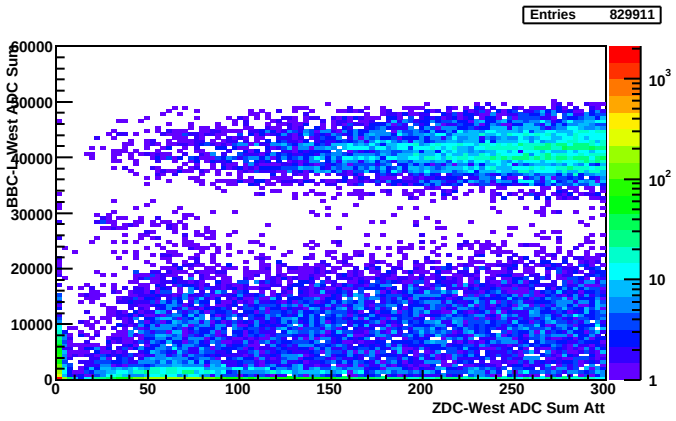

Entries 829911
TOF Mult
ZDC-East ADC Sum Att

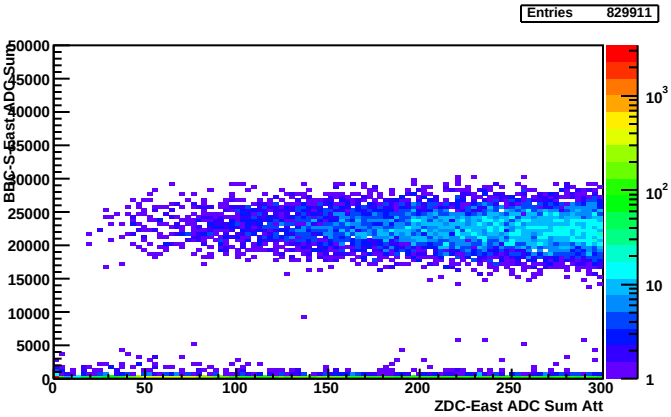

Entries 829911
TOF Mult
ZDC-West ADC Sum Att

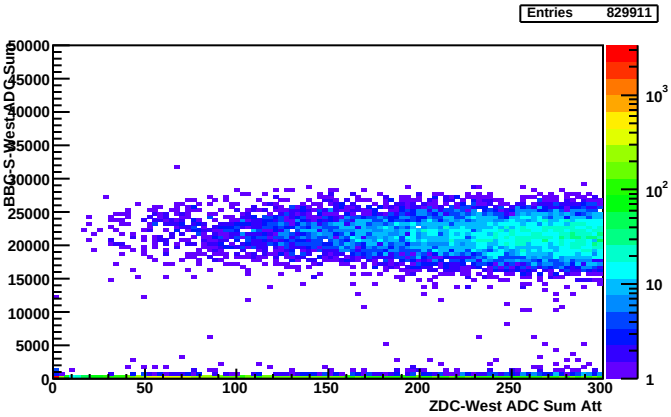

Entries 829911
BBC-L-East ADC Sum
BBC-S-East ADC Sum

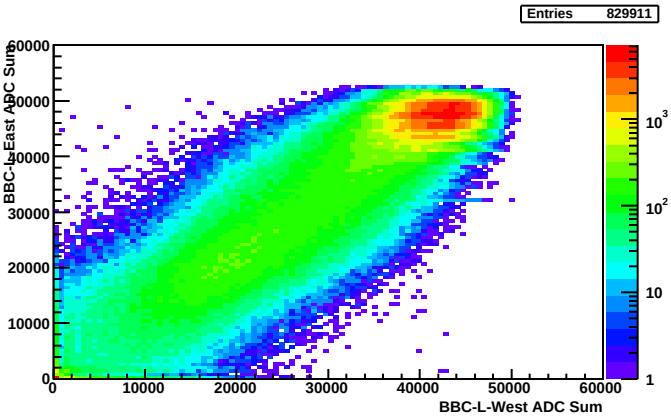

Entries 829911
BBC-L-West ADC Sum
BBC-S-West ADC Sum

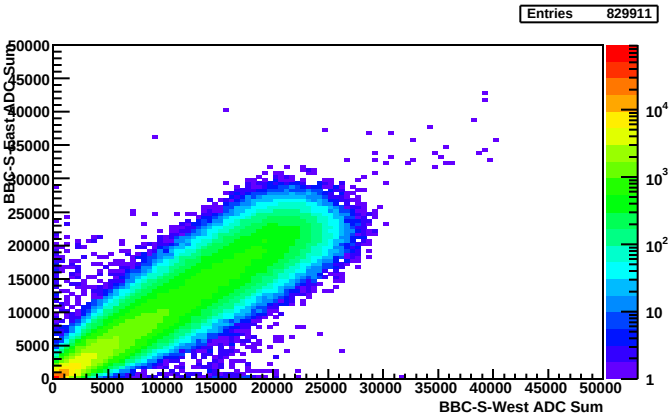

Entries 829911
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

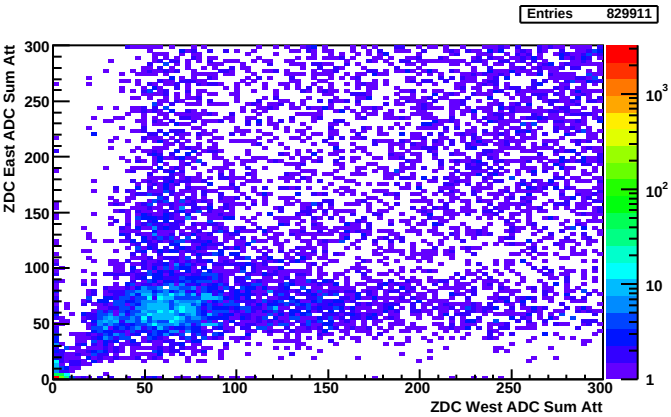

Entries 829911
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

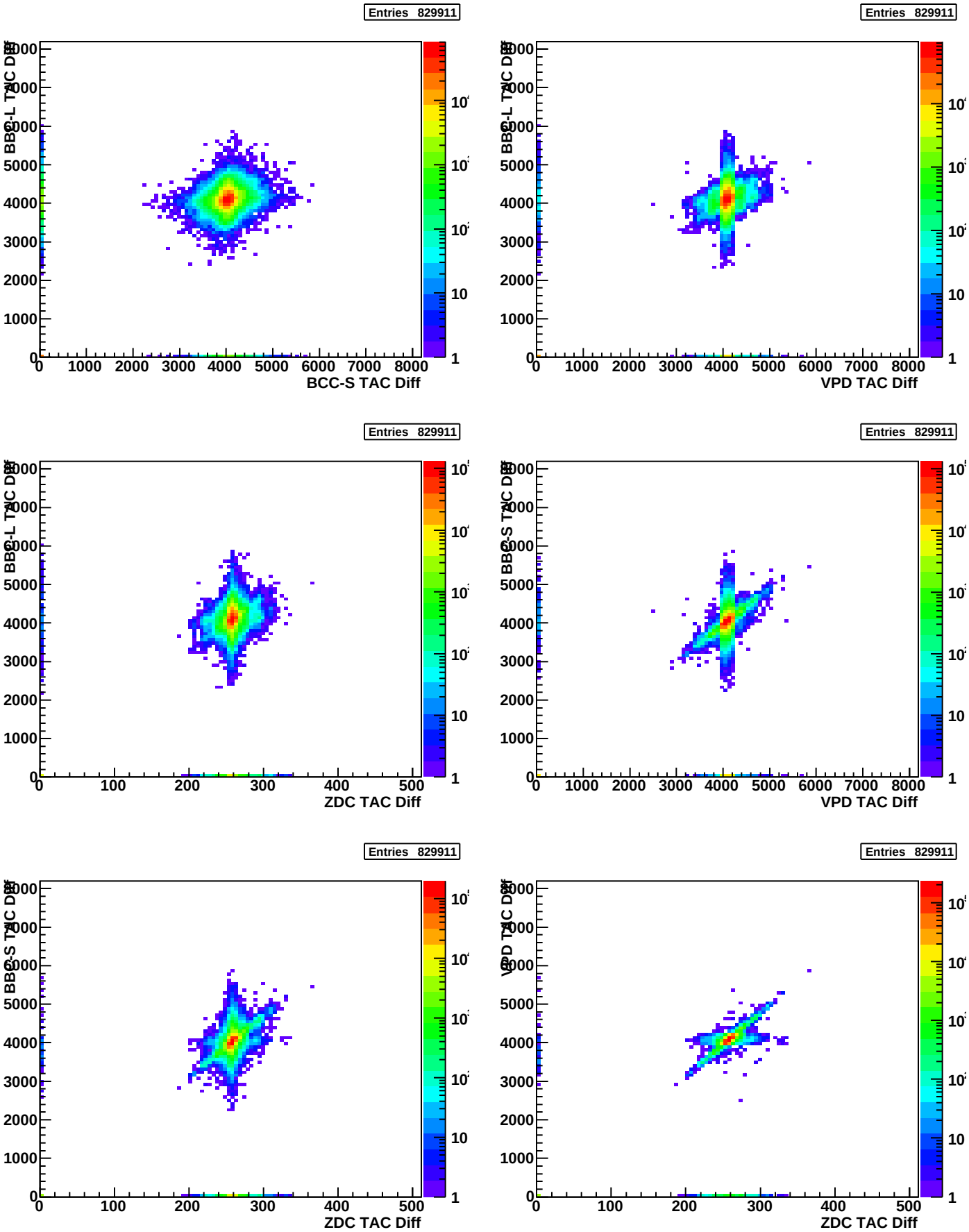

Entries 829911
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

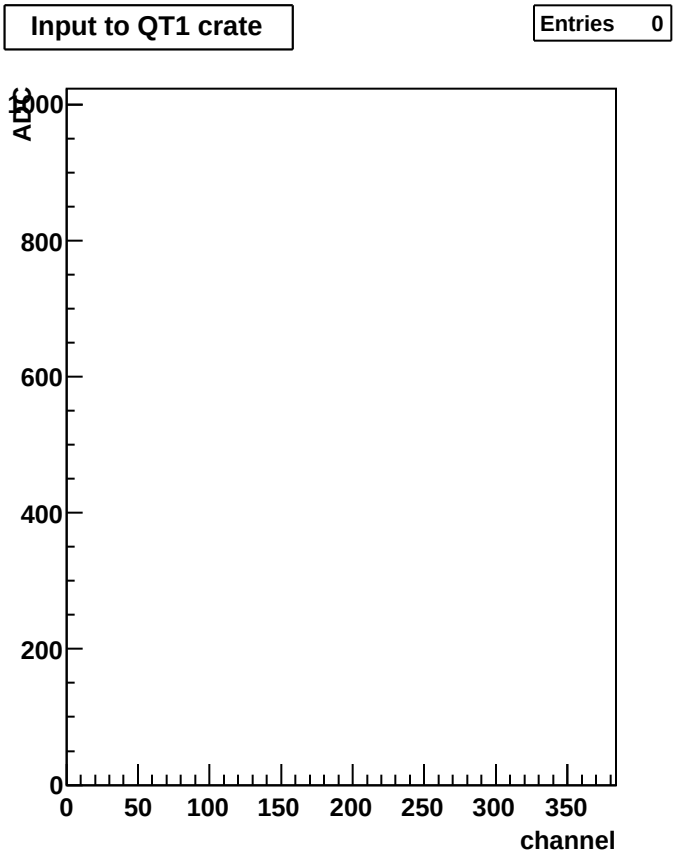

Entries 829911
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

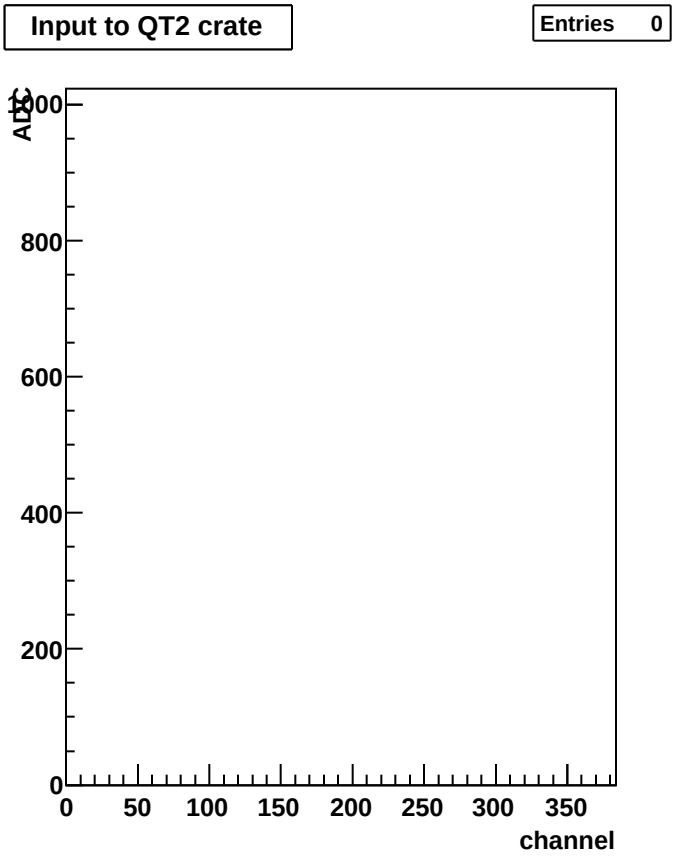

Entries 829911
BBC-L-East ADC Sum
BBC-L-West ADC Sum

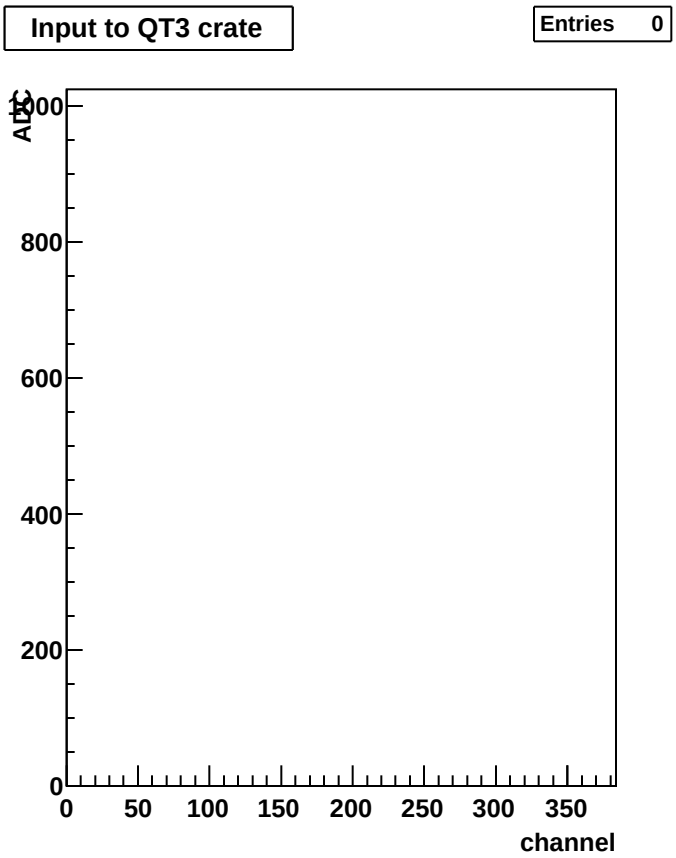

Entries 829911
BBC-S-East ADC Sum
BBC-S-West ADC Sum

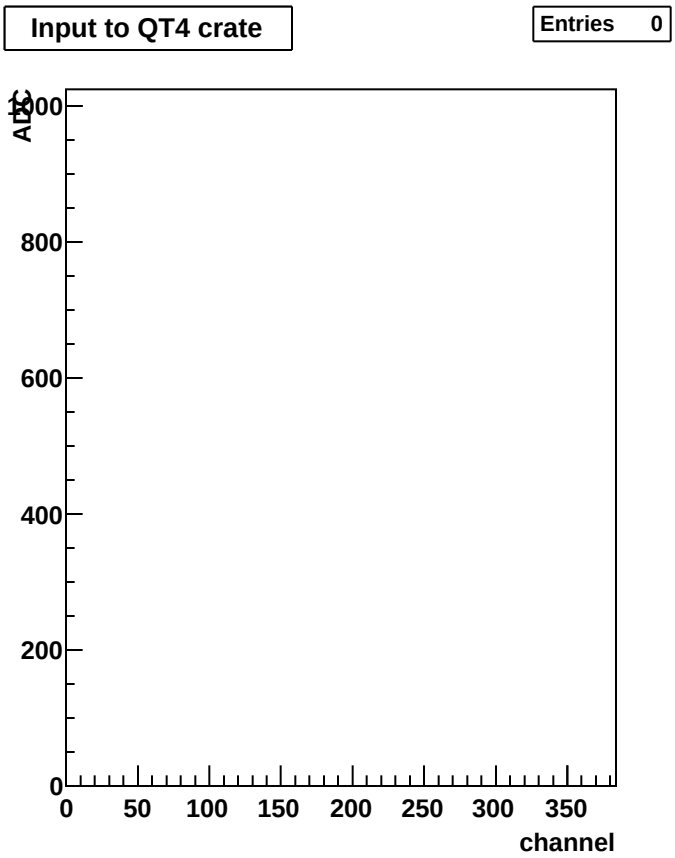

Entries 829911
ZDC East ADC Sum Att
ZDC West ADC Sum Att

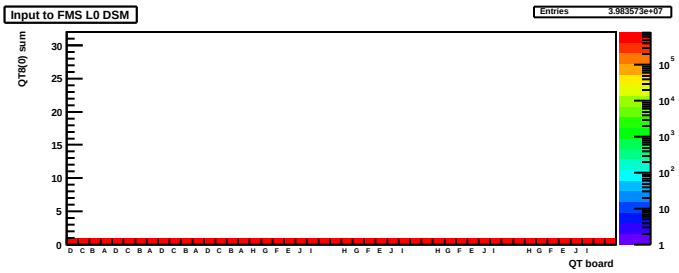

Entries 829911
BBC-L TAC Diff
BCC-S TAC Diff
Entries 829911
BBC-L TAC Diff
VPD TAC Diff
Entries 829911
BBC-L TAC Diff
ZDC TAC Diff
Entries 829911
BBC-S TAC Diff
VPD TAC Diff
Entries 829911
BBC-S TAC Diff
ZDC TAC Diff
Entries 829911
VPD TAC Diff
ZDC TAC Diff

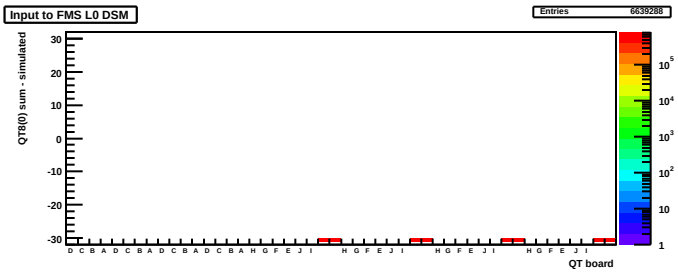

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

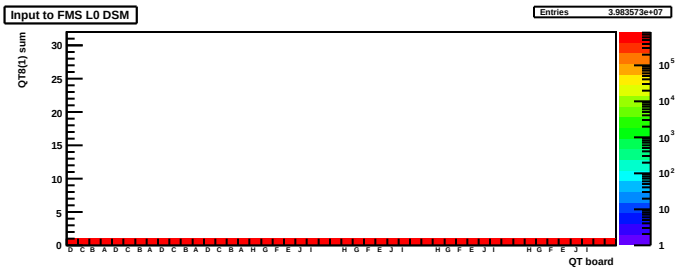

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

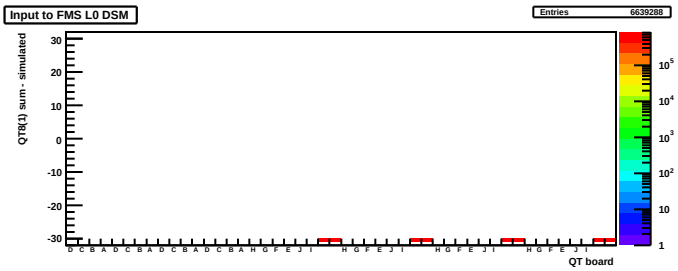

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

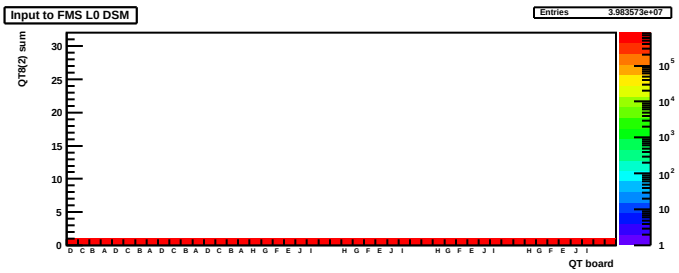

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

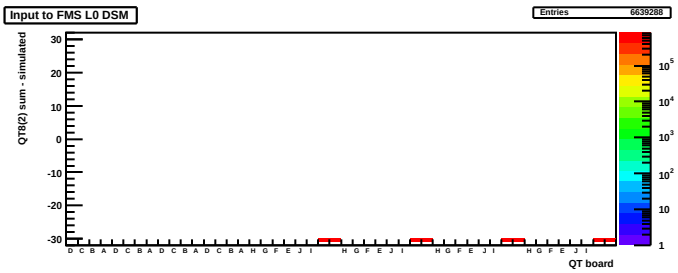

Input to FMS L0 DSM
Entries 3.983573e+07
QT8(0) sum
QT board

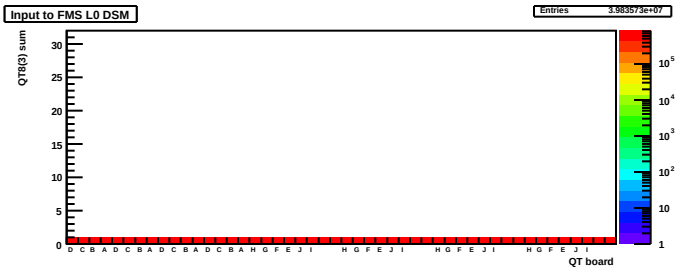

Input to FMS L0 DSM
Entries 6639288
QT8(0) sum - simulated
QT board

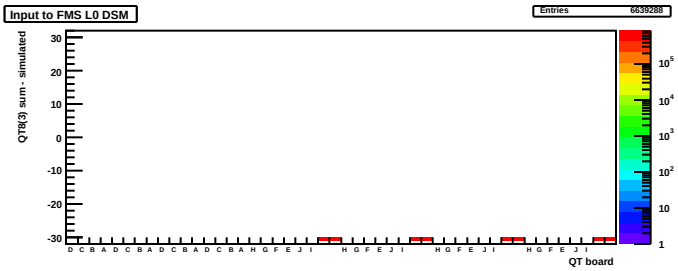

Input to FMS L0 DSM
Entries 3.983573e+07
QT8(1) sum
QT board

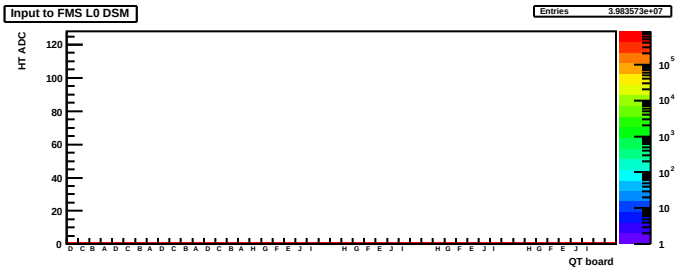

Input to FMS L0 DSM
Entries 6639288
QT8(1) sum - simulated
QT board

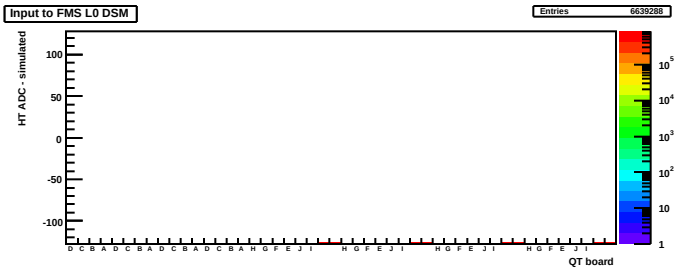

Input to FMS L0 DSM
Entries 3.983573e+07
QT8(2) sum
QT board

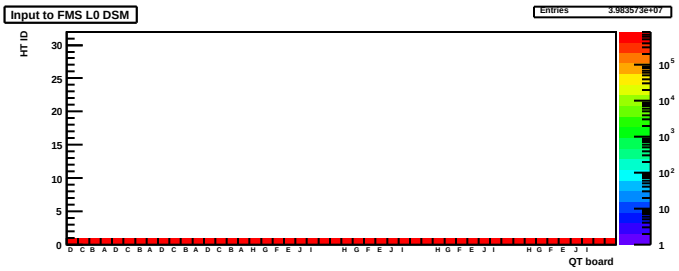

Input to FMS L0 DSM
Entries 6639288
QT8(2) sum - simulated
QT board

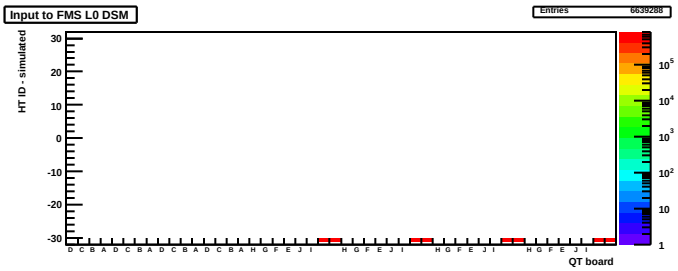

Input to FMS L0 DSM
Entries 3.983573e+07
QT8(3) sum
QT board

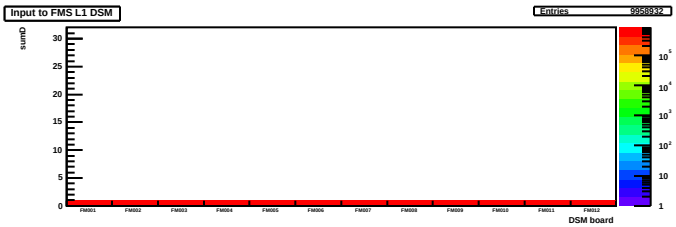

Input to FMS L0 DSM
Entries 6639288
QT8(3) sum - simulated
QT board

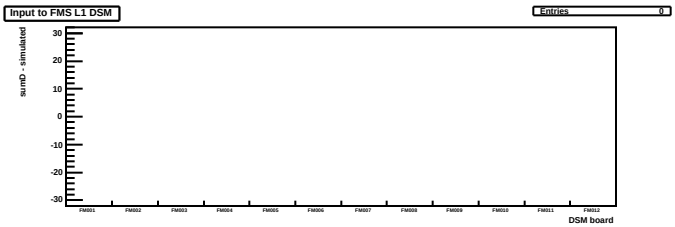

Input to FMS L0 DSM
Entries 3.983573e+07
HT ADC
QT board

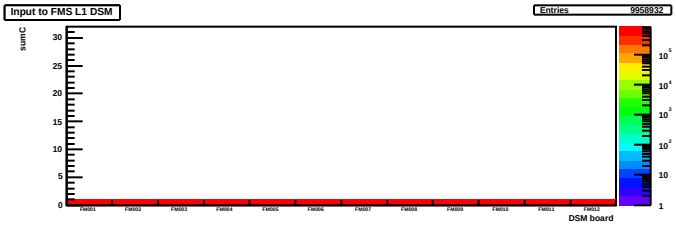

Input to FMS L0 DSM
Entries 6639288
HT ADC - simulated
QT board

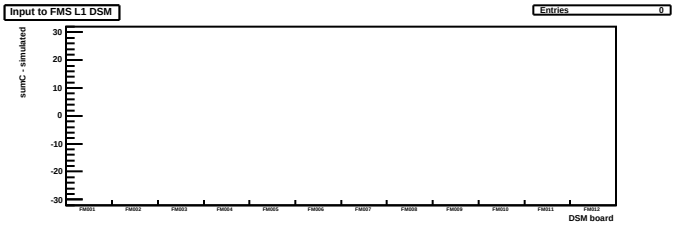

Input to FMS L0 DSM
Entries 3.983573e+07
HT ID
QT board

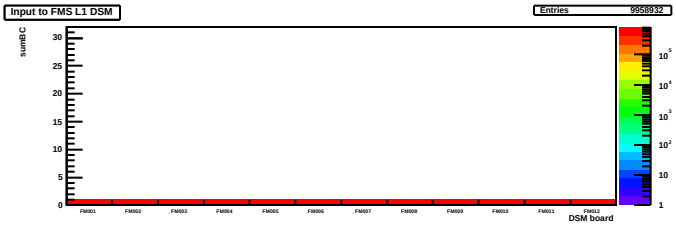

Input to FMS L0 DSM
Entries 6639288
HT ID - simulated
QT board

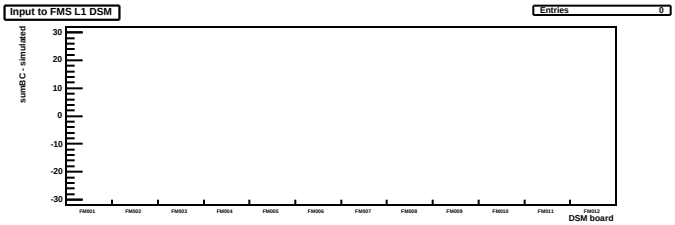

Input to FMS L1 DSM
Entries 9958932
sumD
DSM board

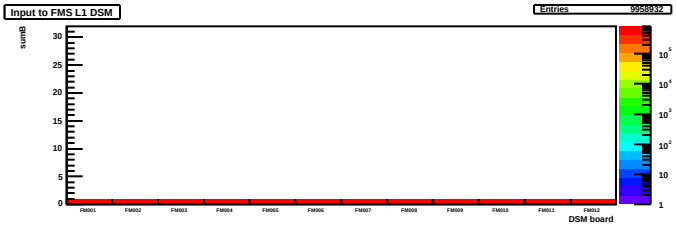

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

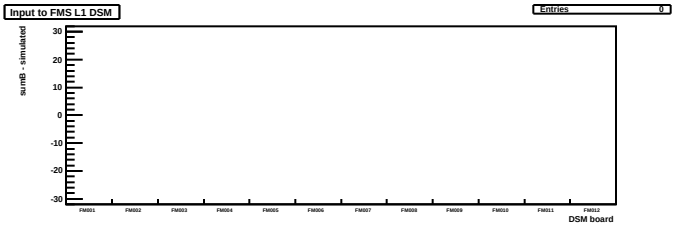

Input to FMS L1 DSM
Entries 9958932
sumC
DSM board

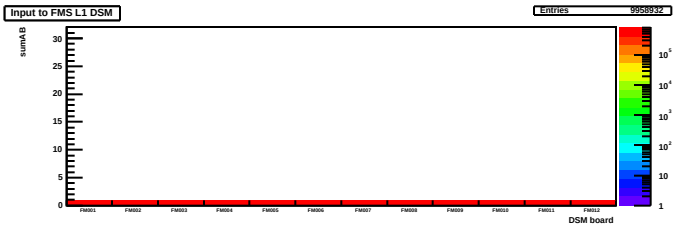

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

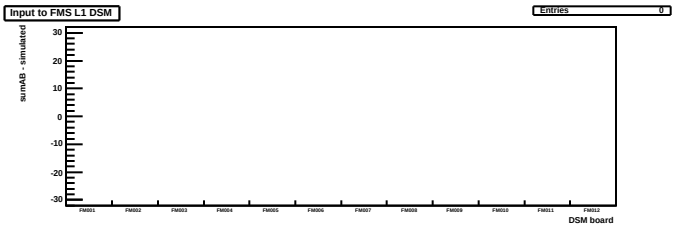

Input to FMS L1 DSM
Entries 9958932
sumBC
DSM board

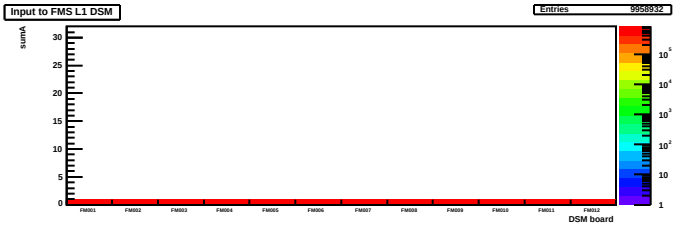

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

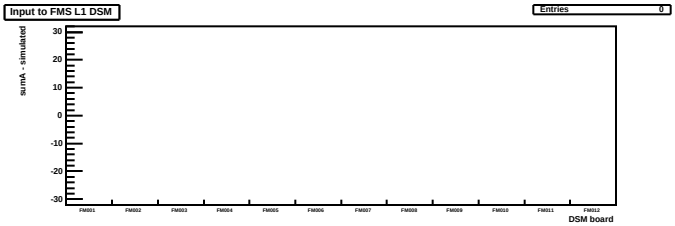

Input to FMS L1 DSM
Entries 9958932
sumB
DSM board

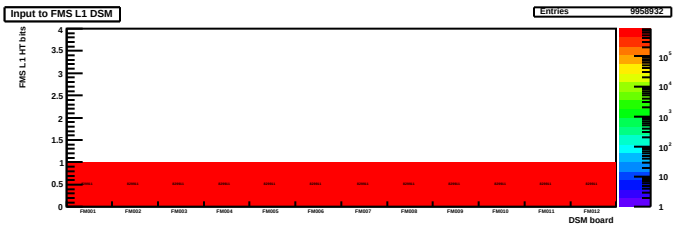

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

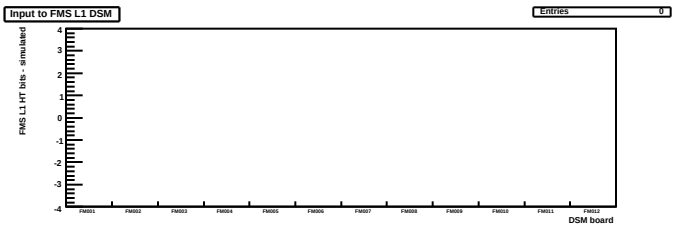

Input to FMS L1 DSM
Entries 9958932
sumAB
DSM board

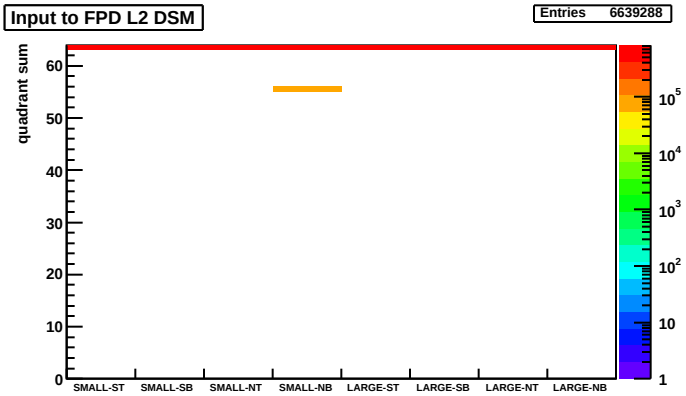

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

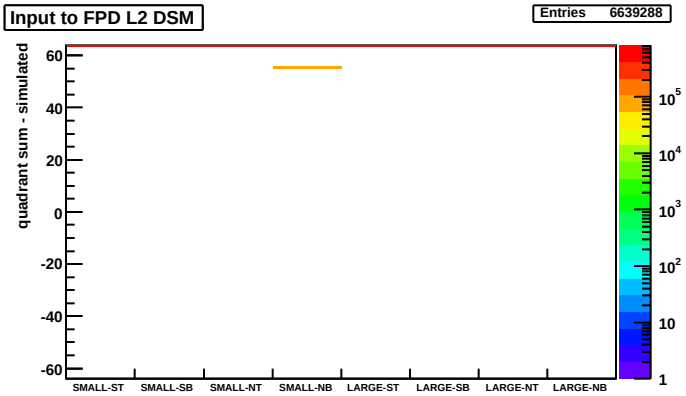

Input to FMS L1 DSM
Entries 9958932
sumA
DSM board

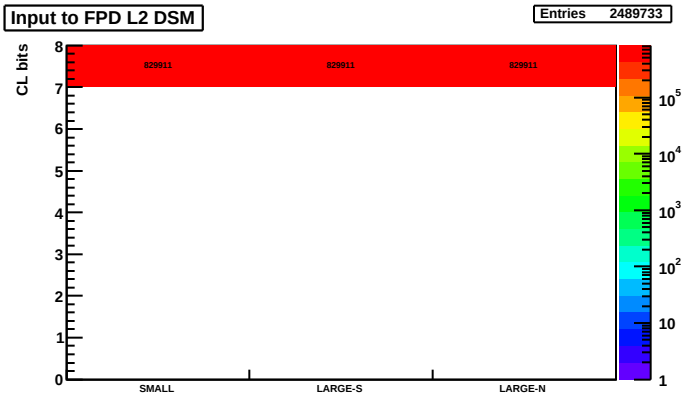

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

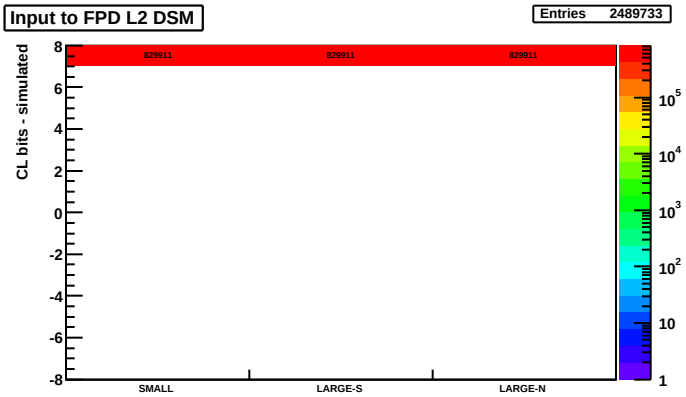

Input to FMS L1 DSM
Entries 9958932
FMS L1 HT bits
DSM board

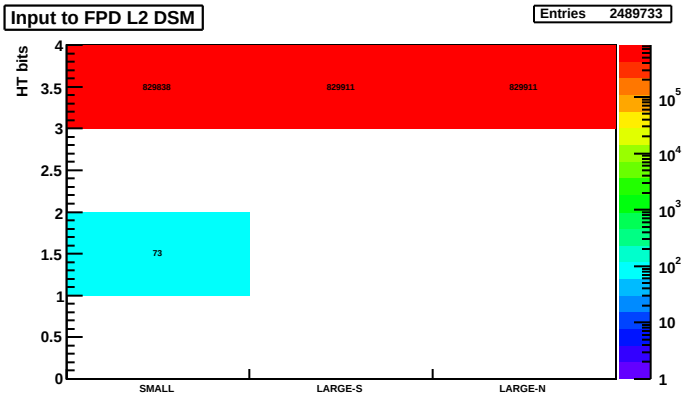

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

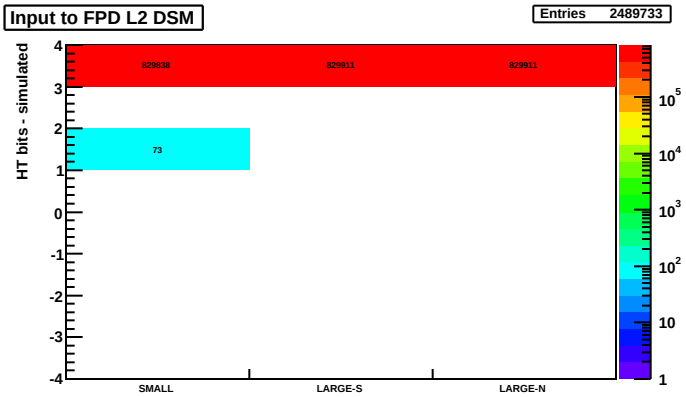

Input to FPD L2 DSM
Entries 6639288
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

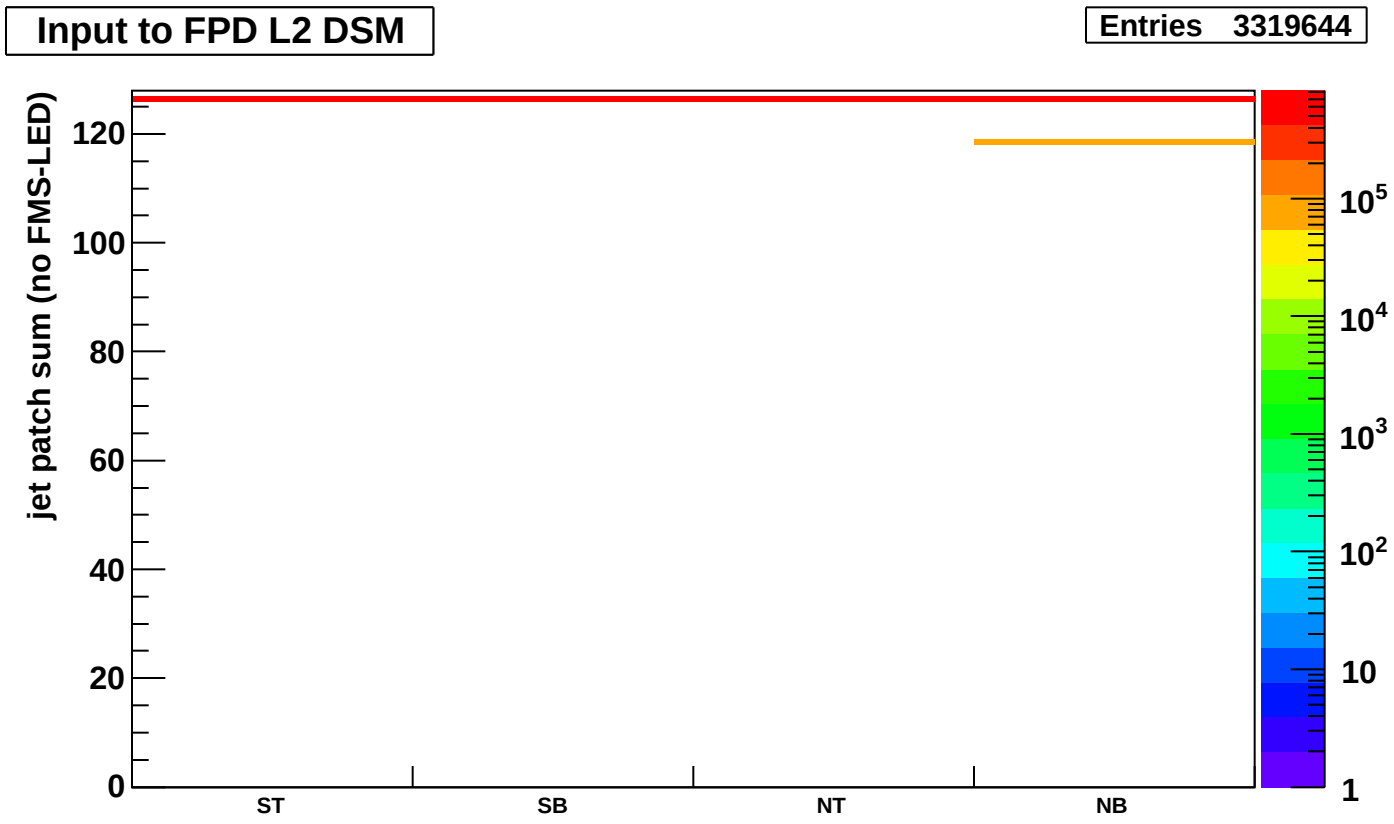

Input to FPD L2 DSM
Entries 6639288
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

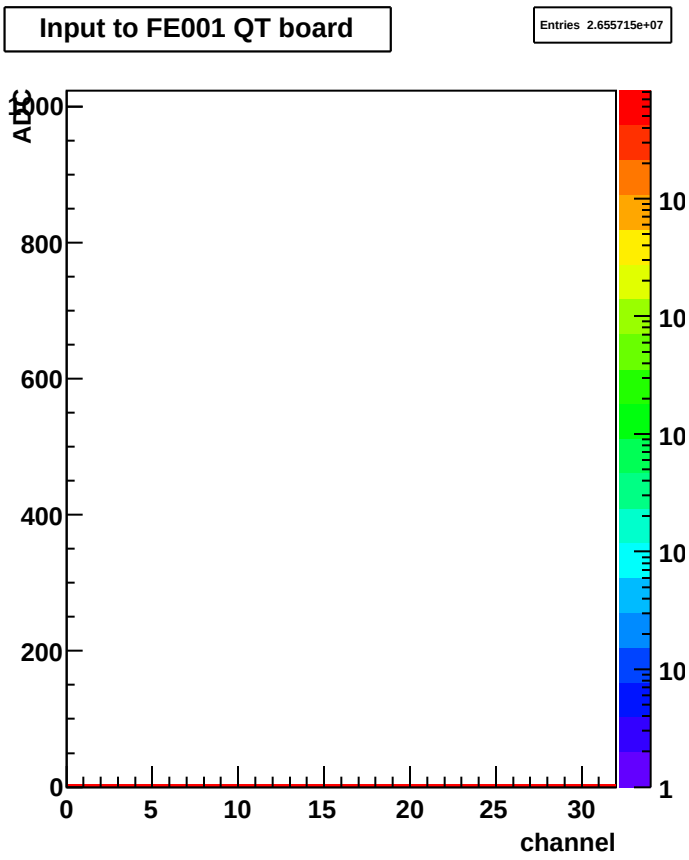

Input to FPD L2 DSM
Entries 2489733
CL bits
829911
829911
829911
SMALL
LARGE-S
LARGE-N

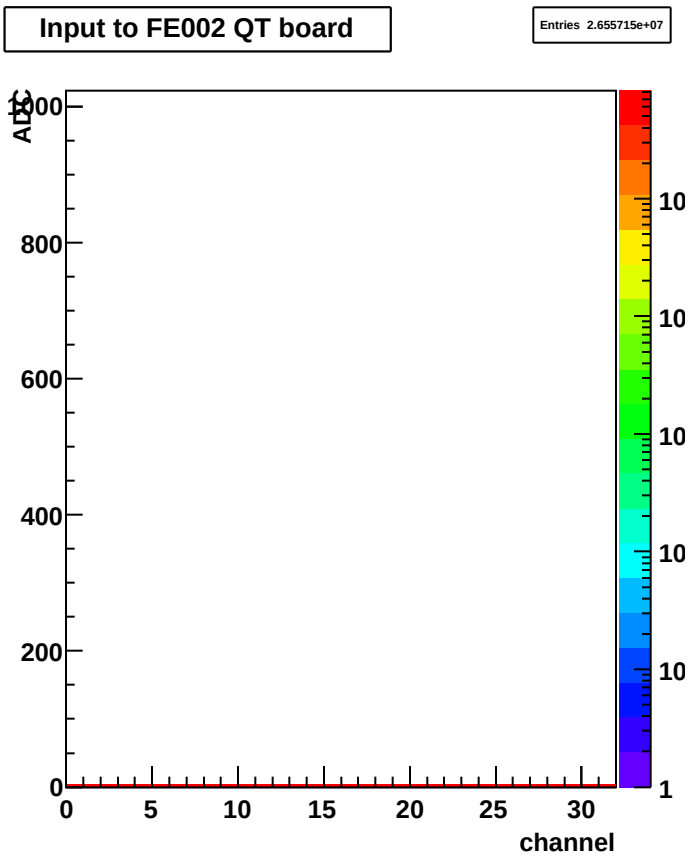

Input to FPD L2 DSM
Entries 2489733
CL bits - simulated
829911
829911
829911
SMALL
LARGE-S
LARGE-N

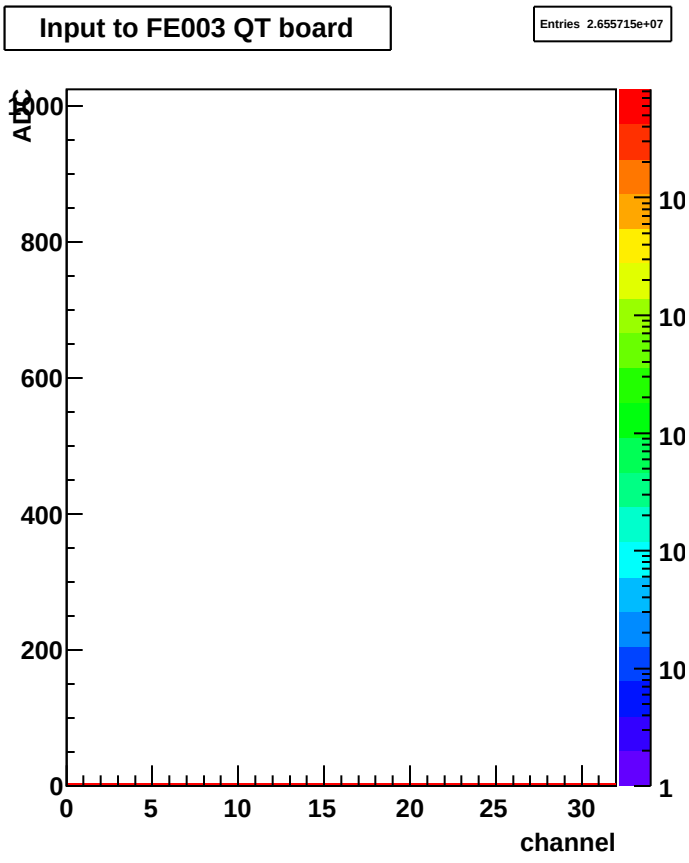

Input to FPD L2 DSM
Entries 2489733
HT bits
829838
829911
829911
73
SMALL
LARGE-S
LARGE-N

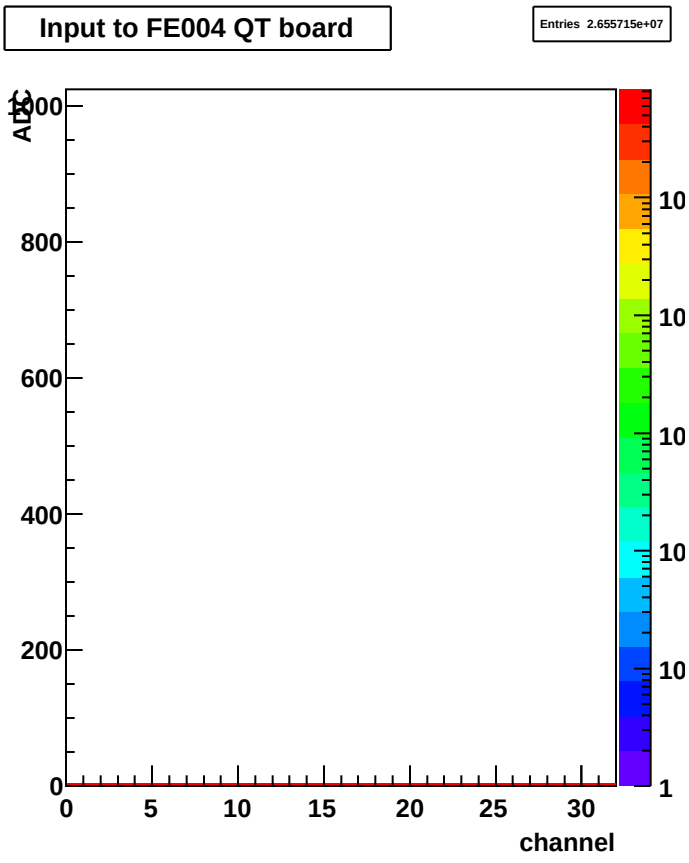

Input to FPD L2 DSM
Entries 2489733
HT bits - simulated
829838
829911
829911
73
SMALL
LARGE-S
LARGE-N


Input to FPD L2 DSM
Entries 3319644
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

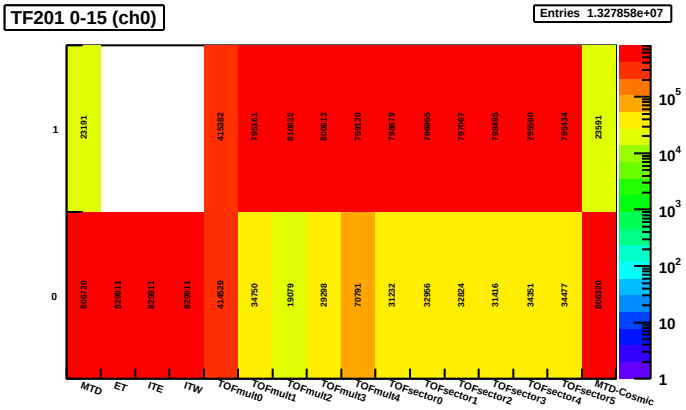

Input to FE001 QT board
Entries 2.655715e+07
ADC
channel


Input to FE002 QT board
Entries 2.655715e+07
ADC
channel

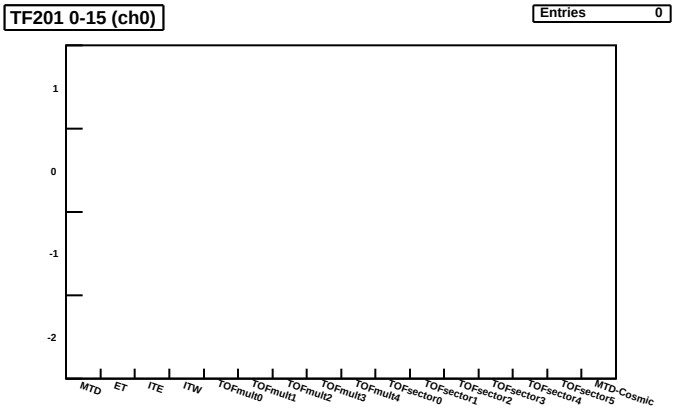

Input to FE003 QT board
Entries 2.655715e+07
ADC
channel


Input to FE004 QT board
Entries 2.655715e+07
ADC
channel

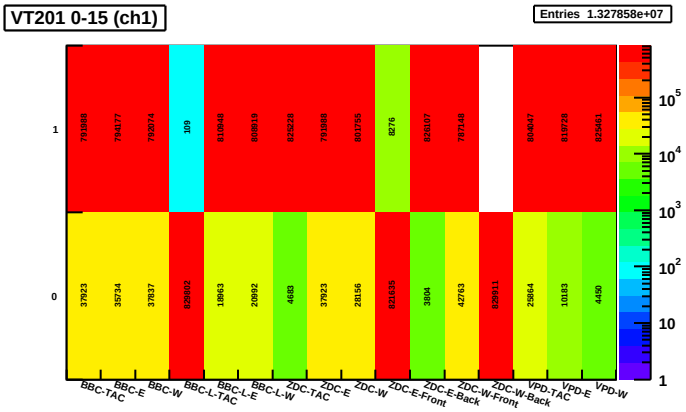
TF201 0-15 (ch0)



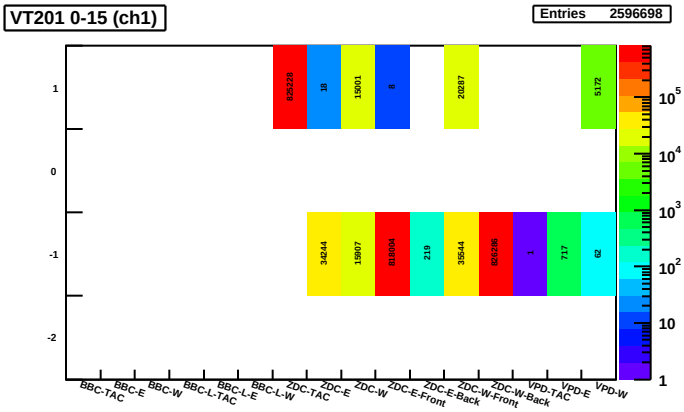
TF201 0-15 (ch0)



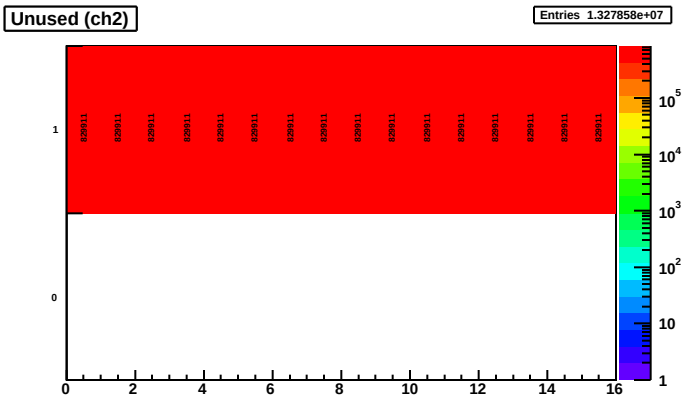
VT201 0-15 (ch1)

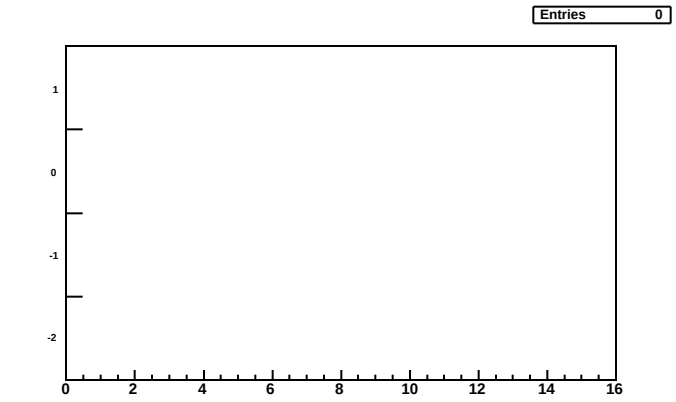


VT201 0-15 (ch1)

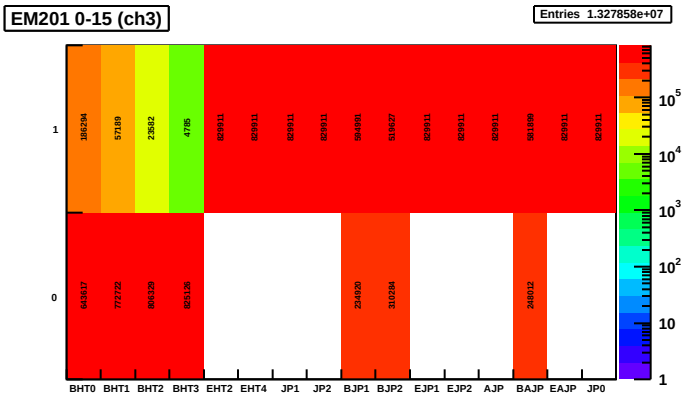


Unused (ch2)

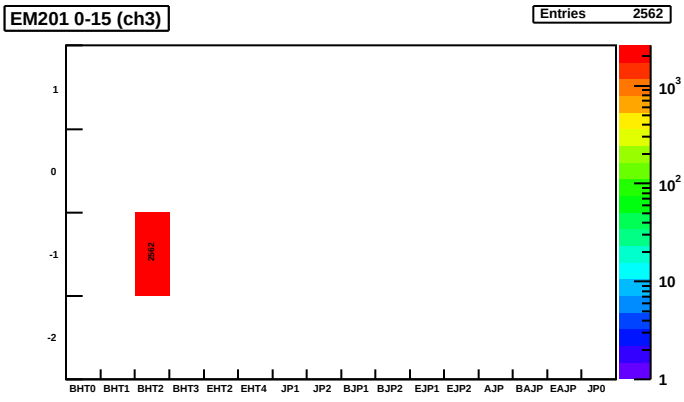




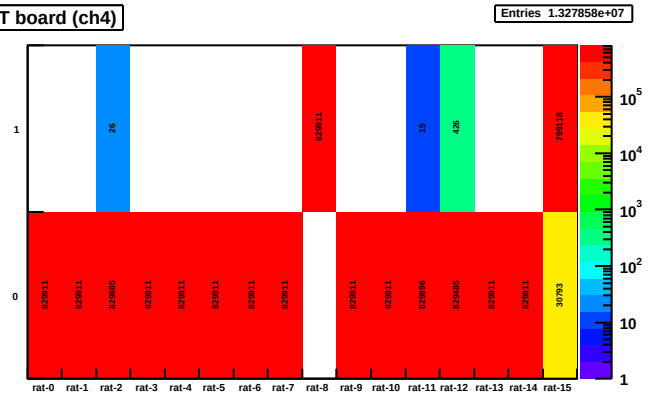
EM201 0-15 (ch3)

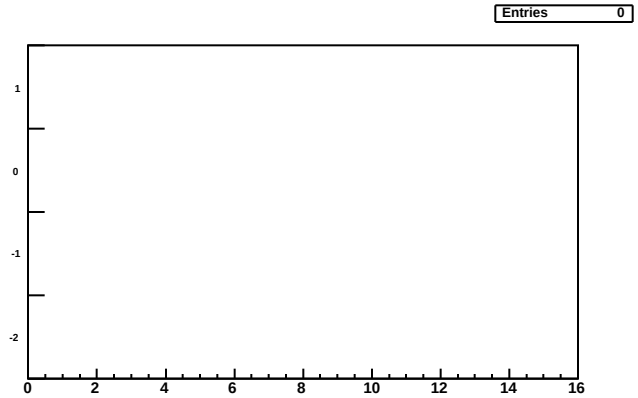


EM201 0-15 (ch3)

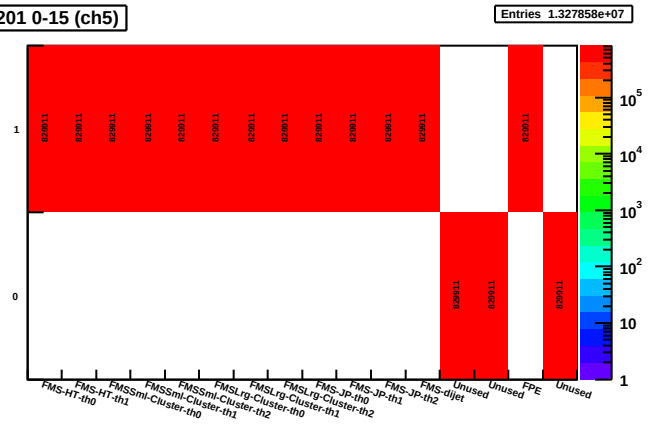


RAT board (ch4)

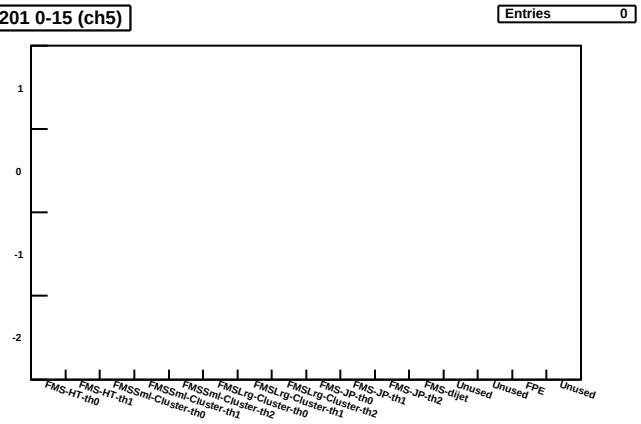




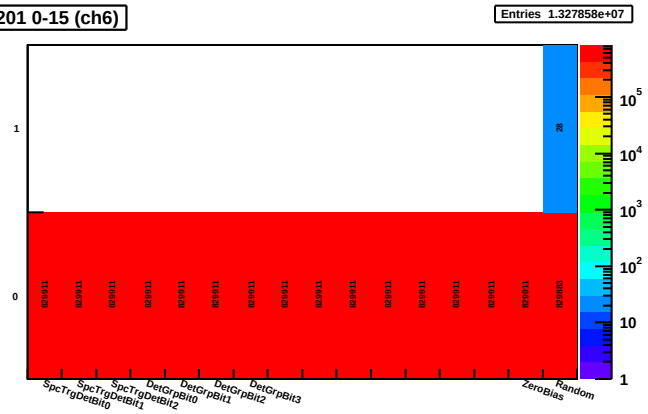
FP201 0-15 (ch5)

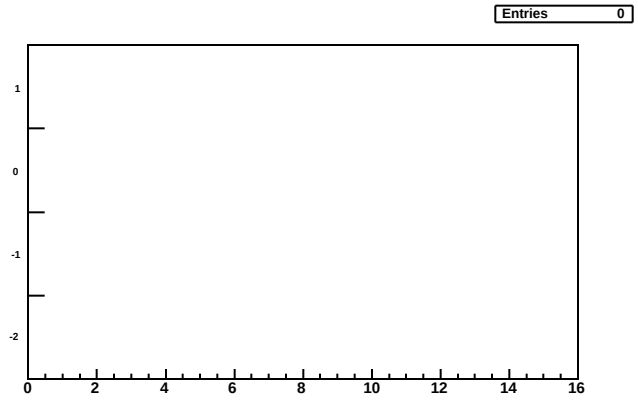
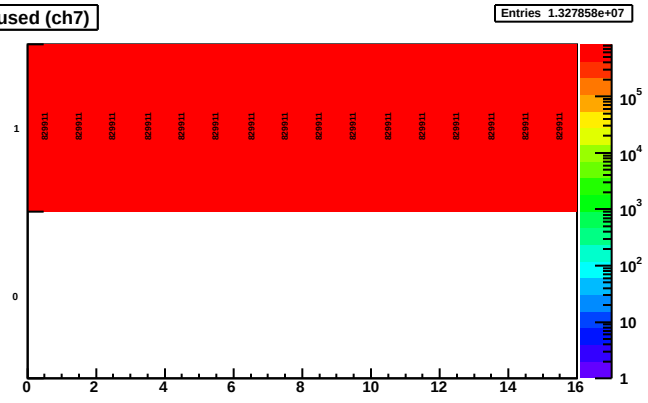
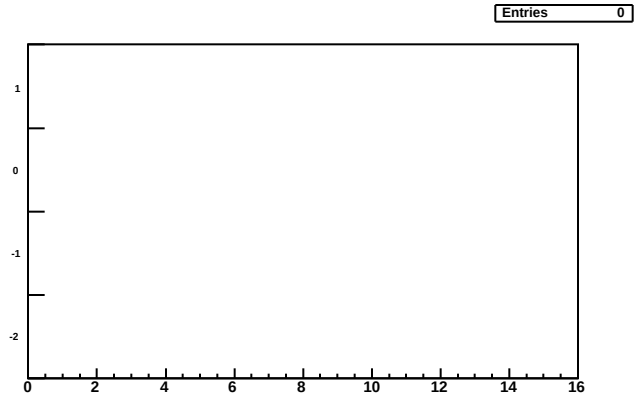


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)