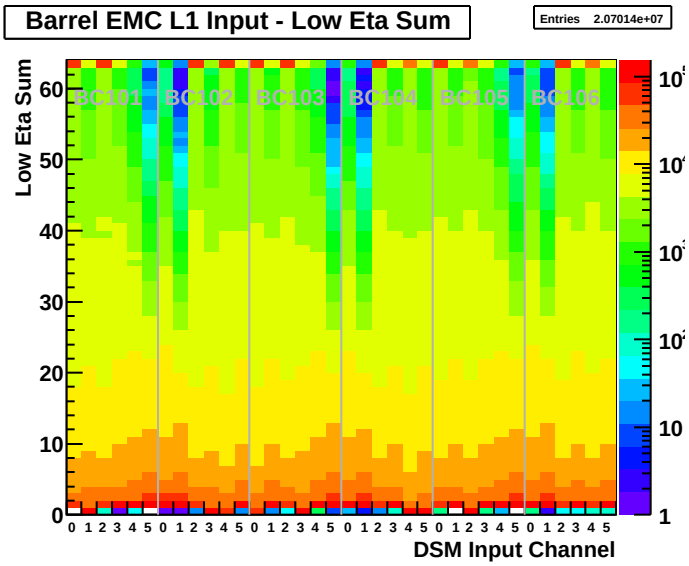
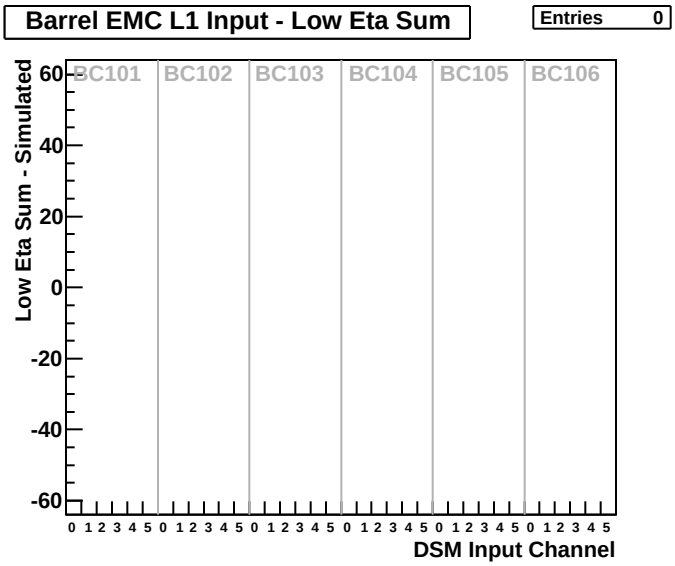
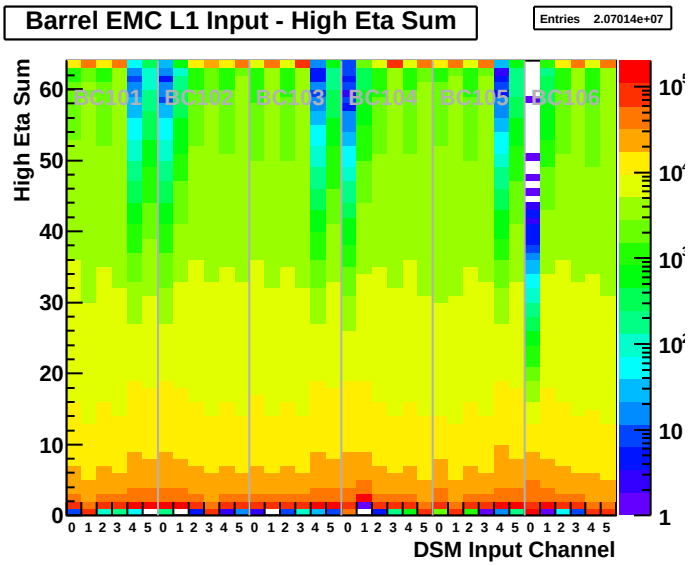
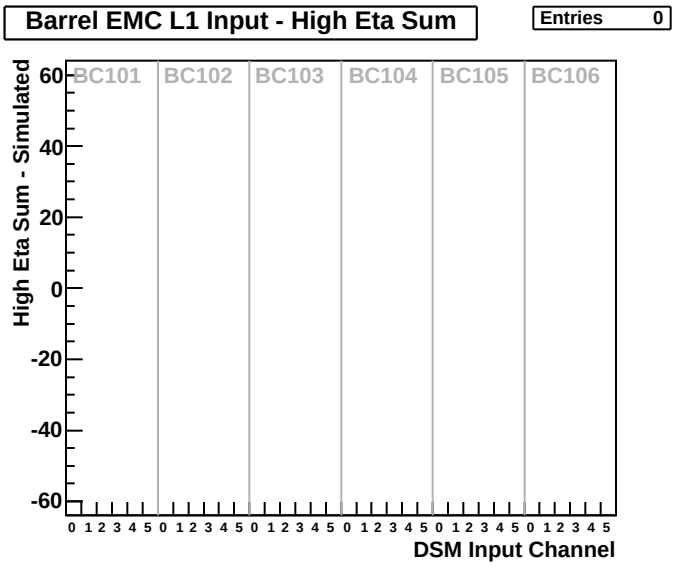
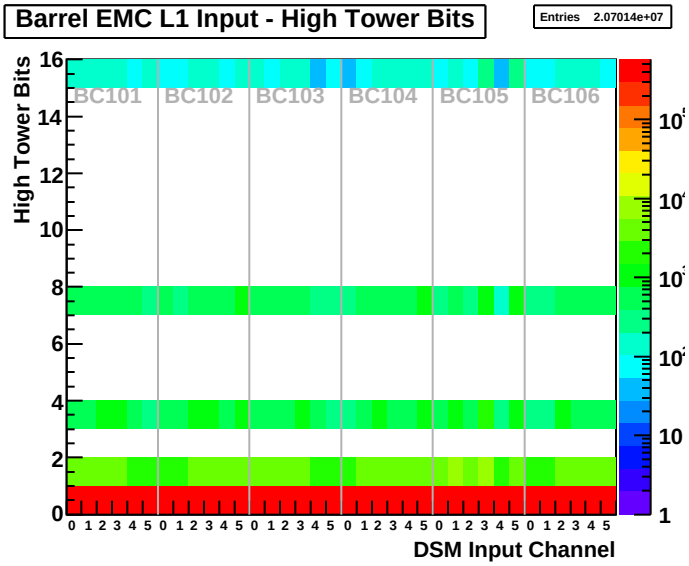
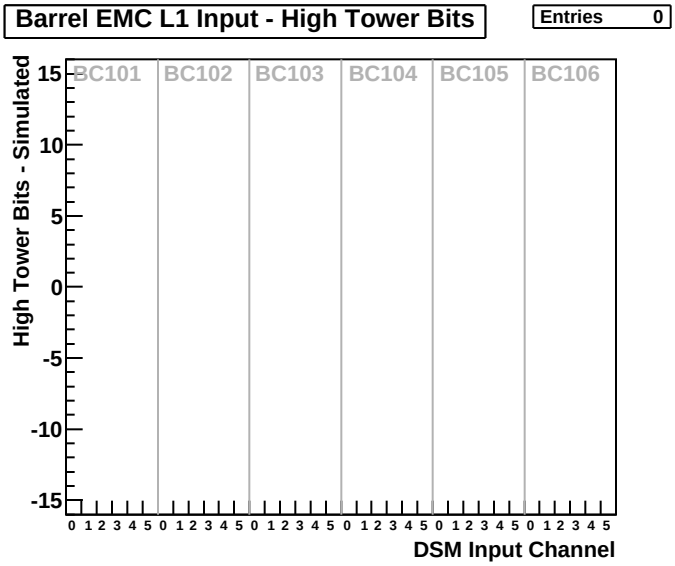

Barrel EMC L1 Input - Low Eta Sum
Entries 2.07014e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

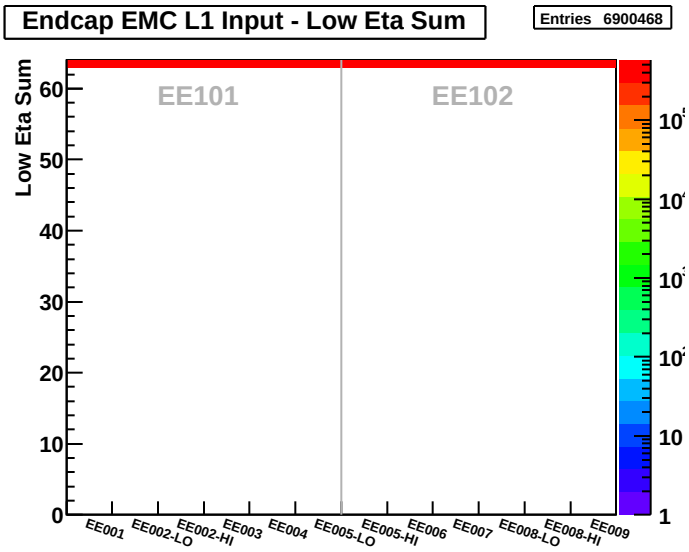

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

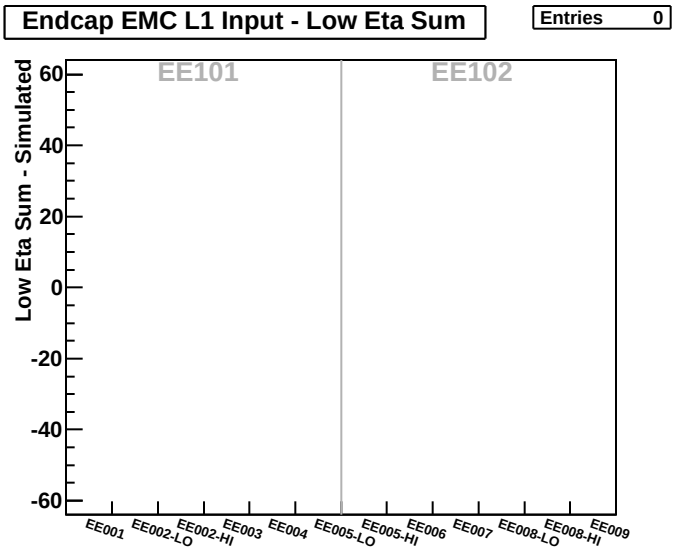

Barrel EMC L1 Input - High Eta Sum
Entries 2.07014e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

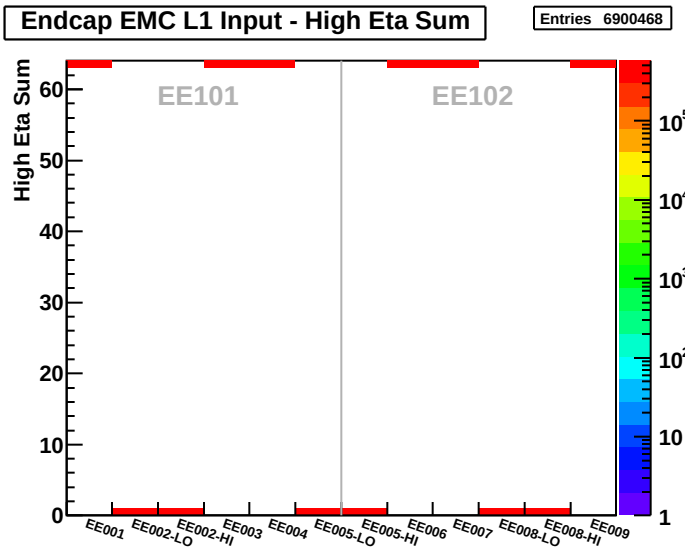

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

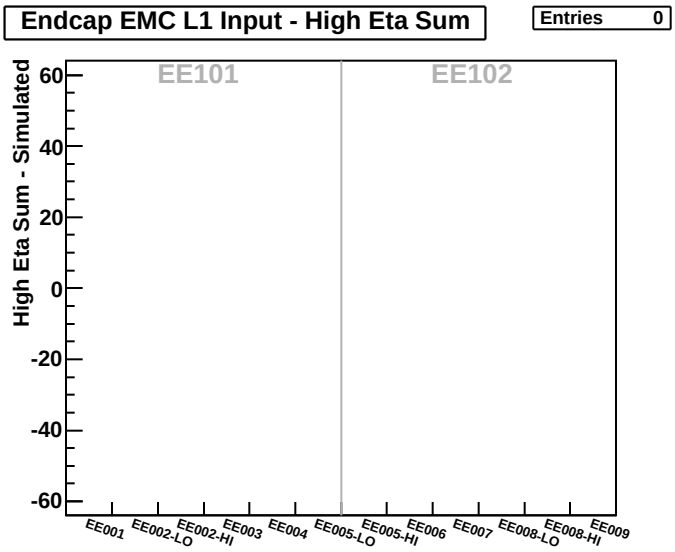

Barrel EMC L1 Input - High Tower Bits
Entries 2.07014e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

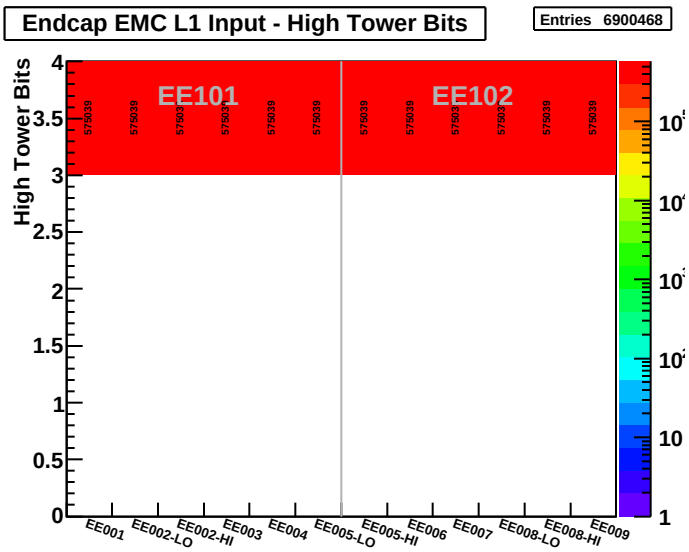

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

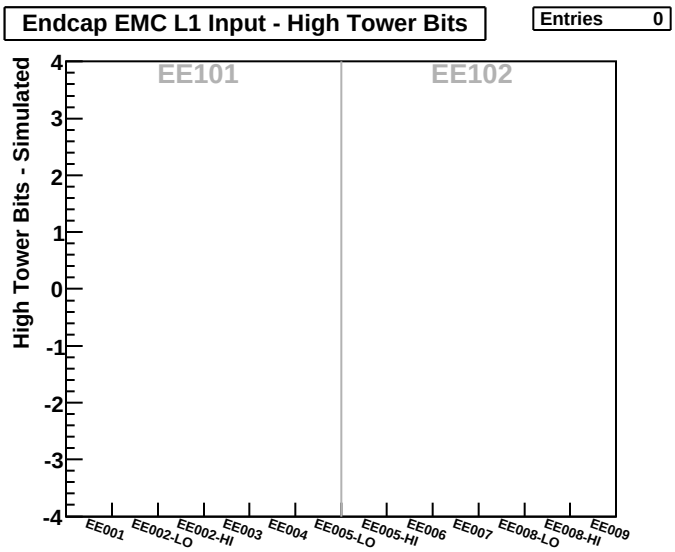

Endcap EMC L1 Input - Low Eta Sum
Entries 6900468
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

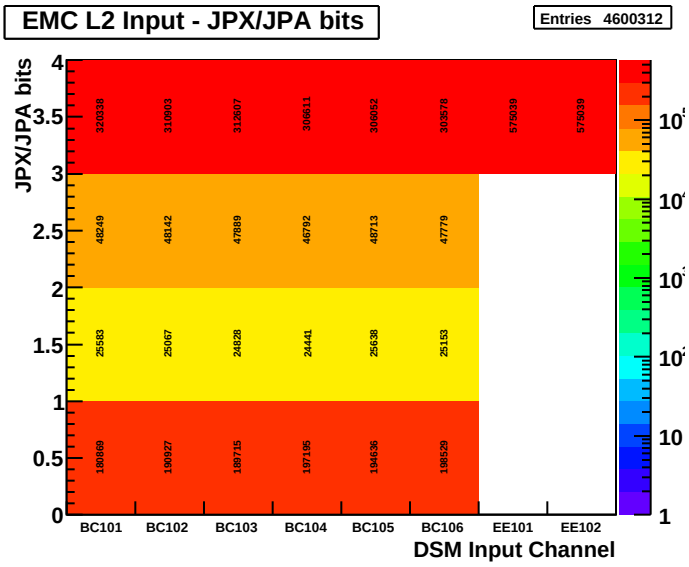

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

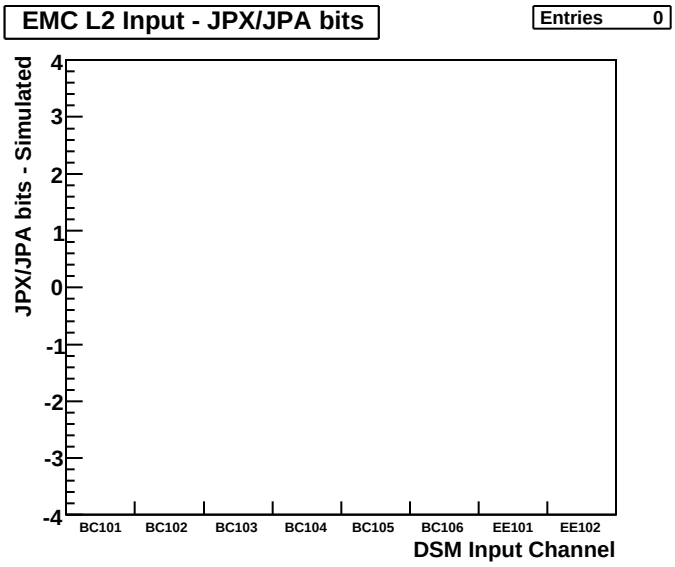

Endcap EMC L1 Input - High Eta Sum
Entries 6900468
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

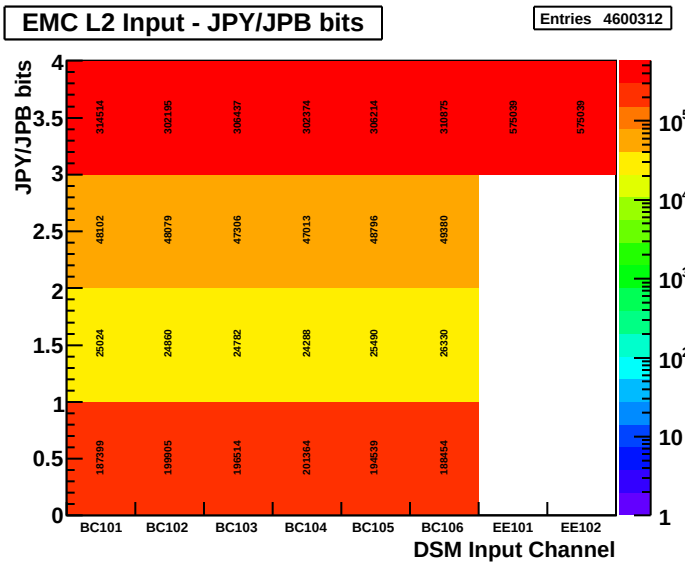

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

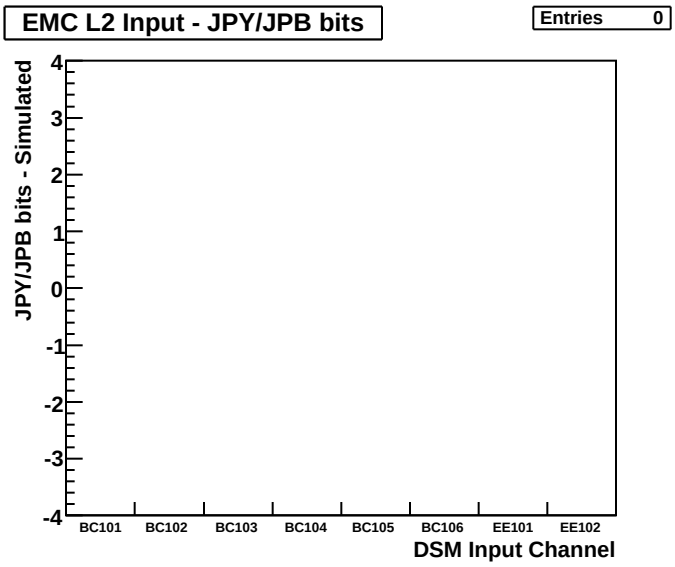

Endcap EMC L1 Input - High Tower Bits
Entries 6900468
High Tower Bits
EE101
EE102
575039
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

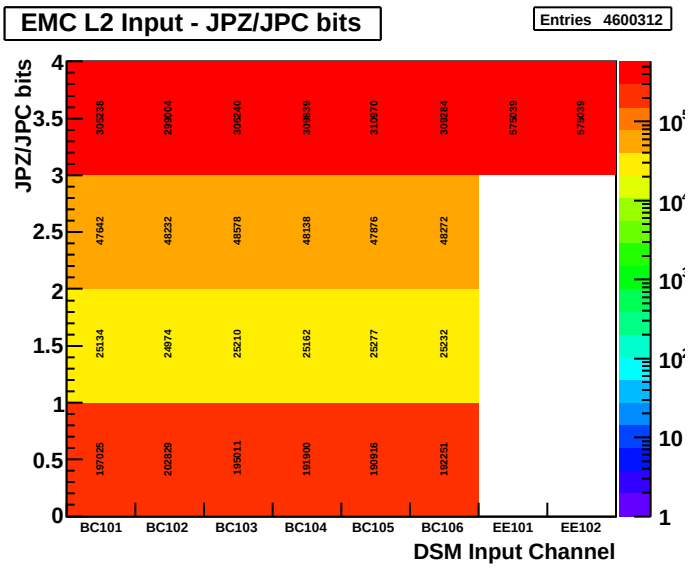

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

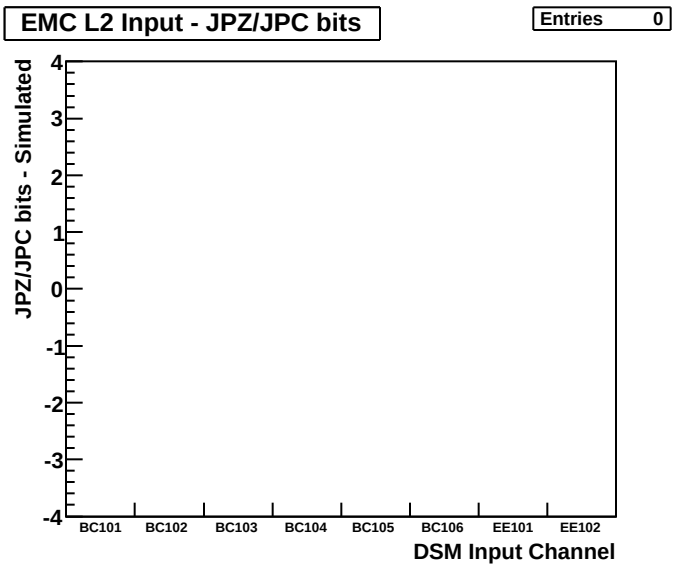

EMC L2 Input - JPX/JPA bits
Entries 4600312
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
320338 310903 312607 306611 306052 303578 575039 575039
48249 48142 47689 46792 48713 47779
25583 25067 24828 24441 25638 25153
180869 190927 188715 197195 194636 198529

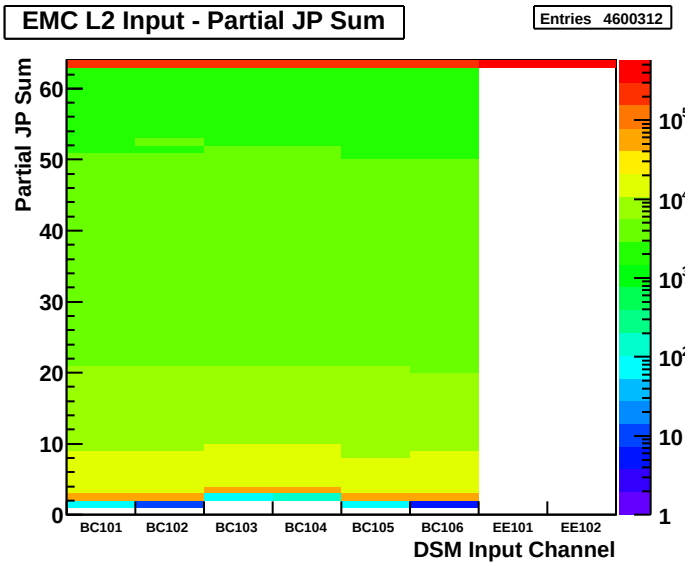

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

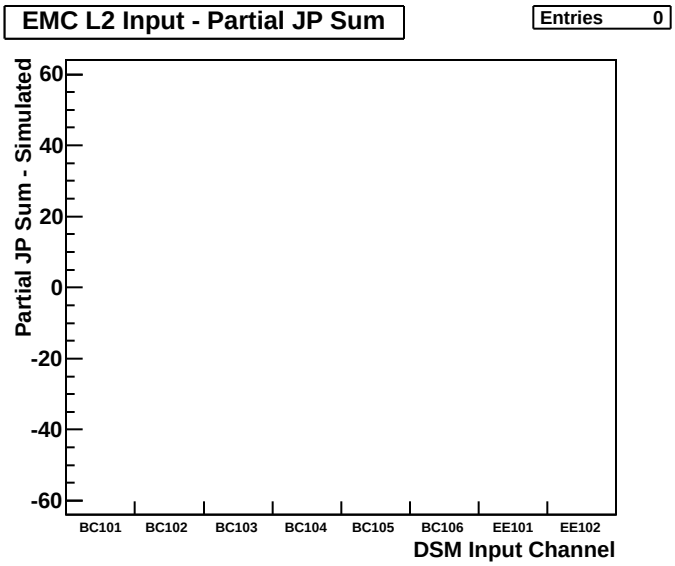

EMC L2 Input - JPY/JPB bits
Entries 4600312
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
314514 302195 306437 302374 306214 310875 575039 575039
48102 48079 47306 47013 48796 49380
25024 24860 24782 24288 25490 26330
187399 199805 196514 201364 194539 188454

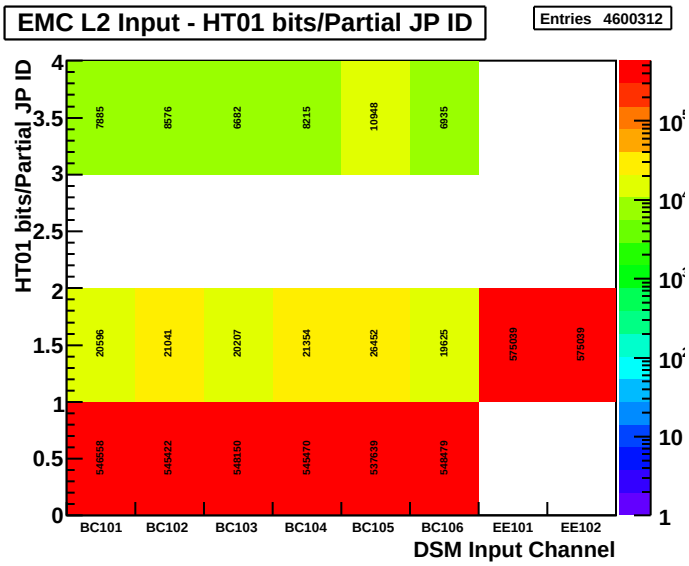

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

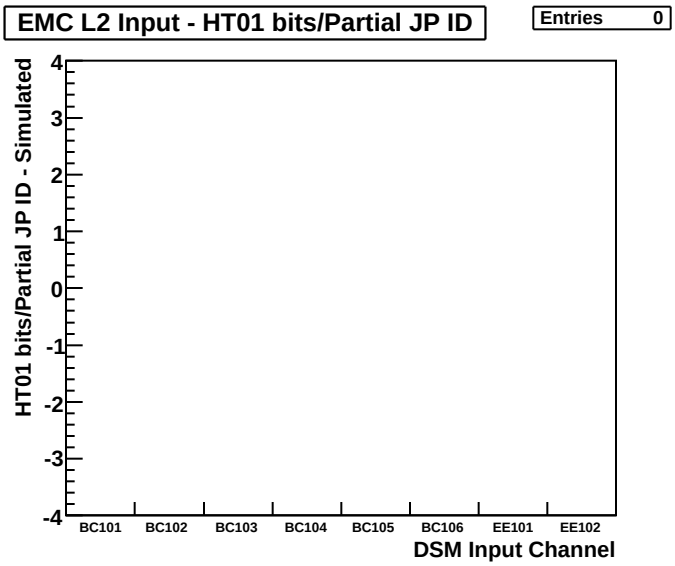

EMC L2 Input - JPZ/JPC bits
Entries 4600312
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
305238 299004 308240 308839 310970 309284 575039 575039
47642 48232 48578 48138 47876 48272
25134 24974 25210 25162 25277 25232
197025 202829 199011 191900 190916 192251

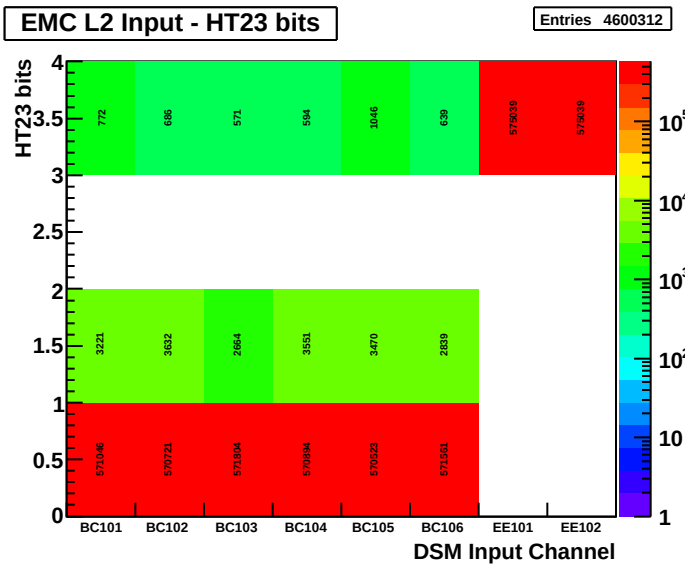

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

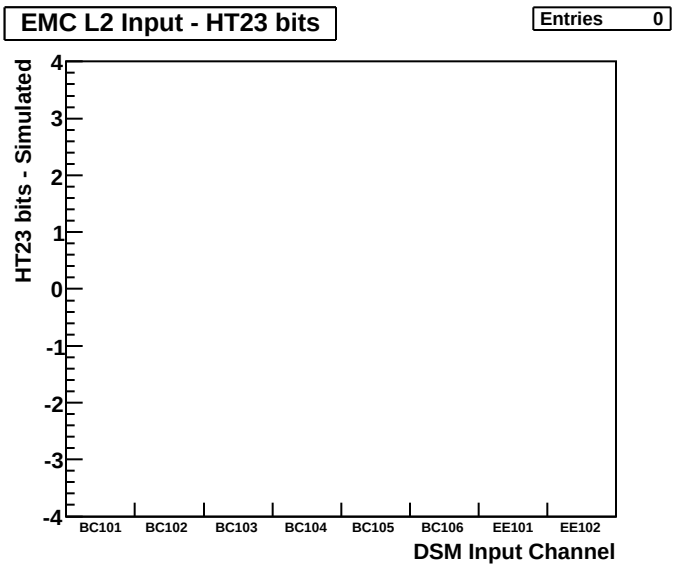

EMC L2 Input - Partial JP Sum
Entries 4600312
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

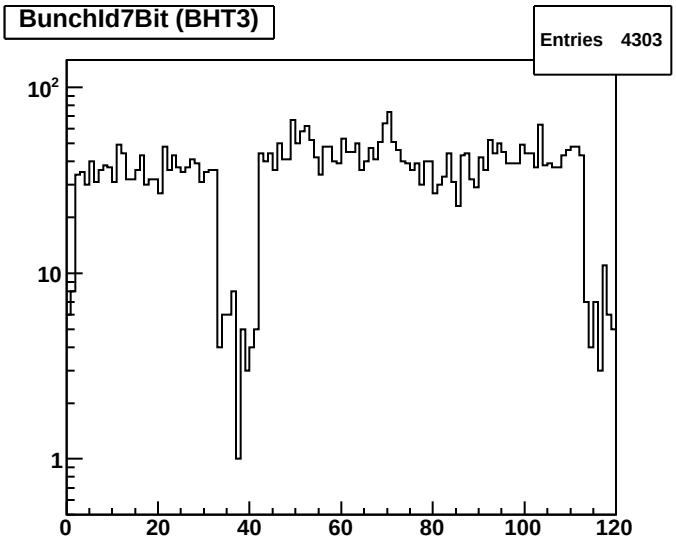

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

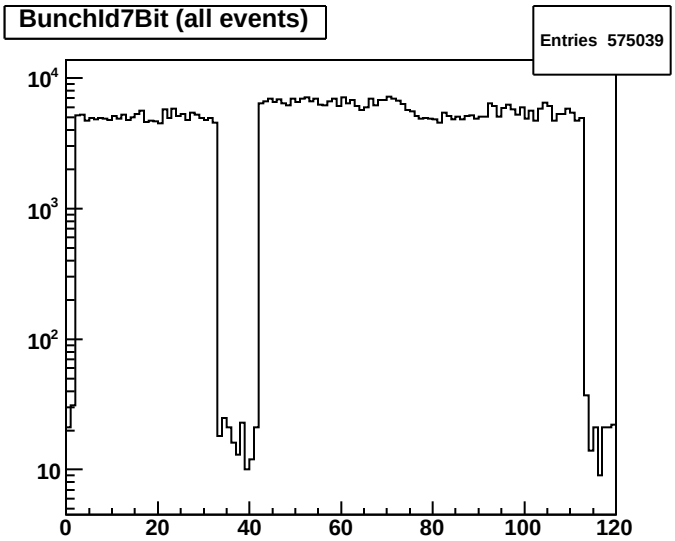

EMC L2 Input - HT01 bits/Partial JP ID
Entries 4600312
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

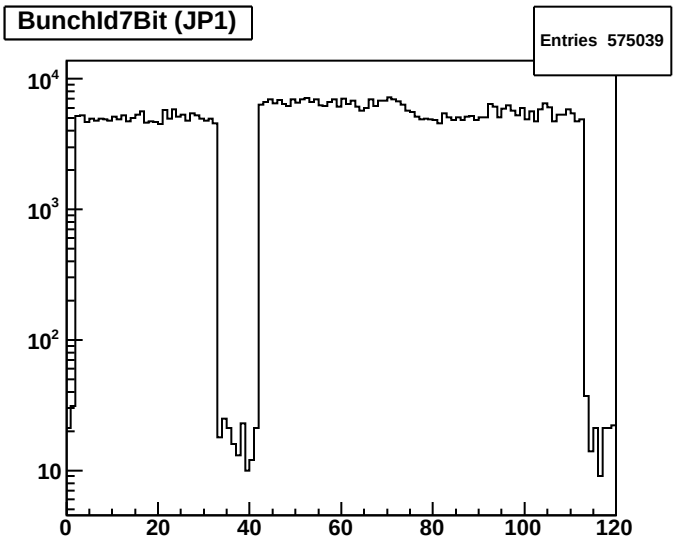

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

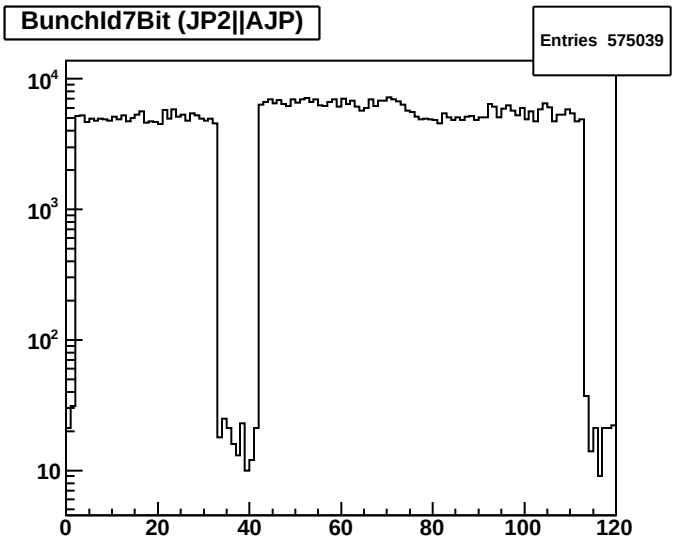

EMC L2 Input - HT23 bits
Entries 4600312
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

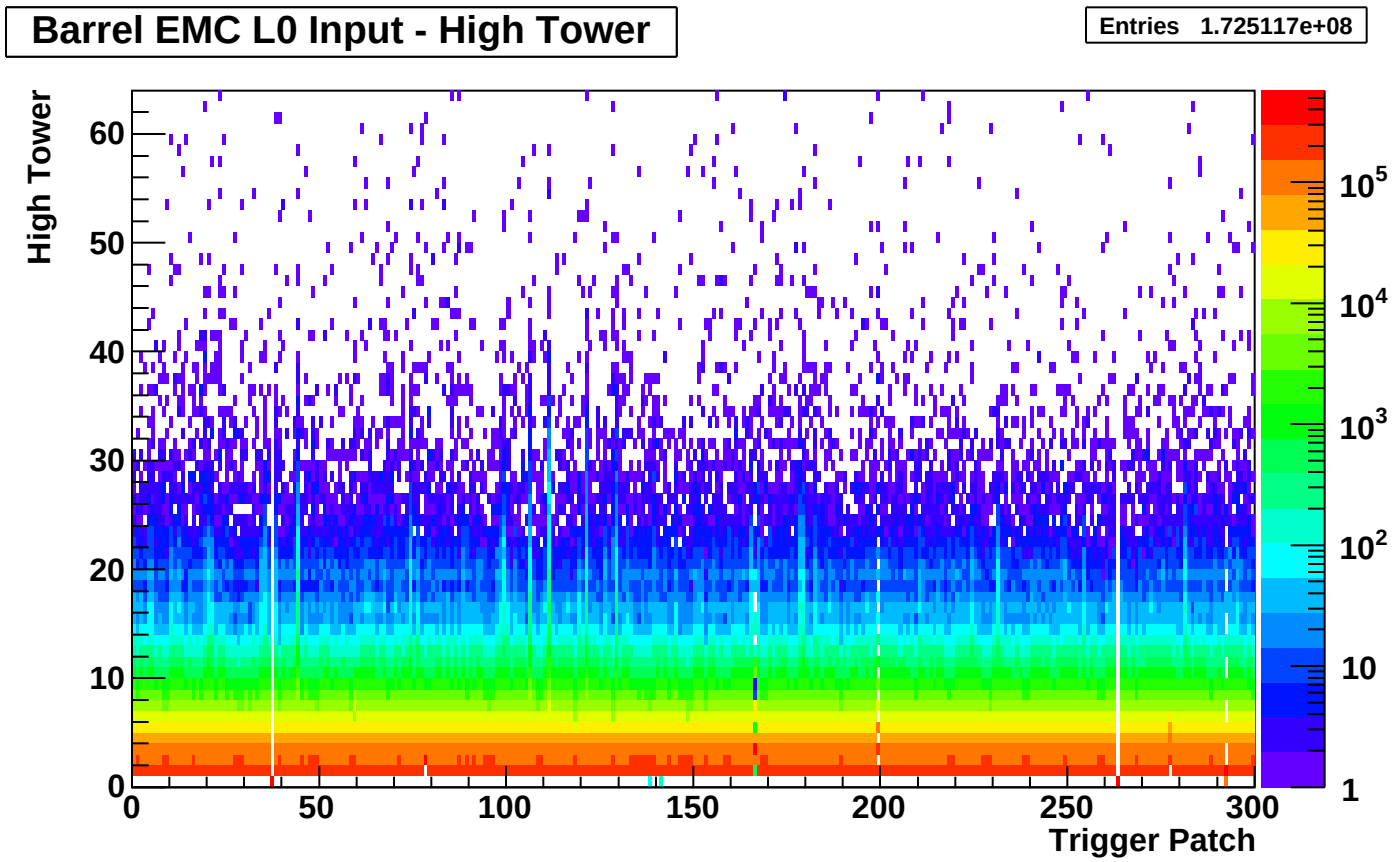

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

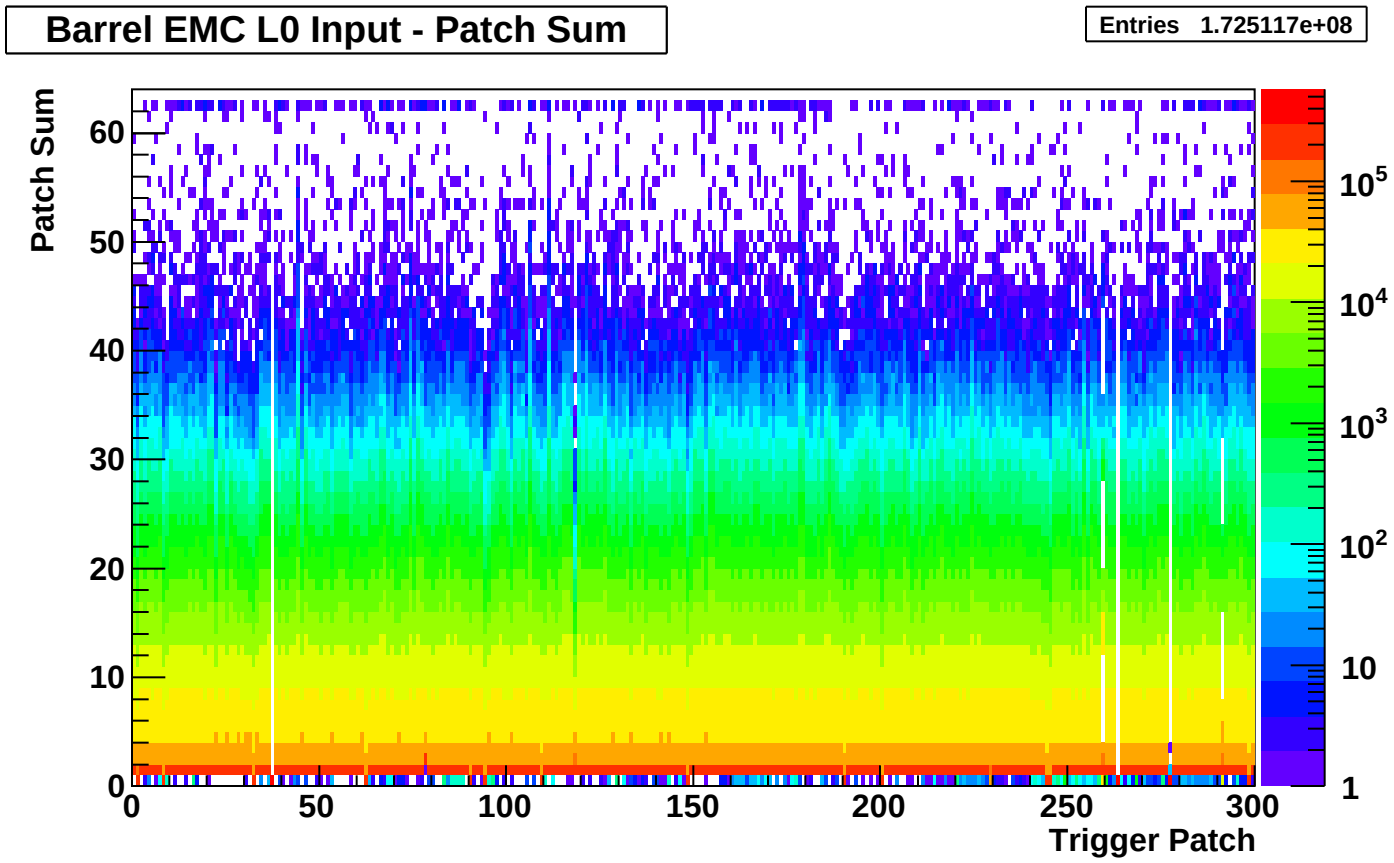

BunchId7Bit (BHT3)
Entries 4303
10^2
10
1
0
20
40
60
80
100
120

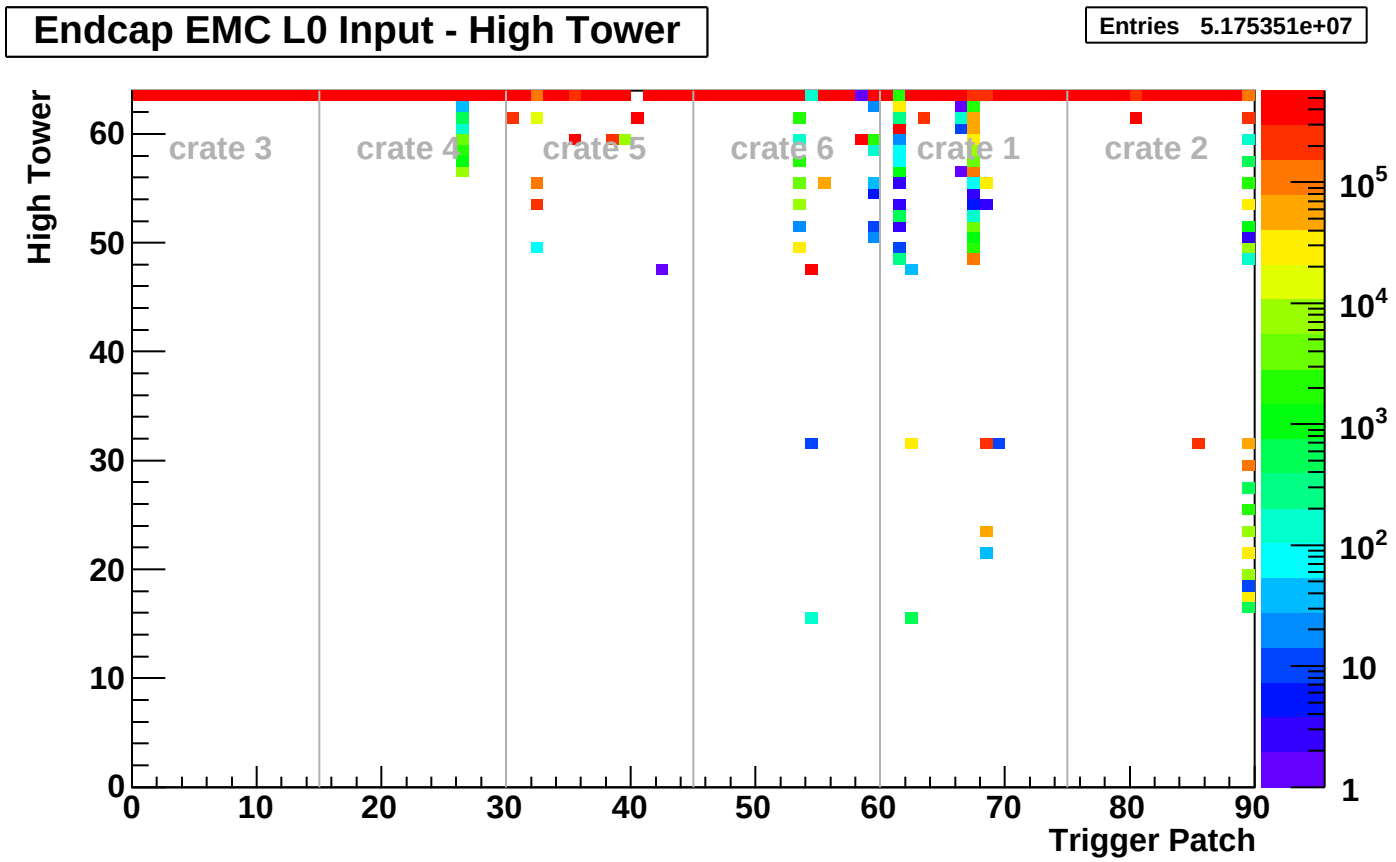

BunchId7Bit (all events)
Entries 575039
10^4
10^3
10^2
10
0
20
40
60
80
100
120

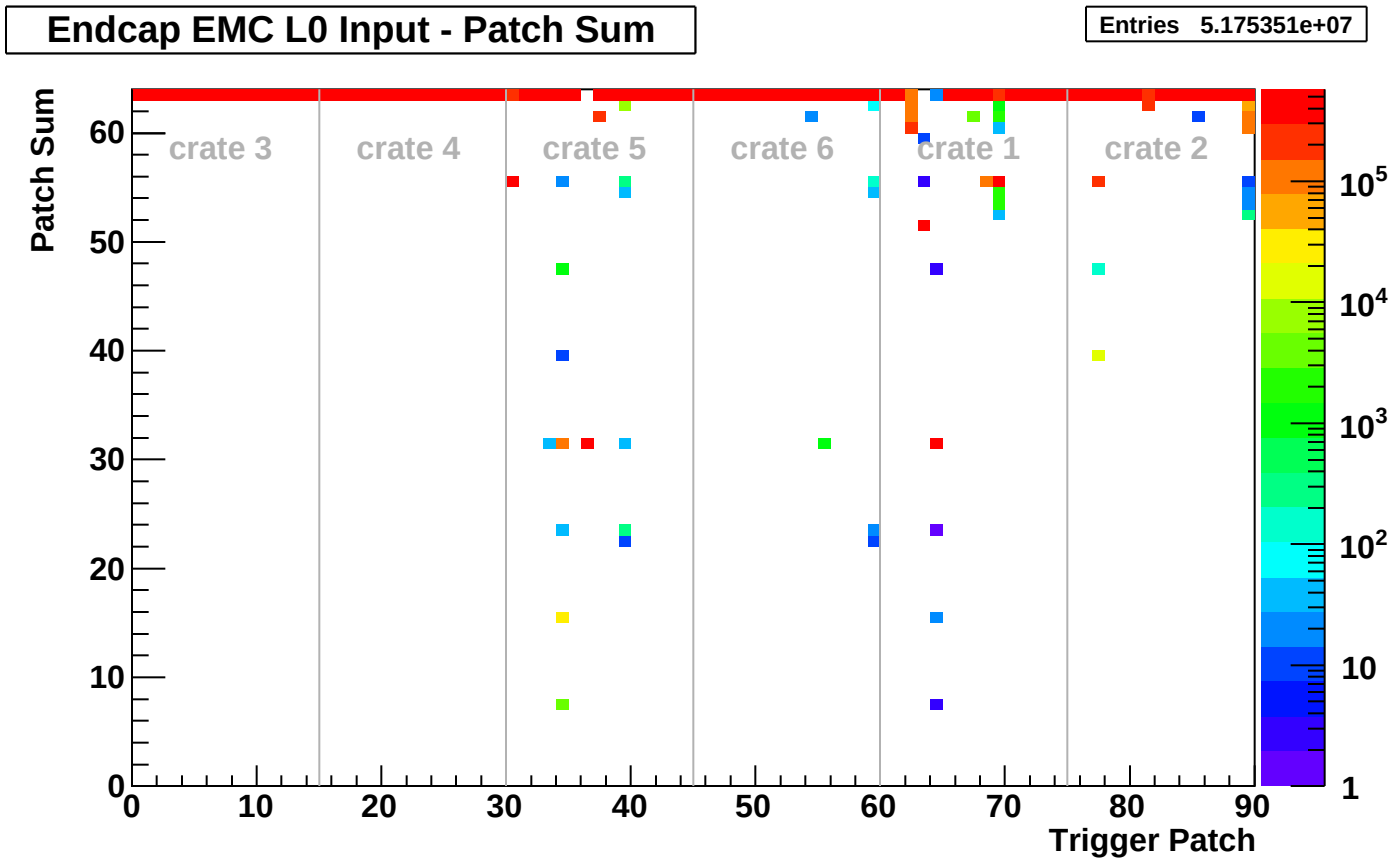

BunchId7Bit (JP1)
Entries 575039
10^4
10^3
10^2
10
0
20
40
60
80
100
120

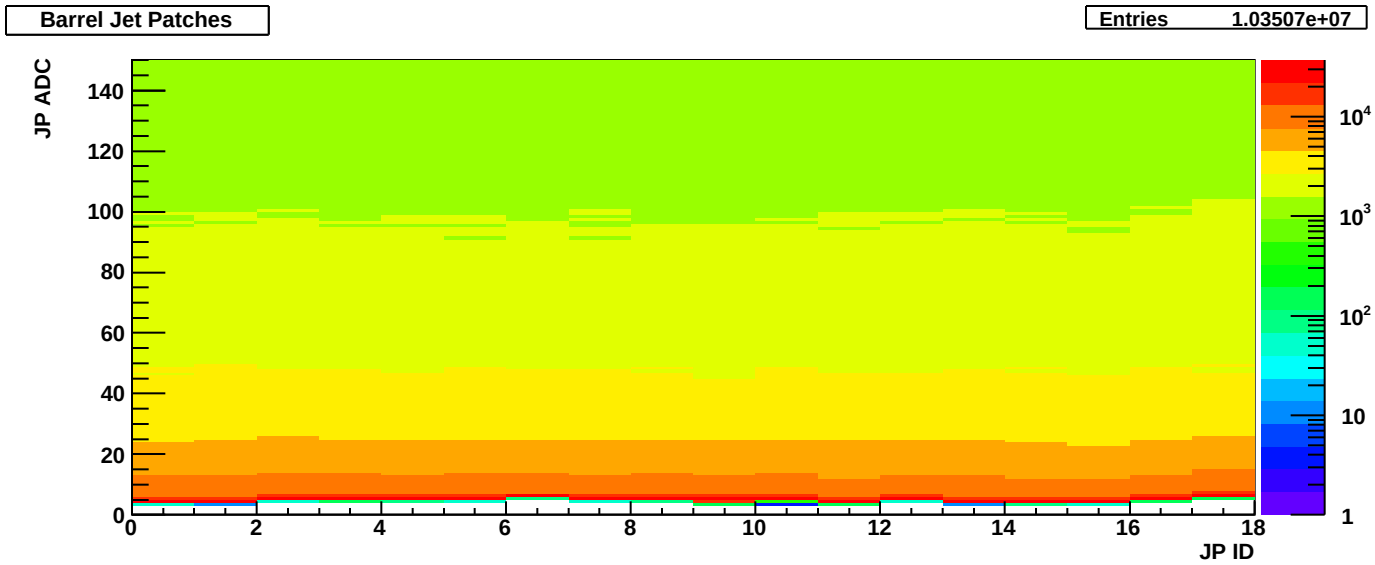

BunchId7Bit (JP2||AJP)
Entries 575039
10^4
10^3
10^2
10
0
20
40
60
80
100
120

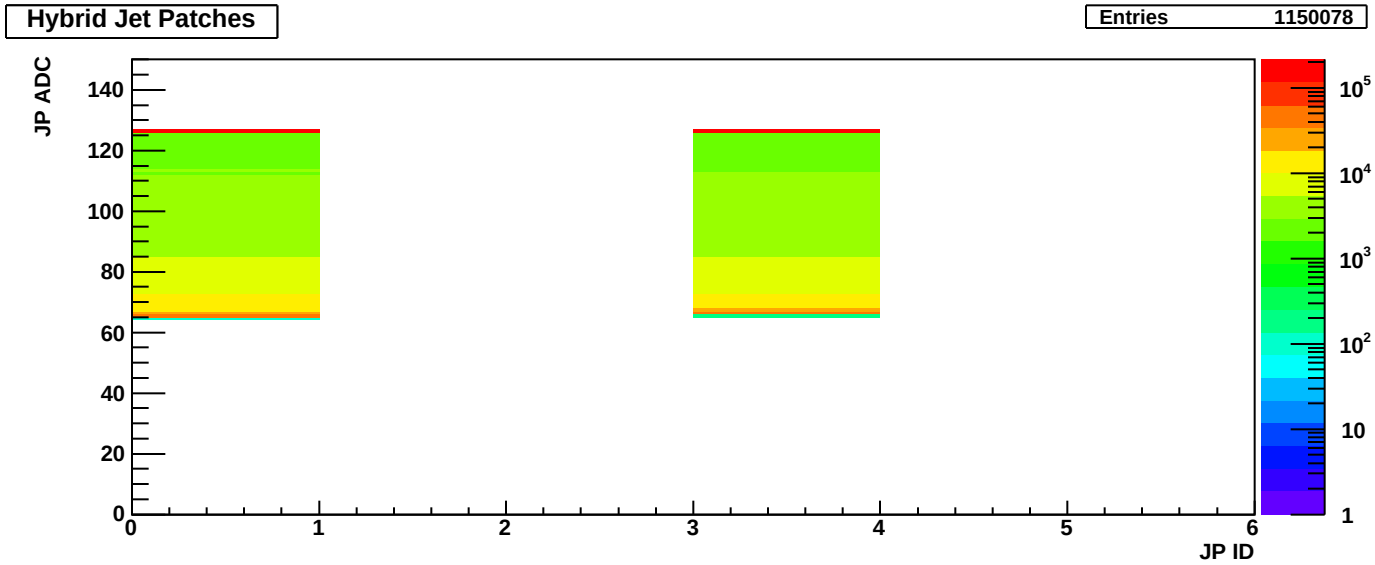

Barrel EMC L0 Input - High Tower
Entries 1.725117e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

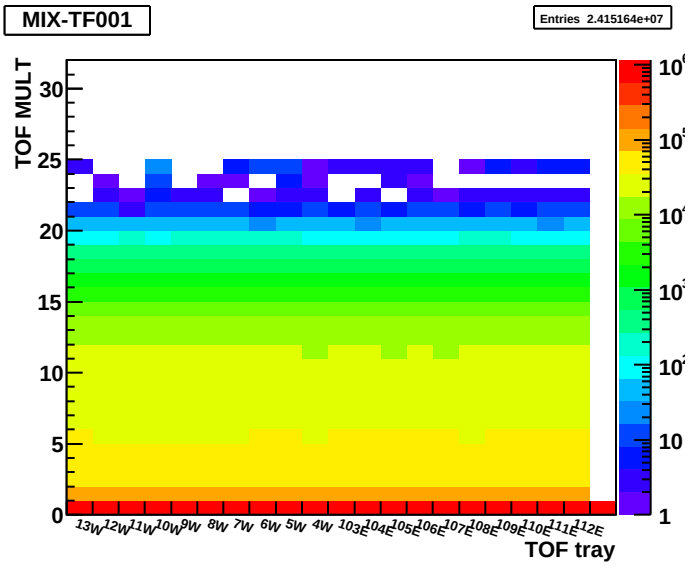

Barrel EMC L0 Input - Patch Sum
Entries 1.725117e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

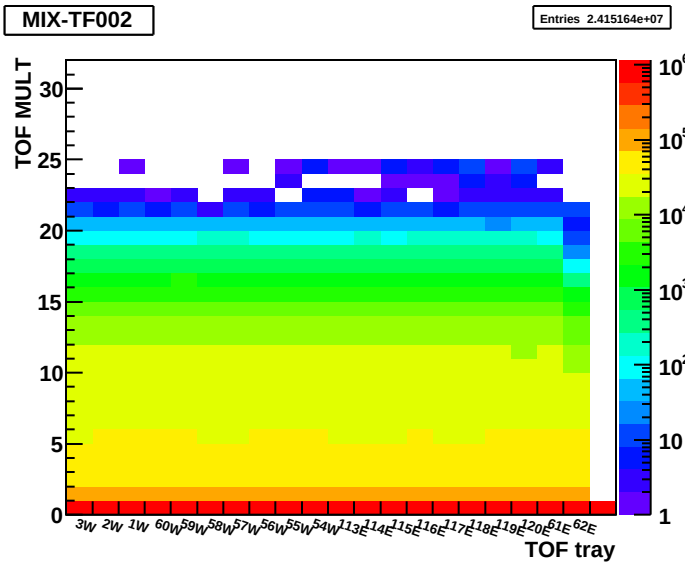

Endcap EMC L0 Input - High Tower
Entries 5.175351e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

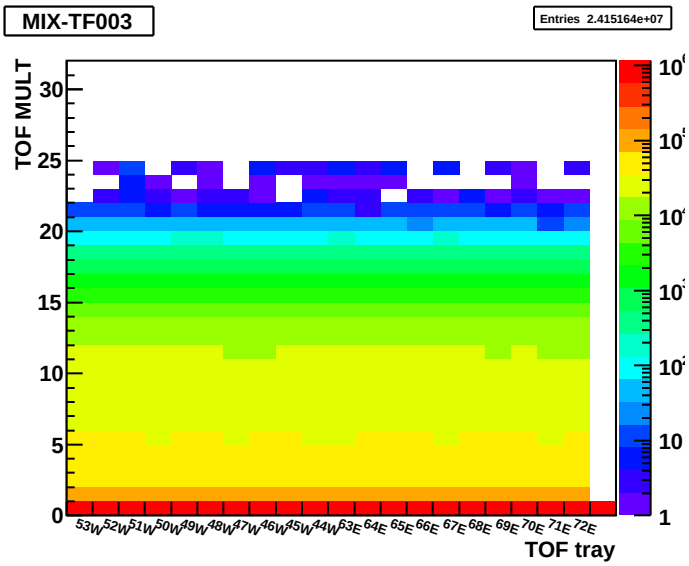

Endcap EMC L0 Input - Patch Sum
Entries 5.175351e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

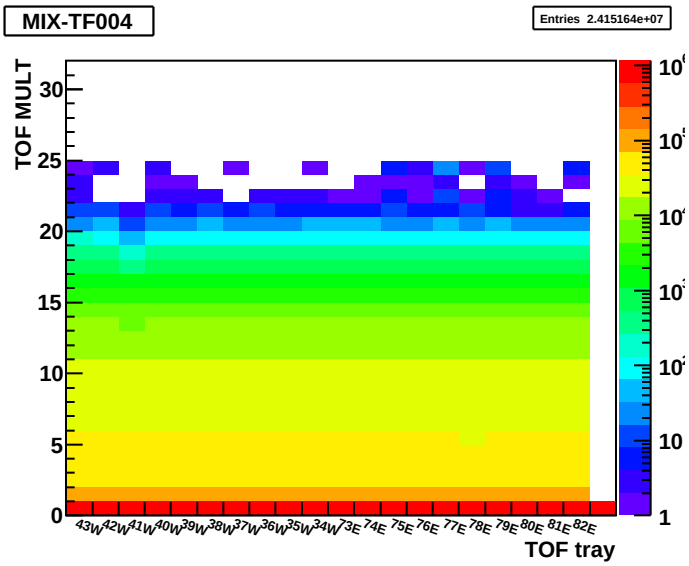

Barrel Jet Patches
Entries 1.03507e+07
JP ADC
JP ID

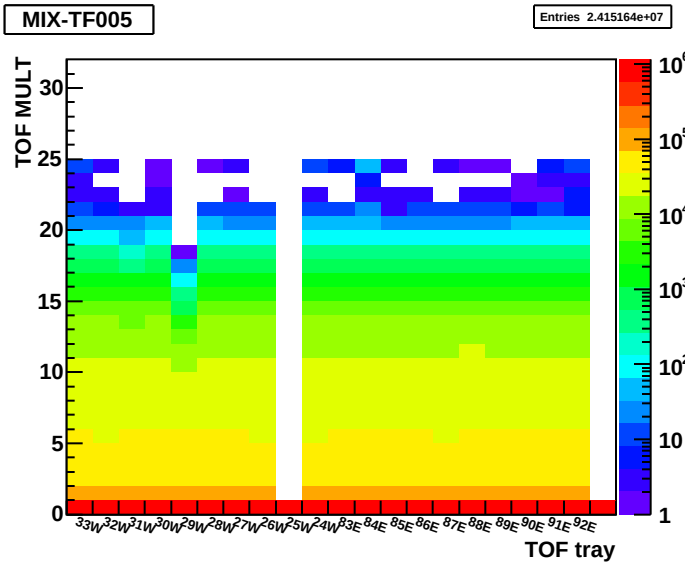

Hybrid Jet Patches
Entries 1150078
JP ADC
JP ID

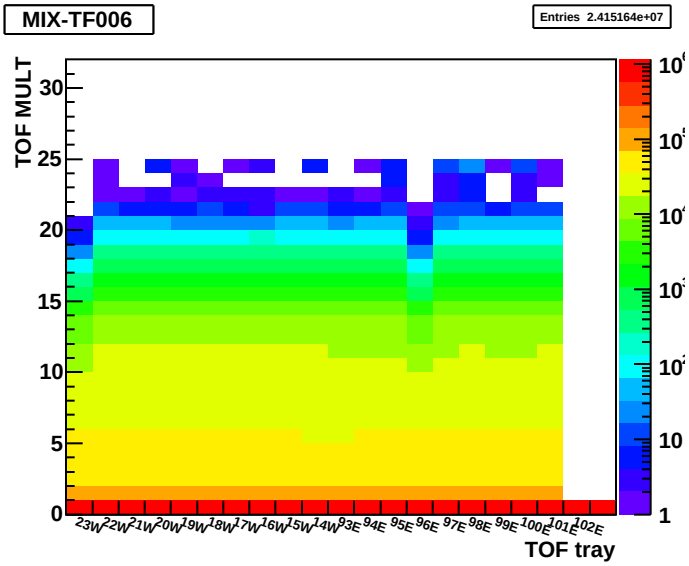

MIX-TF001
Entries 2.415164e+07
TOF MULT
TOF tray

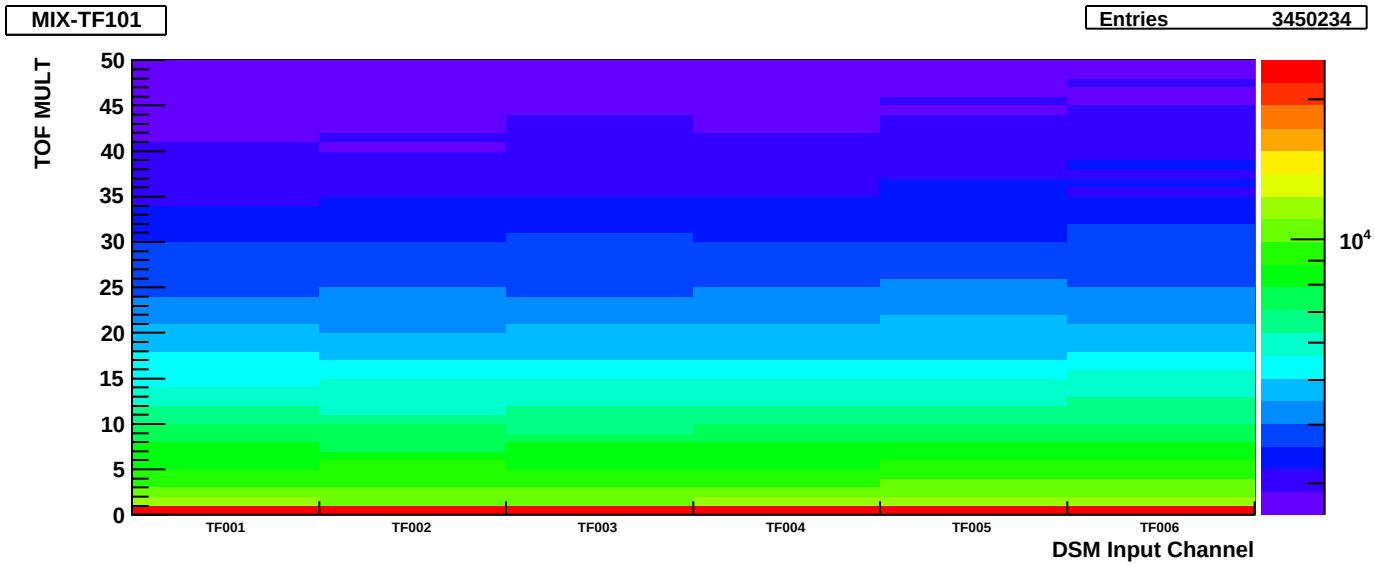

MIX-TF002
Entries 2.415164e+07
TOF MULT
TOF tray

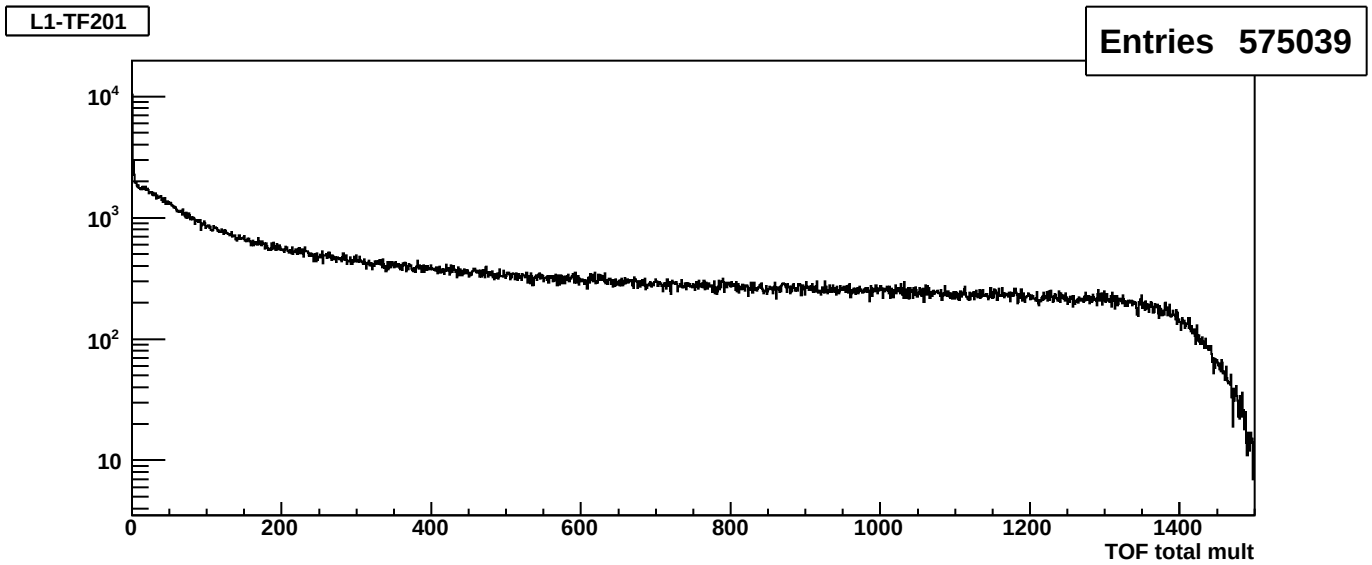

MIX-TF003
Entries 2.415164e+07
TOF MULT
TOF tray

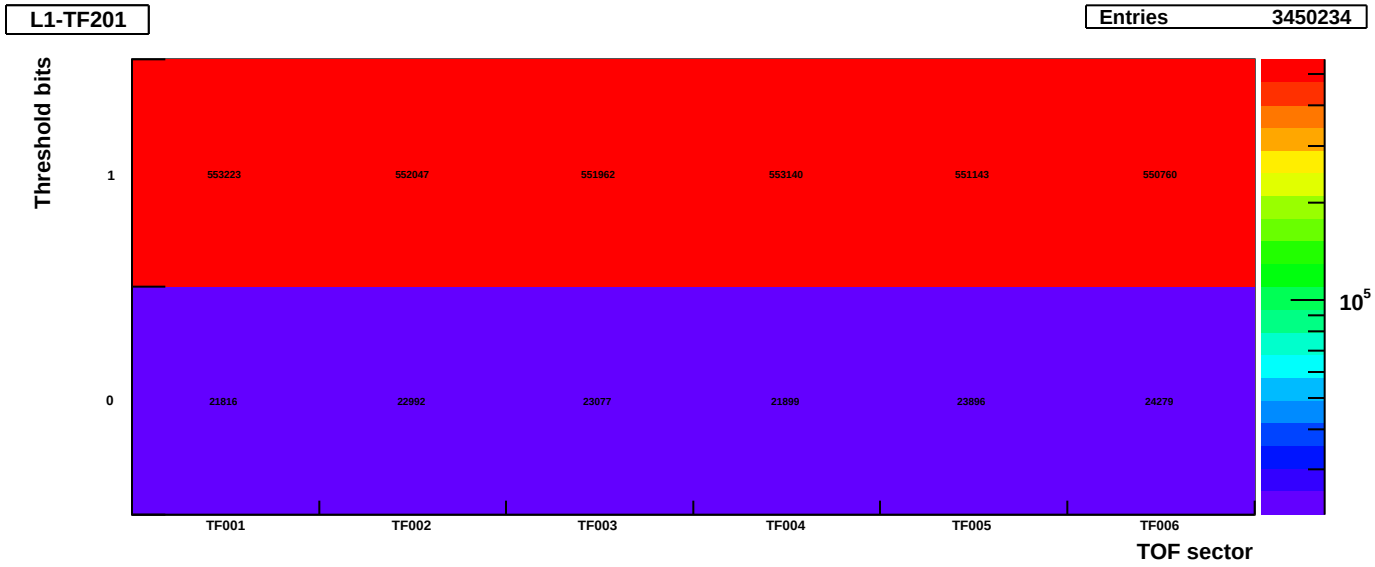

MIX-TF004
Entries 2.415164e+07
TOF MULT
TOF tray

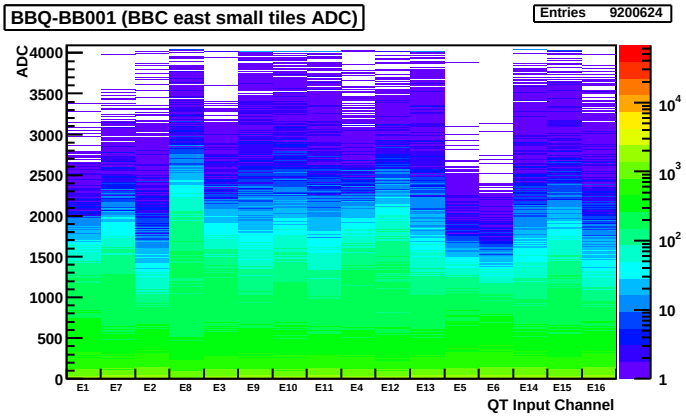

MIX-TF005
Entries 2.415164e+07
TOF MULT
TOF tray

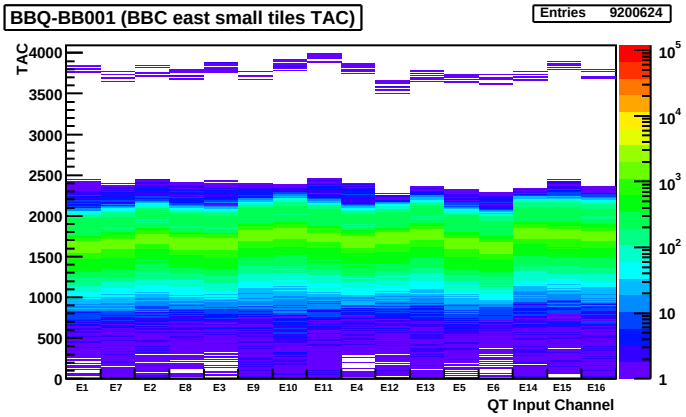

MIX-TF006
Entries 2.415164e+07
TOF MULT
TOF tray

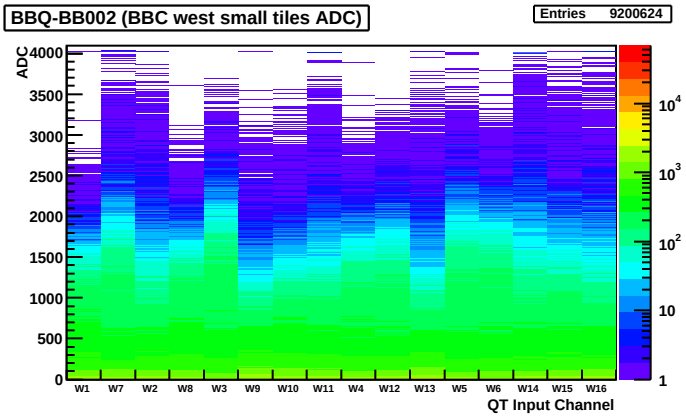

MIX-TF101
Entries 3450234
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

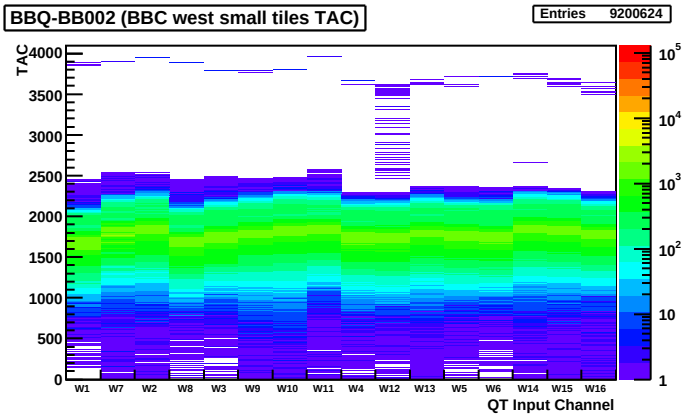

L1-TF201
Entries 575039
10^4
10^3
10^2
10
0
200
400
600
800
1000
1200
1400
TOF total mult

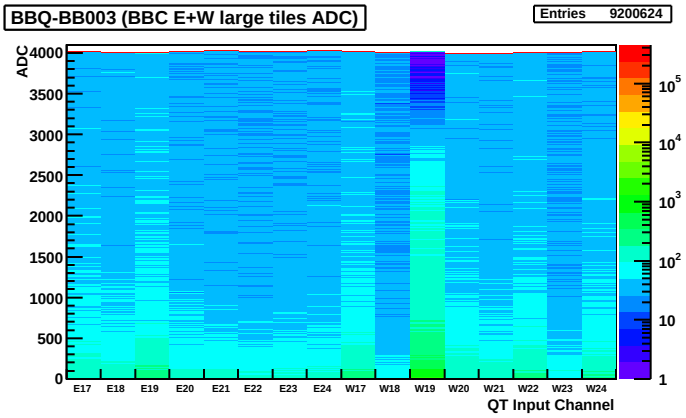

L1-TF201
Entries 3450234
Threshold bits
1
0
553223
552047
551962
553140
551143
550760
21816
22992
23077
21899
23896
24279
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

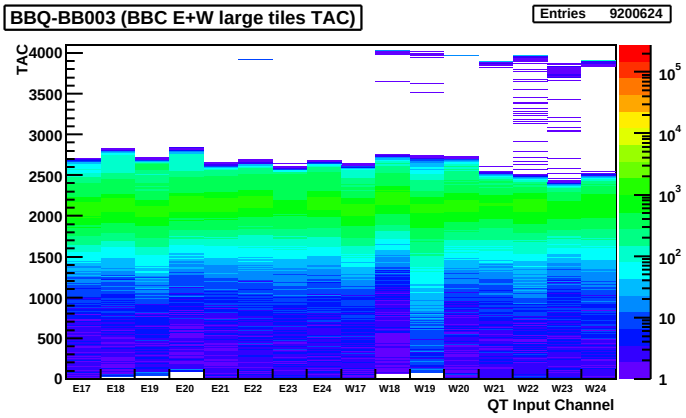

BBQ-BB001 (BBC east small tiles ADC)
Entries 9200624
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

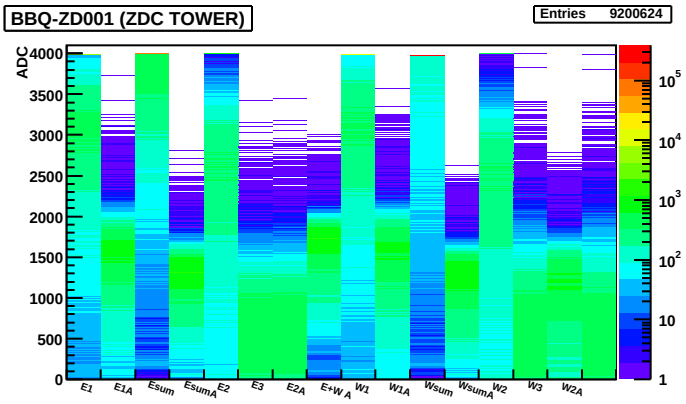

BBQ-BB001 (BBC east small tiles TAC)
Entries 9200624
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

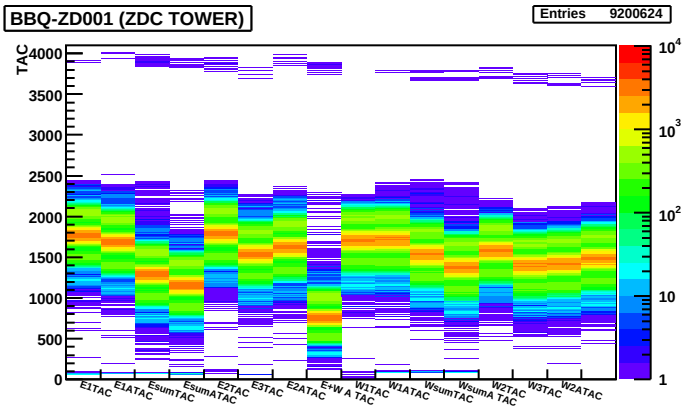

BBQ-BB002 (BBC west small tiles ADC)
Entries 9200624
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

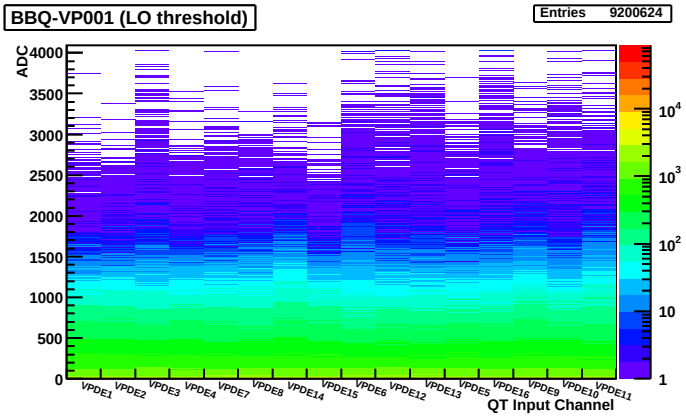

BBQ-BB002 (BBC west small tiles TAC)
Entries 9200624
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

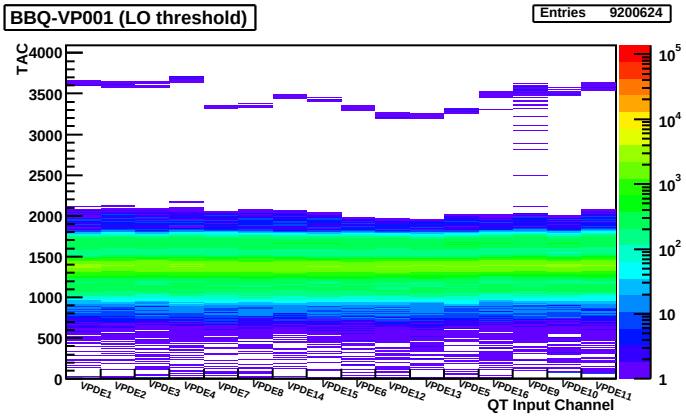

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 9200624
ADC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

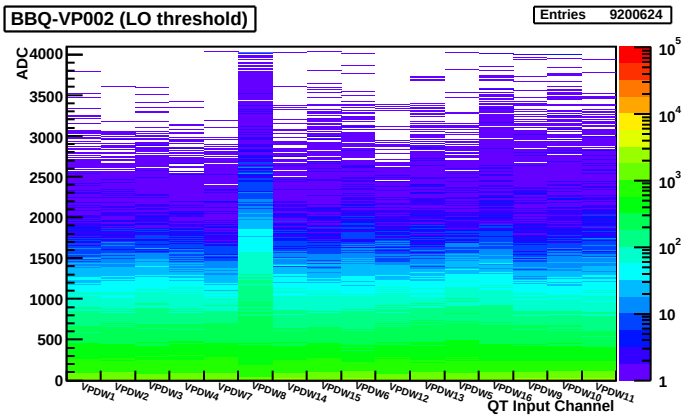

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 9200624
TAC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

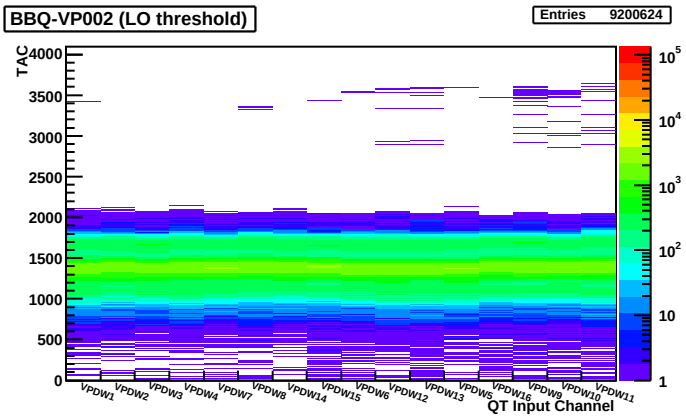

BBQ-ZD001 (ZDC TOWER)
Entries 9200624
ADC
E1 E1A Esum EsumA E2 E3 E2A E+W A W1 W1A Wsum WsumA W2 W3 W2A

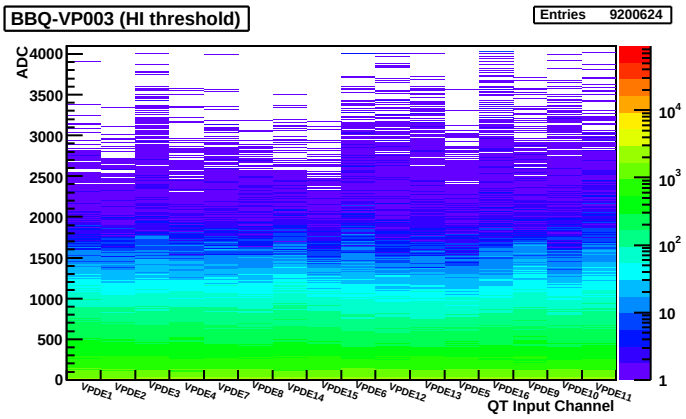

BBQ-ZD001 (ZDC TOWER)
Entries 9200624
TAC
E1TAC E1ATAC EsumTAC EsumATAC E2TAC E3TAC E2ATAC E+W A TAC W1TAC W1ATAC WsumTAC WsumA TAC W2TAC W3TAC W2ATAC

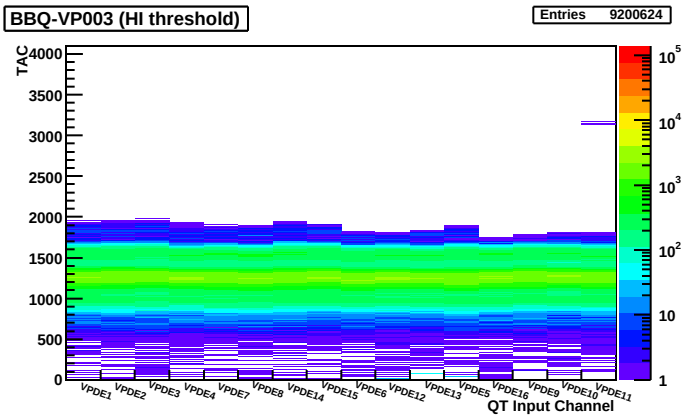

BBQ-VP001 (LO threshold)
Entries 9200624
ADC
QT Input Channel

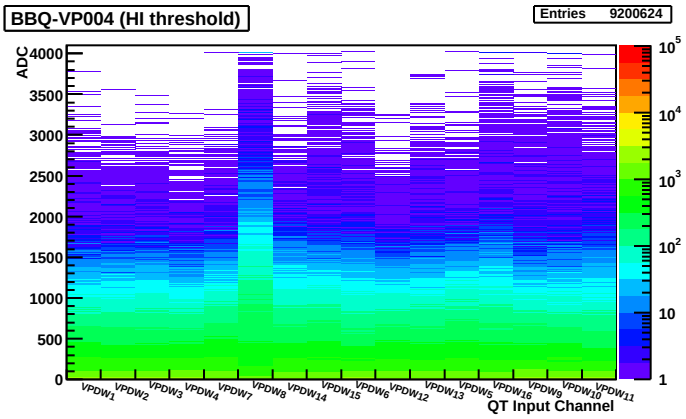

BBQ-VP001 (LO threshold)
Entries 9200624
TAC
QT Input Channel

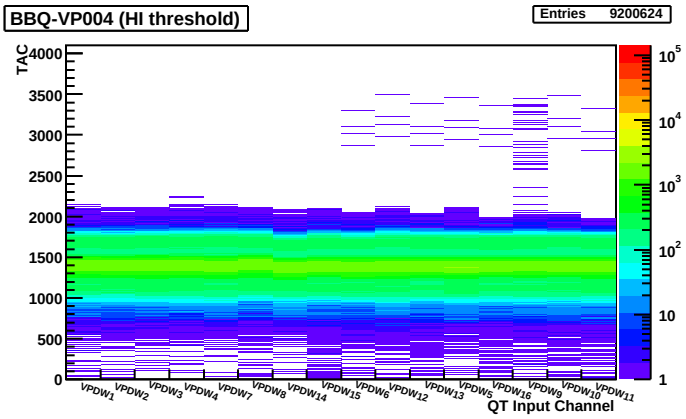

BBQ-VP002 (LO threshold)
Entries 9200624
ADC
QT Input Channel

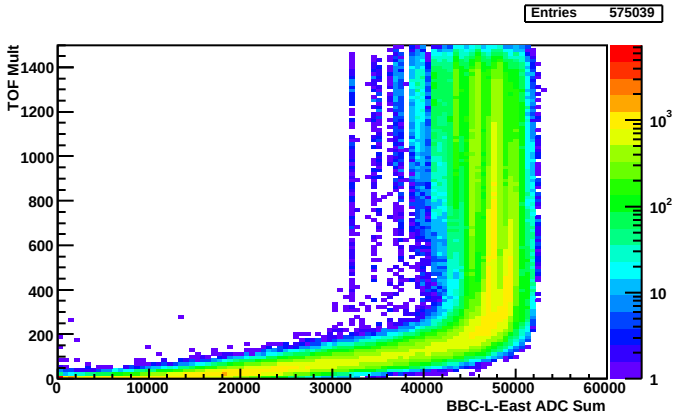

BBQ-VP002 (LO threshold)
Entries 9200624
TAC
QT Input Channel

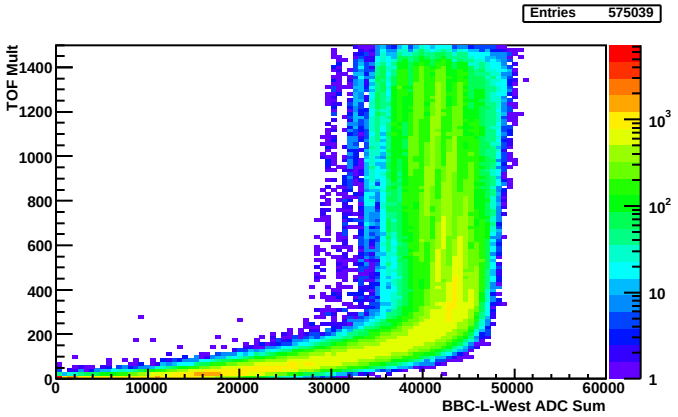

BBQ-VP003 (HI threshold)
Entries 9200624
ADC
QT Input Channel

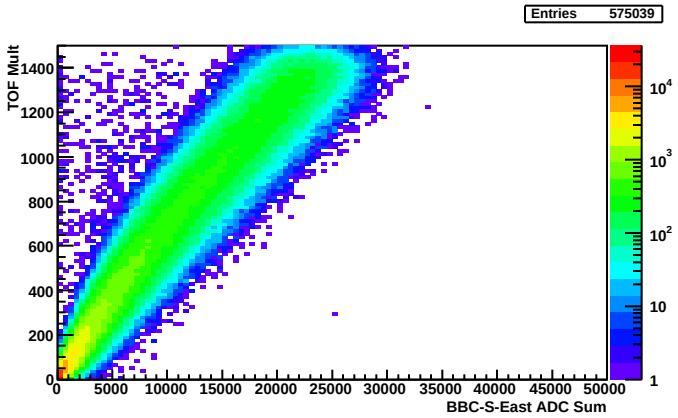

BBQ-VP003 (HI threshold)
Entries 9200624
TAC
QT Input Channel

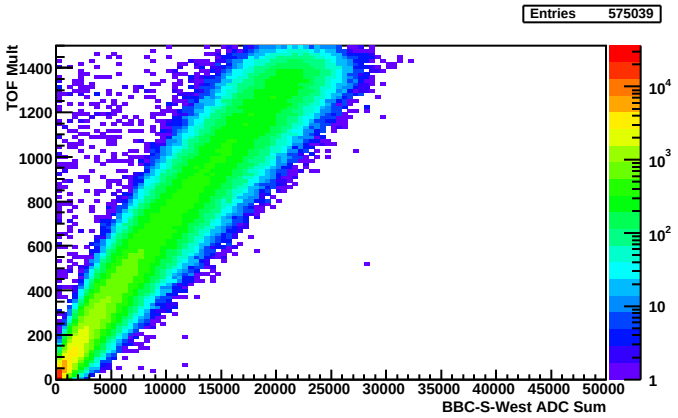

BBQ-VP004 (HI threshold)
Entries 9200624
ADC
QT Input Channel

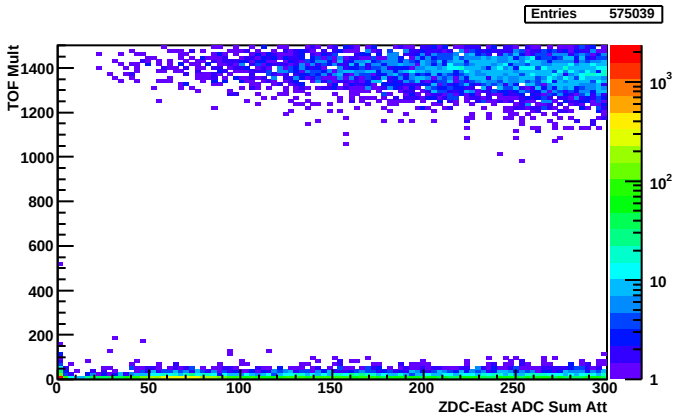

BBQ-VP004 (HI threshold)
Entries 9200624
TAC
QT Input Channel

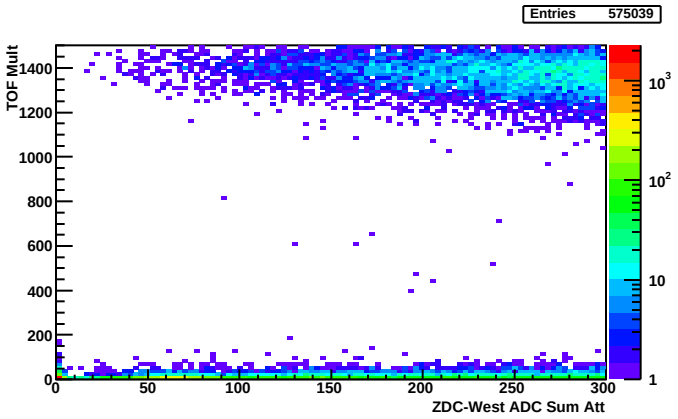

Entries 575039
TOF Mult
BBC-L-East ADC Sum

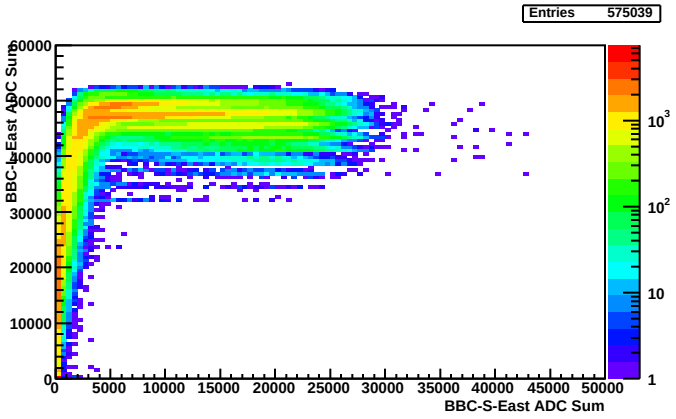

Entries 575039
TOF Mult
BBC-L-West ADC Sum

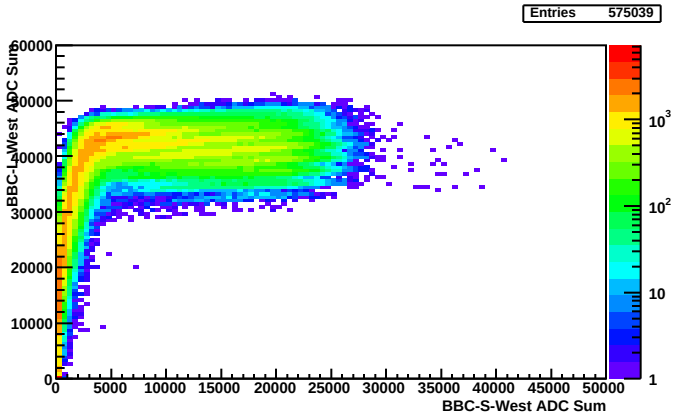

Entries 575039
TOF Mult
BBC-S-East ADC Sum

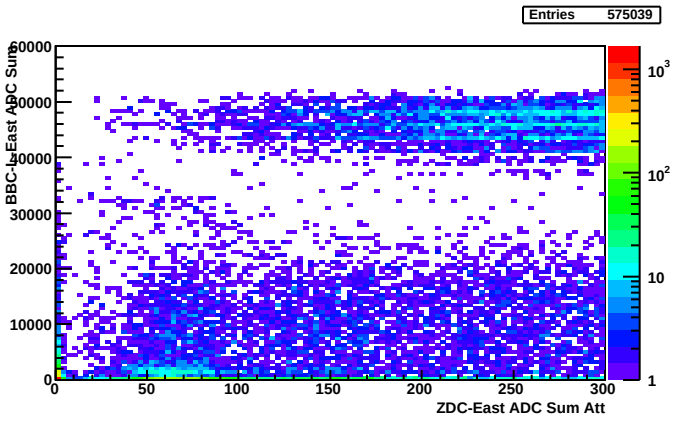

Entries 575039
TOF Mult
BBC-S-West ADC Sum

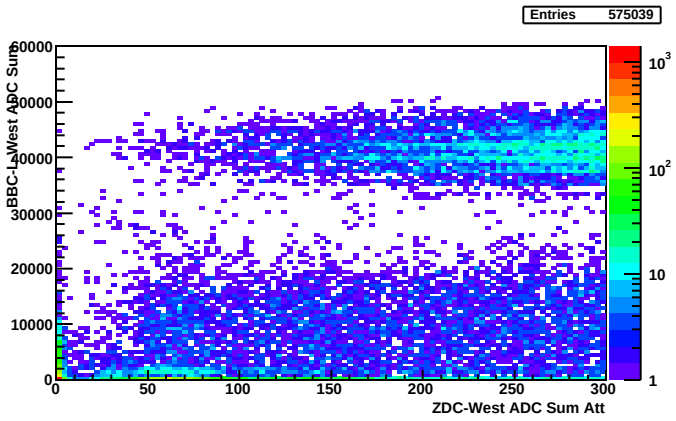

Entries 575039
TOF Mult
ZDC-East ADC Sum Att

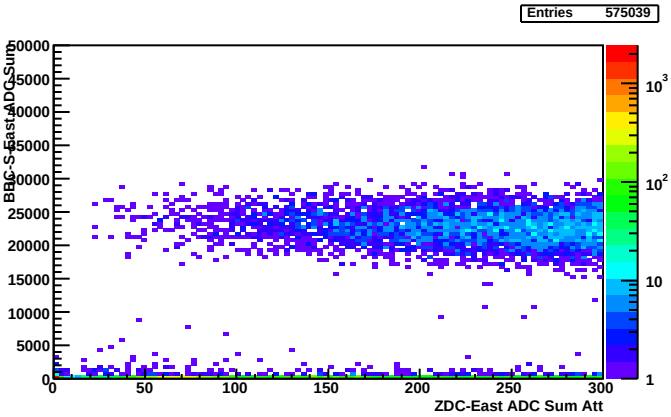

Entries 575039
TOF Mult
ZDC-West ADC Sum Att

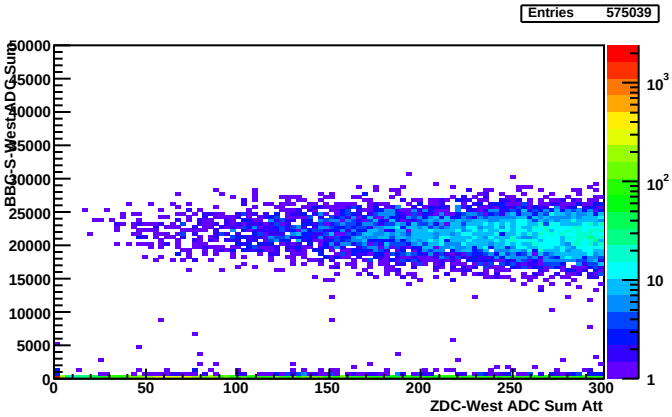

Entries 575039
BBC-L-East ADC Sum
BBC-S-East ADC Sum

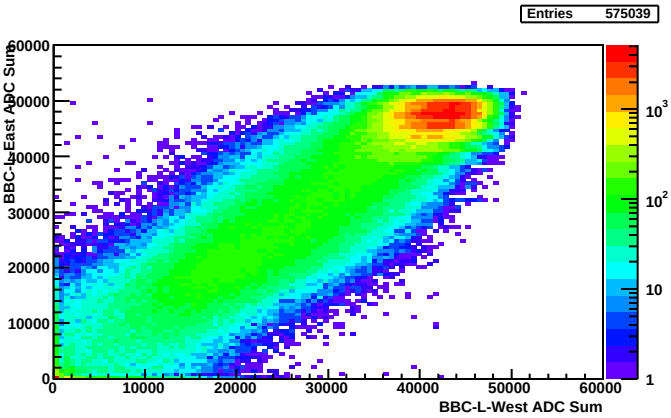

Entries 575039
BBC-L-West ADC Sum
BBC-S-West ADC Sum

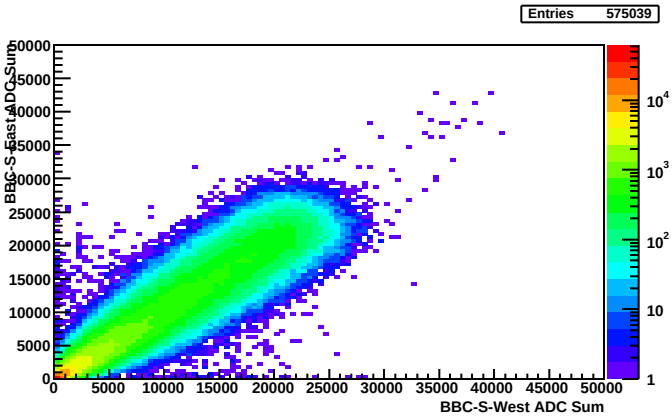

Entries 575039
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

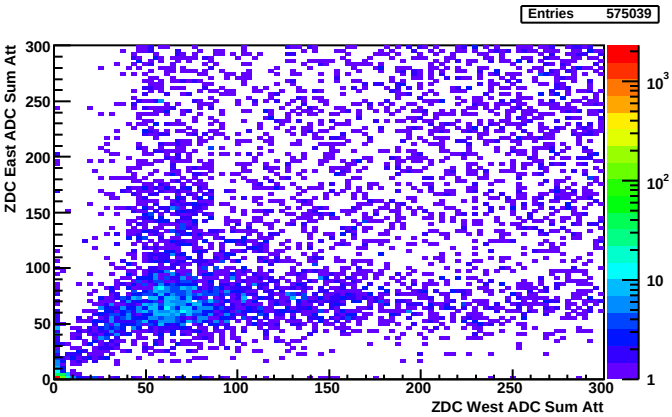

Entries 575039
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

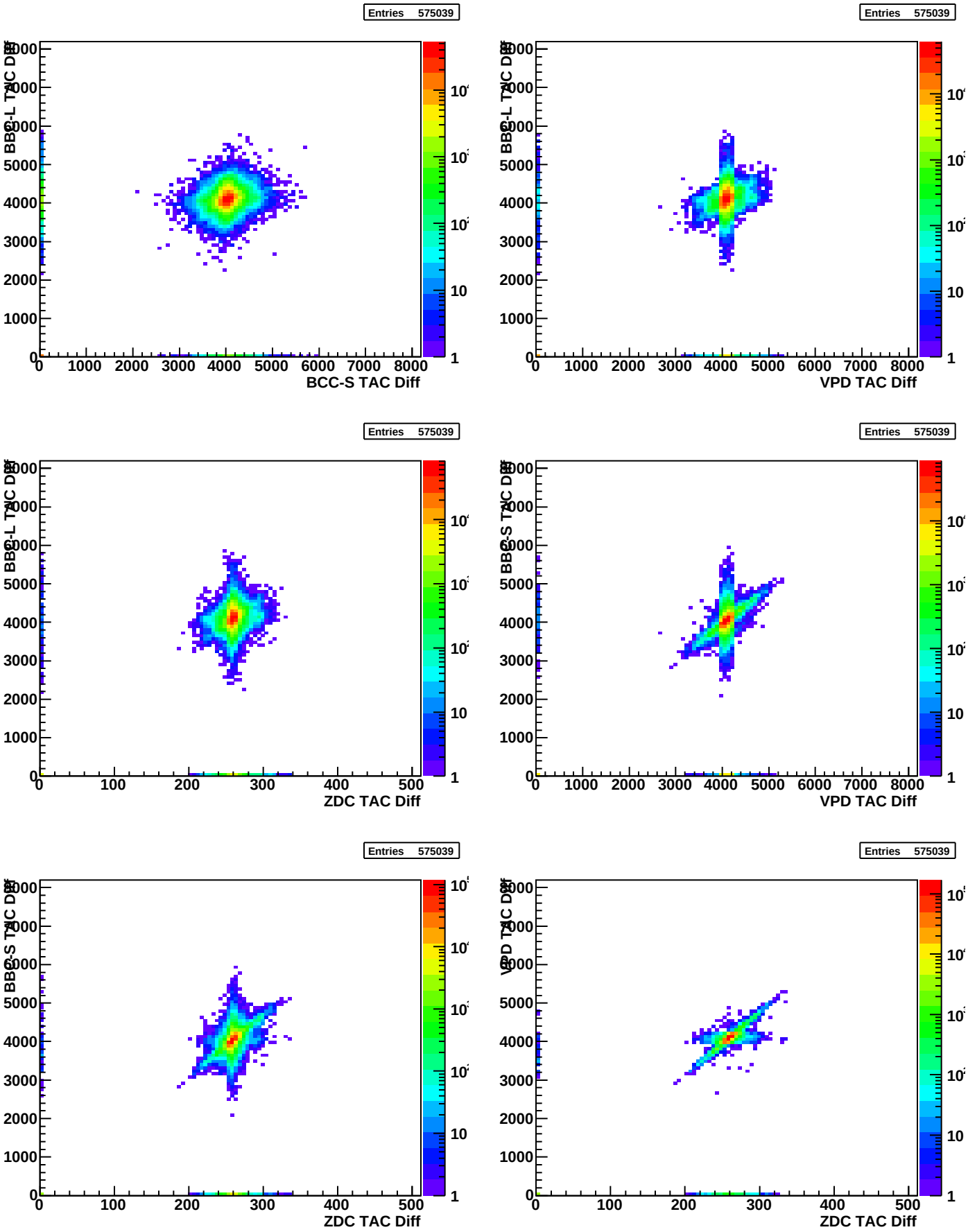

Entries 575039
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

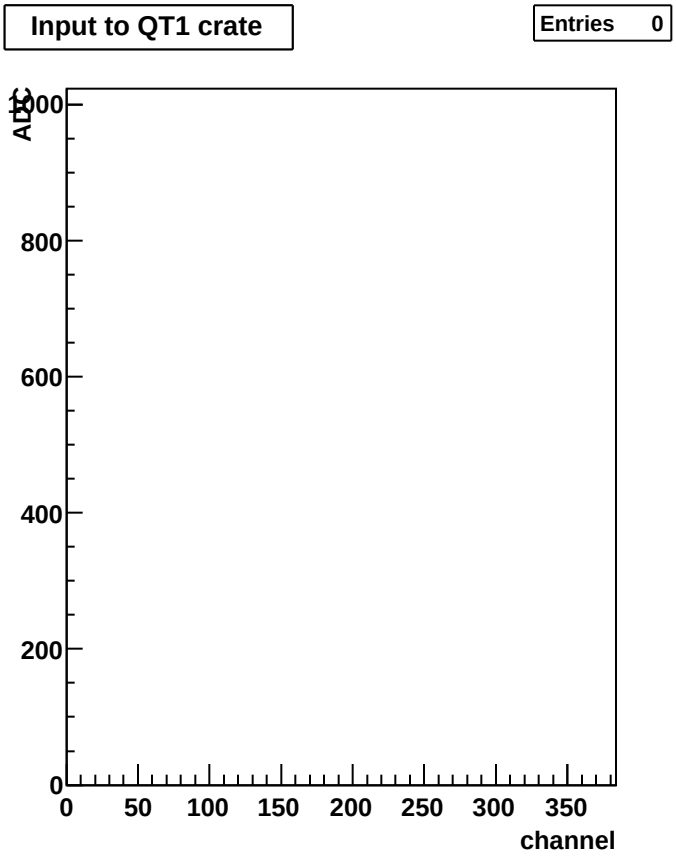

Entries 575039
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

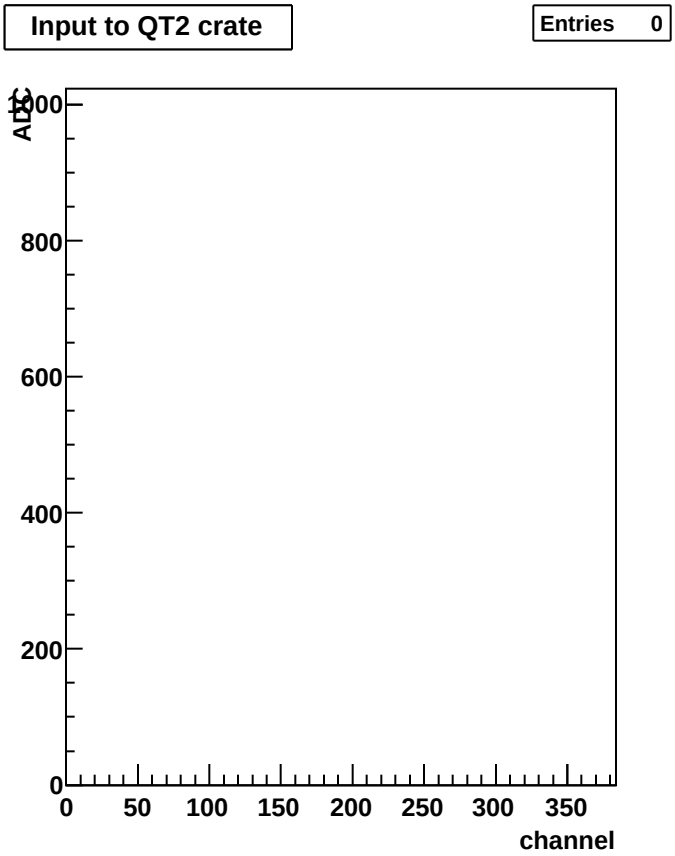

Entries 575039
BBC-L-East ADC Sum
BBC-L-West ADC Sum

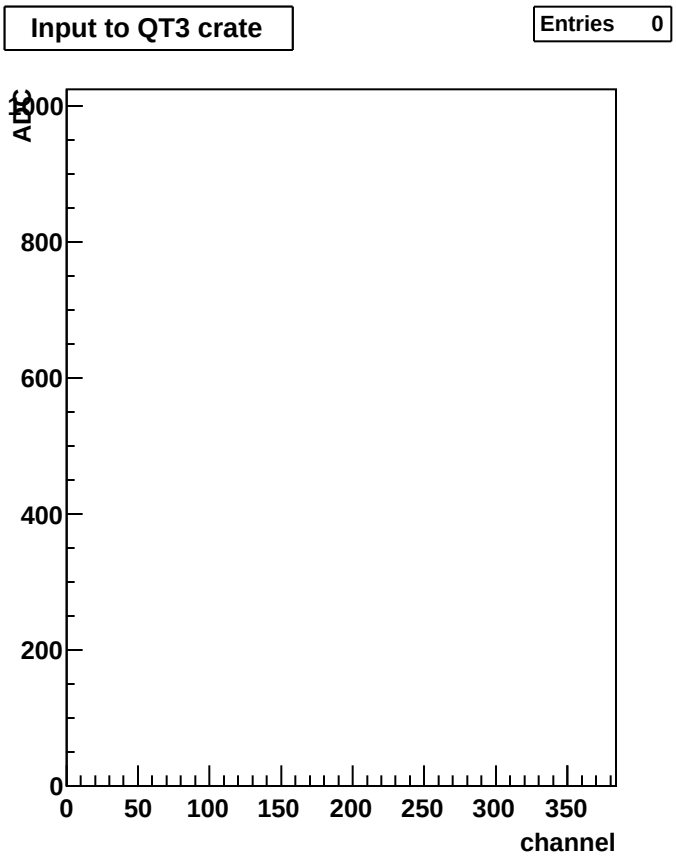

Entries 575039
BBC-S-East ADC Sum
BBC-S-West ADC Sum

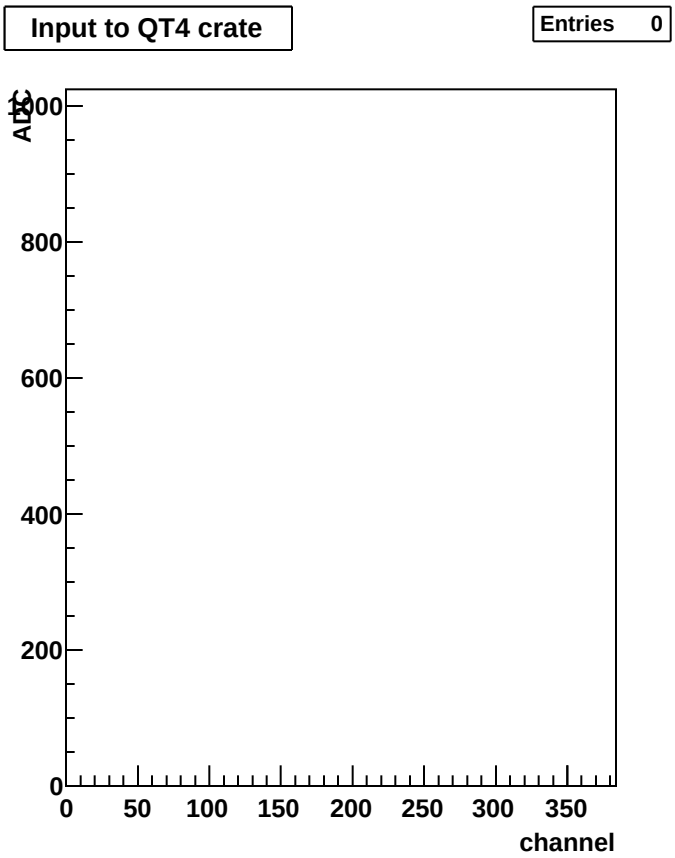

Entries 575039
ZDC East ADC Sum Att
ZDC West ADC Sum Att

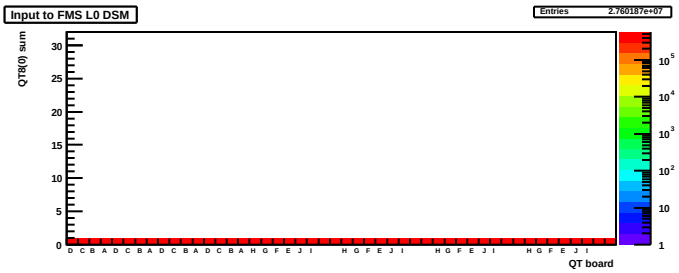

Entries 575039
BBC-L TAC Diff
BCC-S TAC Diff
Entries 575039
BBC-L TAC Diff
VPD TAC Diff
Entries 575039
BBC-L TAC Diff
ZDC TAC Diff
Entries 575039
BBC-S TAC Diff
VPD TAC Diff
Entries 575039
BBC-S TAC Diff
ZDC TAC Diff
Entries 575039
VPD TAC Diff
ZDC TAC Diff

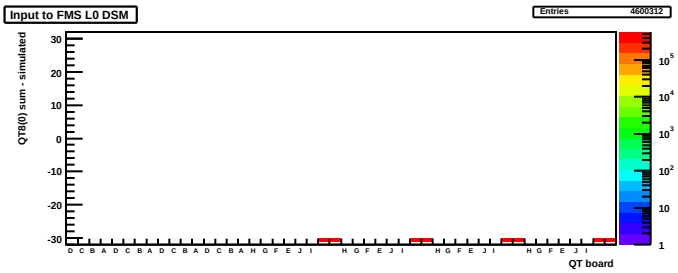

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

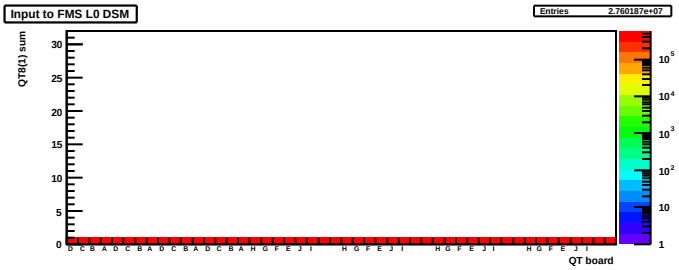

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

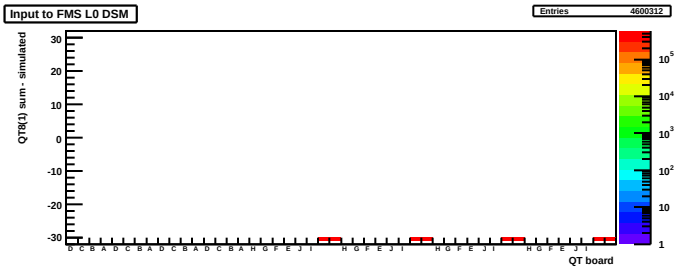

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

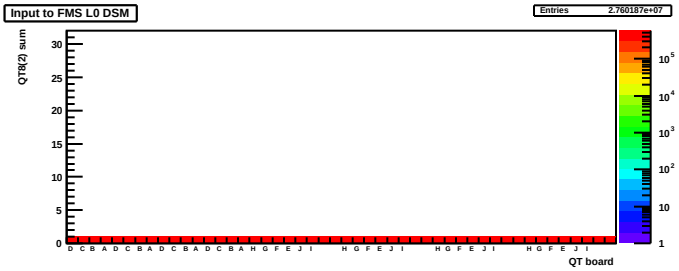

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

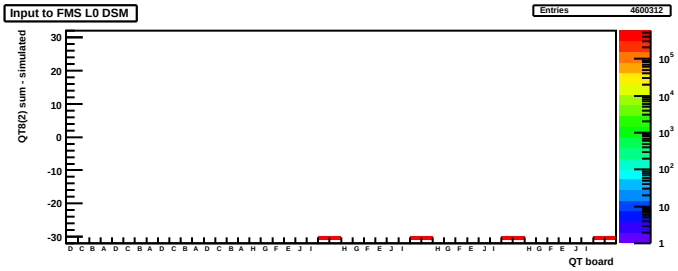

Input to FMS L0 DSM
Entries 2.760187e+07
QT8(0) sum
QT board

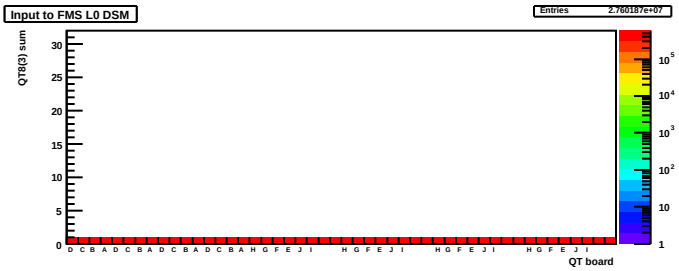

Input to FMS L0 DSM
Entries 4600312
QT8(0) sum - simulated
QT board

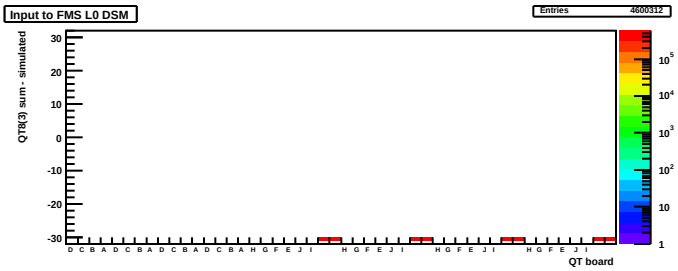

Input to FMS L0 DSM
Entries 2.760187e+07
QT8(1) sum
QT board

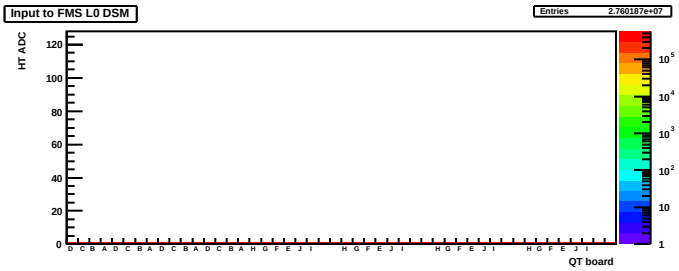

Input to FMS L0 DSM
Entries 4600312
QT8(1) sum - simulated
QT board

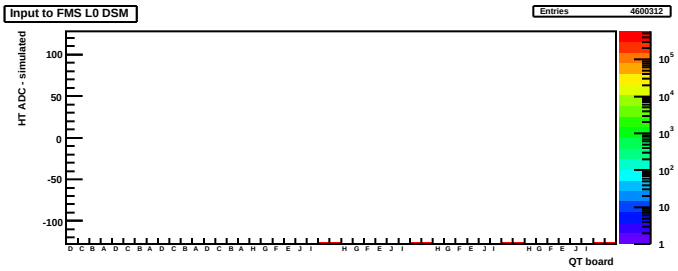

Input to FMS L0 DSM
Entries 2.760187e+07
QT8(2) sum
QT board

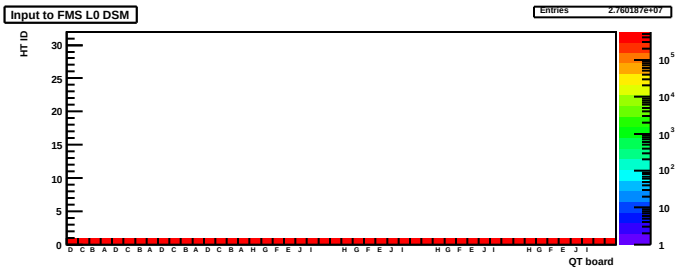

Input to FMS L0 DSM
Entries 4600312
QT8(2) sum - simulated
QT board

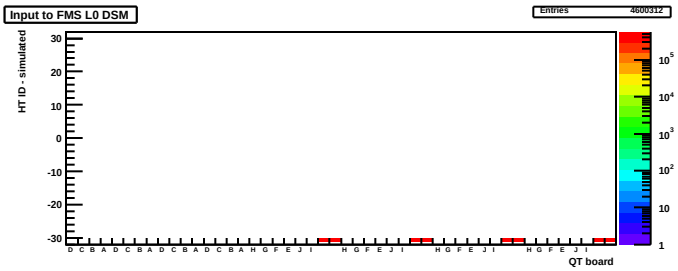

Input to FMS L0 DSM
Entries 2.760187e+07
QT8(3) sum
QT board

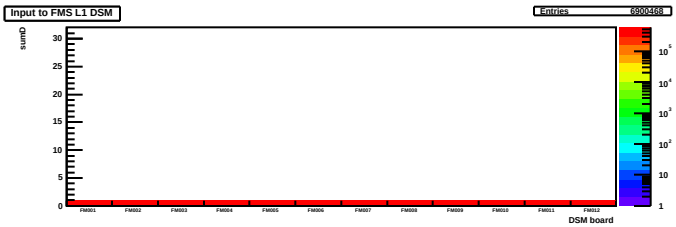

Input to FMS L0 DSM
Entries 4600312
QT8(3) sum - simulated
QT board

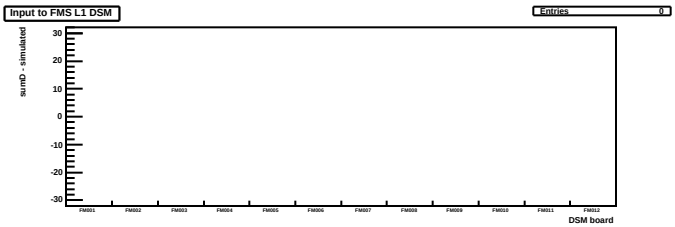

Input to FMS L0 DSM
Entries 2.760187e+07
HT ADC
QT board

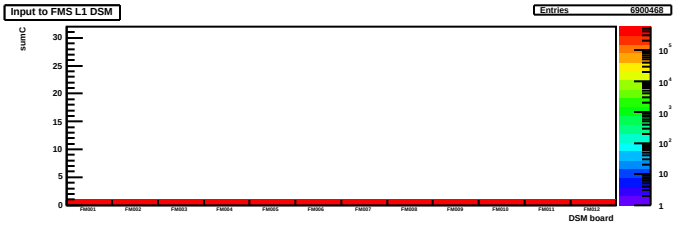

Input to FMS L0 DSM
Entries 4600312
HT ADC - simulated
QT board

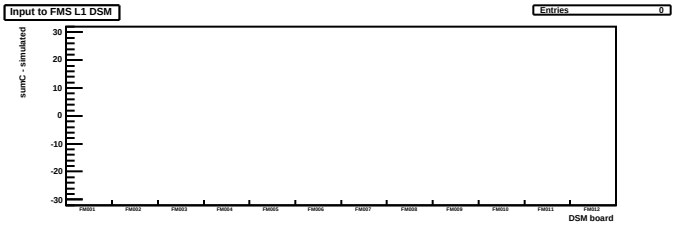

Input to FMS L0 DSM
Entries 2.760187e+07
HT ID
QT board

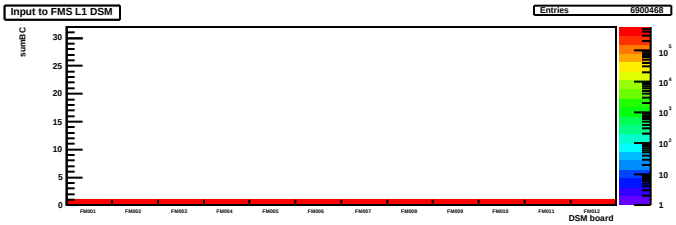

Input to FMS L0 DSM
Entries 4600312
HT ID - simulated
QT board

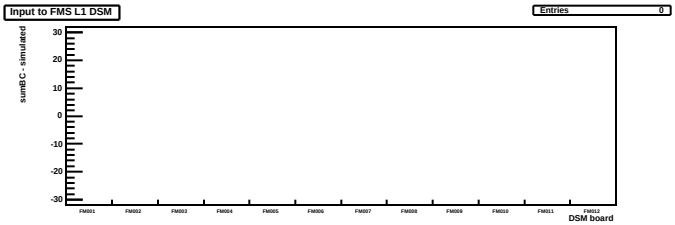

Input to FMS L1 DSM
Entries 6900468
sumD
DSM board

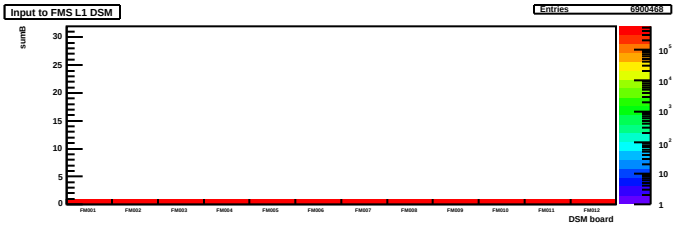

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

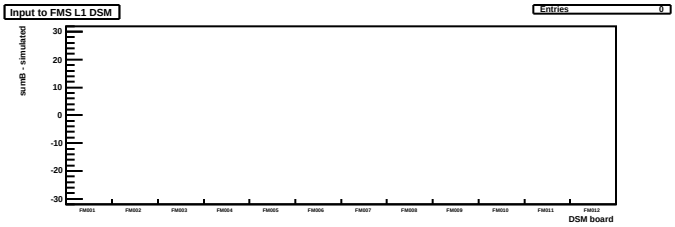

Input to FMS L1 DSM
Entries 6900468
sumC
DSM board

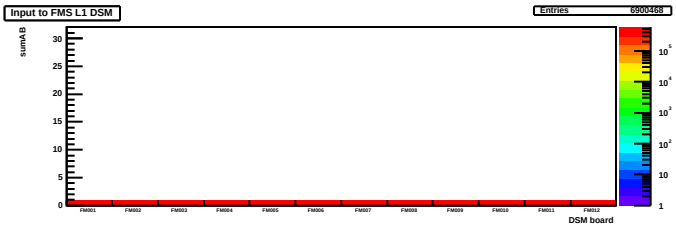

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

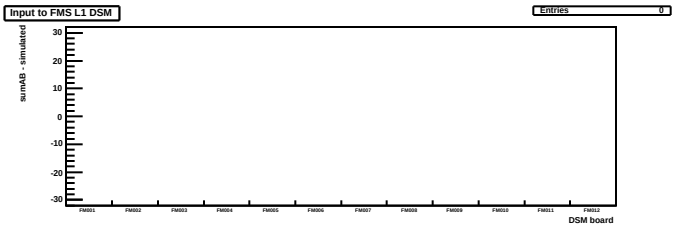

Input to FMS L1 DSM
Entries 6900468
sumBC
DSM board

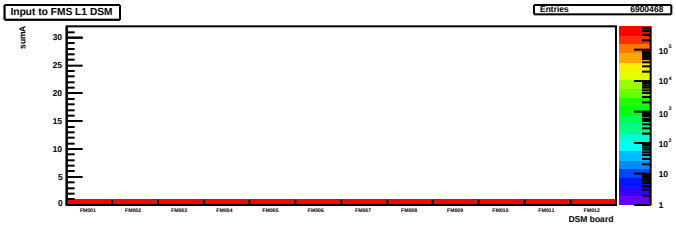

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

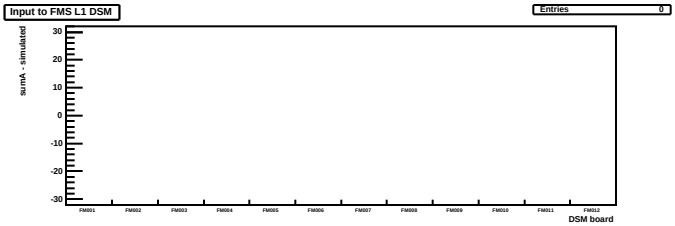

Input to FMS L1 DSM
Entries 6900468
sumB
DSM board

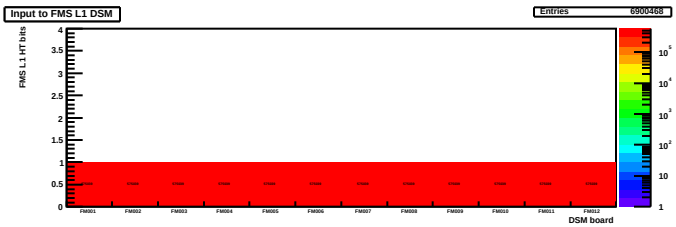

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

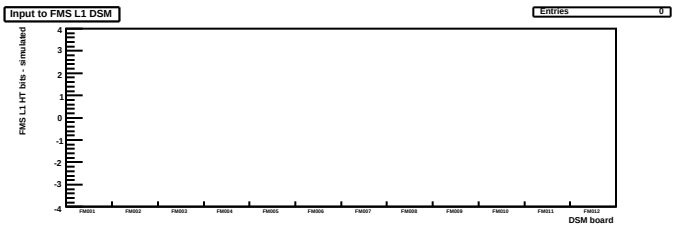

Input to FMS L1 DSM
Entries 6900468
sumAB
DSM board

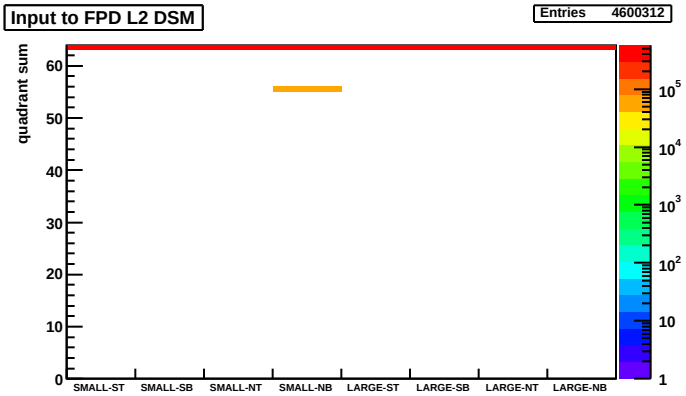

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

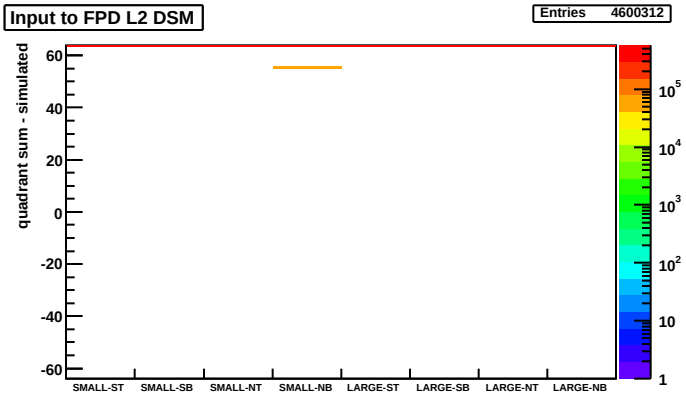

Input to FMS L1 DSM
Entries 6900468
sumA
DSM board

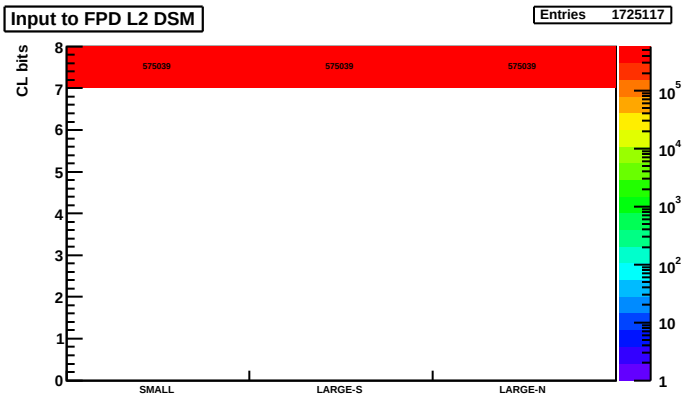

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

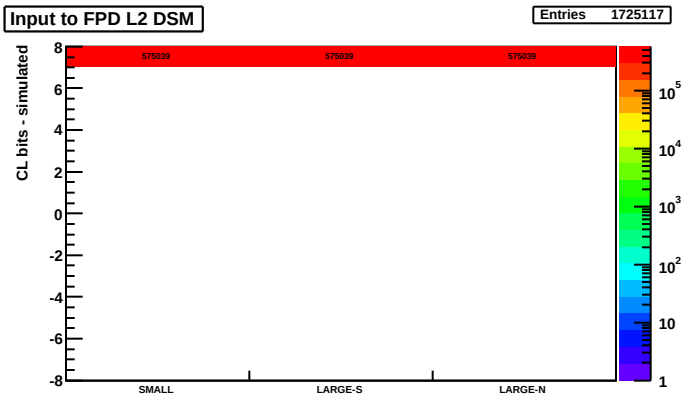

Input to FMS L1 DSM
Entries 6900468
FMS L1 HT bits
DSM board

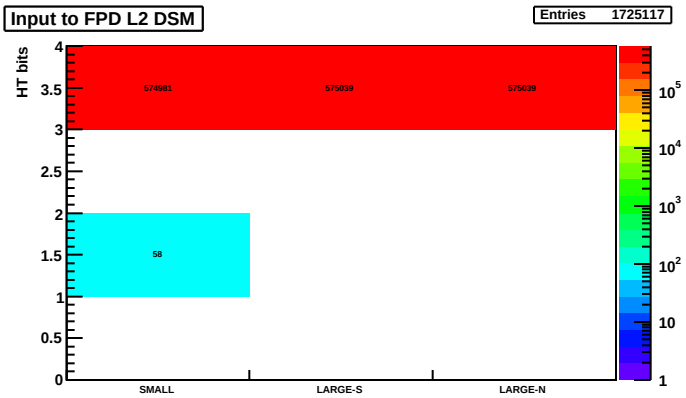

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

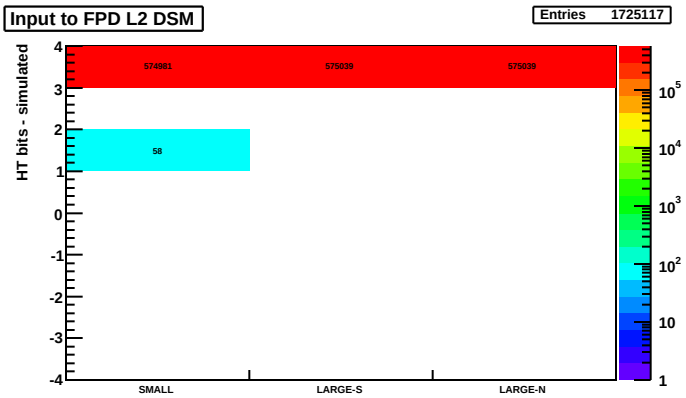

Input to FPD L2 DSM
Entries 4600312
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

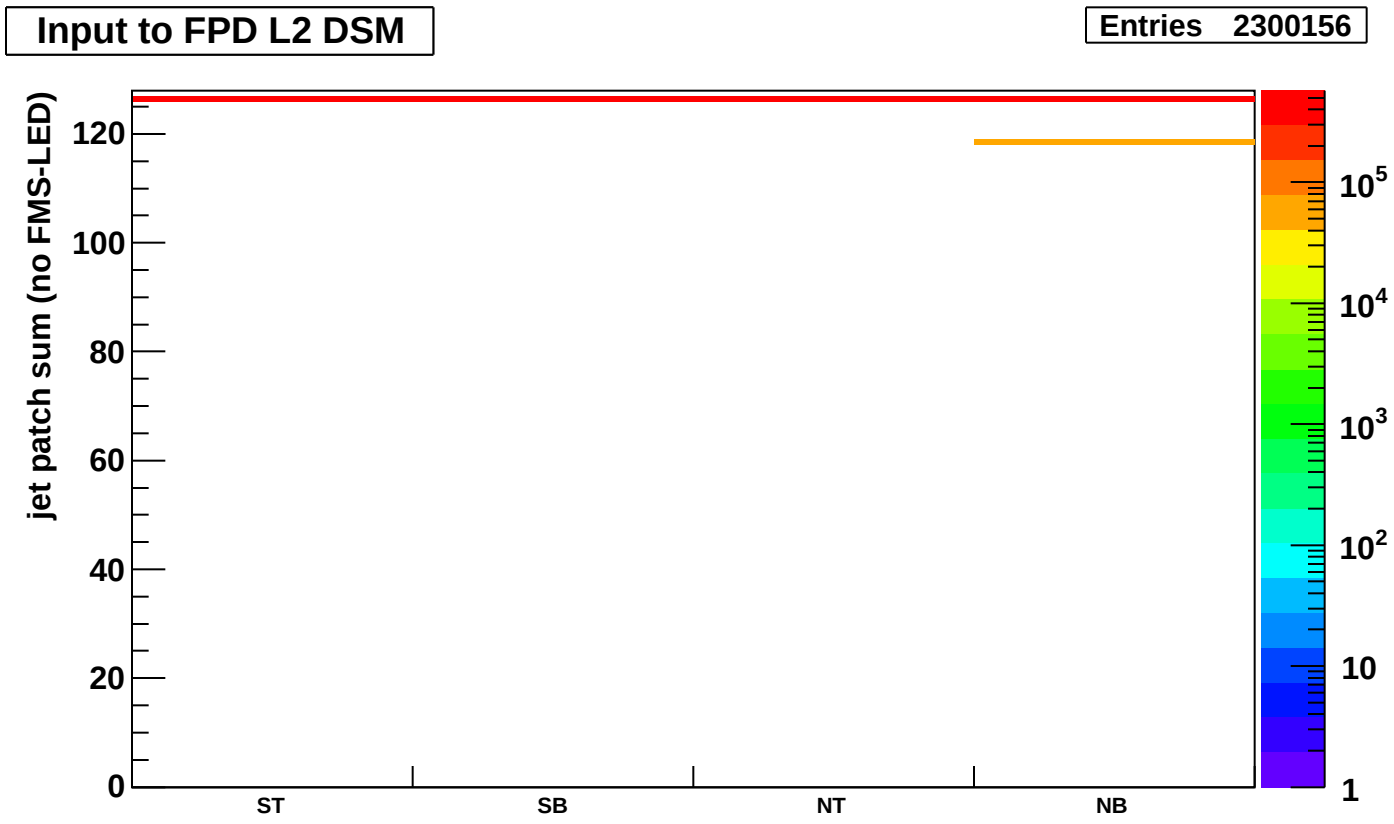

Input to FPD L2 DSM
Entries 4600312
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

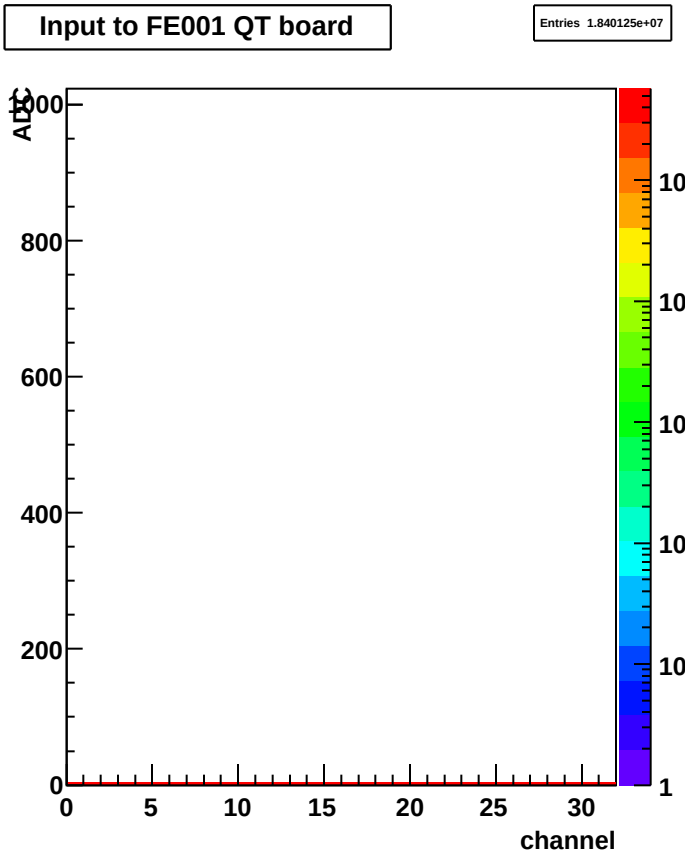

Input to FPD L2 DSM
Entries 1725117
CL bits
575039
575039
575039
SMALL
LARGE-S
LARGE-N

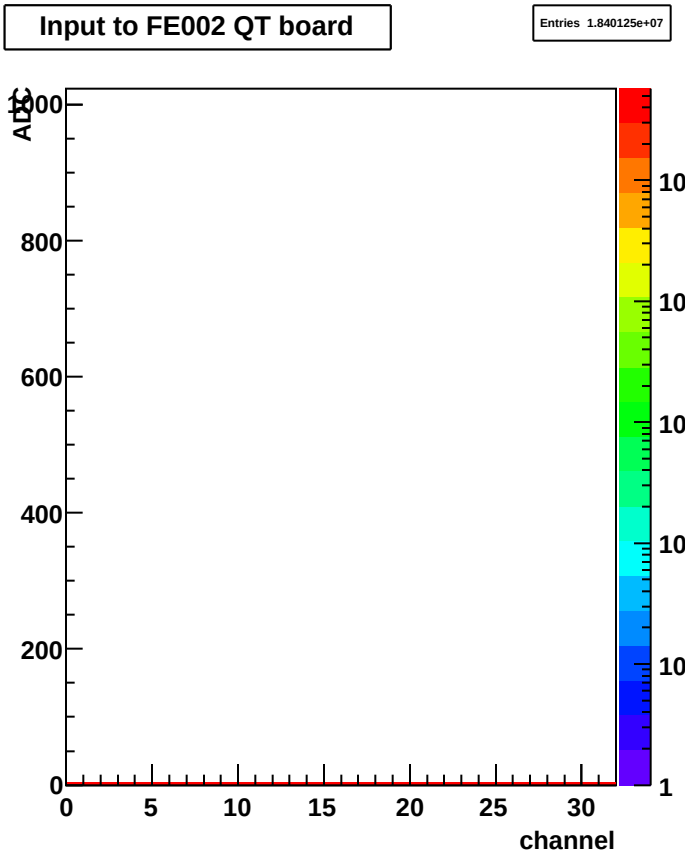

Input to FPD L2 DSM
Entries 1725117
CL bits - simulated
575039
575039
575039
SMALL
LARGE-S
LARGE-N

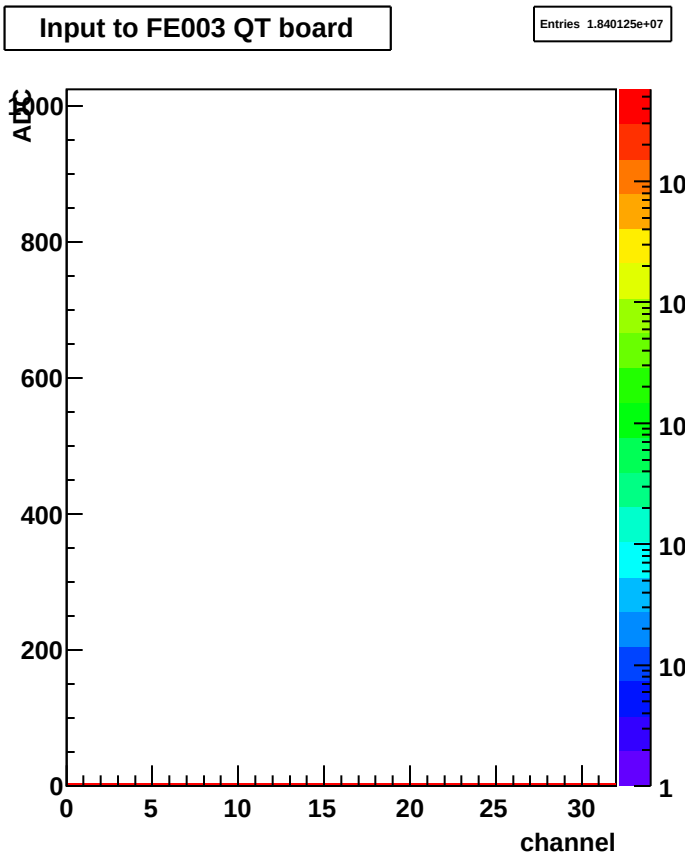

Input to FPD L2 DSM
Entries 1725117
HT bits
574981
575039
575039
58
SMALL
LARGE-S
LARGE-N

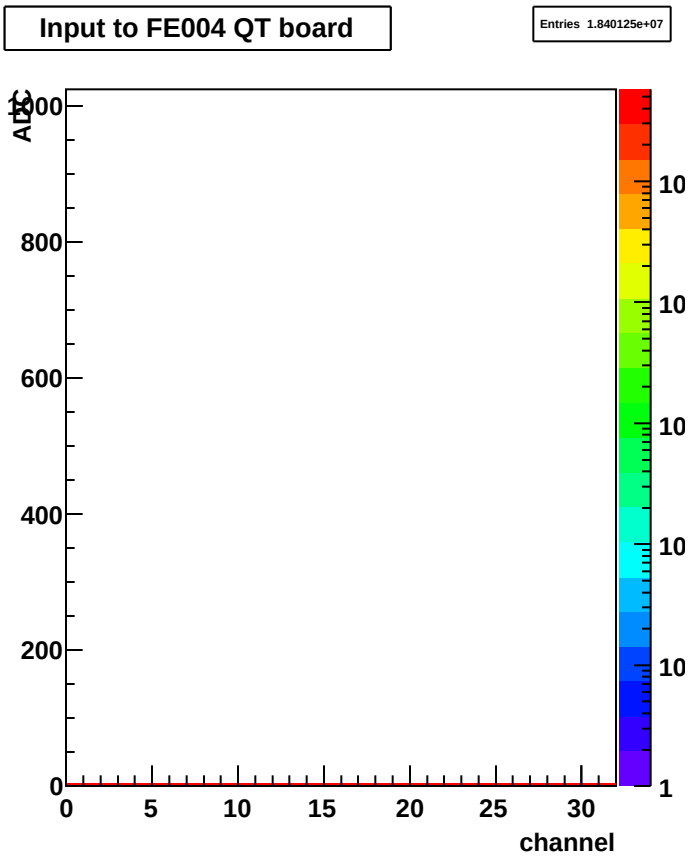

Input to FPD L2 DSM
Entries 1725117
HT bits - simulated
574981
575039
575039
58
SMALL
LARGE-S
LARGE-N


Input to FPD L2 DSM
Entries 2300156
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

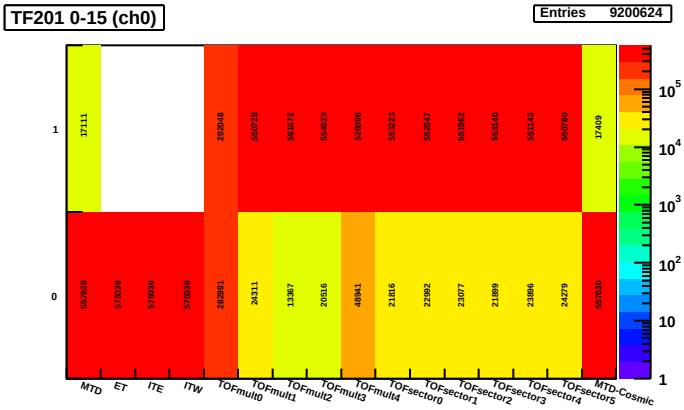

Input to FE001 QT board
Entries 1.840125e+07
ADC
channel


Input to FE002 QT board
Entries 1.840125e+07
ADC
channel

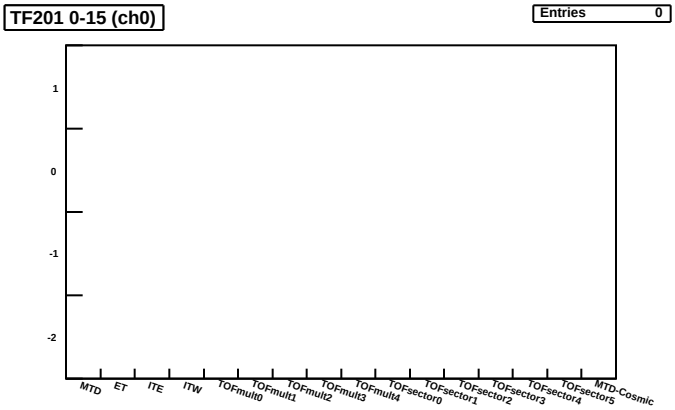

Input to FE003 QT board
Entries 1.840125e+07
ADC
channel


Input to FE004 QT board
Entries 1.840125e+07
ADC
channel

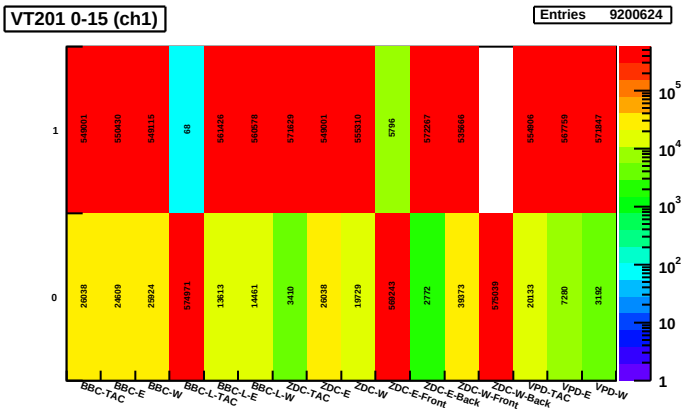
TF201 0-15 (ch0)



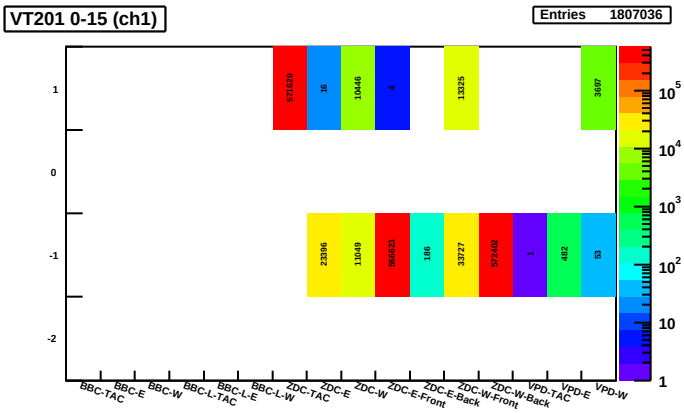
TF201 0-15 (ch0)



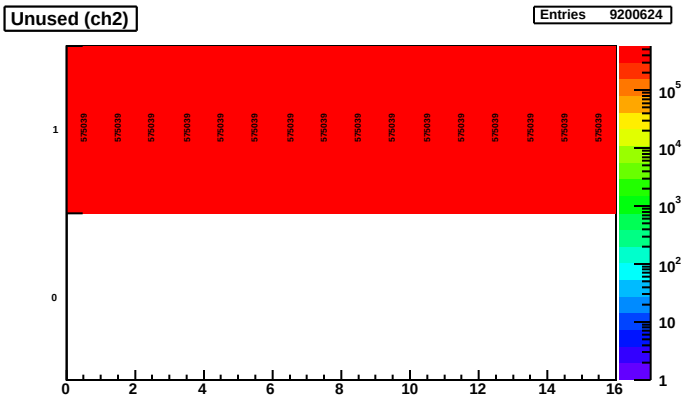
VT201 0-15 (ch1)

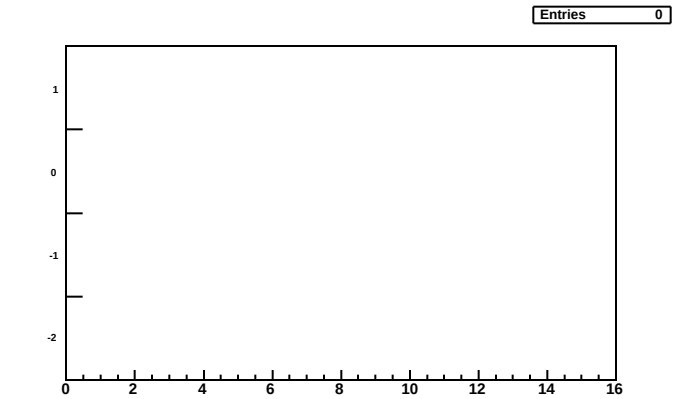


VT201 0-15 (ch1)

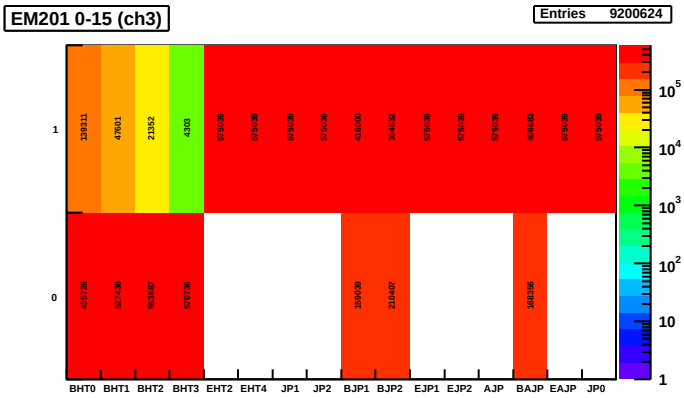


Unused (ch2)

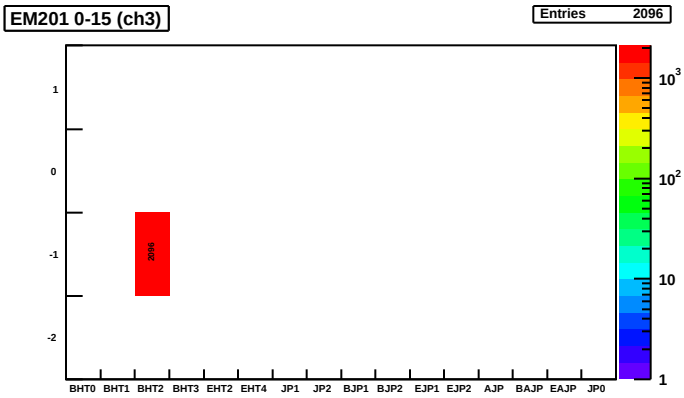




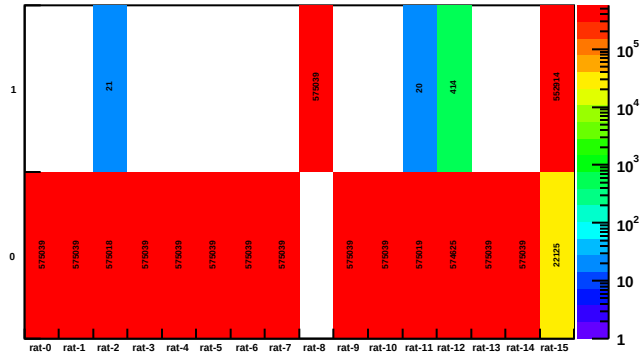
EM201 0-15 (ch3)

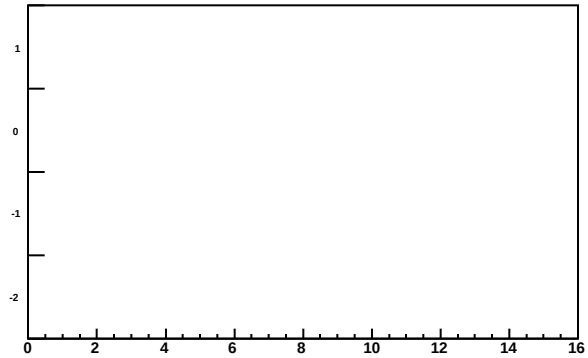


EM201 0-15 (ch3)

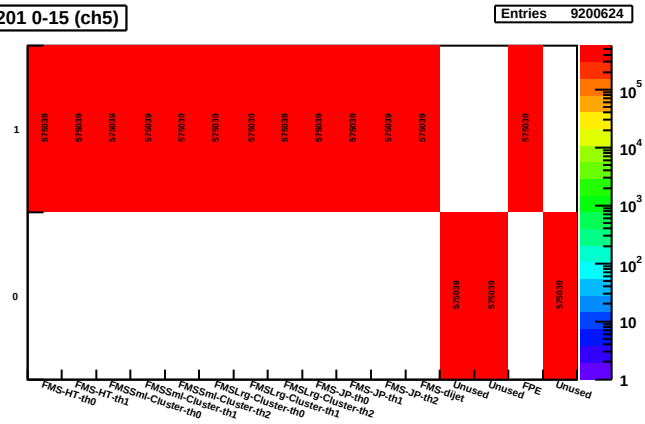


RAT board (ch4)

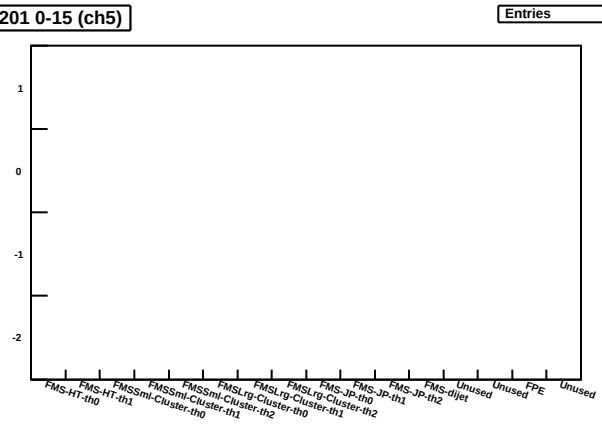




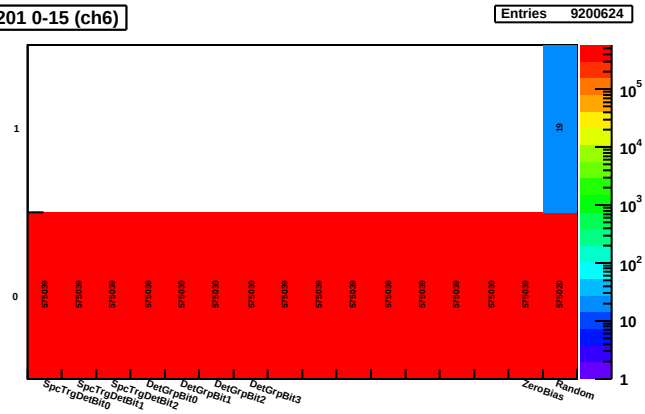
FP201 0-15 (ch5)

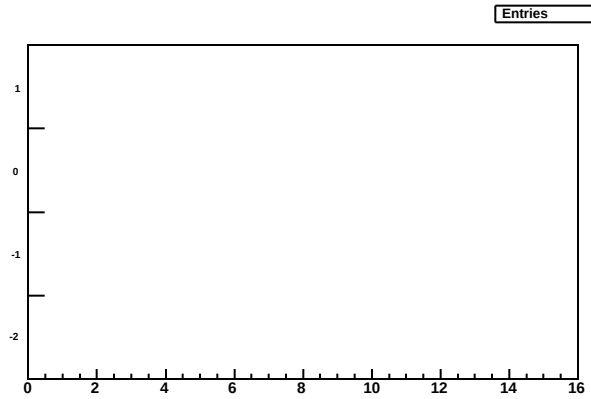
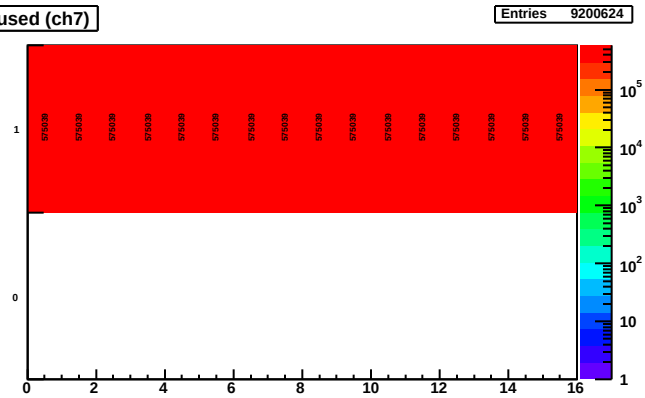
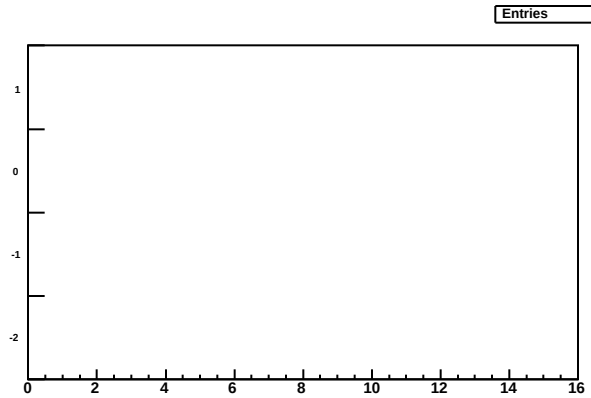


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)