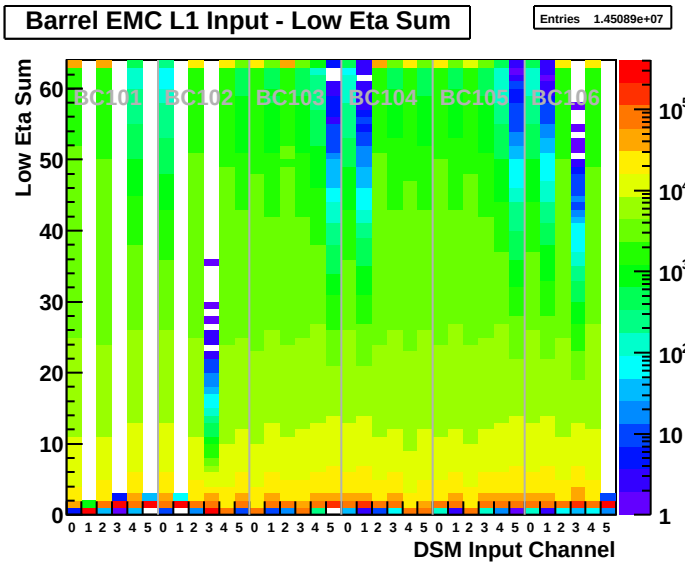
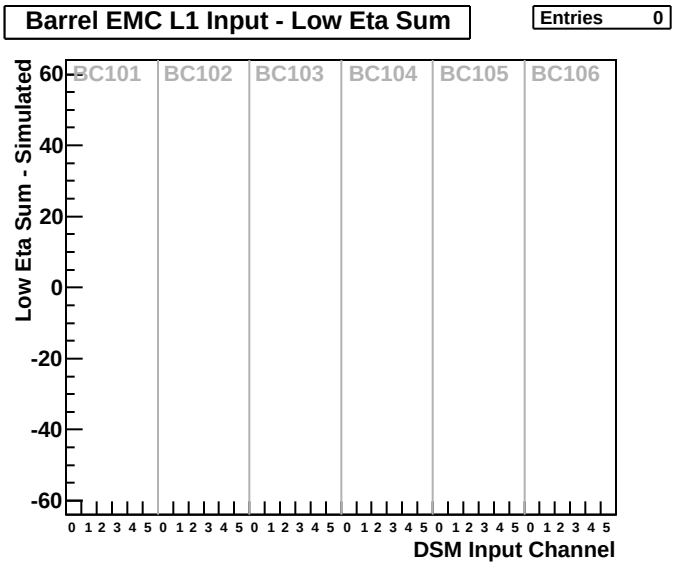
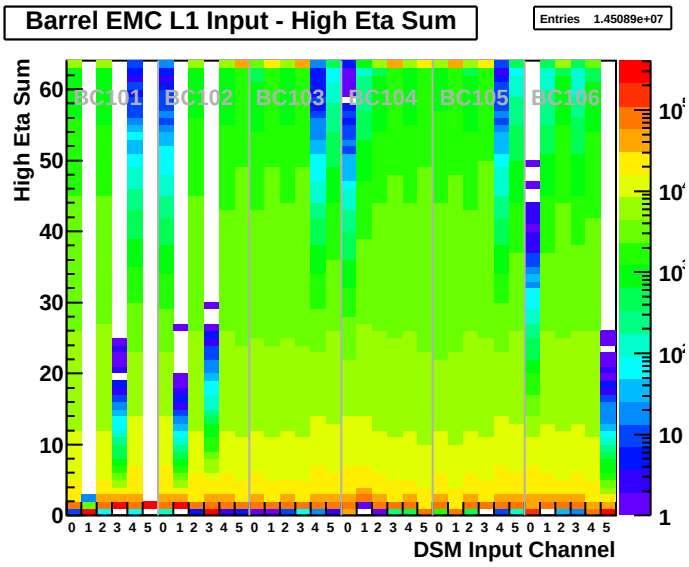
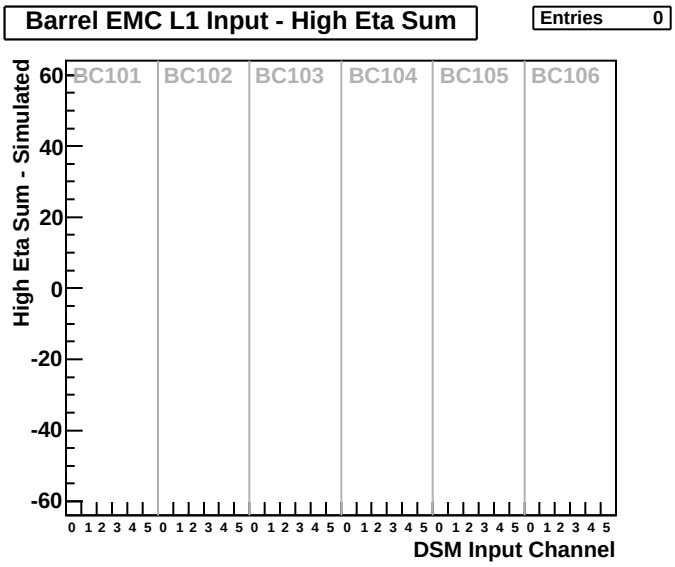
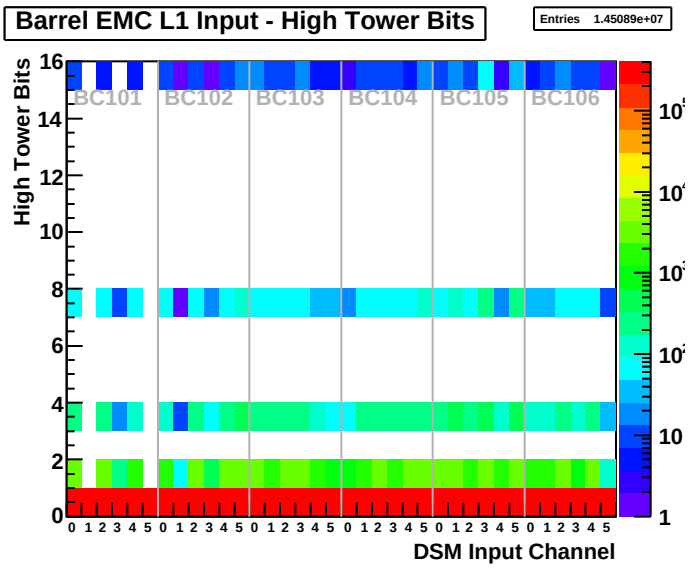
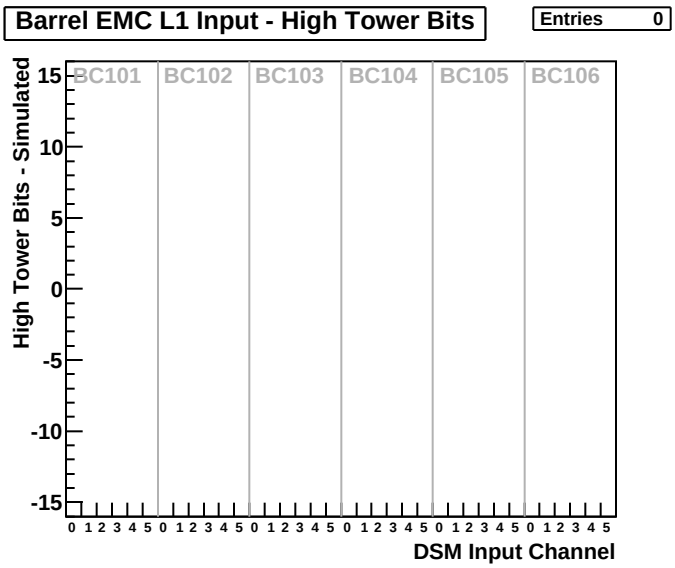

Barrel EMC L1 Input - Low Eta Sum
Entries 1.45089e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

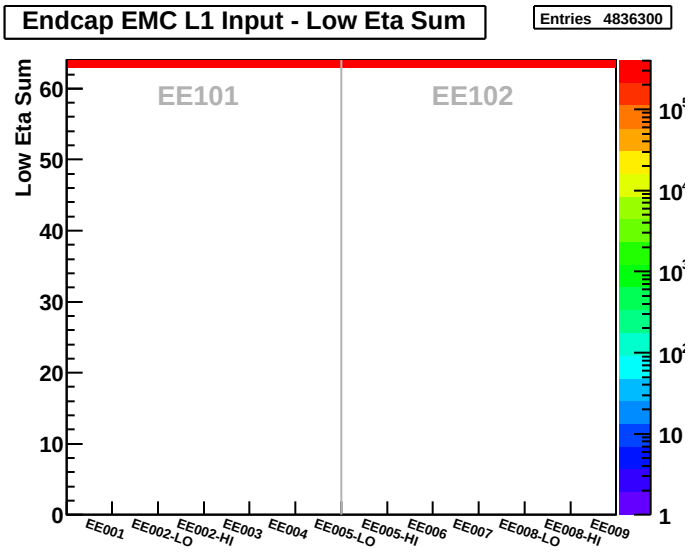

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

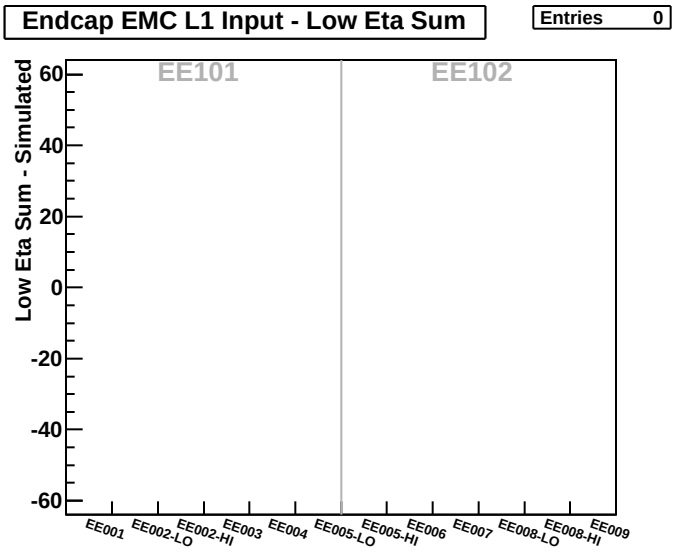

Barrel EMC L1 Input - High Eta Sum
Entries 1.45089e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

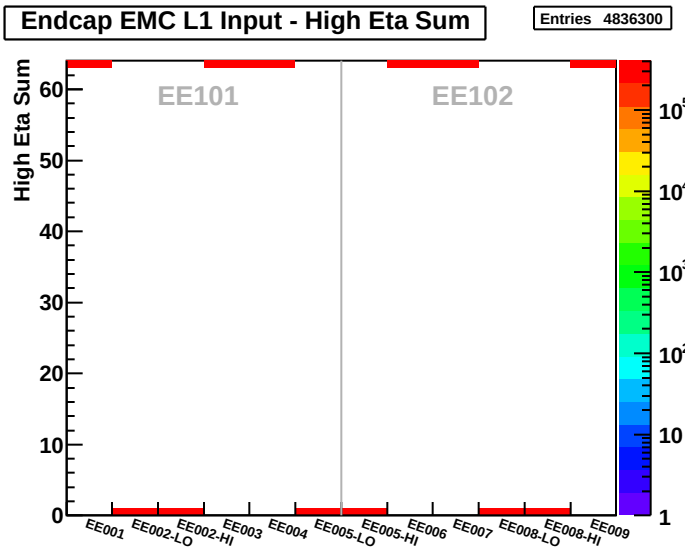

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

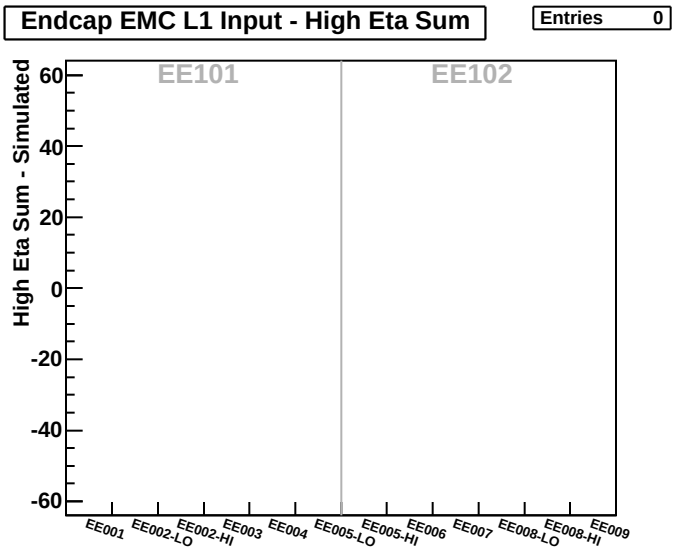

Barrel EMC L1 Input - High Tower Bits
Entries 1.45089e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

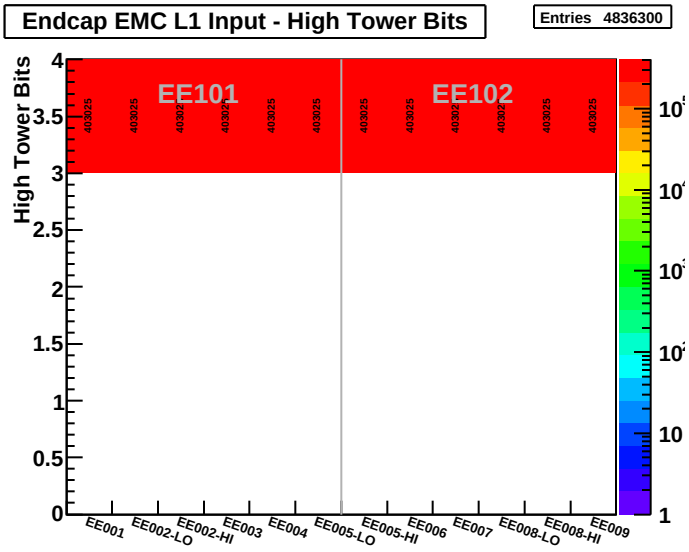

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

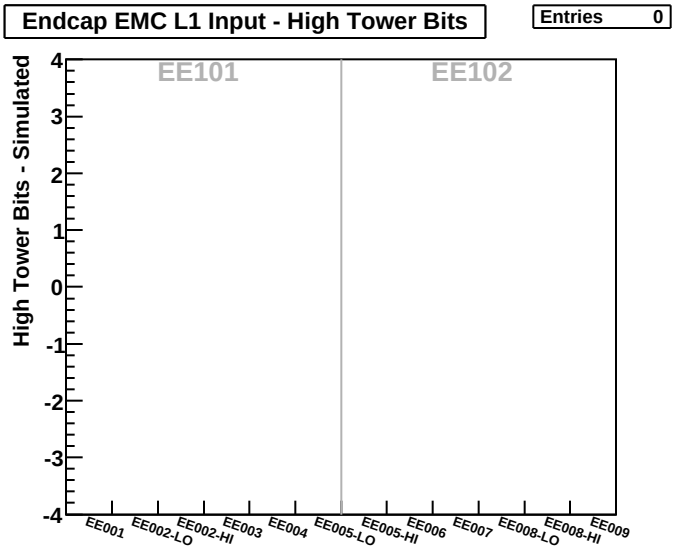

Endcap EMC L1 Input - Low Eta Sum
Entries 4836300
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

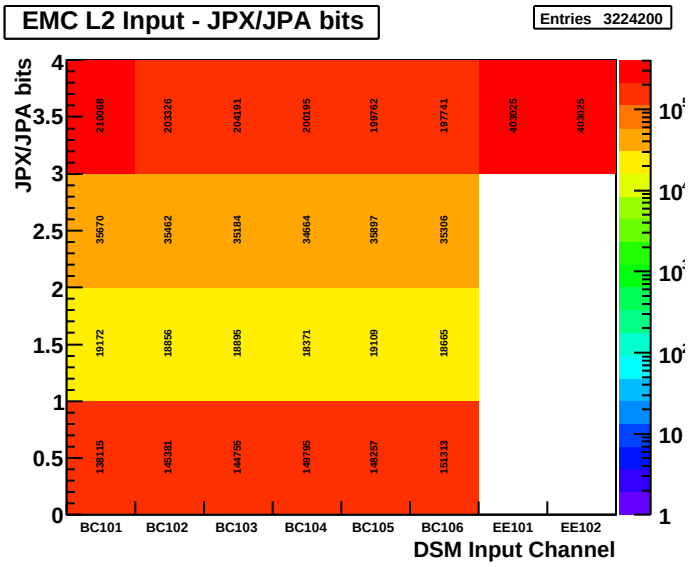

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

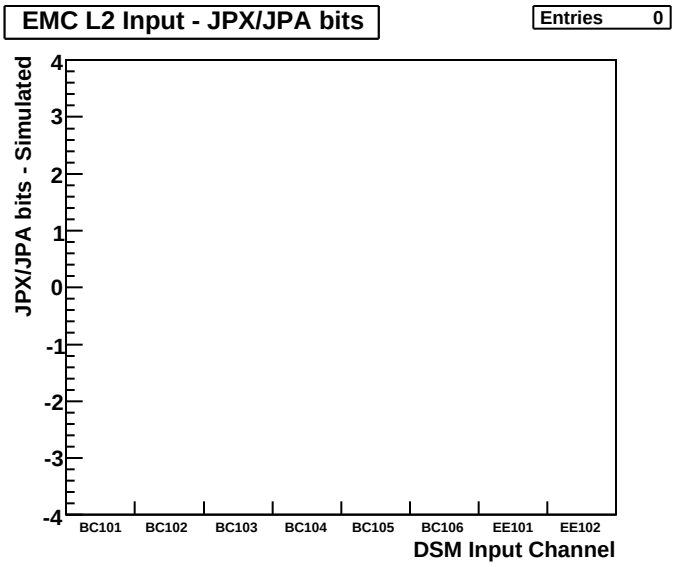

Endcap EMC L1 Input - High Eta Sum
Entries 4836300
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

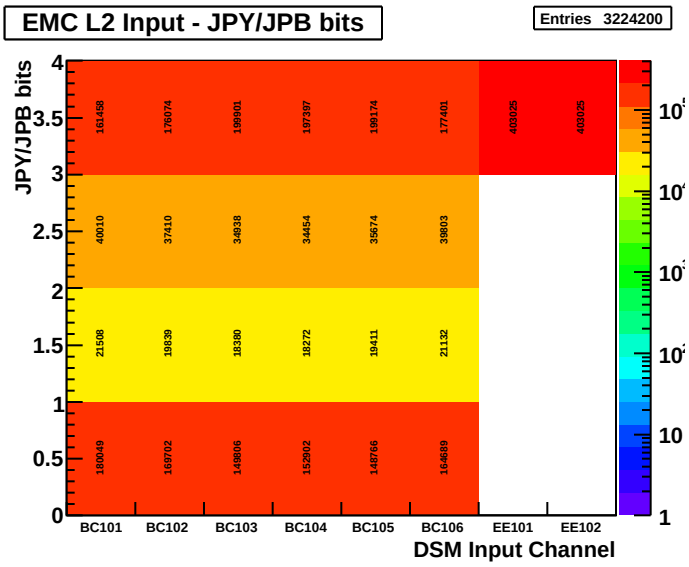

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

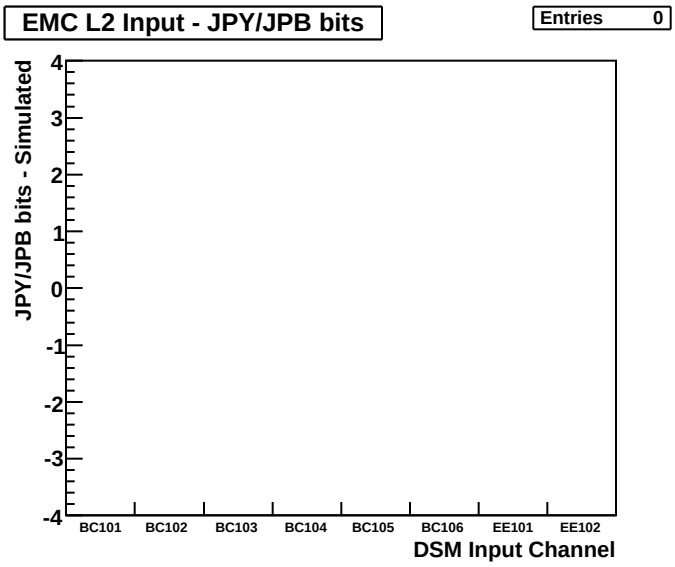

Endcap EMC L1 Input - High Tower Bits
Entries 4836300
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

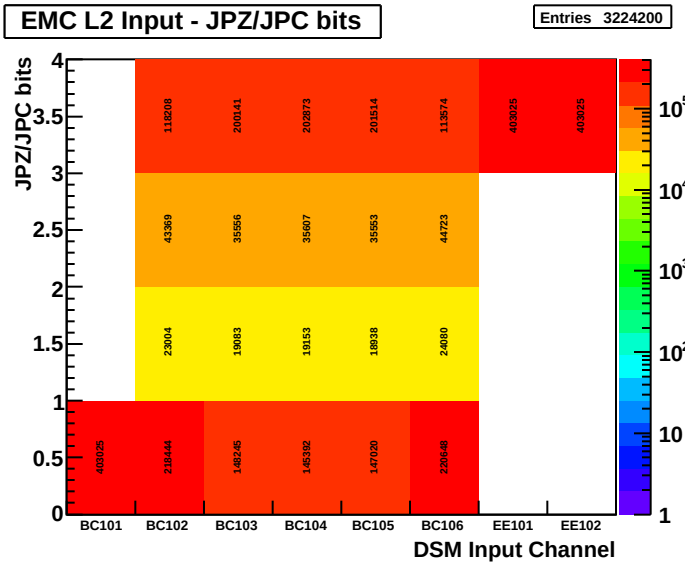

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

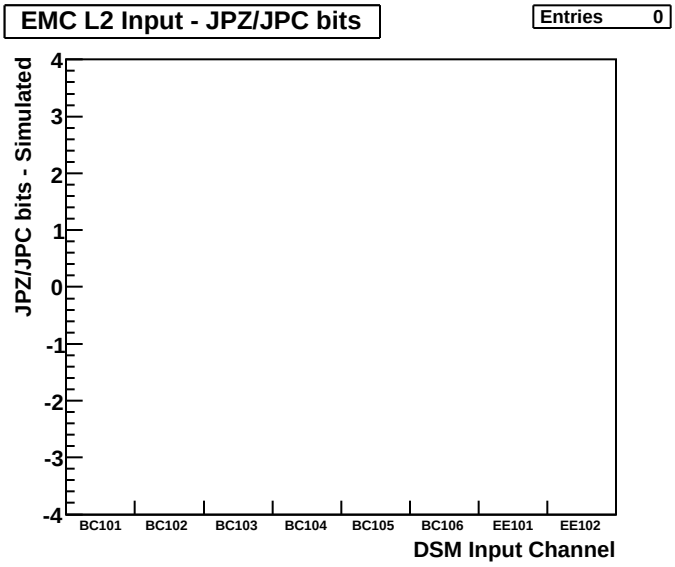

EMC L2 Input - JPX/JPA bits
Entries 3224200
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
210068 203326 204191 200195 199762 197741 403025 403025
35670 35462 35184 34664 35897 35306
19172 18856 18895 18371 19109 18665
138115 145381 144755 140795 148257 151313

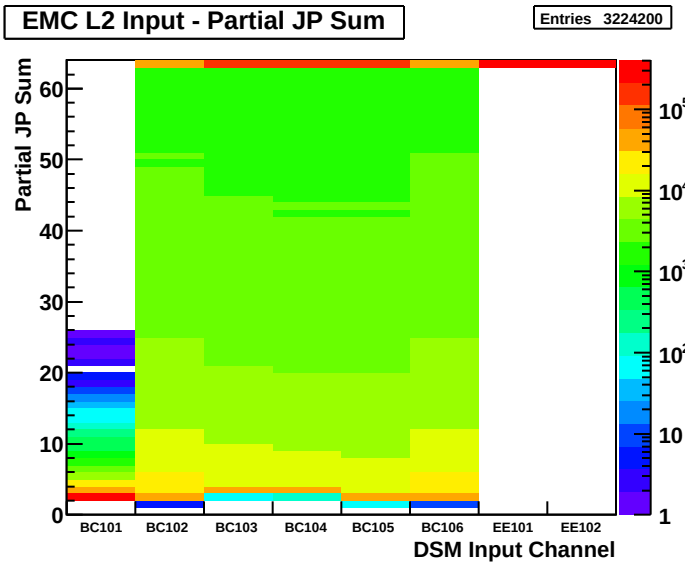

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

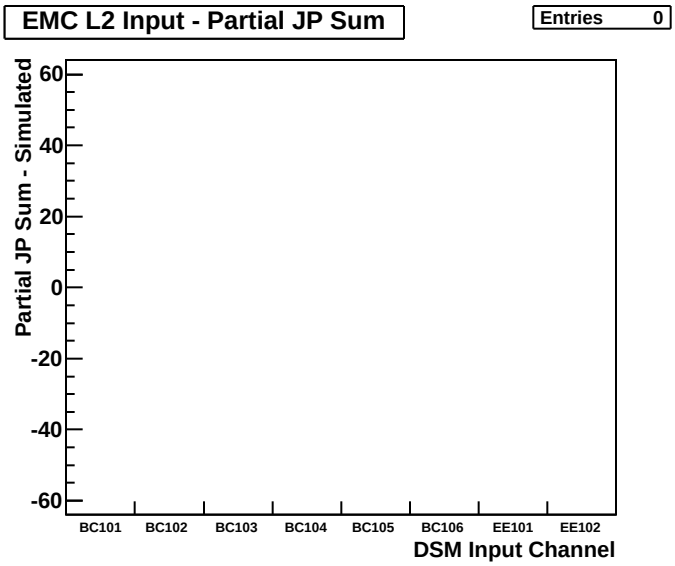

EMC L2 Input - JPY/JPB bits
Entries 3224200
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
161458 176074 199901 197397 199174 177401 403025 403025
40010 37410 34938 34454 35674 38803
21508 19839 18380 18272 19411 21132
180049 169702 149806 152902 148766 164689

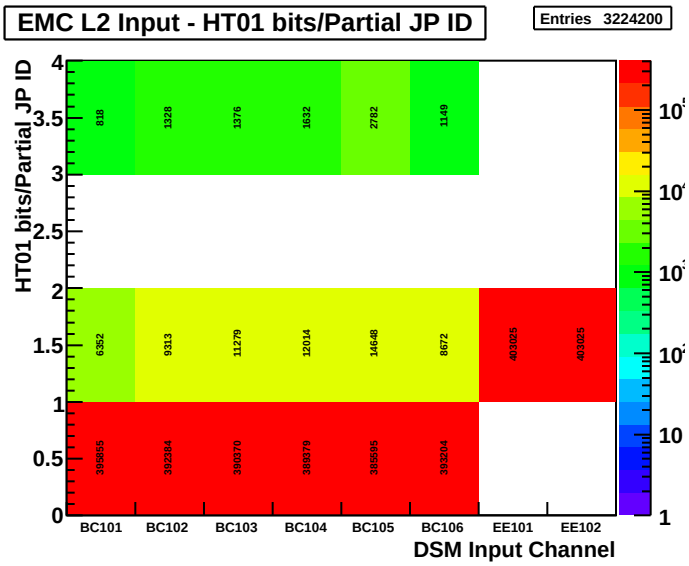

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

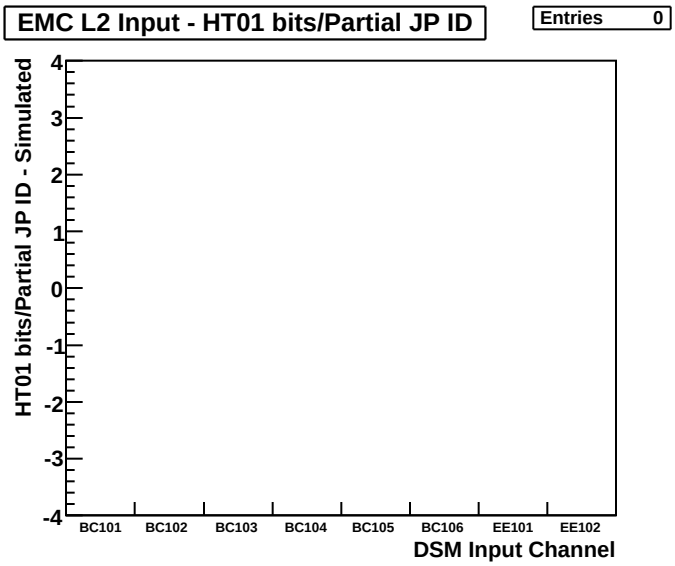

EMC L2 Input - JPZ/JPC bits
Entries 3224200
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
118208 200141 202873 201514 113574 403025 403025
43369 35556 35607 35553 44723
23004 19083 19153 18938 24080
403025 218444 148245 145392 147020 220548

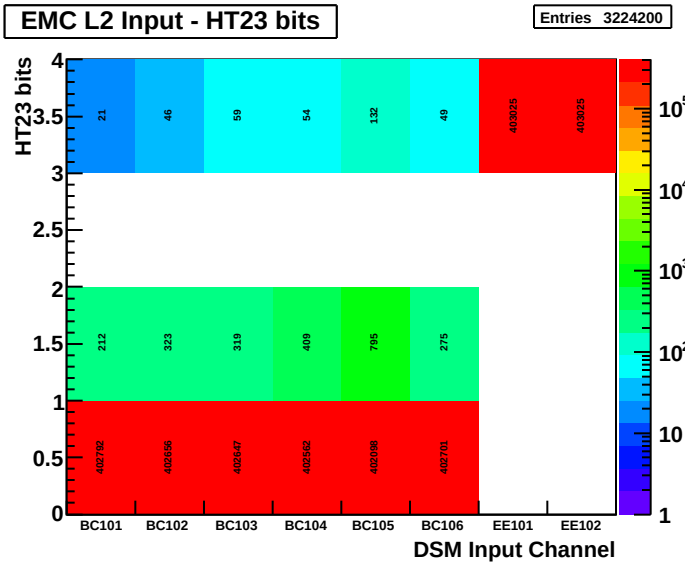

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

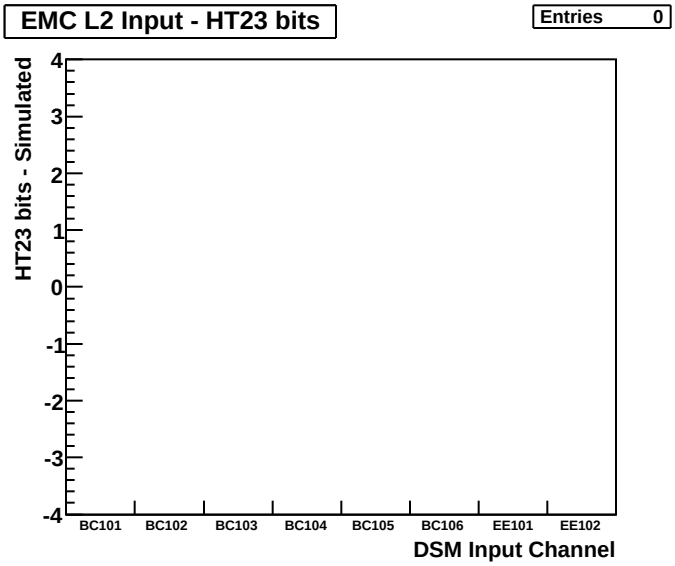

EMC L2 Input - Partial JP Sum
Entries 3224200
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

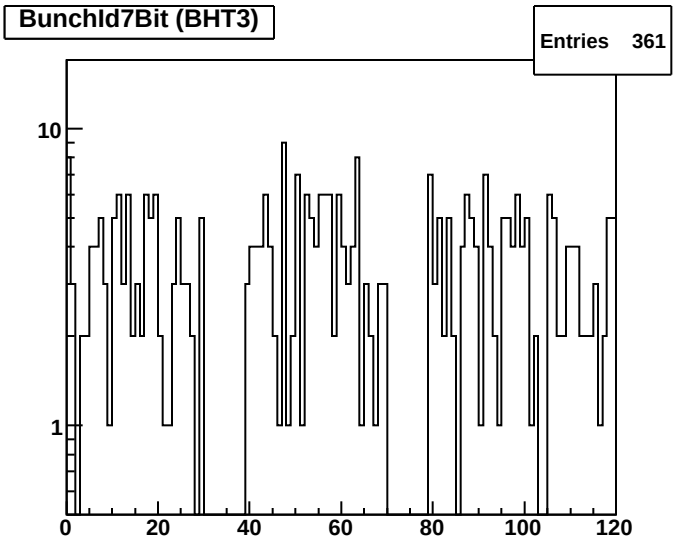

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

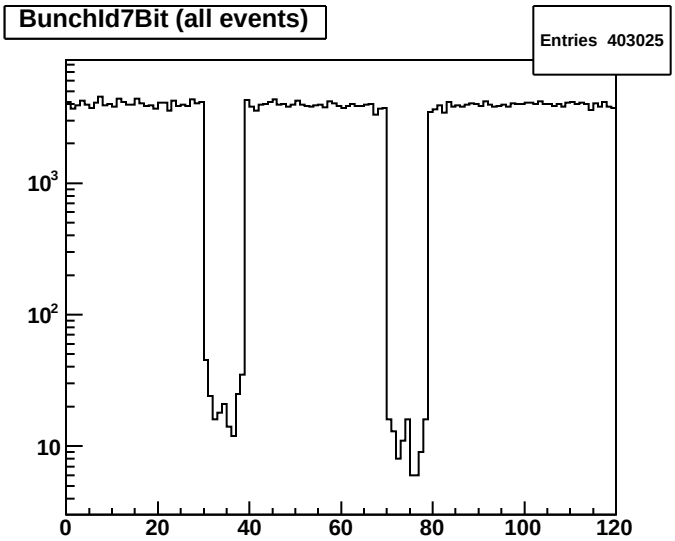

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3224200
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

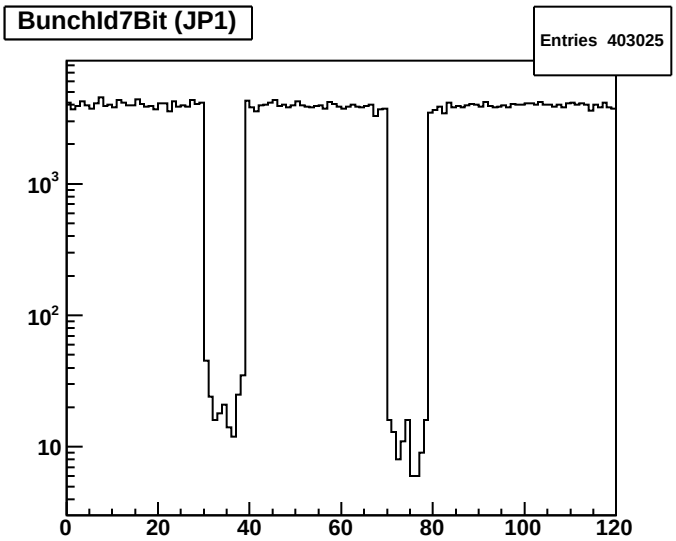

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

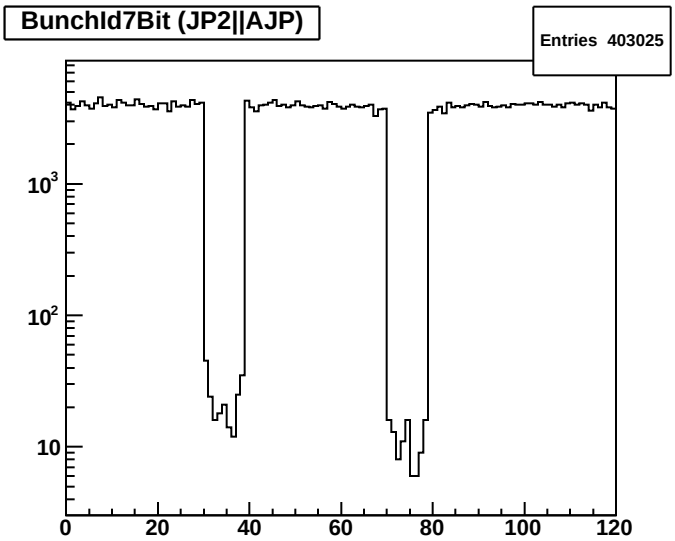

EMC L2 Input - HT23 bits
Entries 3224200
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

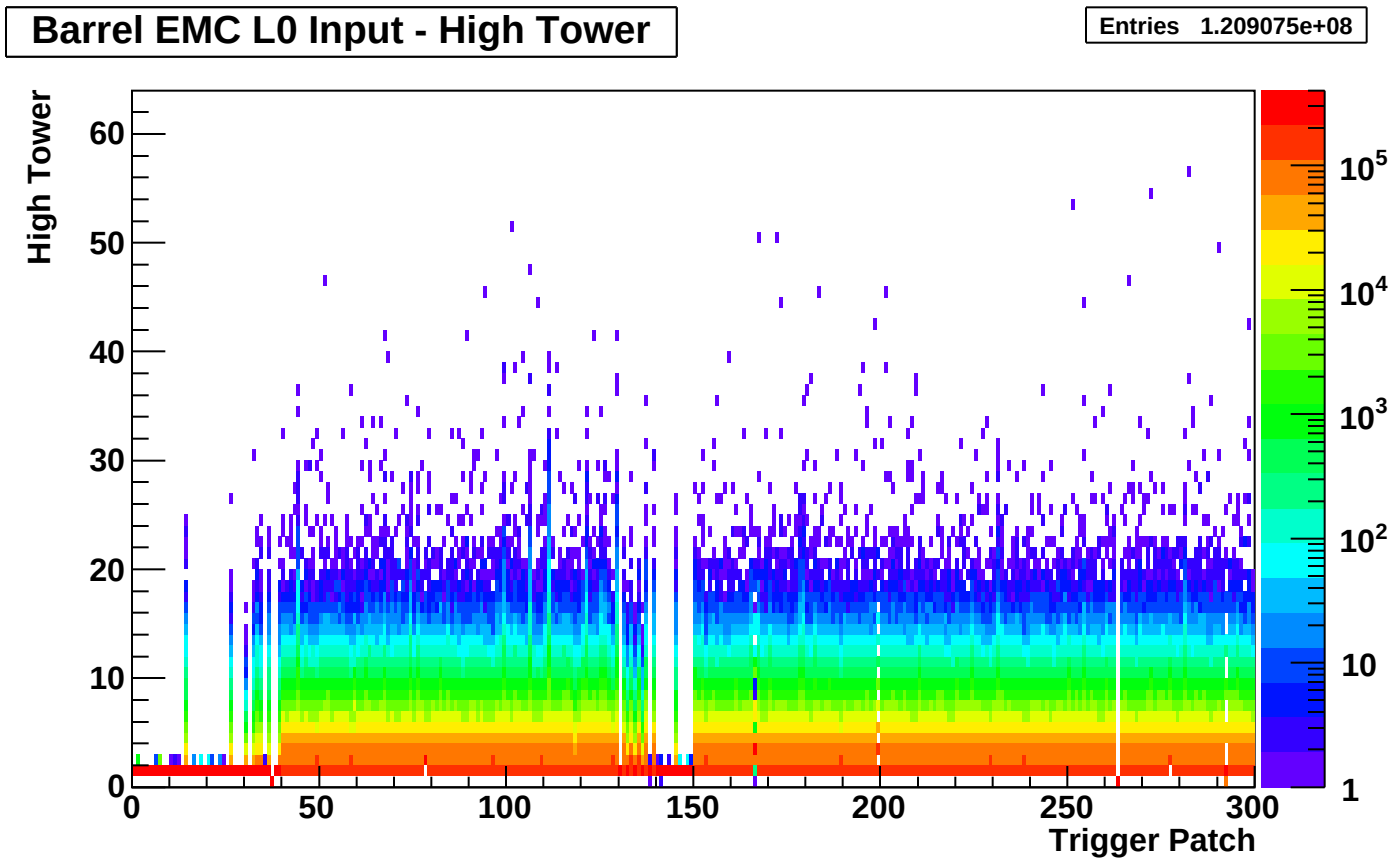

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

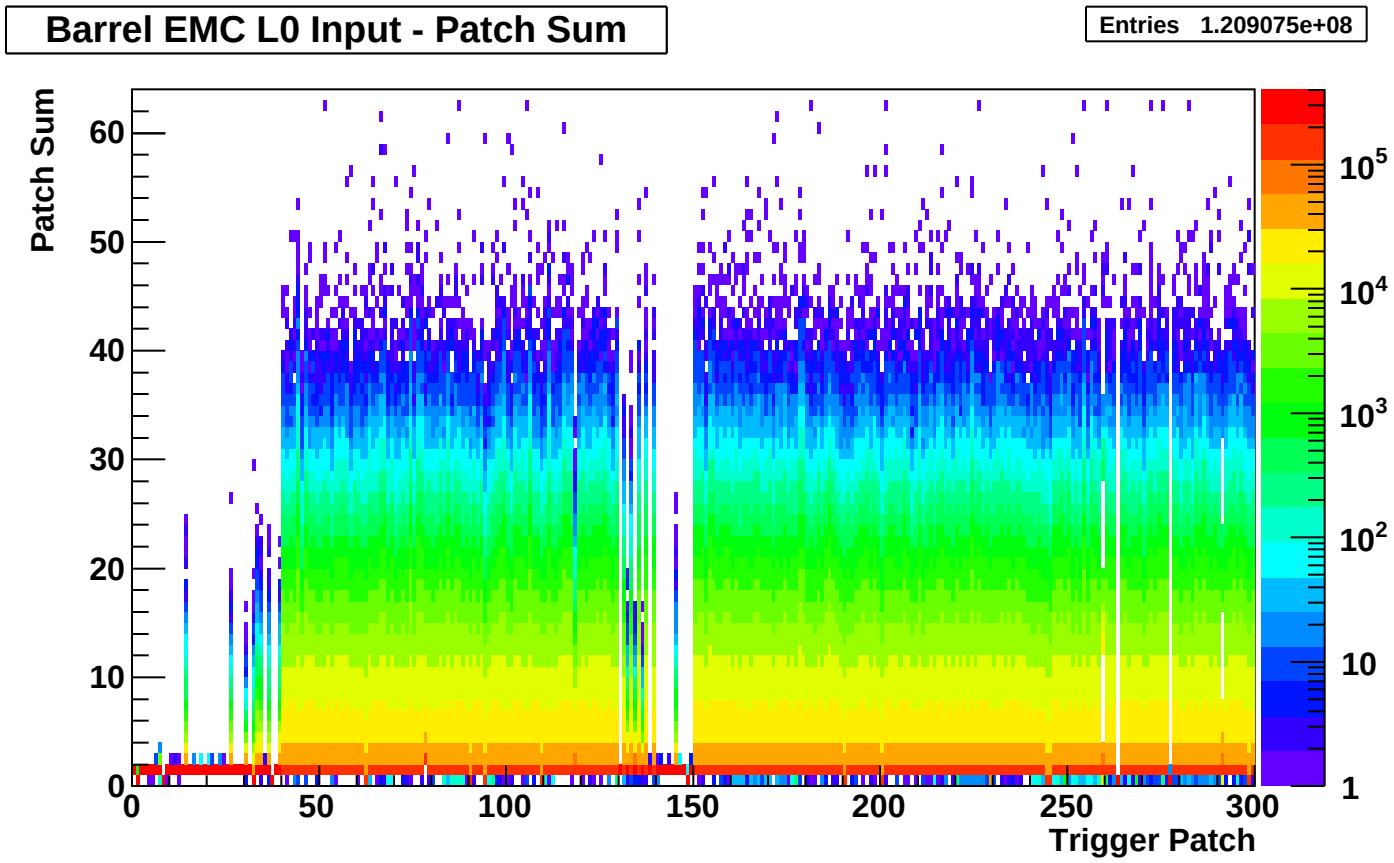

BunchId7Bit (BHT3)
Entries 361
10
1
0
20
40
60
80
100
120

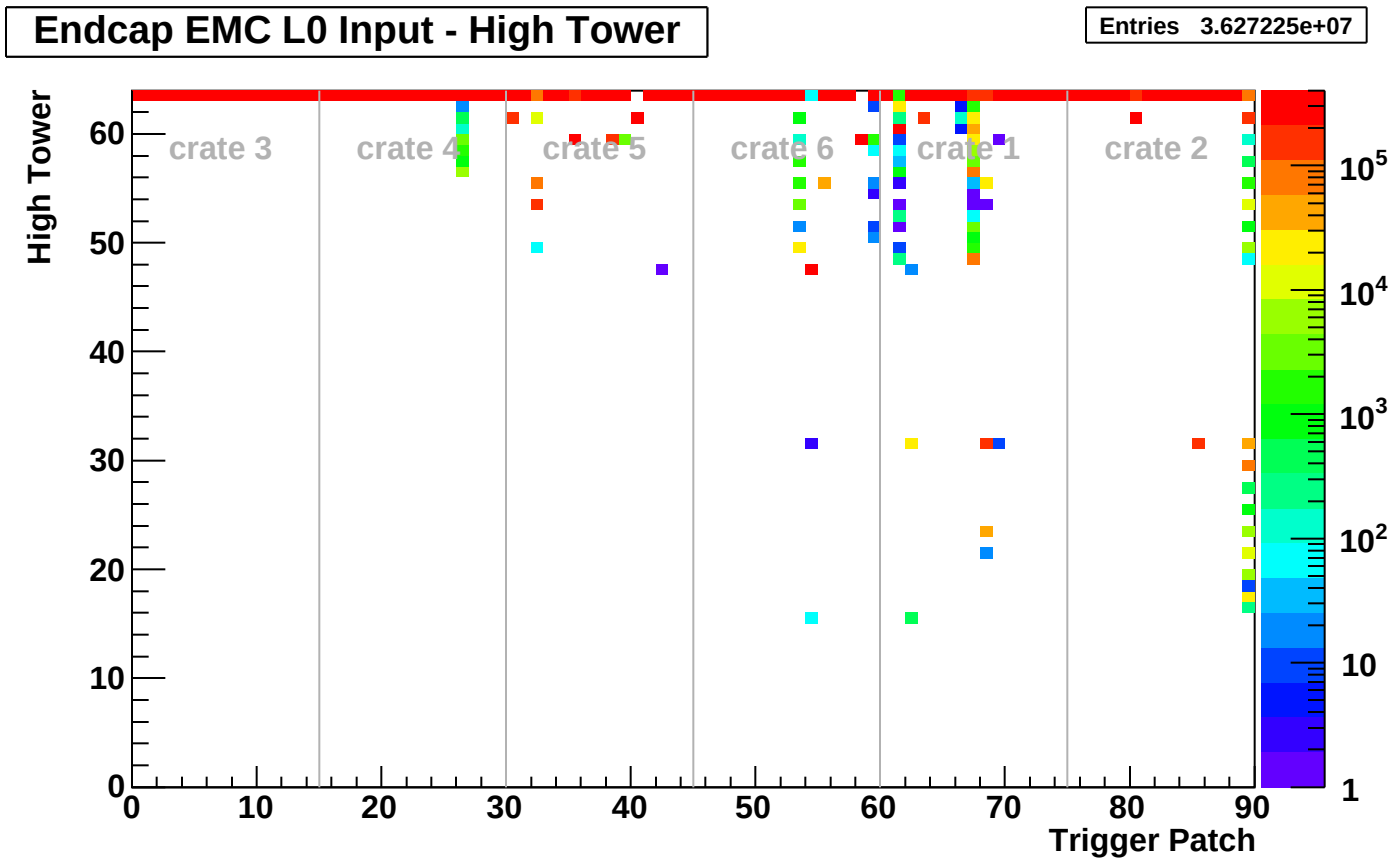

BunchId7Bit (all events)
Entries 403025
10^3
10^2
10
0
20
40
60
80
100
120

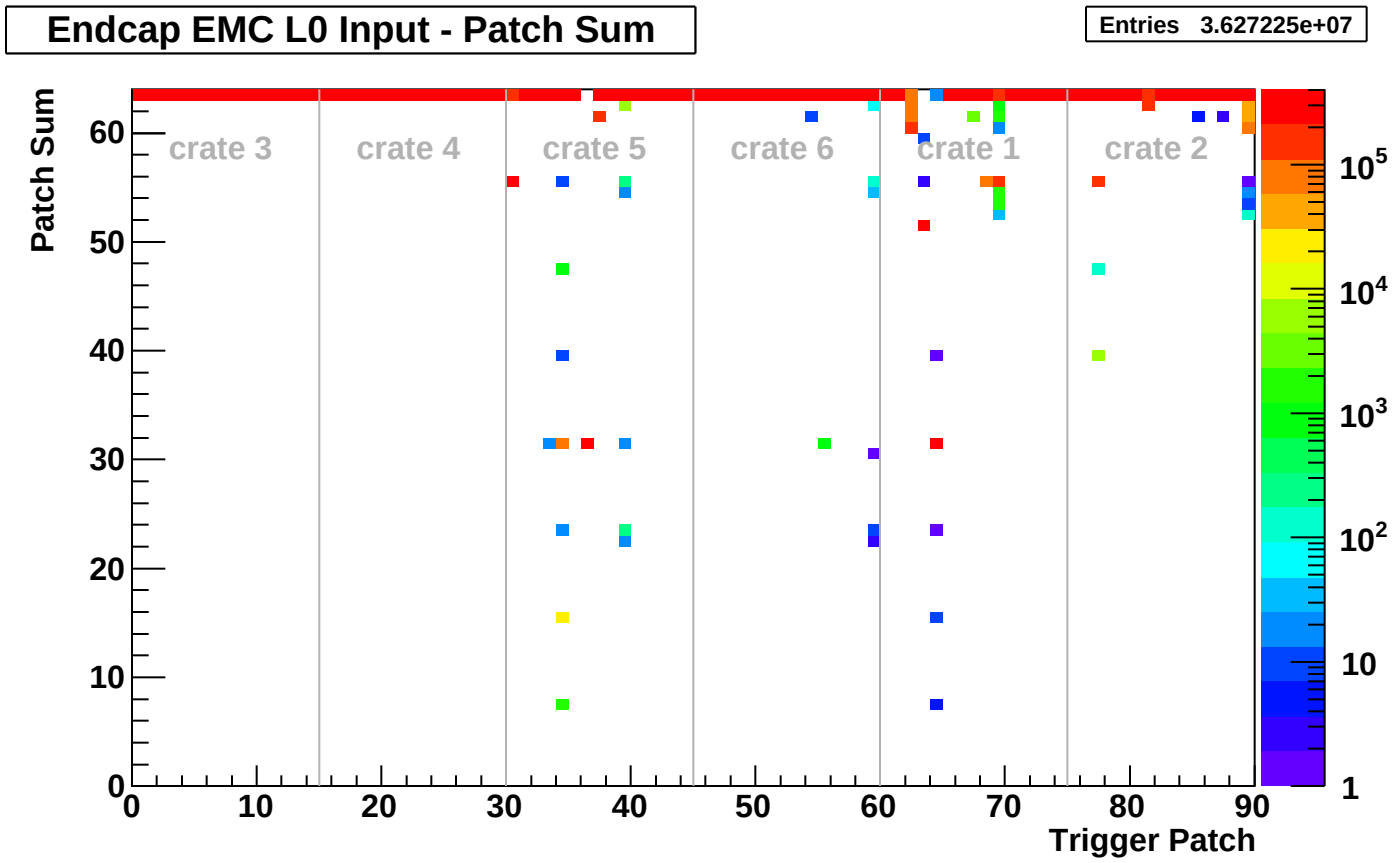

BunchId7Bit (JP1)
Entries 403025
10^3
10^2
10
0
20
40
60
80
100
120

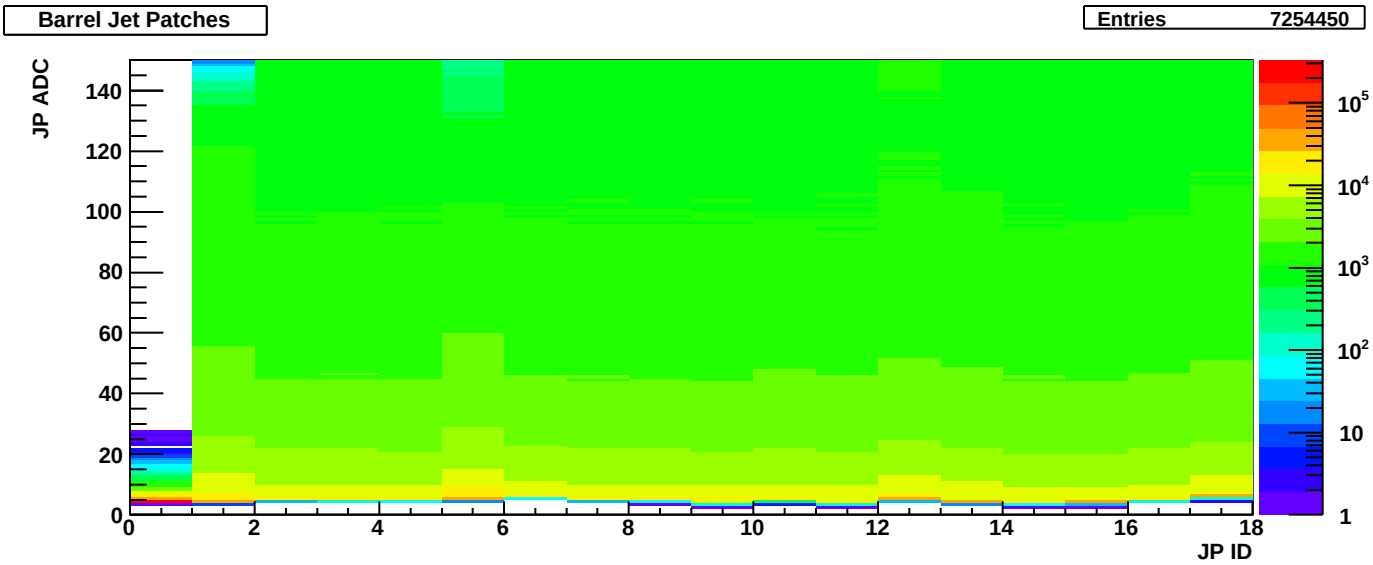

BunchId7Bit (JP2||AJP)
Entries 403025
10^3
10^2
10
0
20
40
60
80
100
120

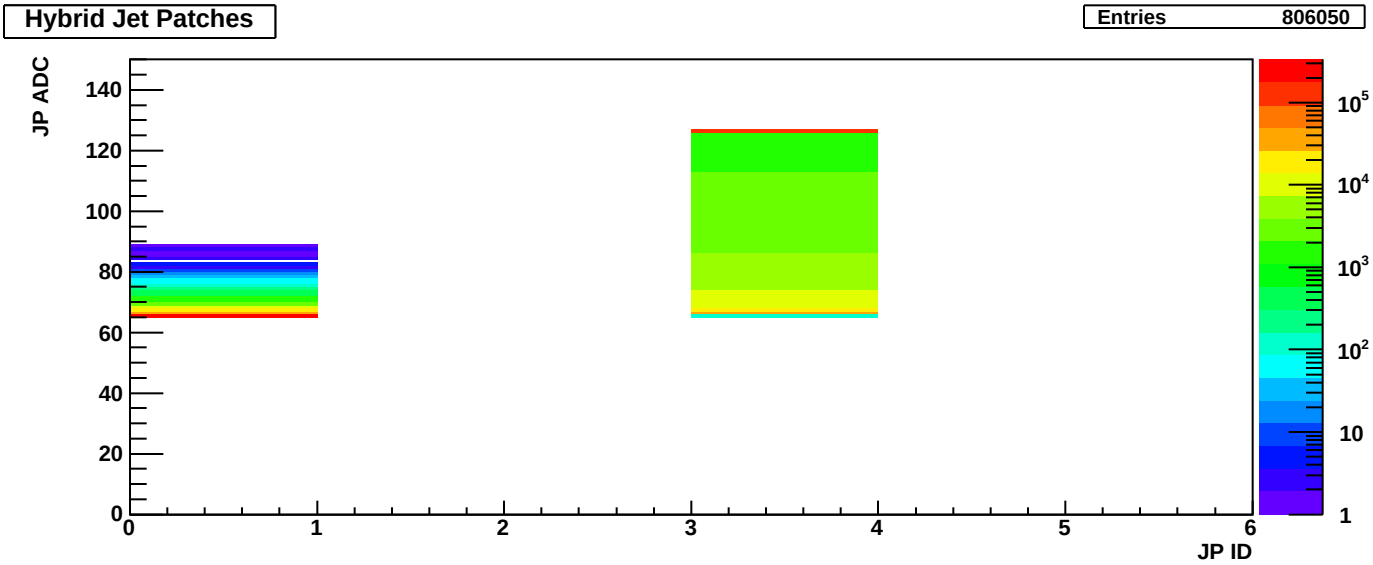

Barrel EMC L0 Input - High Tower
Entries 1.209075e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

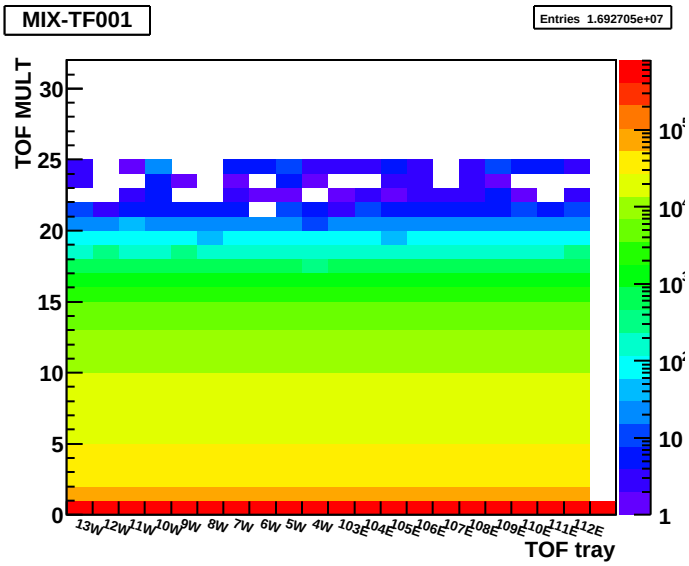

Barrel EMC L0 Input - Patch Sum
Entries 1.209075e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

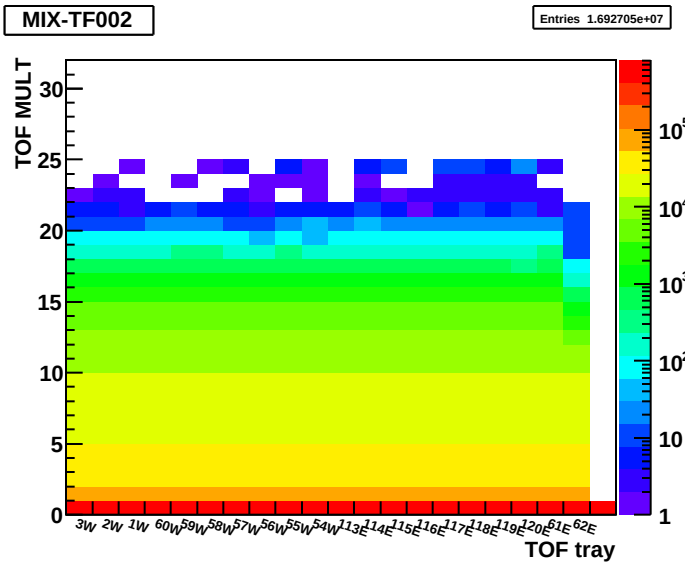

Endcap EMC L0 Input - High Tower
Entries 3.627225e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

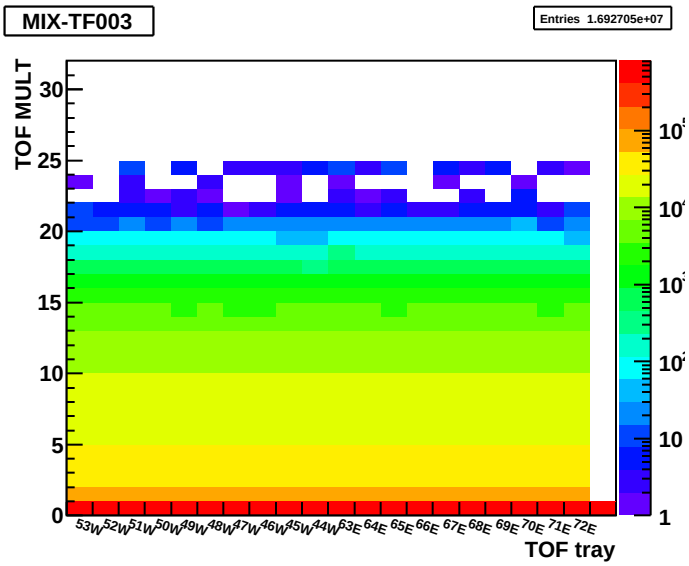

Endcap EMC L0 Input - Patch Sum
Entries 3.627225e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

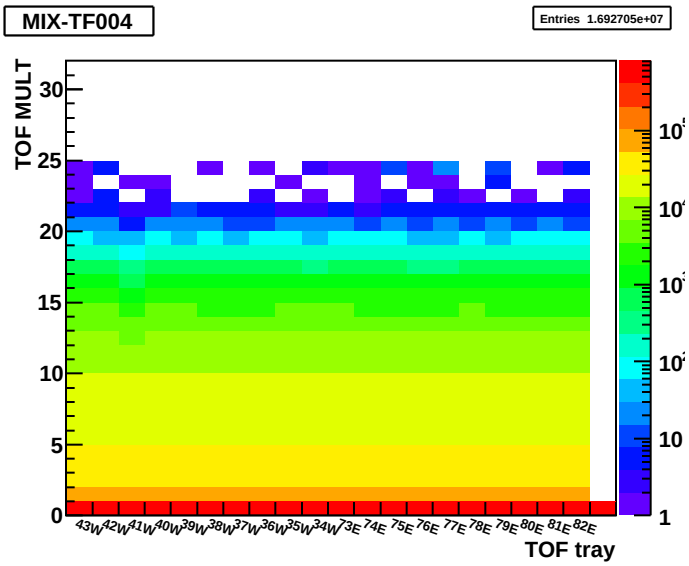

Barrel Jet Patches
Entries 7254450
JP ADC
JP ID

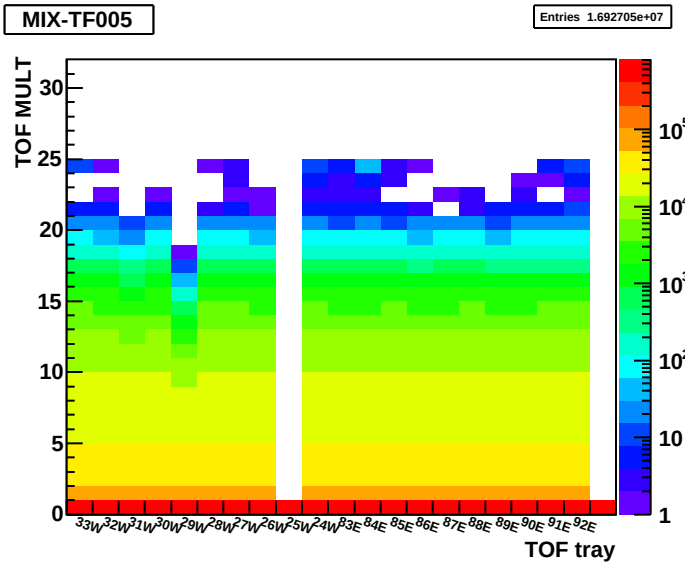

Hybrid Jet Patches
Entries 806050
JP ADC
JP ID

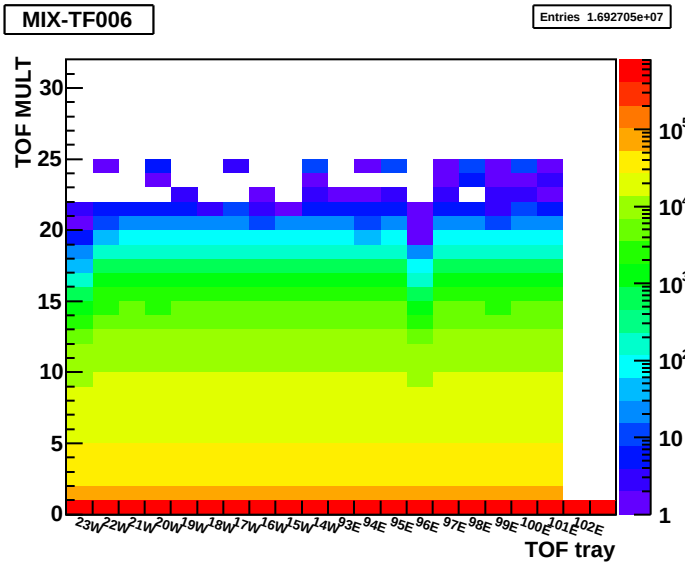

MIX-TF001
Entries 1.692705e+07
TOF MULT
TOF tray

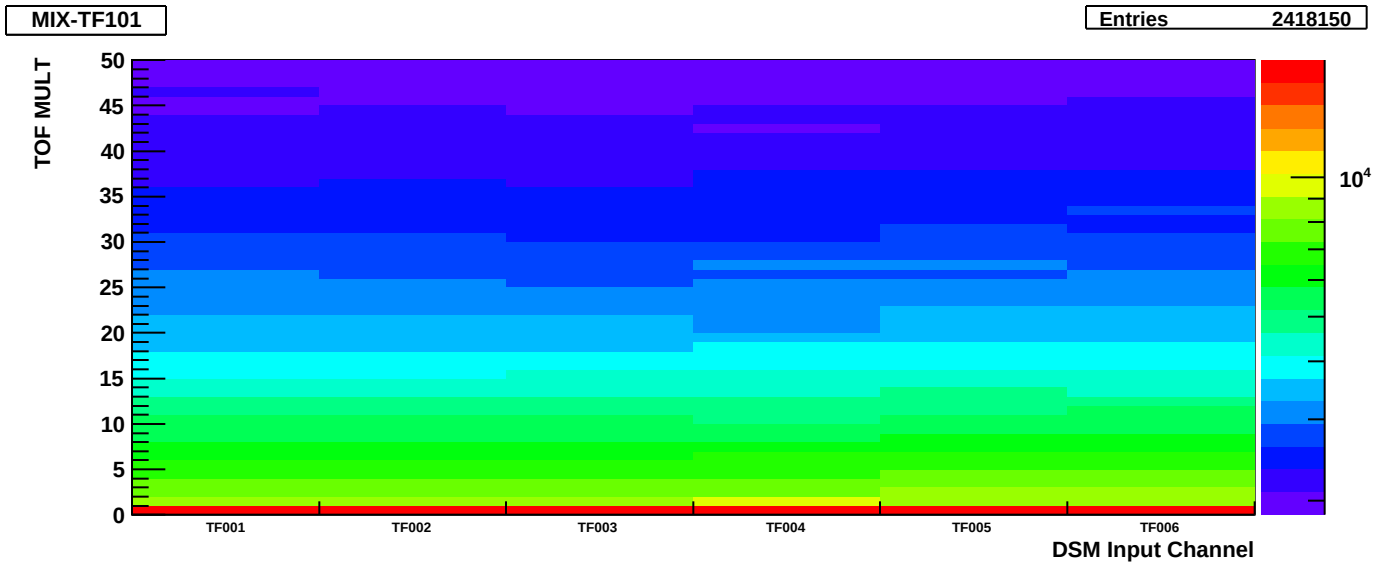

MIX-TF002
Entries 1.692705e+07
TOF MULT
TOF tray

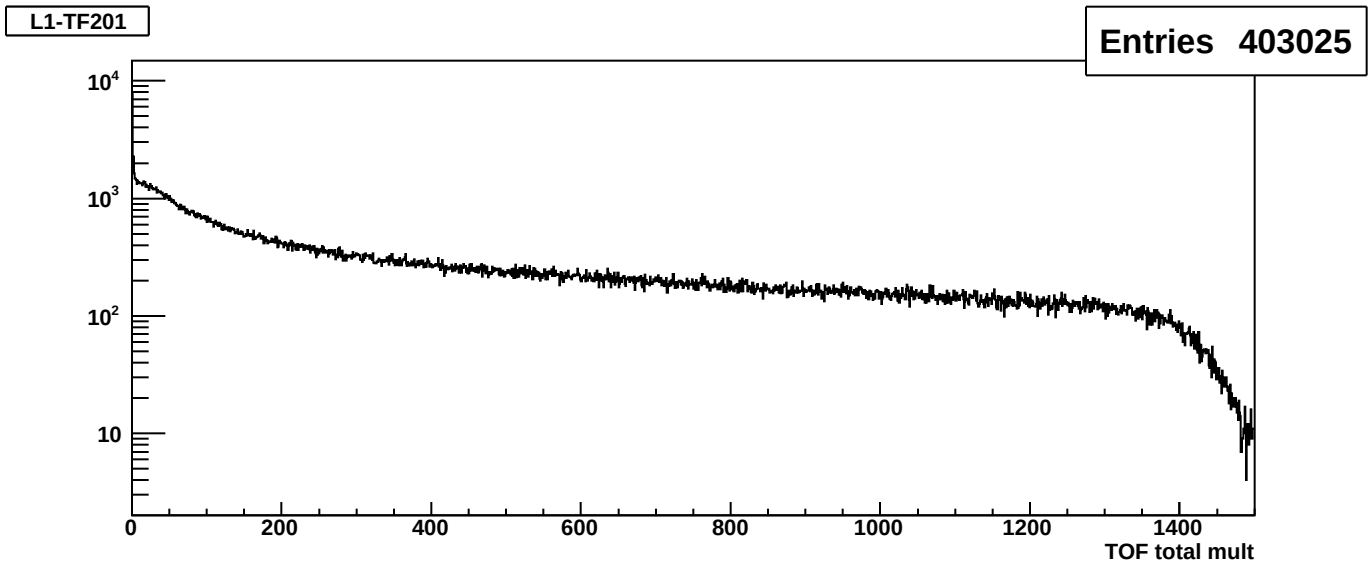

MIX-TF003
Entries 1.692705e+07
TOF MULT
TOF tray

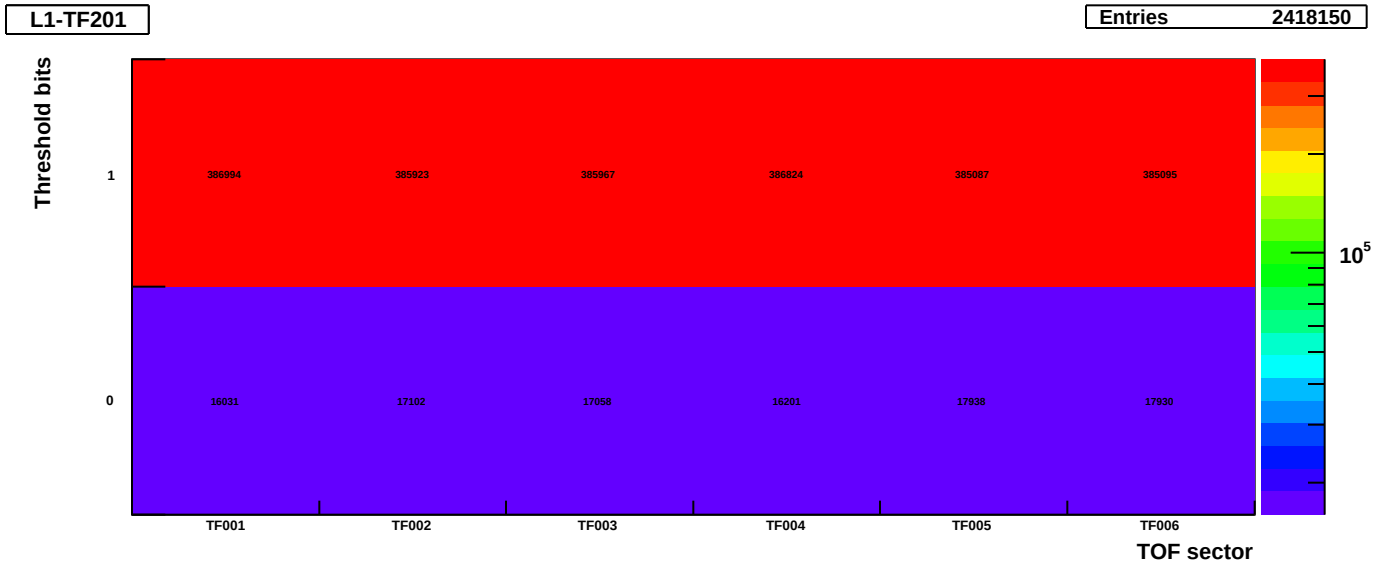

MIX-TF004
Entries 1.692705e+07
TOF MULT
TOF tray

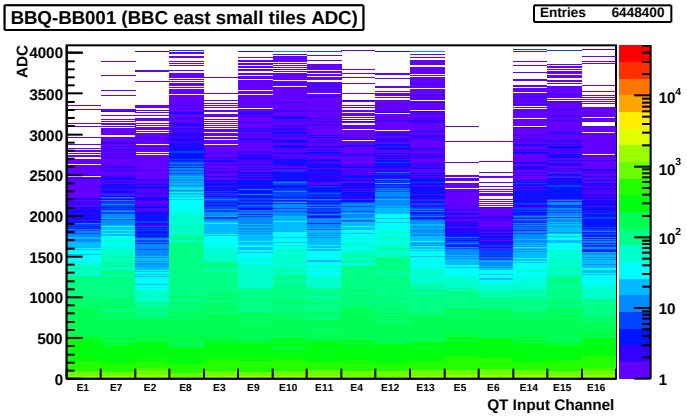

MIX-TF005
Entries 1.692705e+07
TOF MULT
TOF tray

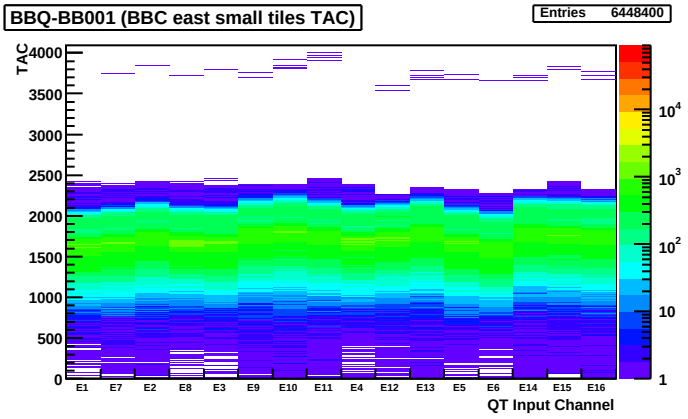

MIX-TF006
Entries 1.692705e+07
TOF MULT
TOF tray

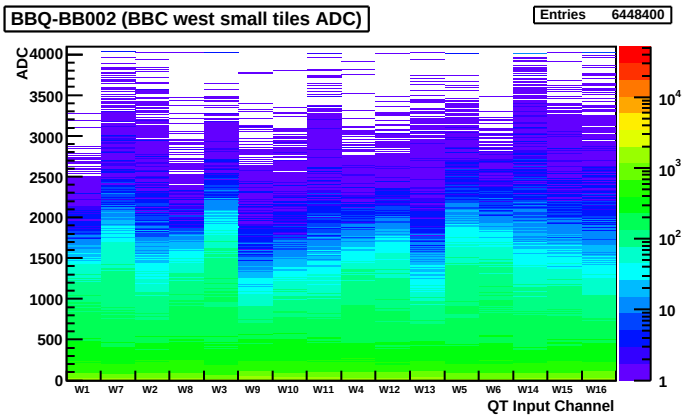

MIX-TF101
Entries 2418150
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

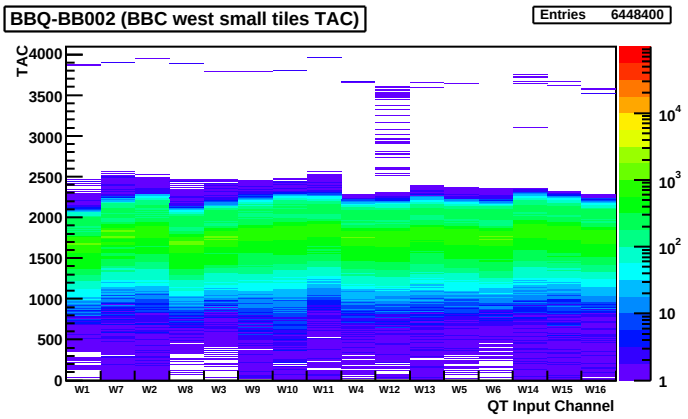

L1-TF201
Entries 403025
10^4
10^3
10^2
10
0
200
400
600
800
1000
1200
1400
TOF total mult

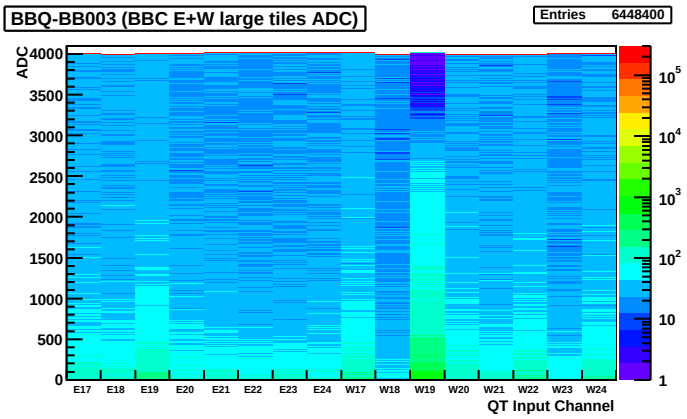

L1-TF201
Entries 2418150
Threshold bits
1
0
386994
385923
385967
386824
385087
385095
16031
17102
17058
16201
17938
17930
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

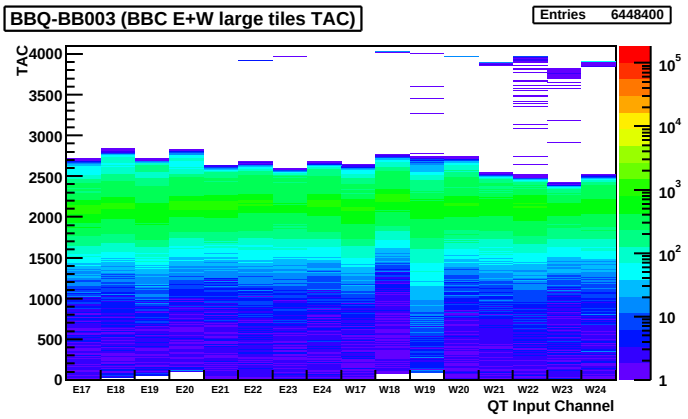

BBQ-BB001 (BBC east small tiles ADC)
Entries 6448400
ADC
QT Input Channel

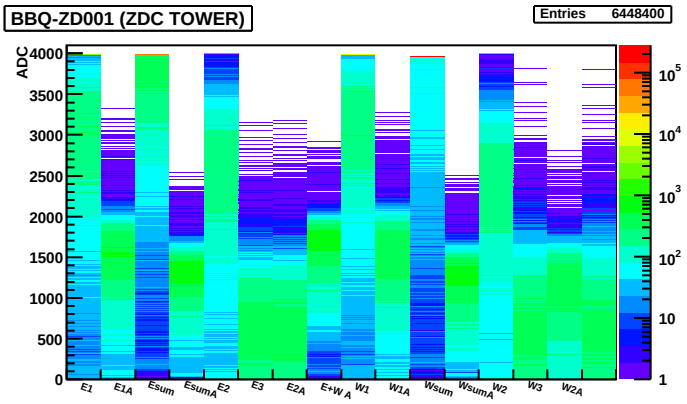

BBQ-BB001 (BBC east small tiles TAC)
Entries 6448400
TAC
QT Input Channel

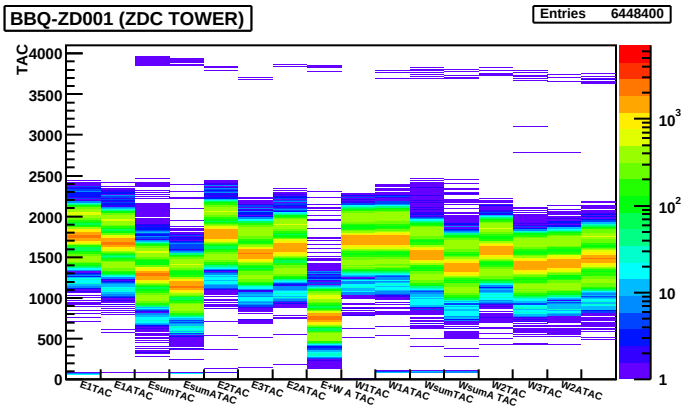

BBQ-BB002 (BBC west small tiles ADC)
Entries 6448400
ADC
QT Input Channel

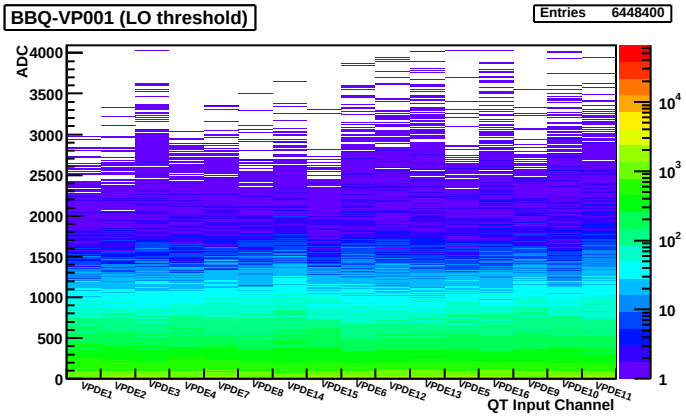

BBQ-BB002 (BBC west small tiles TAC)
Entries 6448400
TAC
QT Input Channel

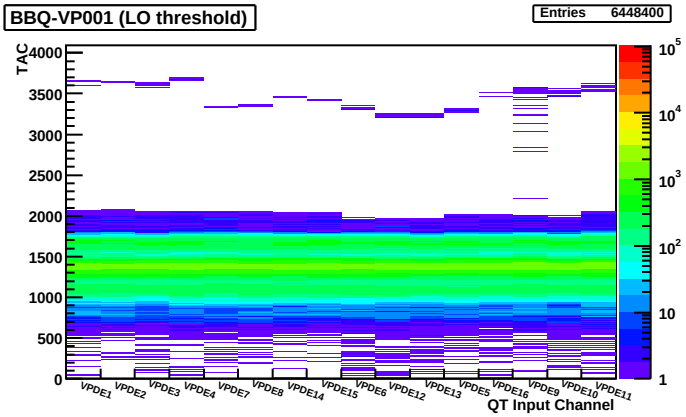

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 6448400
ADC
QT Input Channel

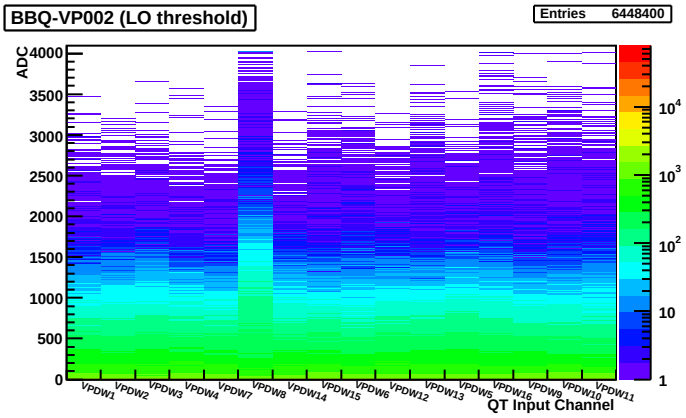

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 6448400
TAC
QT Input Channel

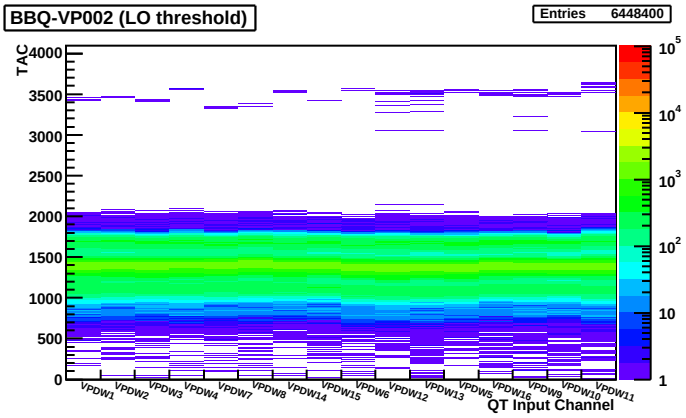

BBQ-ZD001 (ZDC TOWER)
Entries 6448400
ADC

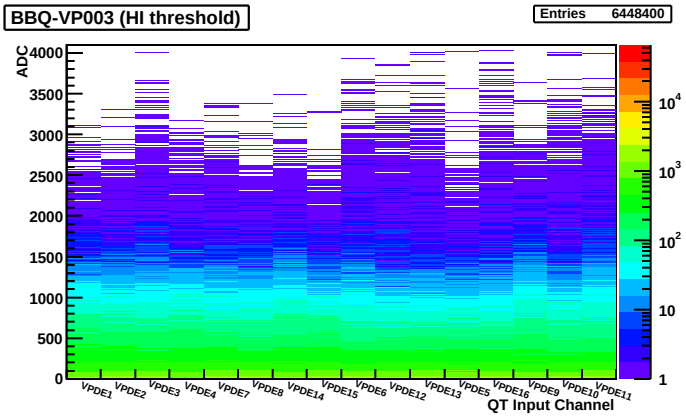

BBQ-ZD001 (ZDC TOWER)
Entries 6448400
TAC

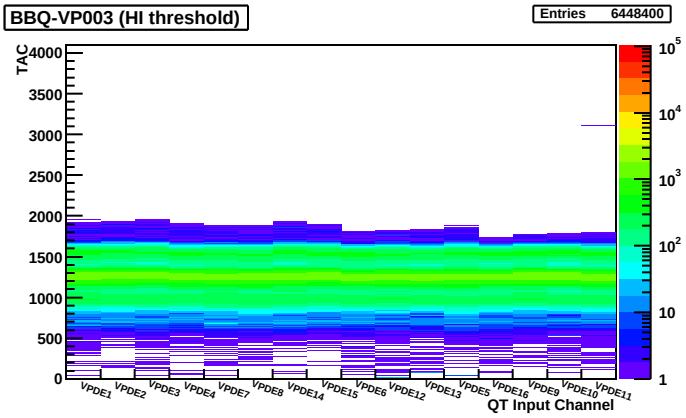

BBQ-VP001 (LO threshold)
Entries 6448400
ADC
QT Input Channel

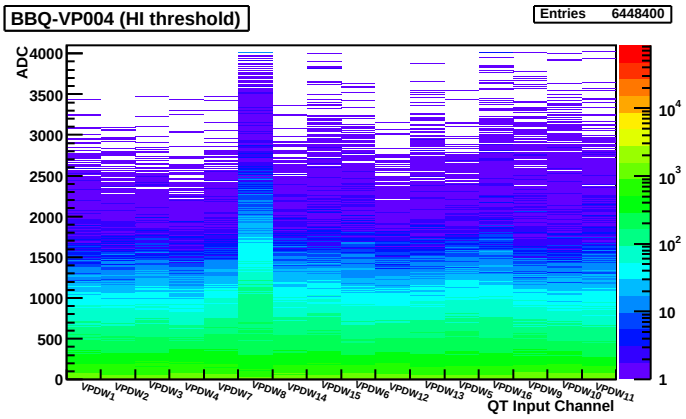

BBQ-VP001 (LO threshold)
Entries 6448400
TAC
QT Input Channel

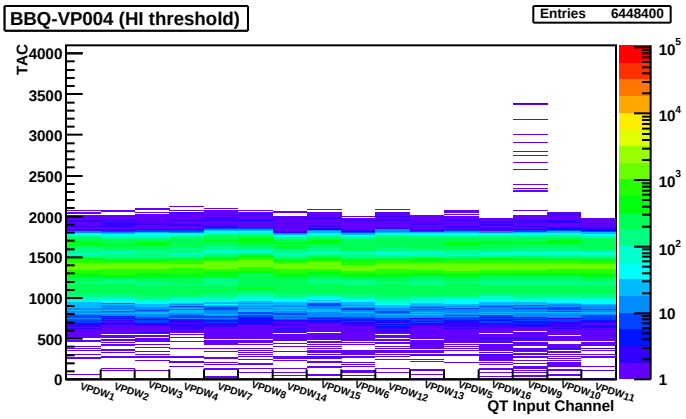

BBQ-VP002 (LO threshold)
Entries 6448400
ADC
QT Input Channel

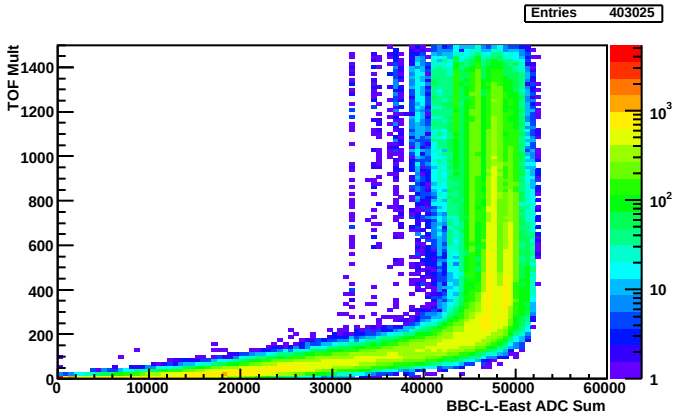

BBQ-VP002 (LO threshold)
Entries 6448400
TAC
QT Input Channel

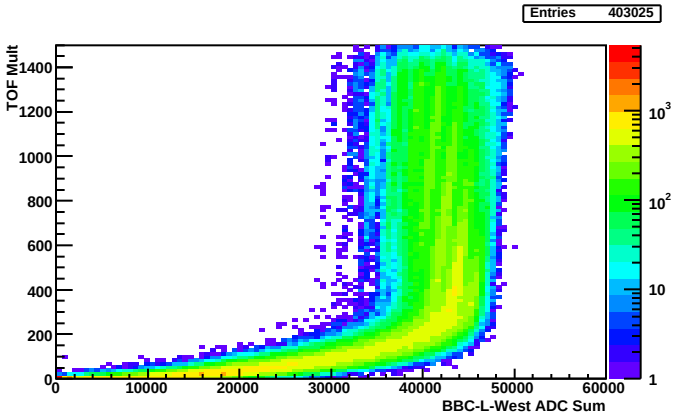

BBQ-VP003 (HI threshold)
Entries 6448400
ADC
QT Input Channel

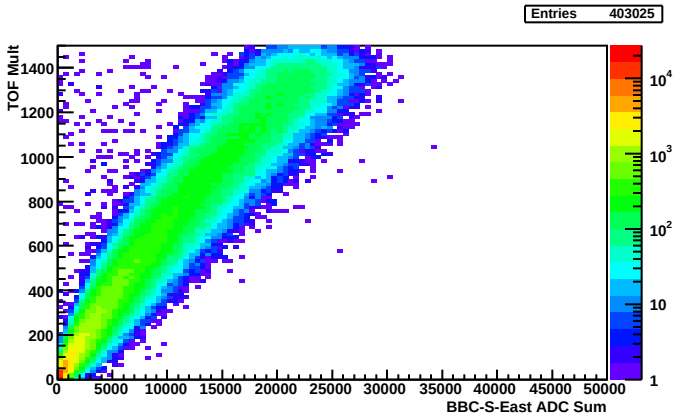

BBQ-VP003 (HI threshold)
Entries 6448400
TAC
QT Input Channel

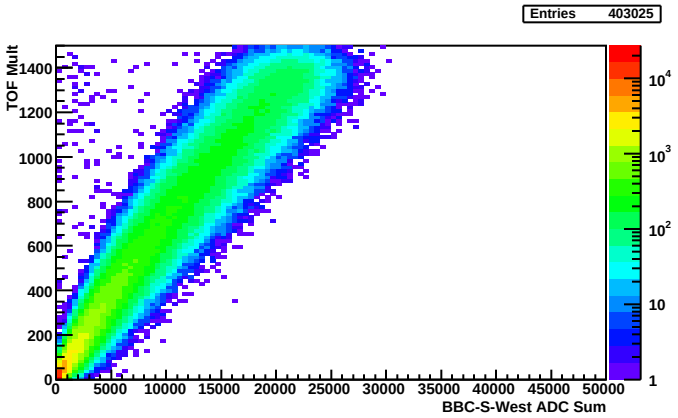

BBQ-VP004 (HI threshold)
Entries 6448400
ADC
QT Input Channel

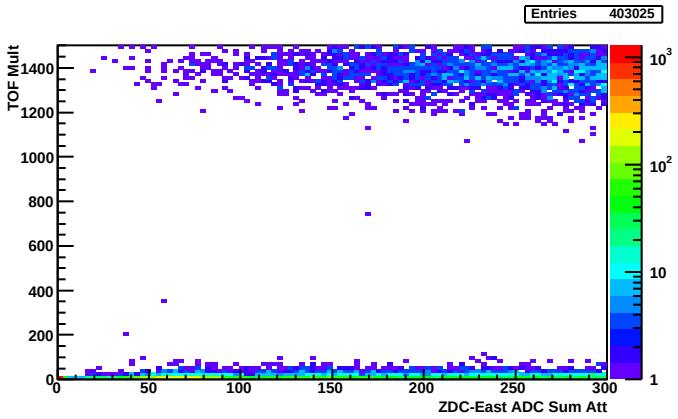

BBQ-VP004 (HI threshold)
Entries 6448400
TAC
QT Input Channel

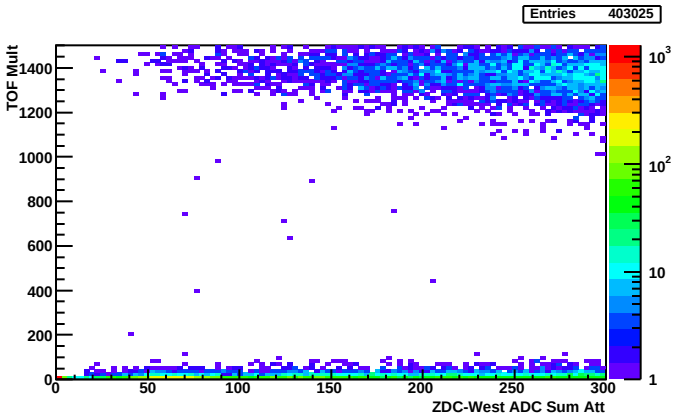

Entries 403025
TOF Mult
BBC-L-East ADC Sum

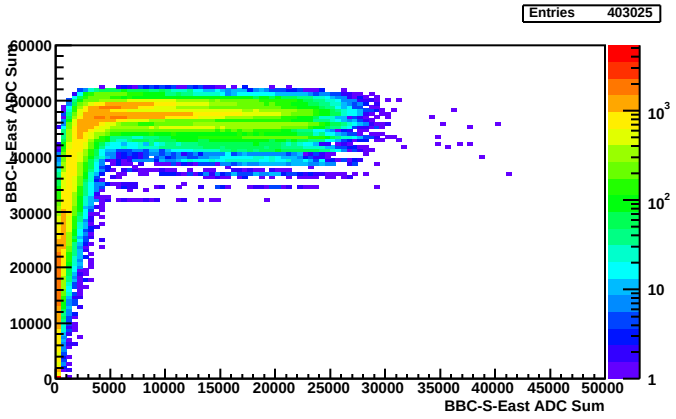

Entries 403025
TOF Mult
BBC-L-West ADC Sum

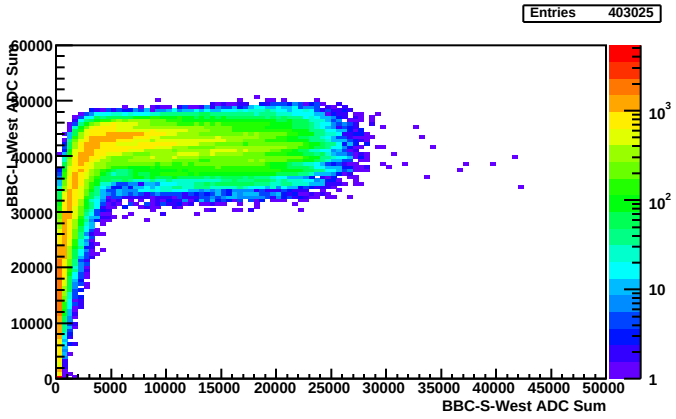

Entries 403025
TOF Mult
BBC-S-East ADC Sum

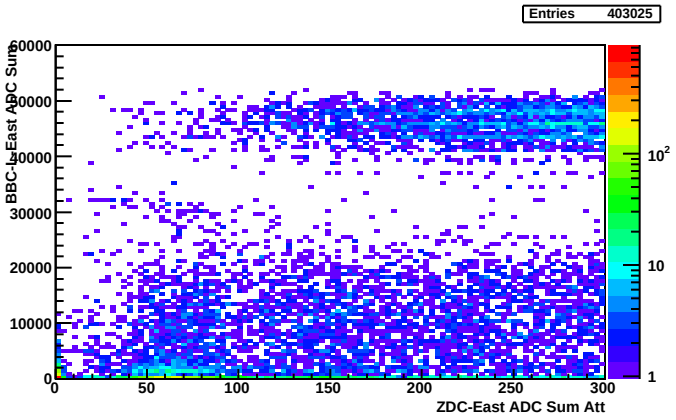

Entries 403025
TOF Mult
BBC-S-West ADC Sum

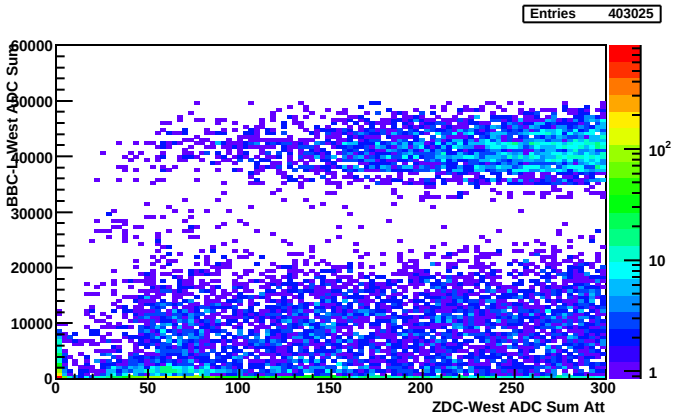

Entries 403025
TOF Mult
ZDC-East ADC Sum Att


Entries 403025
TOF Mult
ZDC-West ADC Sum Att


Entries 403025
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 403025
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 403025
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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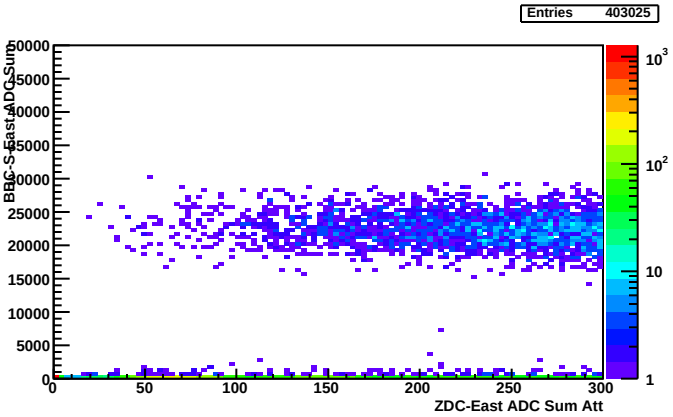
BBC-L-West ADC Sum
ZDC-West ADC Sum Att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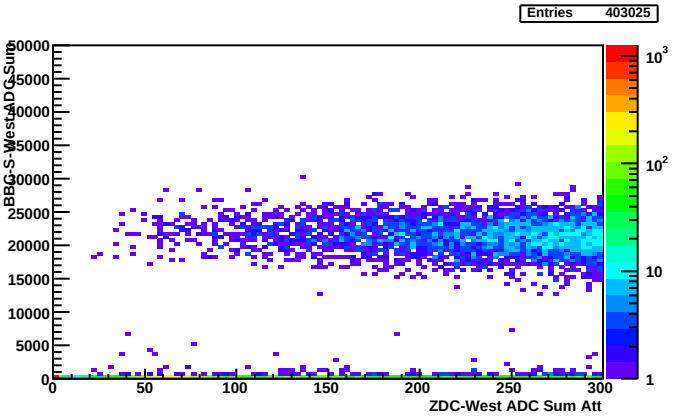
BBC-S-East ADC Sum
ZDC-East ADC Sum Att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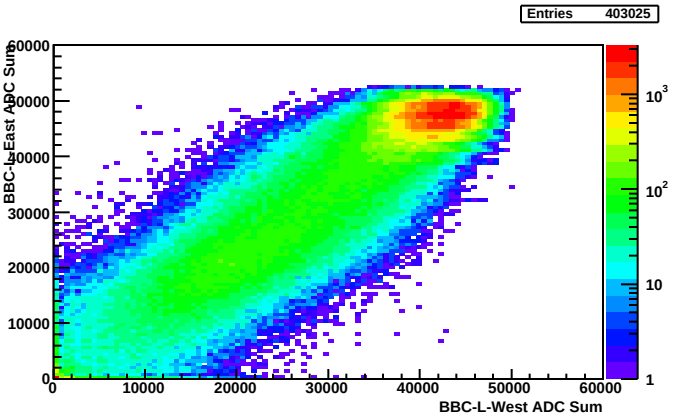
BBC-S-West ADC Sum
ZDC-West ADC Sum Att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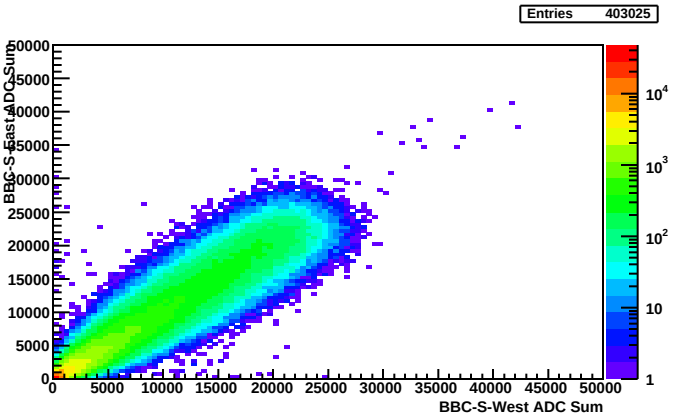
BBC-L-East ADC Sum
BBC-L-West ADC Sum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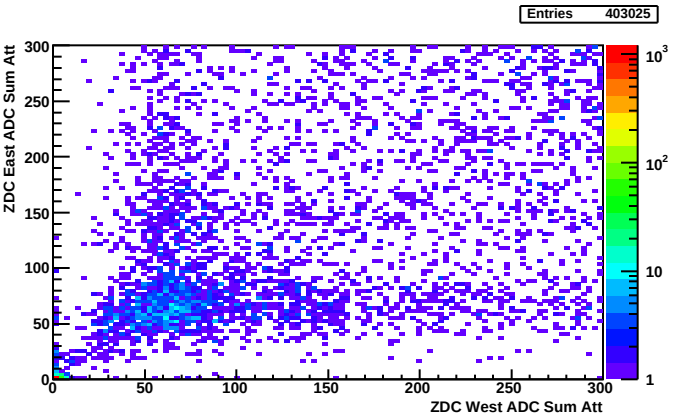
BBC-S-East ADC Sum
BBC-S-West ADC Sum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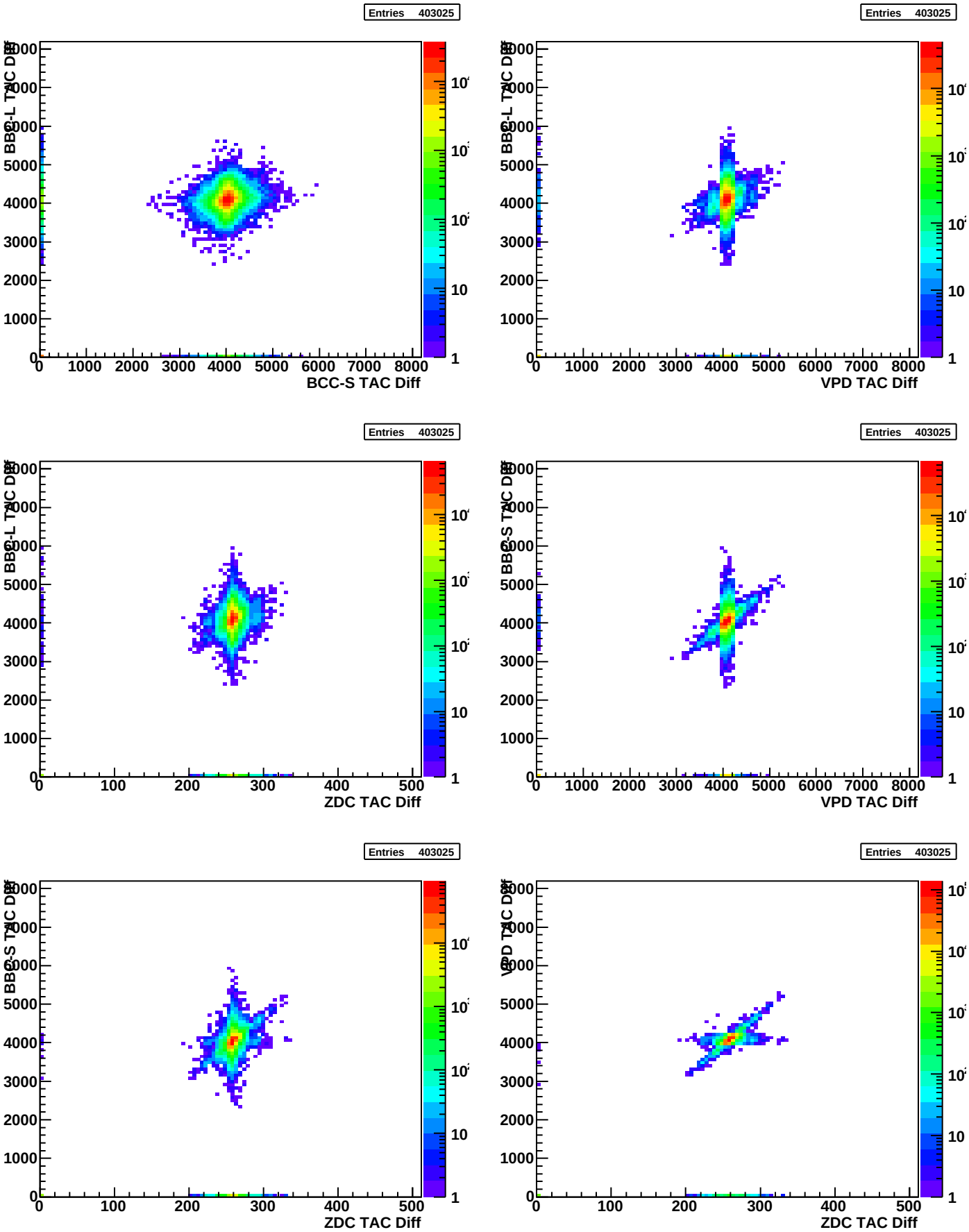
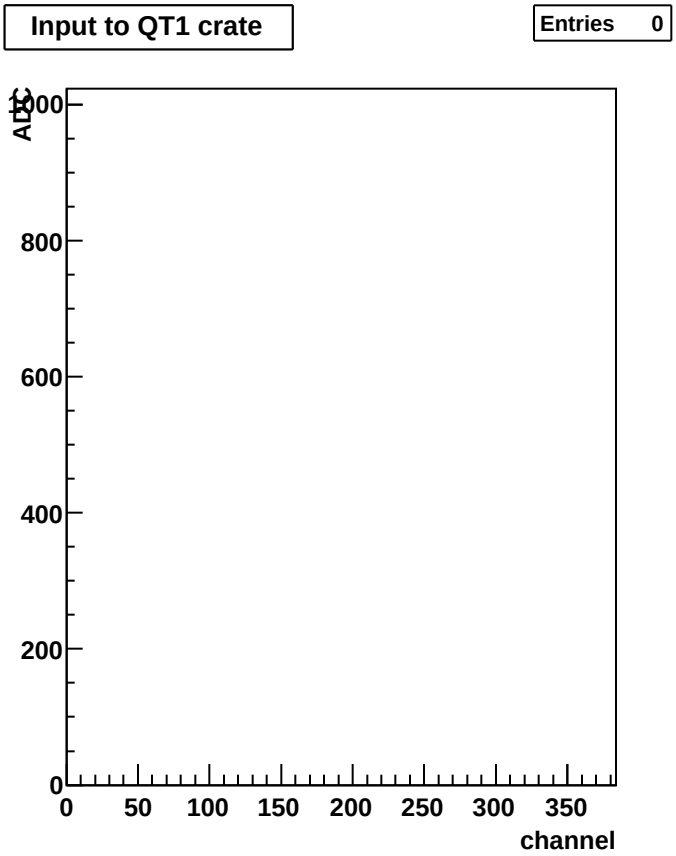
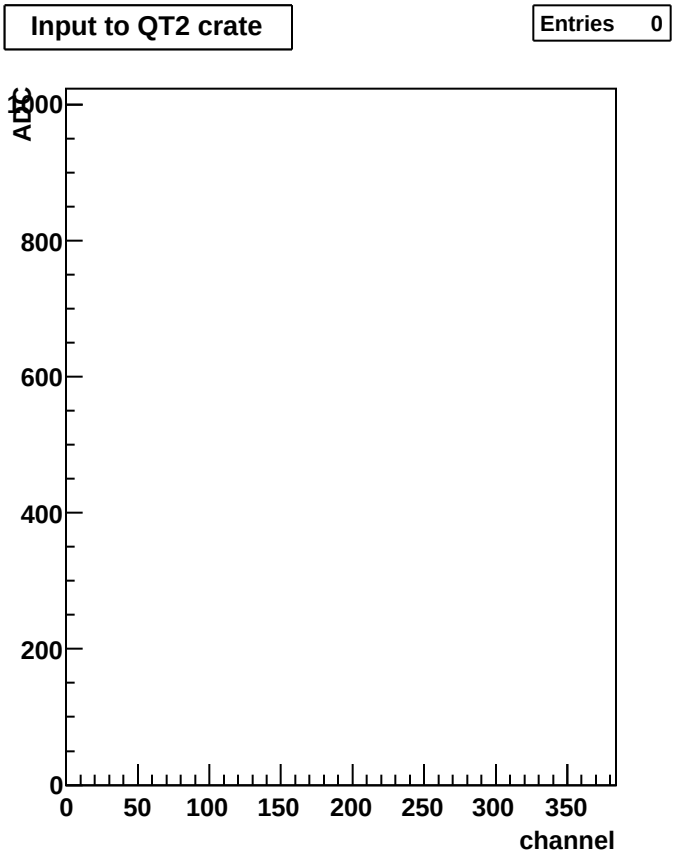
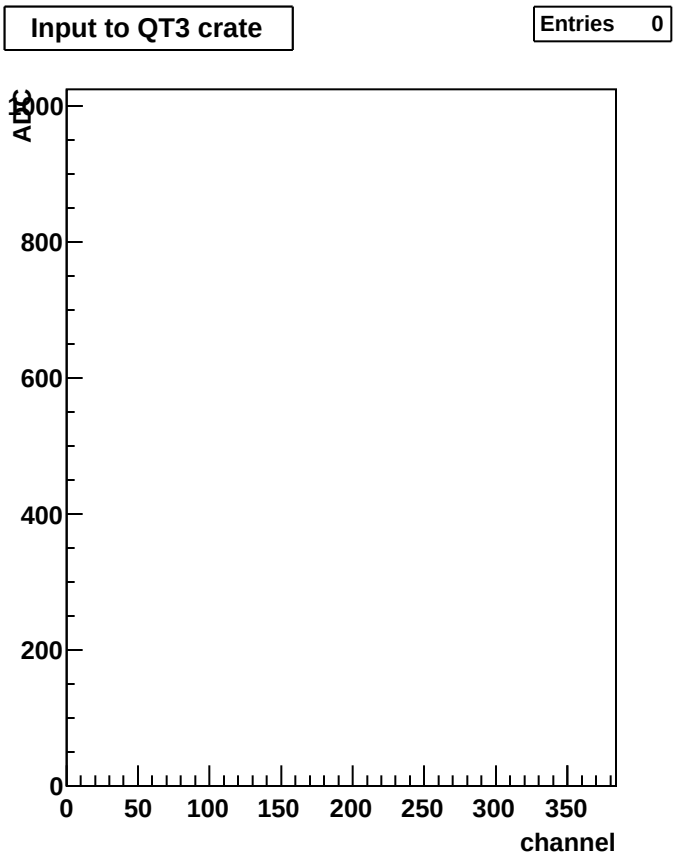
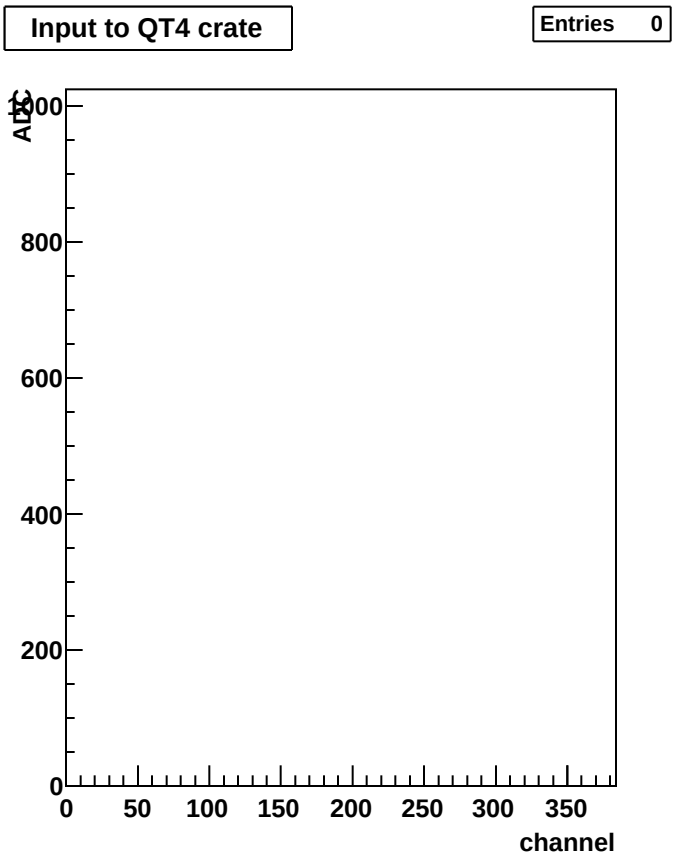
ZDC East ADC Sum Att
ZDC West ADC Sum Att

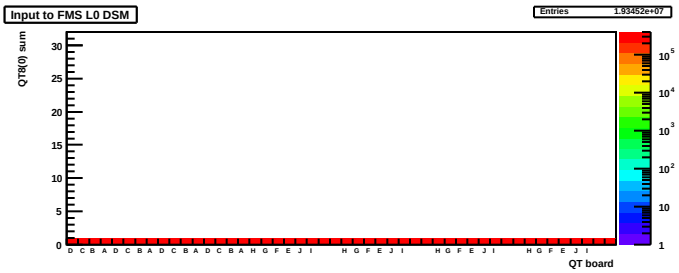

Entries 403025
BBC-L TAC Diff
BCC-S TAC Diff
Entries 403025
BBC-L TAC Diff
VPD TAC Diff
Entries 403025
BBC-L TAC Diff
ZDC TAC Diff
Entries 403025
BBC-S TAC Diff
VPD TAC Diff
Entries 403025
BBC-S TAC Diff
ZDC TAC Diff
Entries 403025
VPD TAC Diff
ZDC TAC Diff

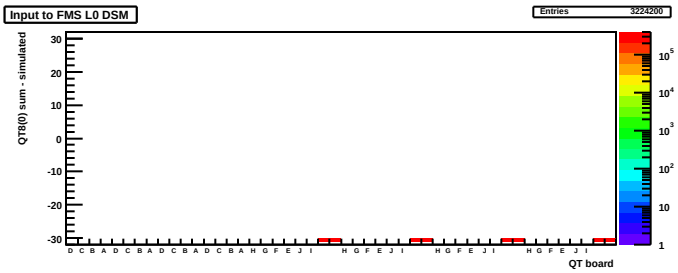

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

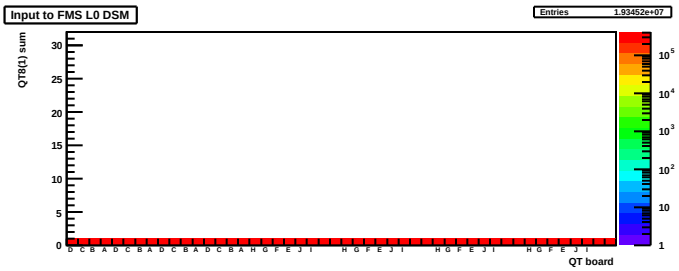

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

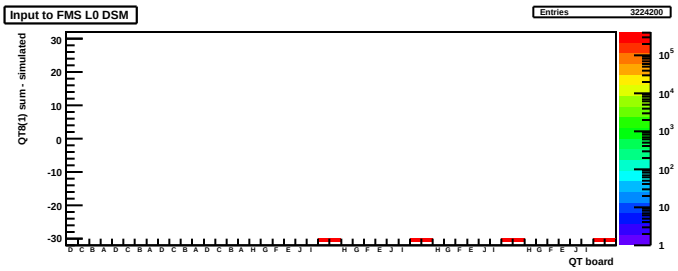

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

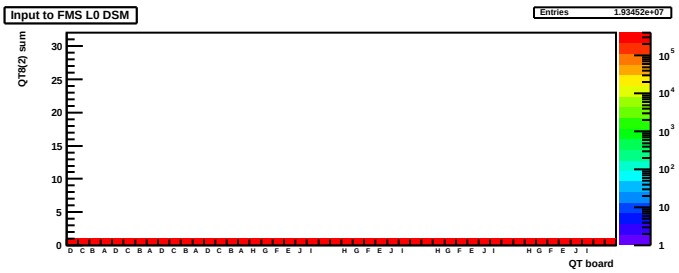

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

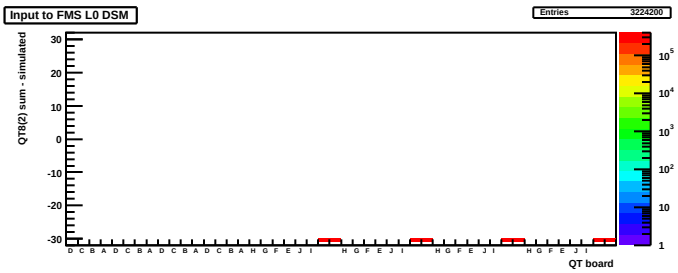

Input to FMS L0 DSM
Entries 1.93452e+07
QT8(0) sum
QT board

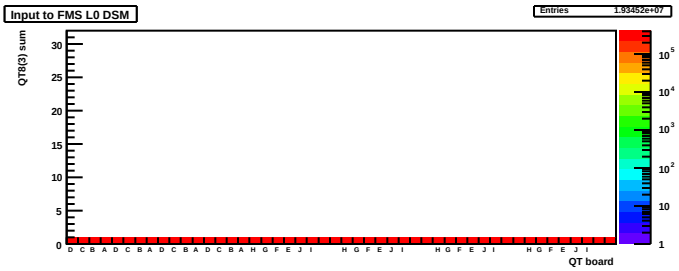

Input to FMS L0 DSM
Entries 3224200
QT8(0) sum - simulated
QT board

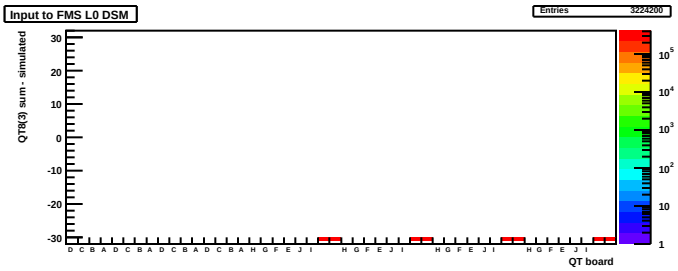

Input to FMS L0 DSM
Entries 1.93452e+07
QT8(1) sum
QT board

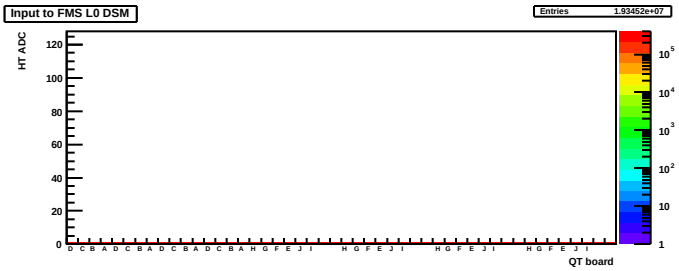

Input to FMS L0 DSM
Entries 3224200
QT8(1) sum - simulated
QT board

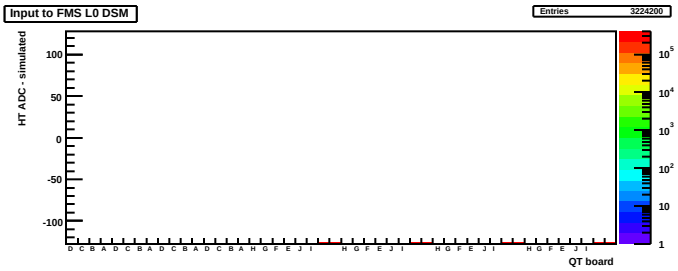

Input to FMS L0 DSM
Entries 1.93452e+07
QT8(2) sum
QT board

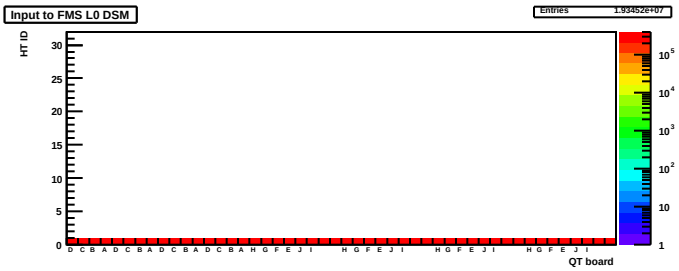

Input to FMS L0 DSM
Entries 3224200
QT8(2) sum - simulated
QT board

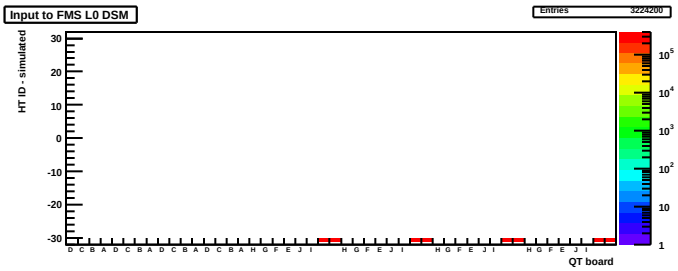

Input to FMS L0 DSM
Entries 1.93452e+07
QT8(3) sum
QT board

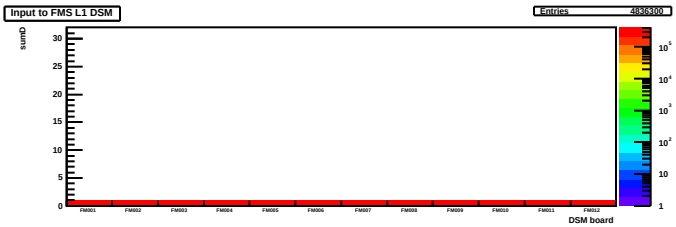

Input to FMS L0 DSM
Entries 3224200
QT8(3) sum - simulated
QT board

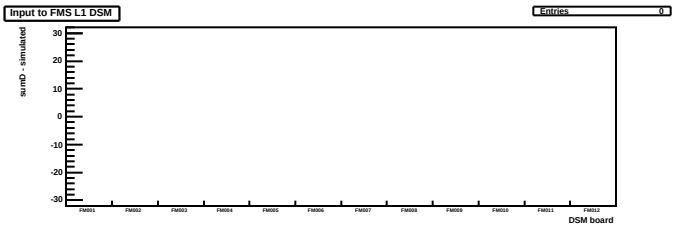

Input to FMS L0 DSM
Entries 1.93452e+07
HT ADC
QT board

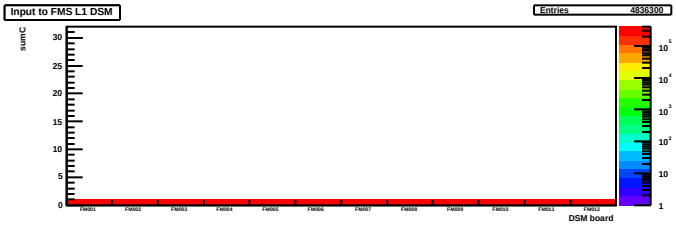

Input to FMS L0 DSM
Entries 3224200
HT ADC - simulated
QT board

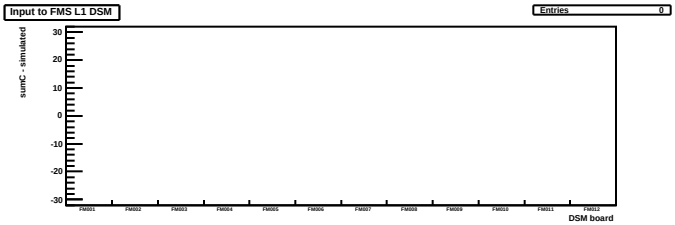

Input to FMS L0 DSM
Entries 1.93452e+07
HT ID
QT board

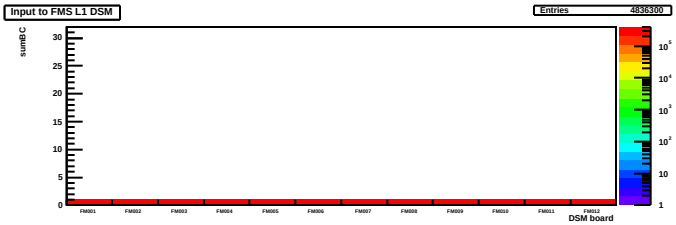

Input to FMS L0 DSM
Entries 3224200
HT ID - simulated
QT board

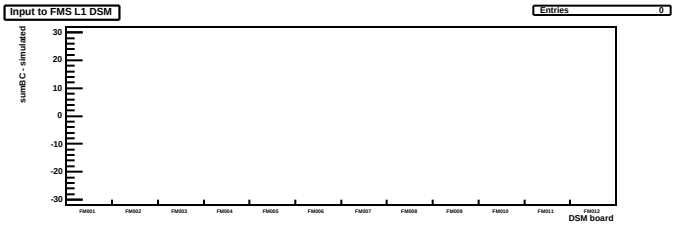

Input to FMS L1 DSM
Entries 4836300
sumD
DSM board

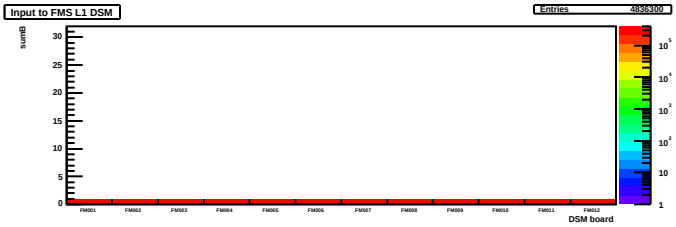

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

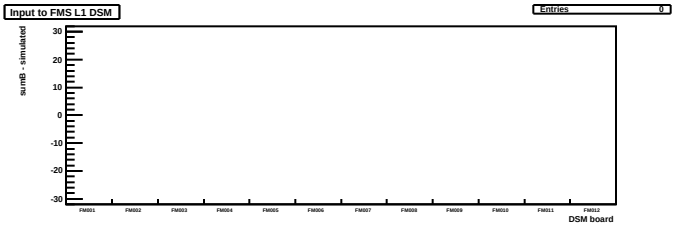

Input to FMS L1 DSM
Entries 4836300
sumC
DSM board

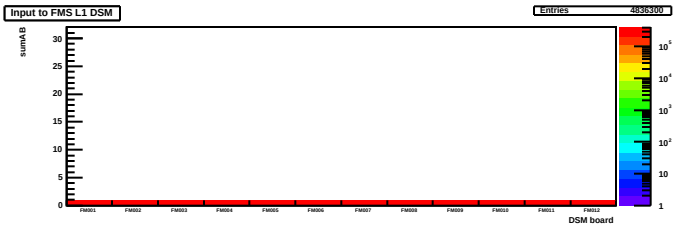

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

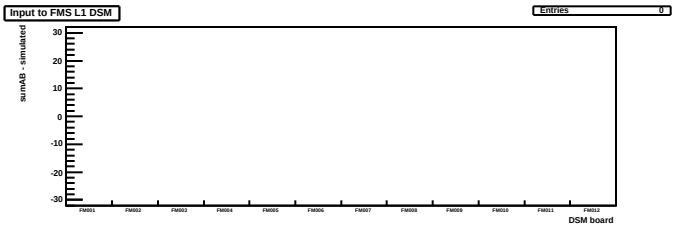

Input to FMS L1 DSM
Entries 4836300
sumBC
DSM board

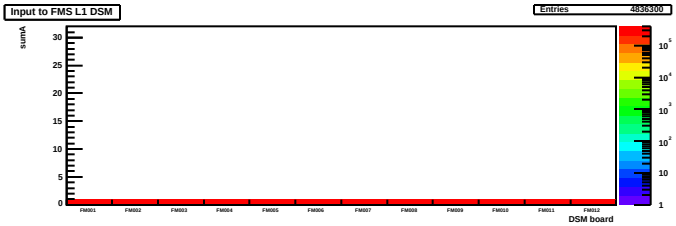

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

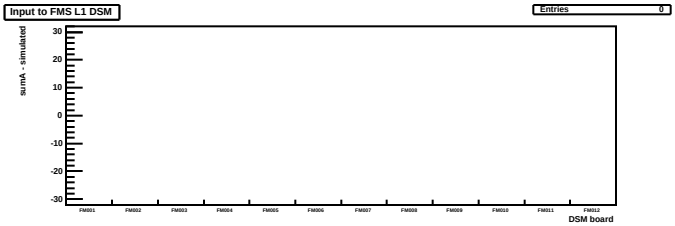

Input to FMS L1 DSM
Entries 4836300
sumB
DSM board

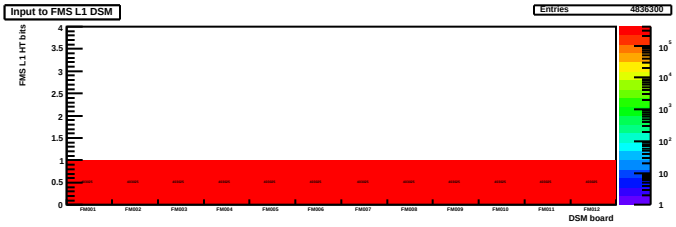

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

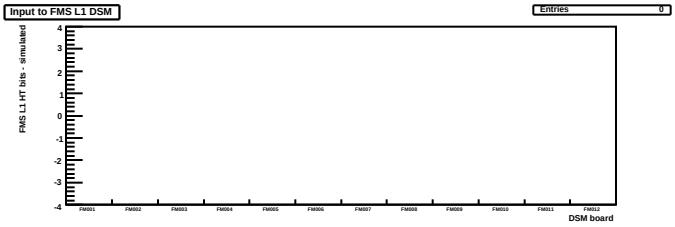

Input to FMS L1 DSM
Entries 4836300
sumAB
DSM board

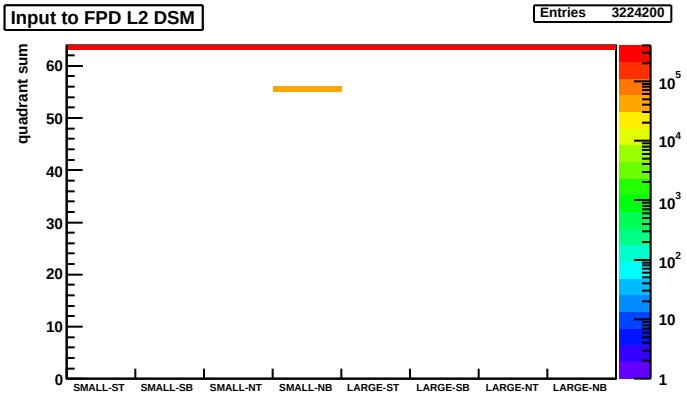

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

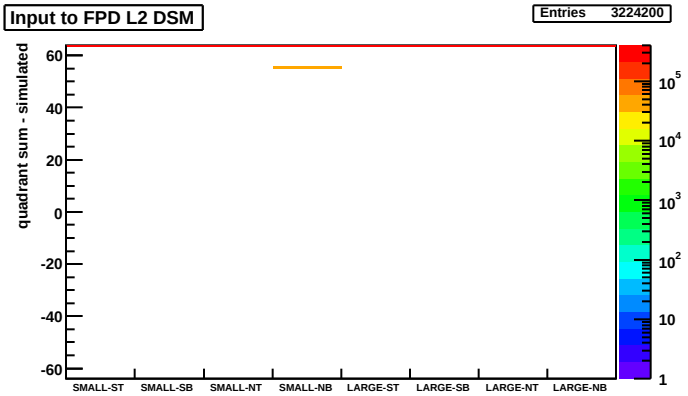

Input to FMS L1 DSM
Entries 4836300
sumA
DSM board

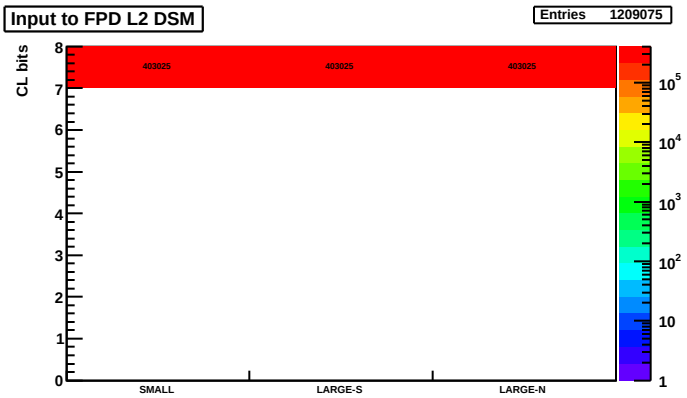

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

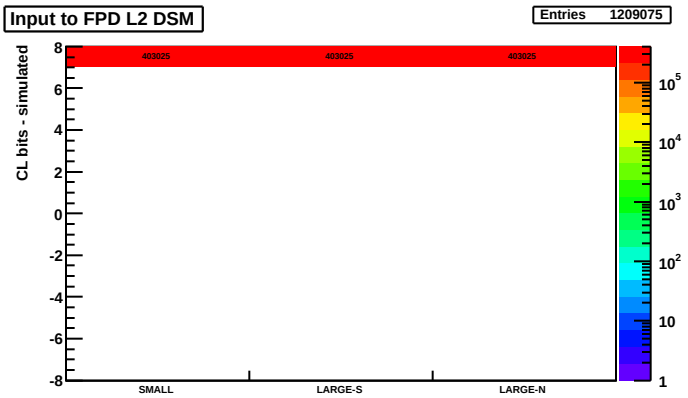

Input to FMS L1 DSM
Entries 4836300
FMS L1 HT bits
DSM board

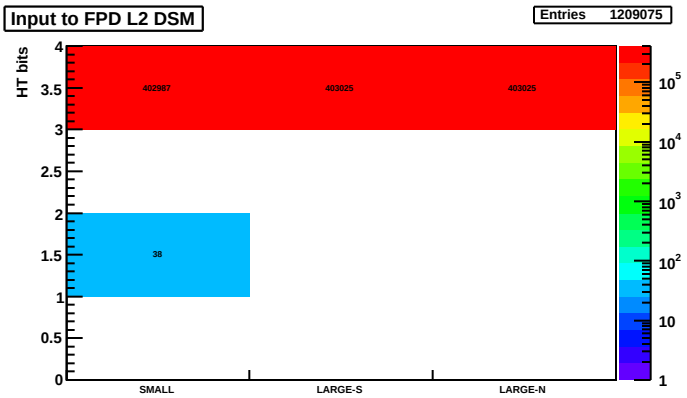

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

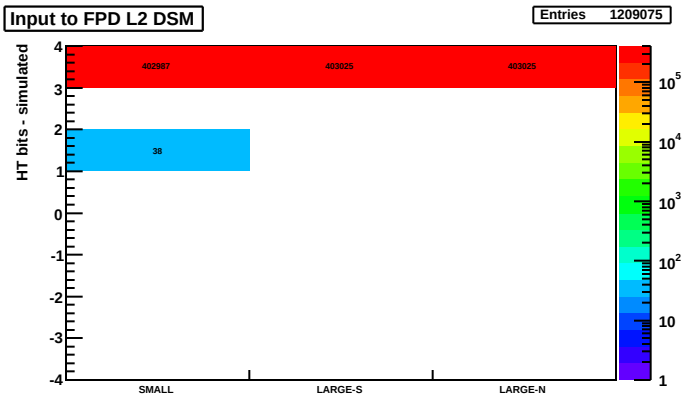

Input to FPD L2 DSM
Entries 3224200
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

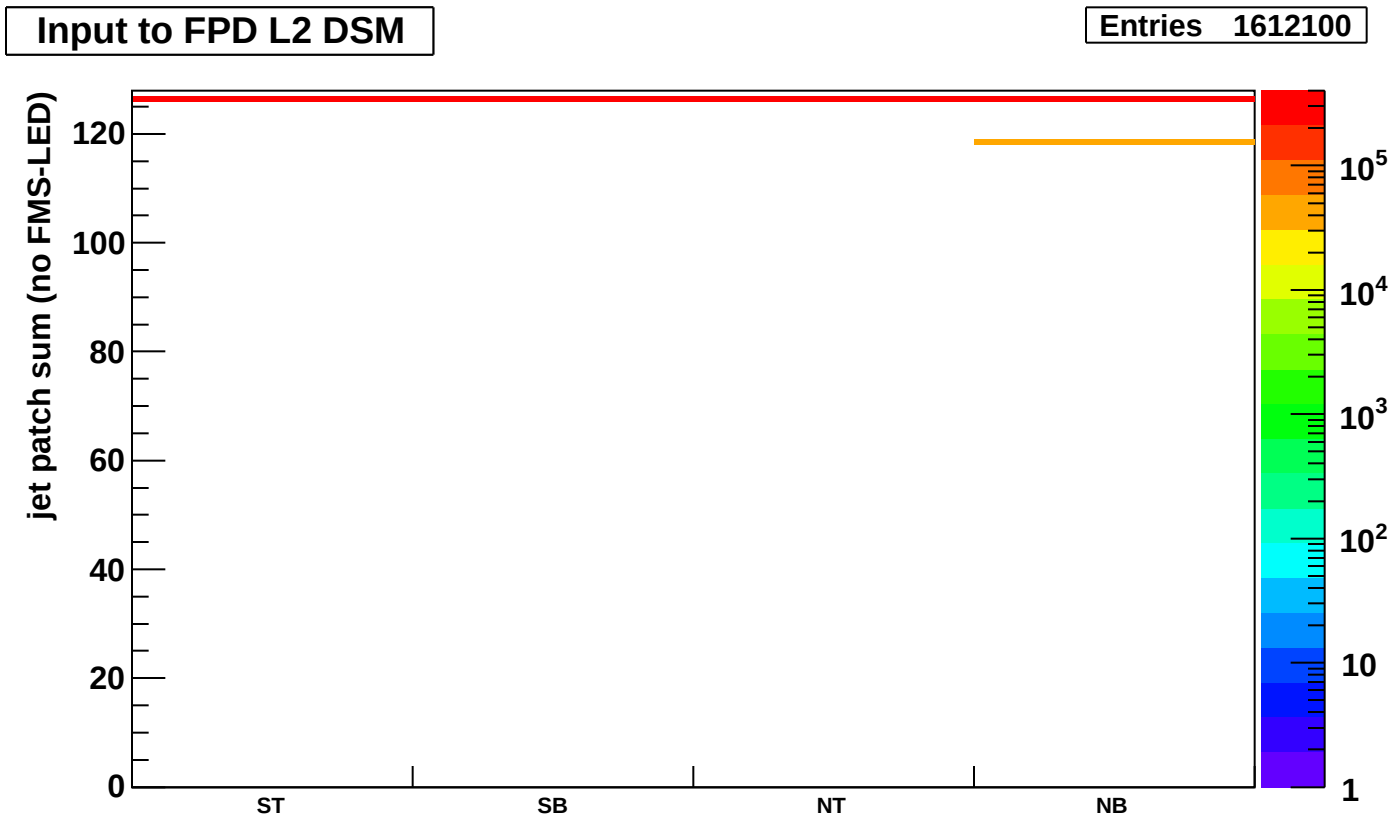

Input to FPD L2 DSM
Entries 3224200
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

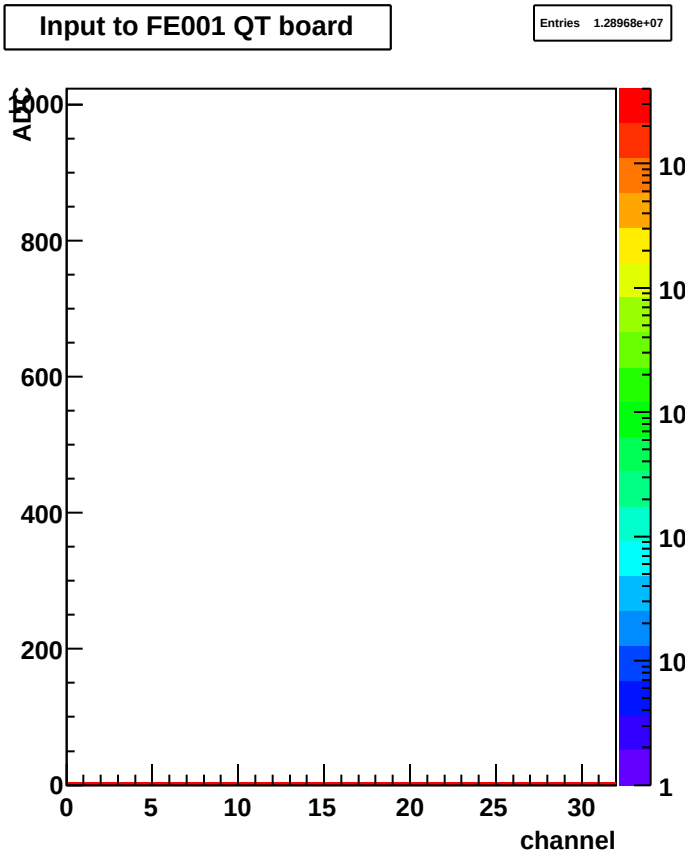

Input to FPD L2 DSM
Entries 1209075
CL bits
403025
403025
403025
SMALL
LARGE-S
LARGE-N

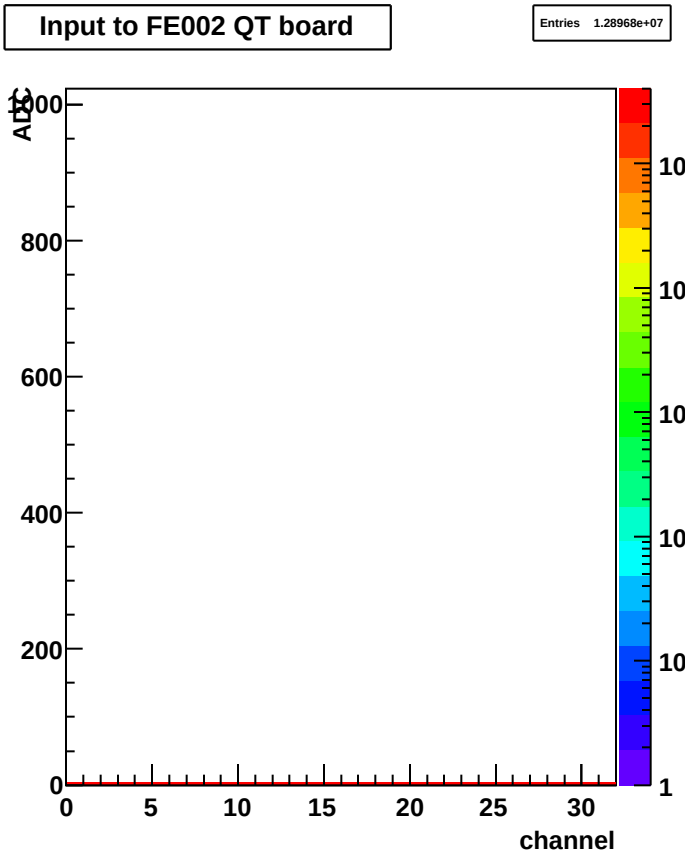

Input to FPD L2 DSM
Entries 1209075
CL bits - simulated
403025
403025
403025
SMALL
LARGE-S
LARGE-N

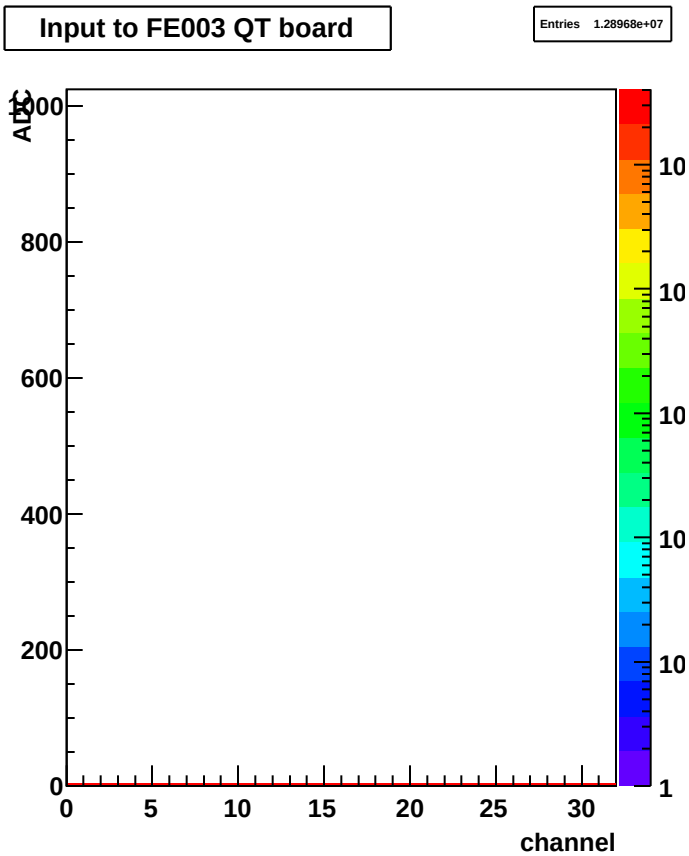

Input to FPD L2 DSM
Entries 1209075
HT bits
402987
403025
403025
38
SMALL
LARGE-S
LARGE-N

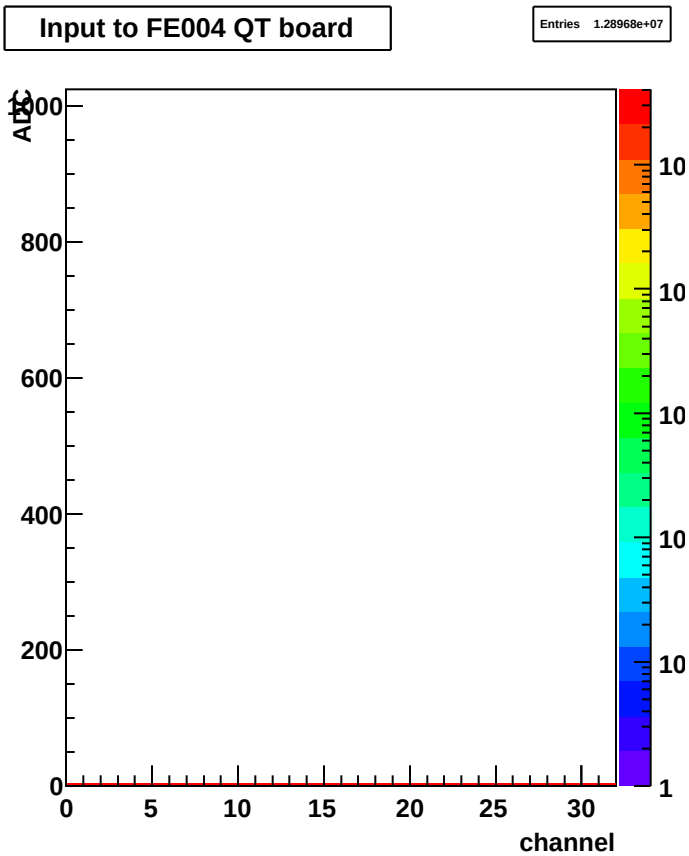

Input to FPD L2 DSM
Entries 1209075
HT bits - simulated
402987
403025
403025
38
SMALL
LARGE-S
LARGE-N


Input to FPD L2 DSM
Entries 1612100
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

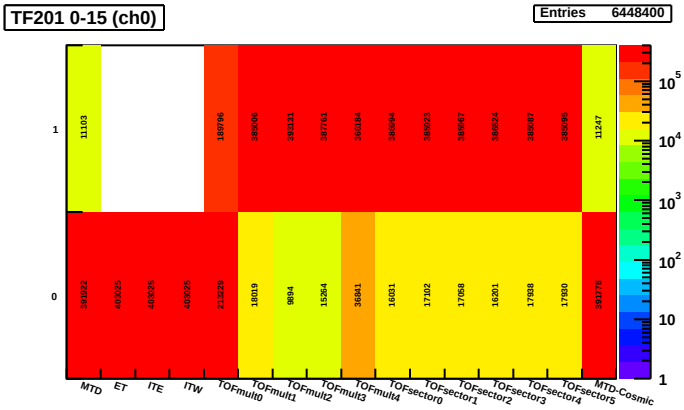

Input to FE001 QT board
Entries 1.28968e+07
ADC
channel


Input to FE002 QT board
Entries 1.28968e+07
ADC
channel

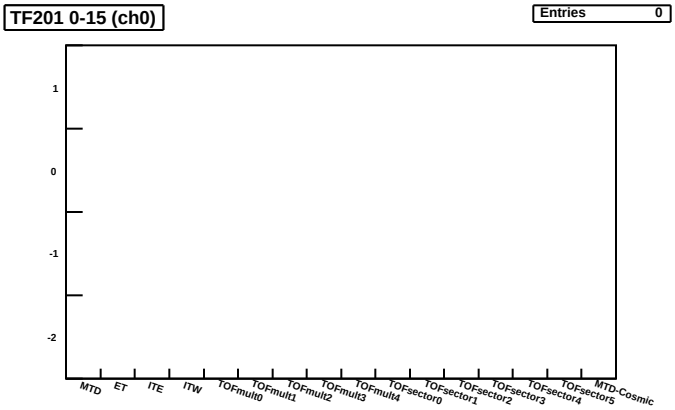

Input to FE003 QT board
Entries 1.28968e+07
ADC
channel


Input to FE004 QT board
Entries 1.28968e+07
ADC
channel

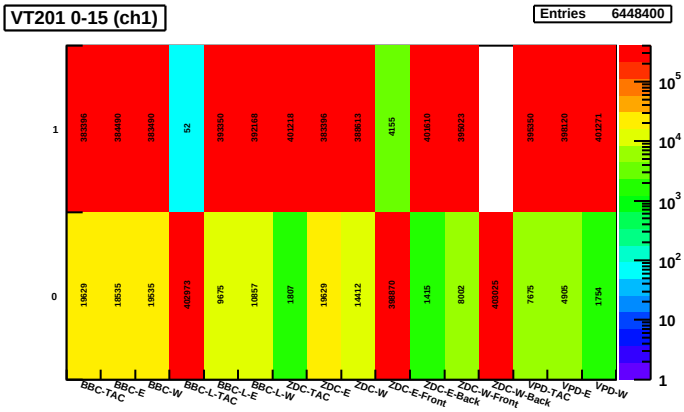
TF201 0-15 (ch0)



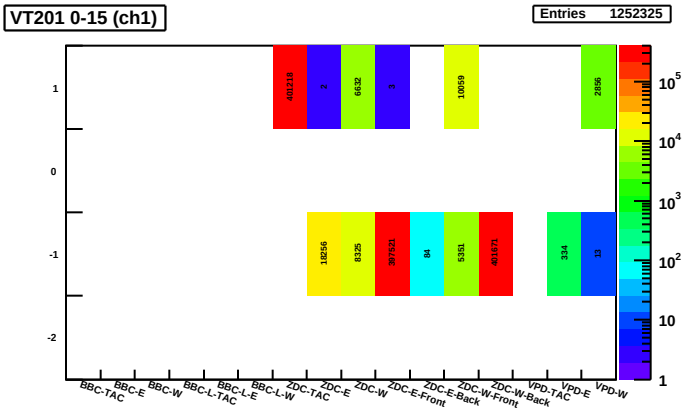
TF201 0-15 (ch0)



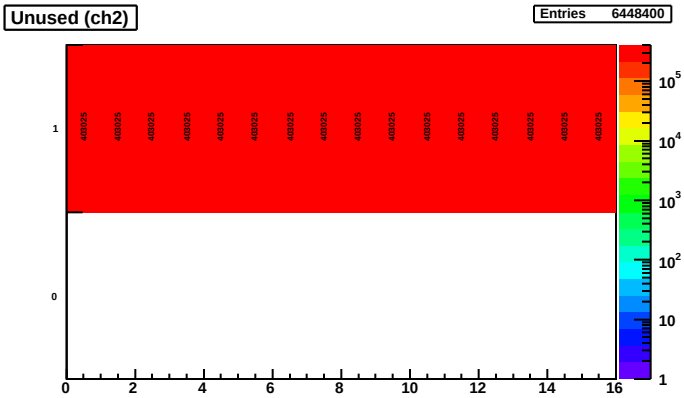
VT201 0-15 (ch1)

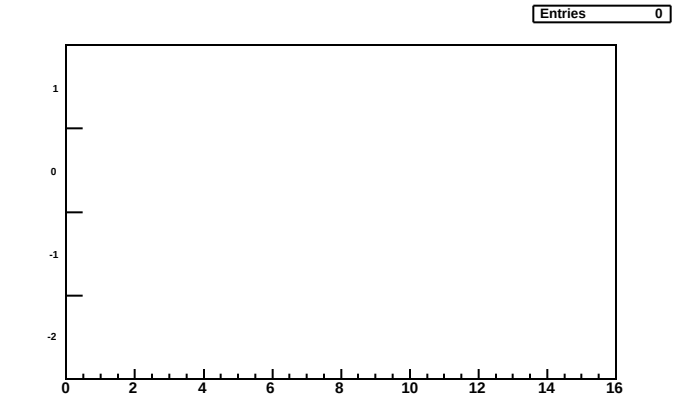


VT201 0-15 (ch1)

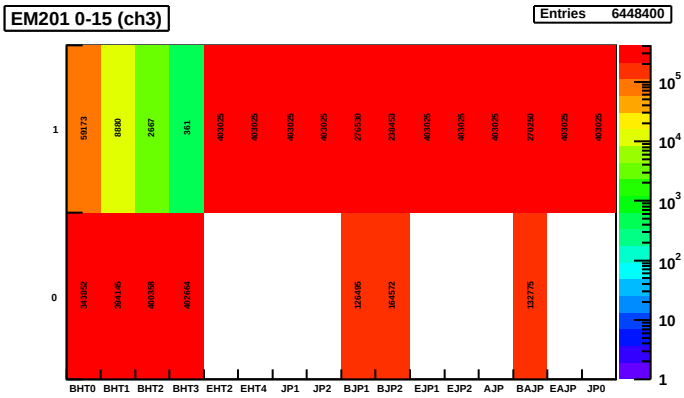


Unused (ch2)

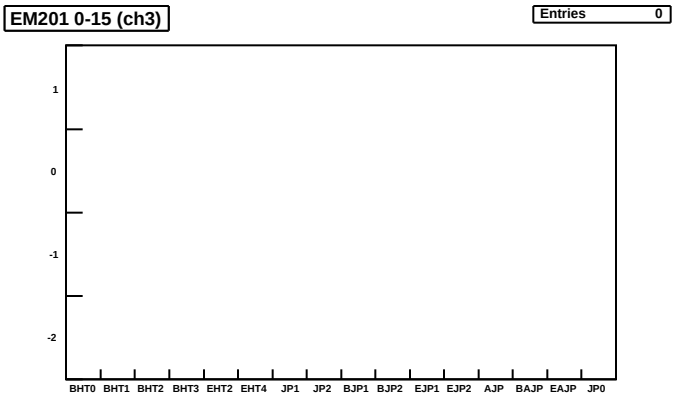




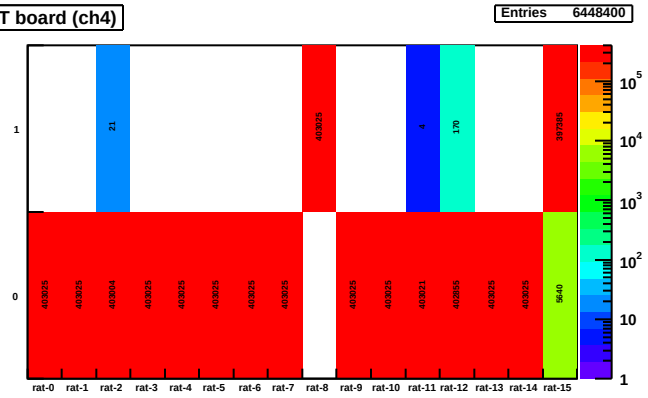
EM201 0-15 (ch3)

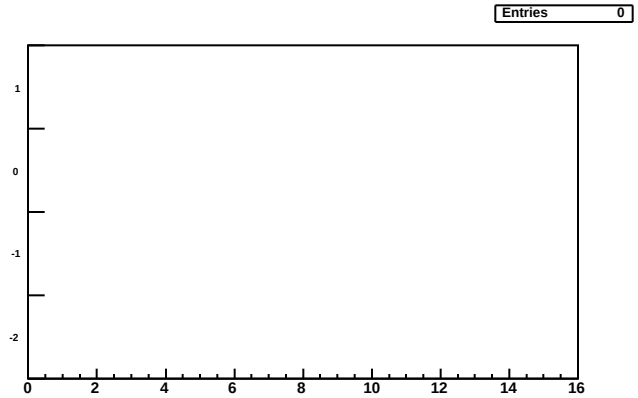


EM201 0-15 (ch3)

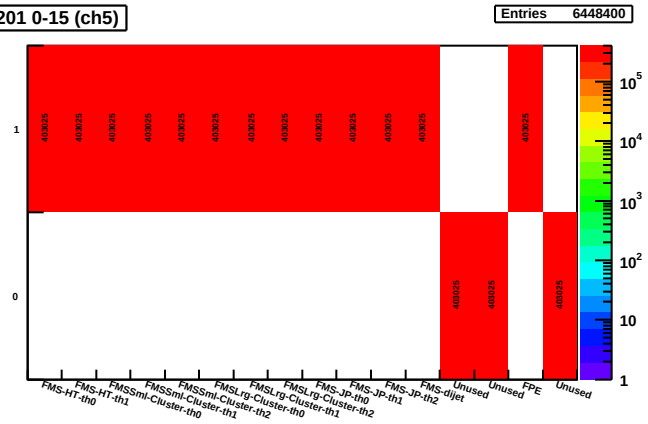


RAT board (ch4)

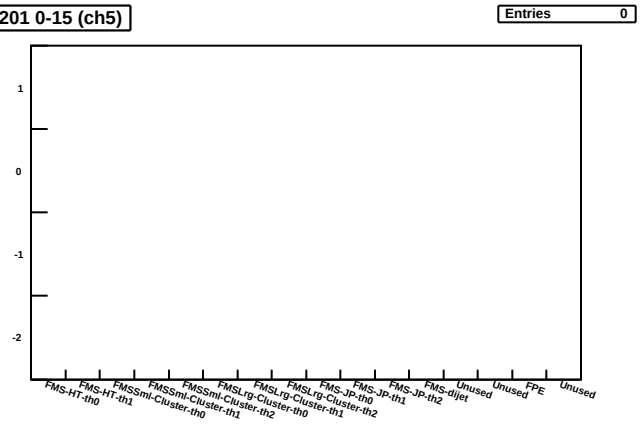




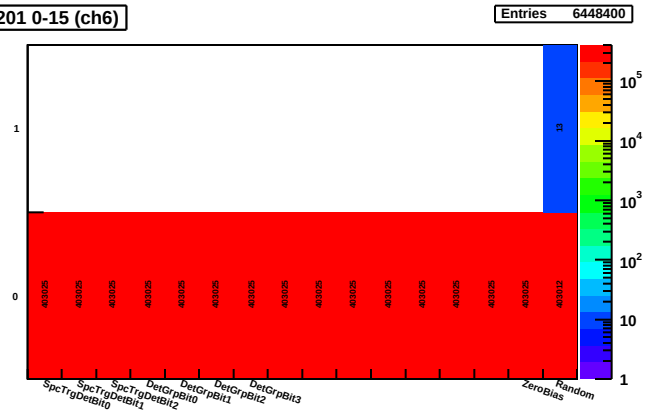
FP201 0-15 (ch5)

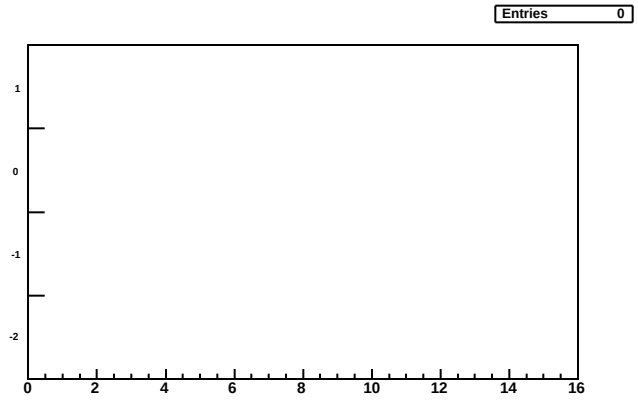
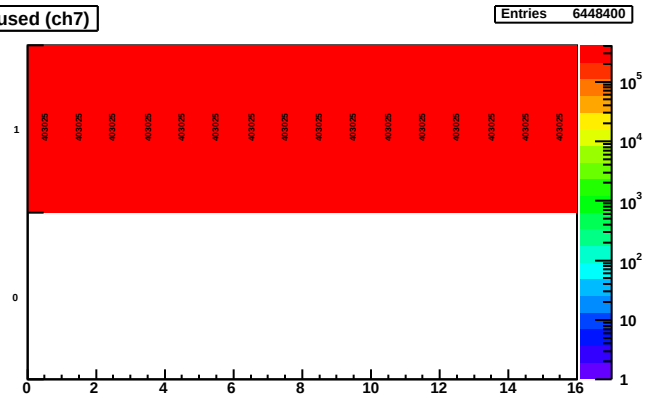
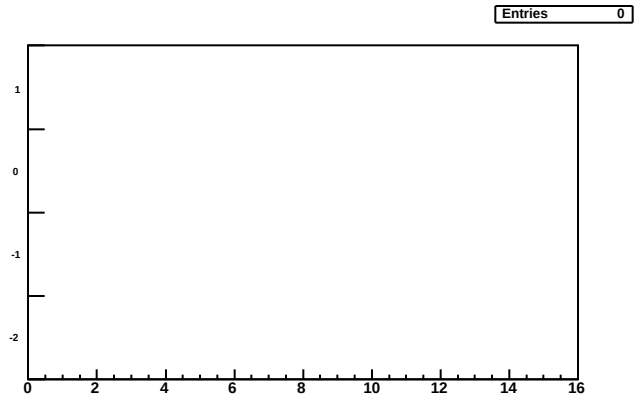


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)