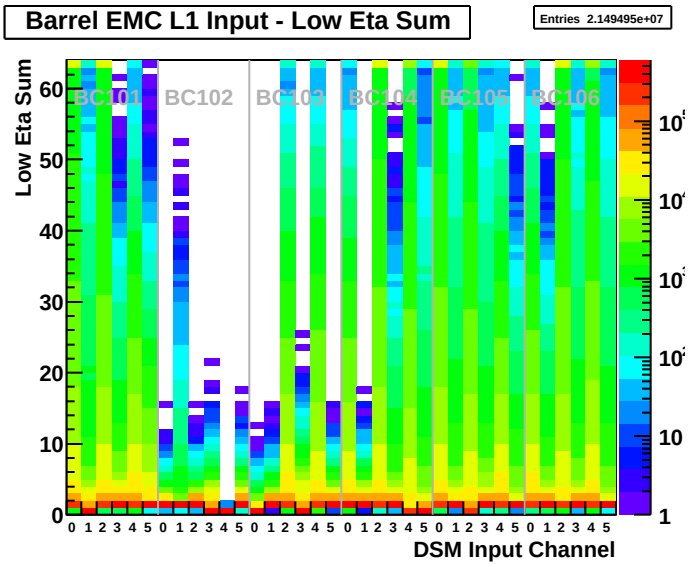
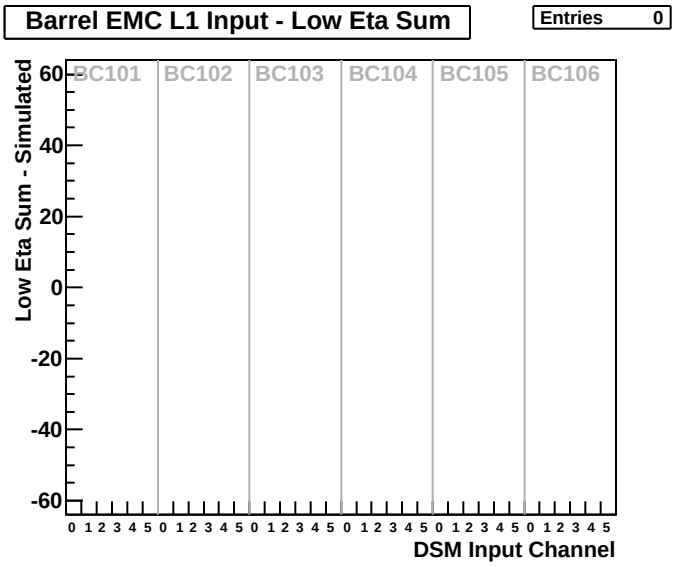
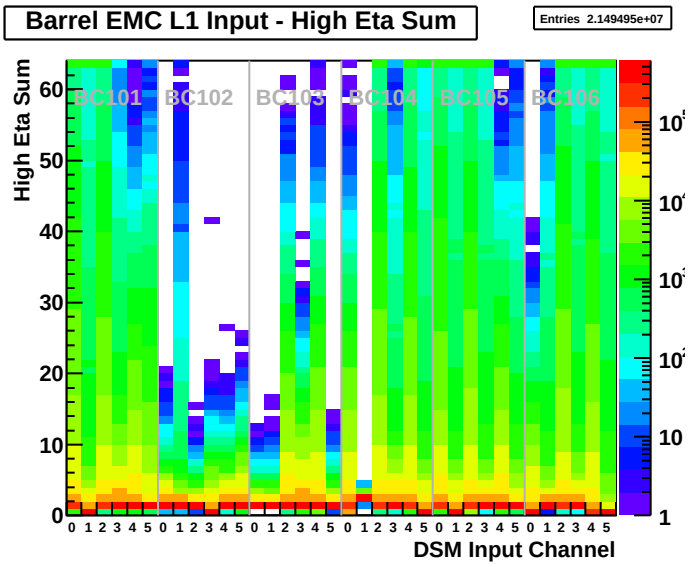
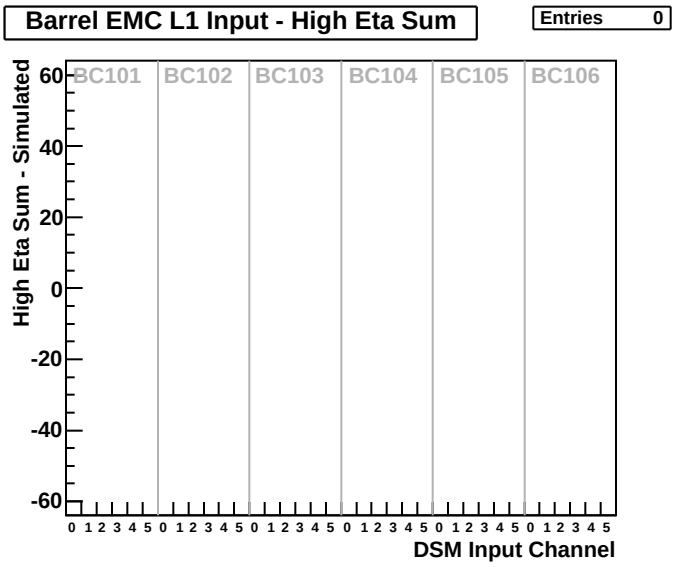
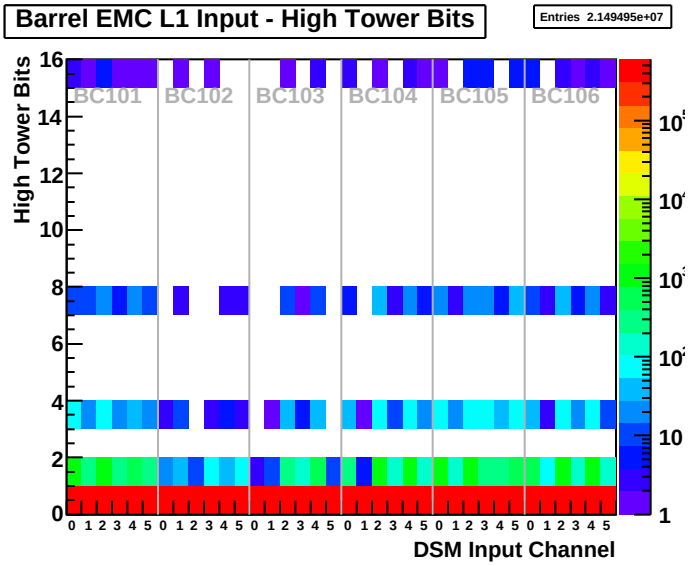
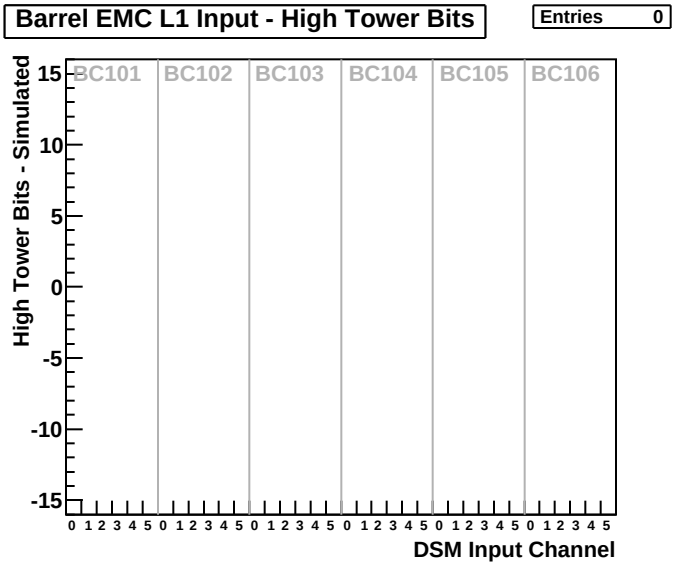

Barrel EMC L1 Input - Low Eta Sum
Entries 2.149495e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

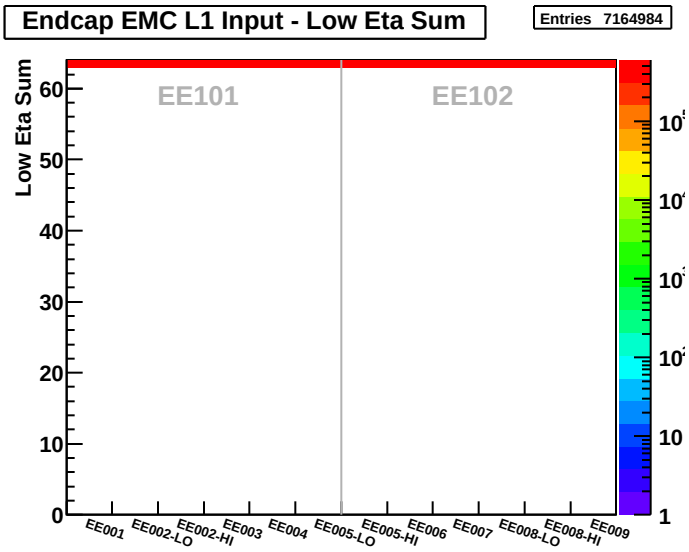

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

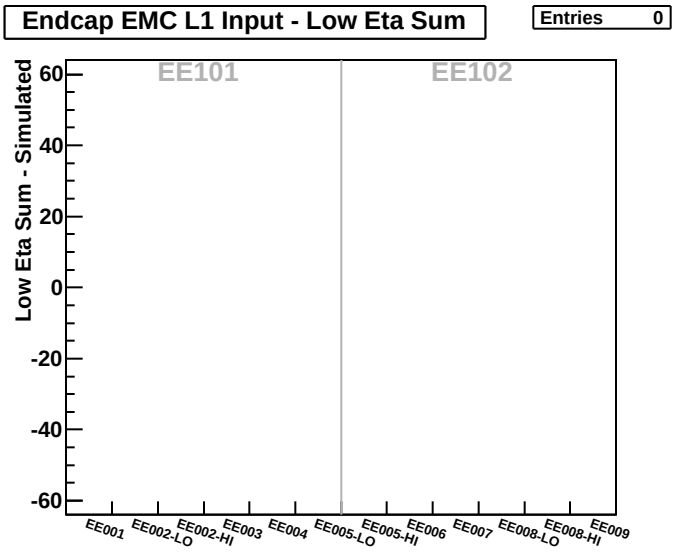

Barrel EMC L1 Input - High Eta Sum
Entries 2.149495e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

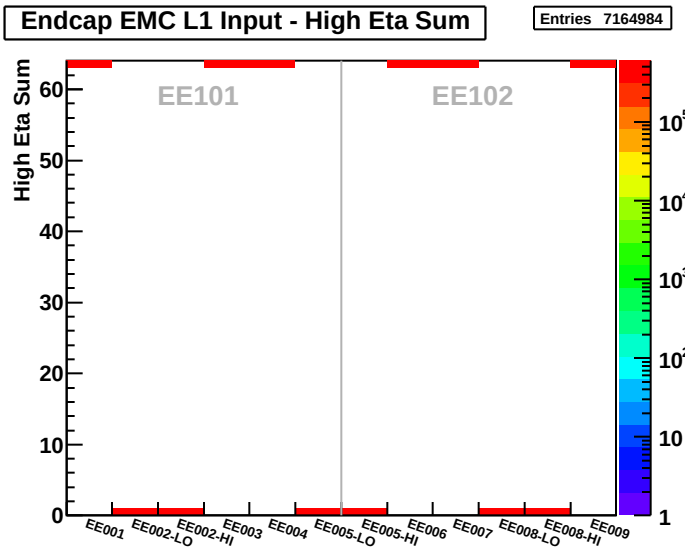

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

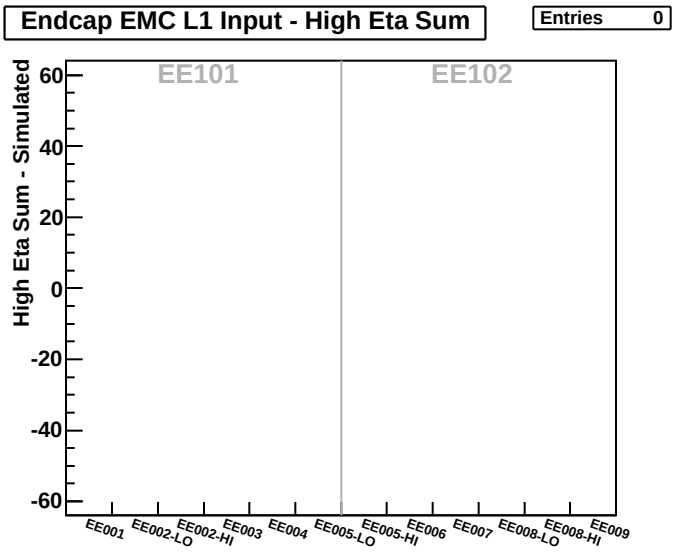

Barrel EMC L1 Input - High Tower Bits
Entries 2.149495e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

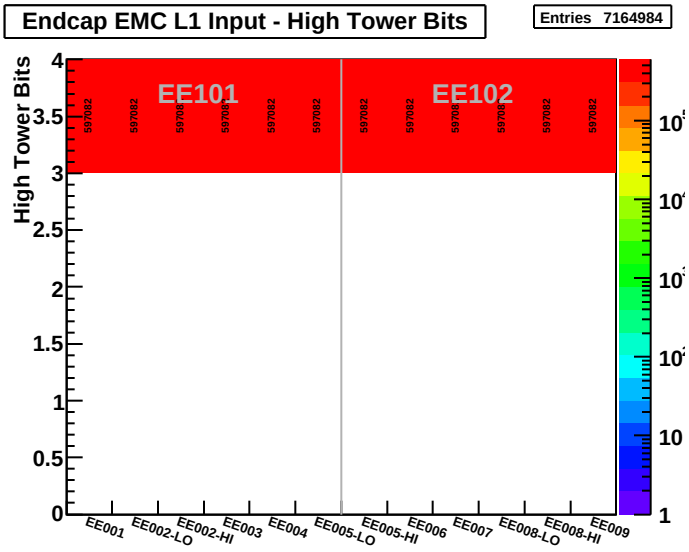

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

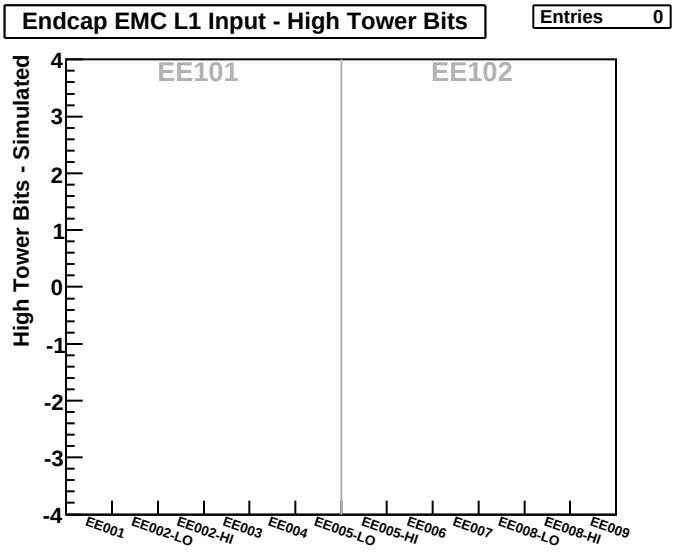

Endcap EMC L1 Input - Low Eta Sum
Entries 7164984
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

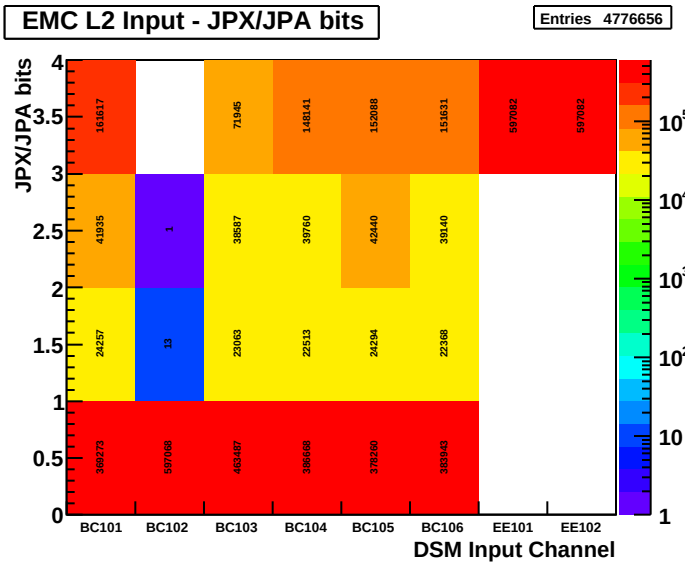

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

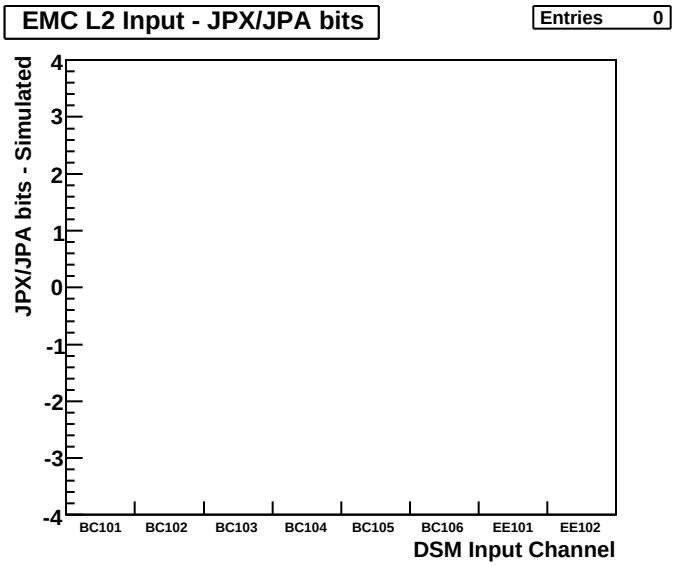

Endcap EMC L1 Input - High Eta Sum
Entries 7164984
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

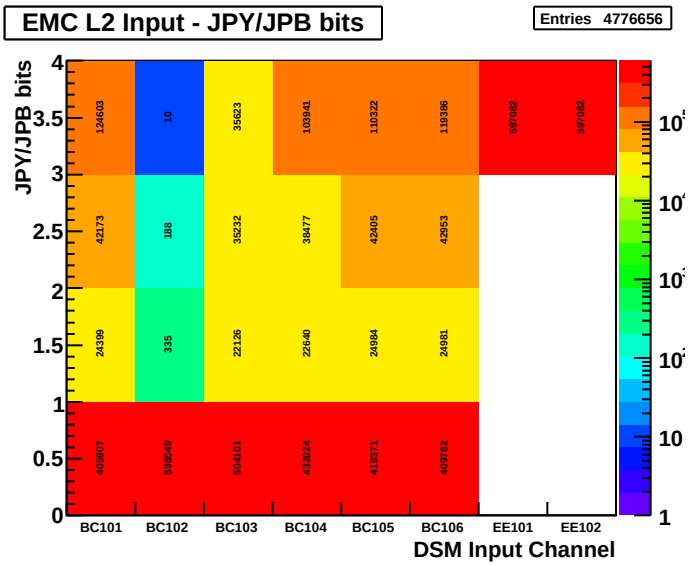

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

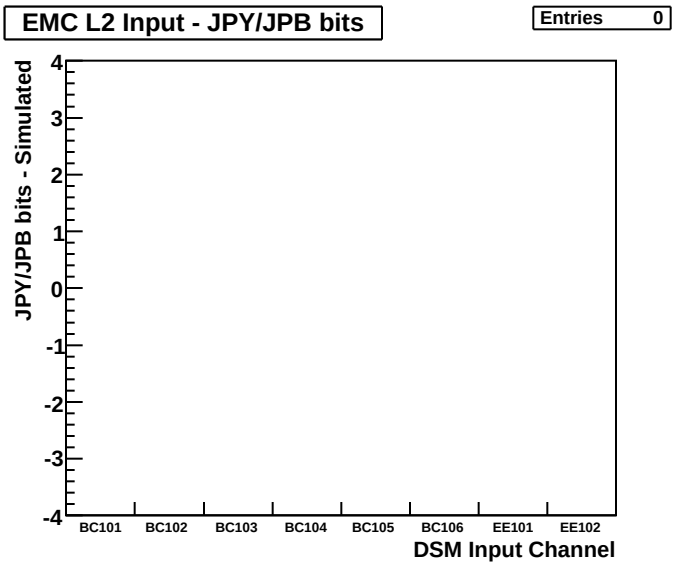

Endcap EMC L1 Input - High Tower Bits
Entries 7164984
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

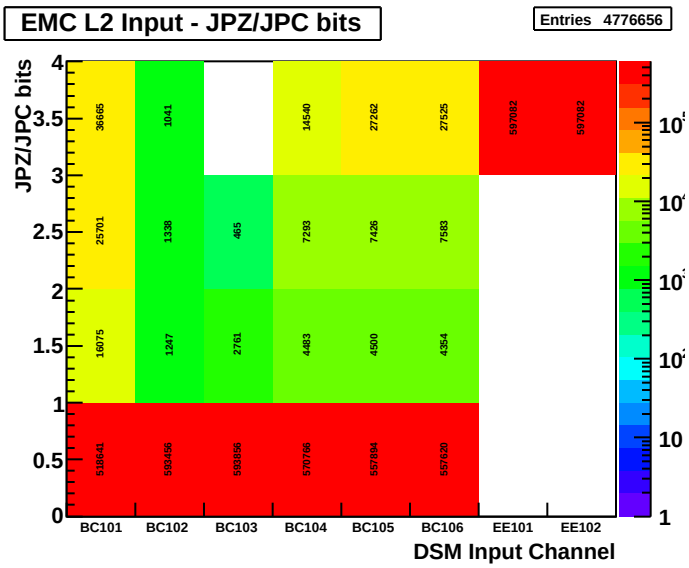

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

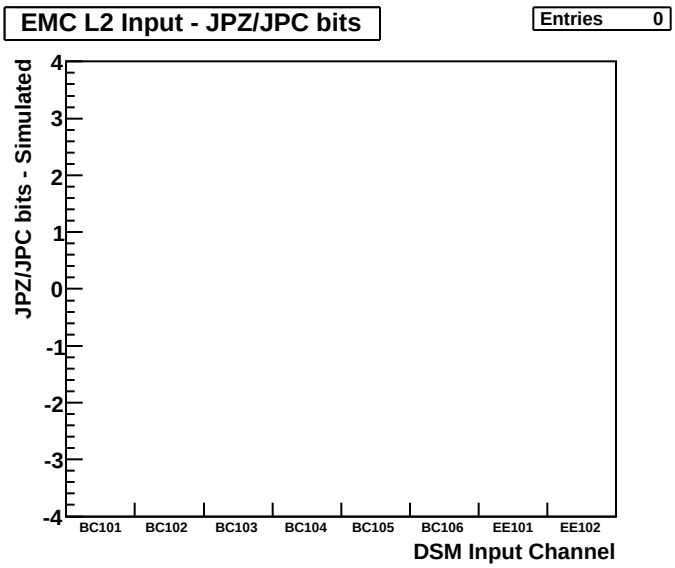

EMC L2 Input - JPX/JPA bits
Entries 4776656
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
151617 71945 148141 152088 151631 597082 597082
41935 1 38587 39760 42440 39140
24257 13 23063 22513 24294 22368
368273 597068 463487 386668 378260 383943

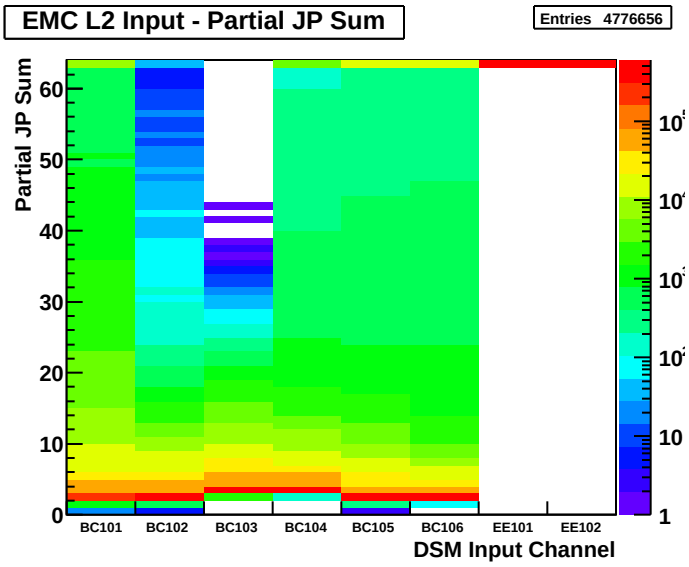

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

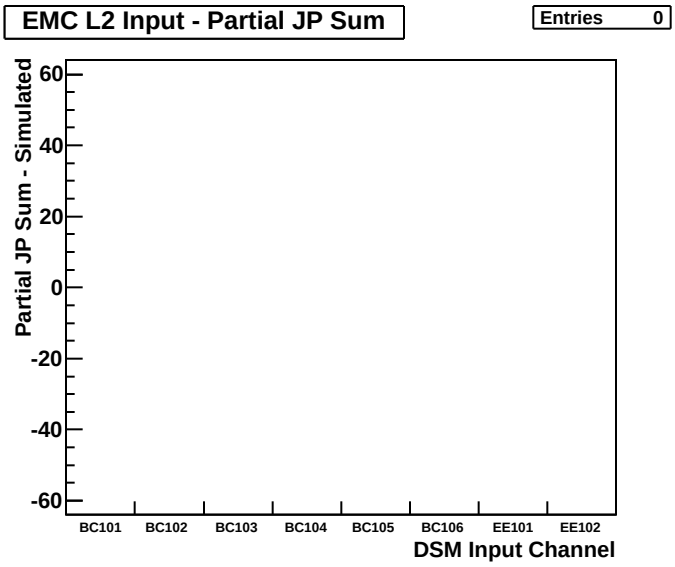

EMC L2 Input - JPY/JPB bits
Entries 4776656
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
124603 10 35623 103941 110322 119386 597082 597082
42173 188 35232 38477 42405 42953
24399 335 22126 22640 24984 24981
409807 596549 504101 433024 419371 409762

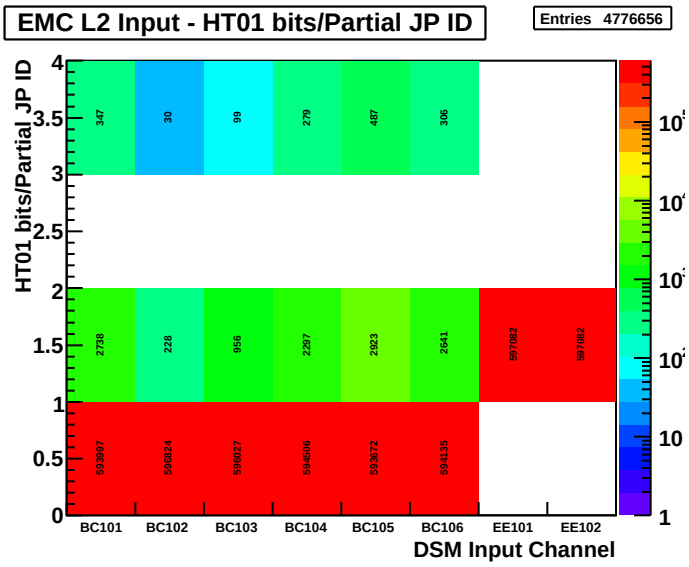

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

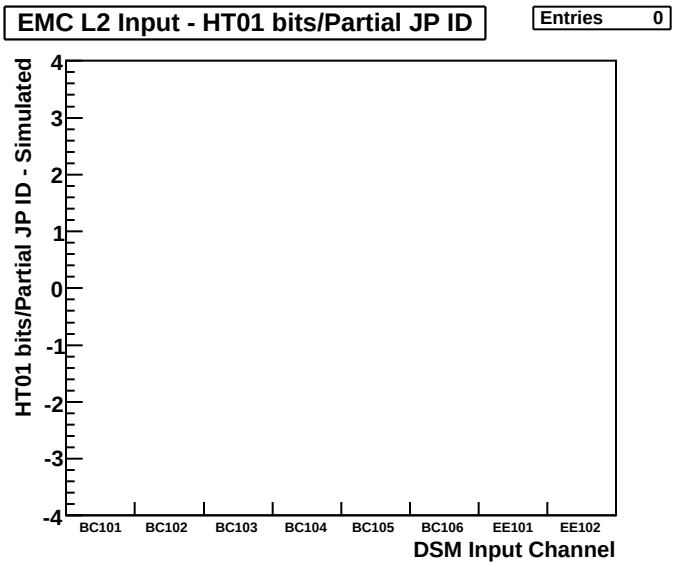

EMC L2 Input - JPZ/JPC bits
Entries 4776656
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
36665 1041 14540 27262 27525 597082 597082
25701 1338 465 7293 7426 7583
16075 1247 2761 4483 4500 4354
518541 593456 593356 570766 557894 557620

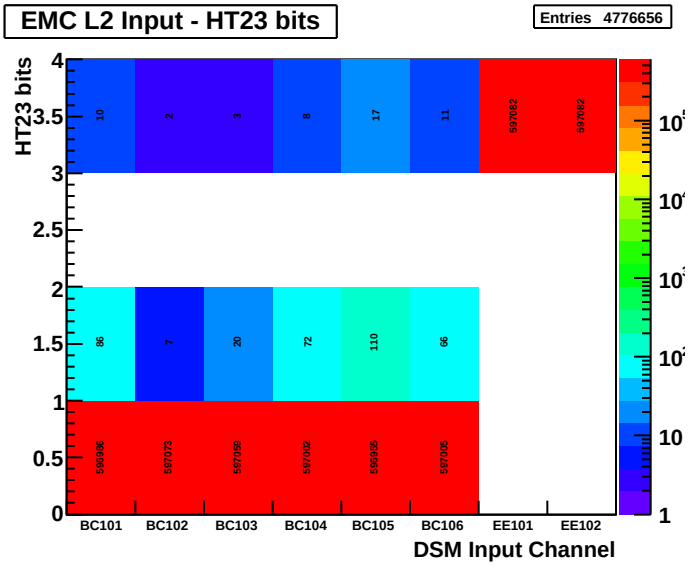

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

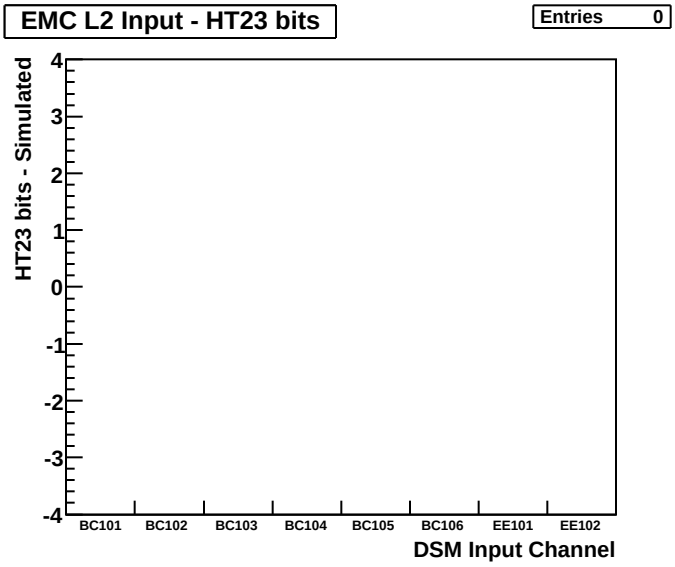

EMC L2 Input - Partial JP Sum
Entries 4776656
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

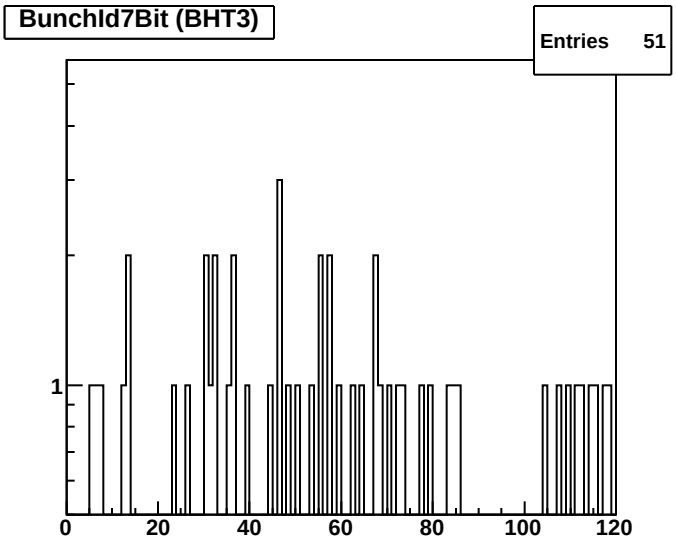

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

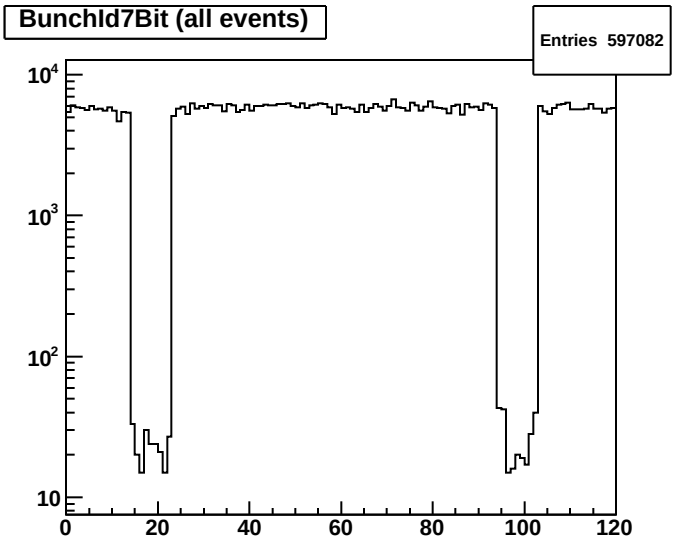

EMC L2 Input - HT01 bits/Partial JP ID
Entries 4776656
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

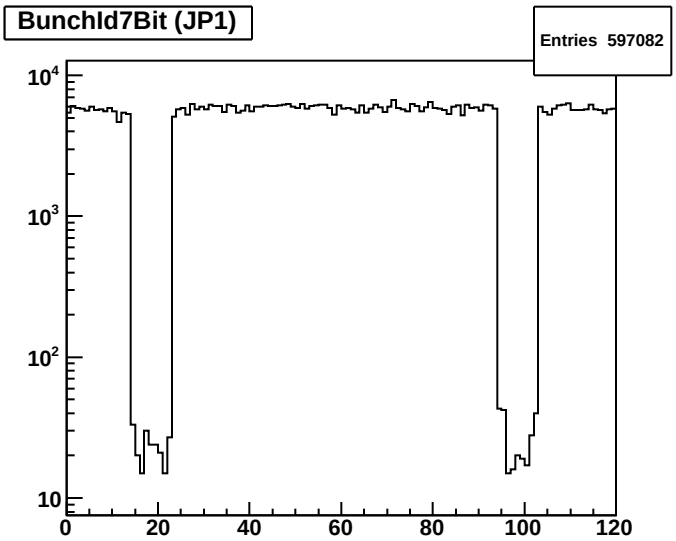

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

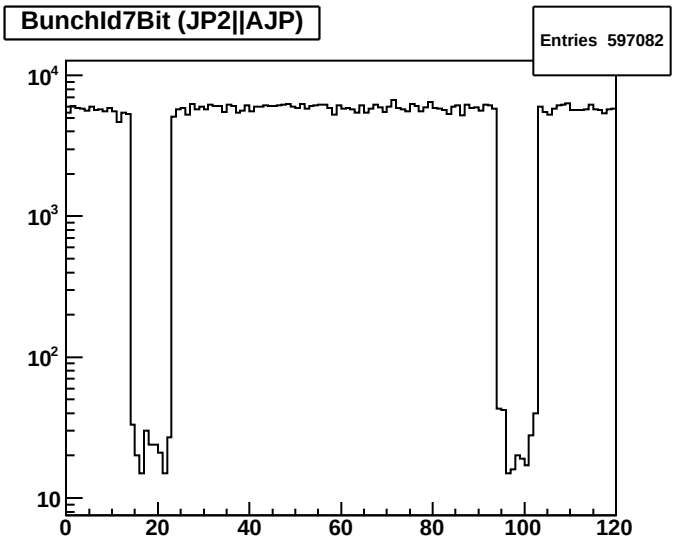

EMC L2 Input - HT23 bits
Entries 4776656
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

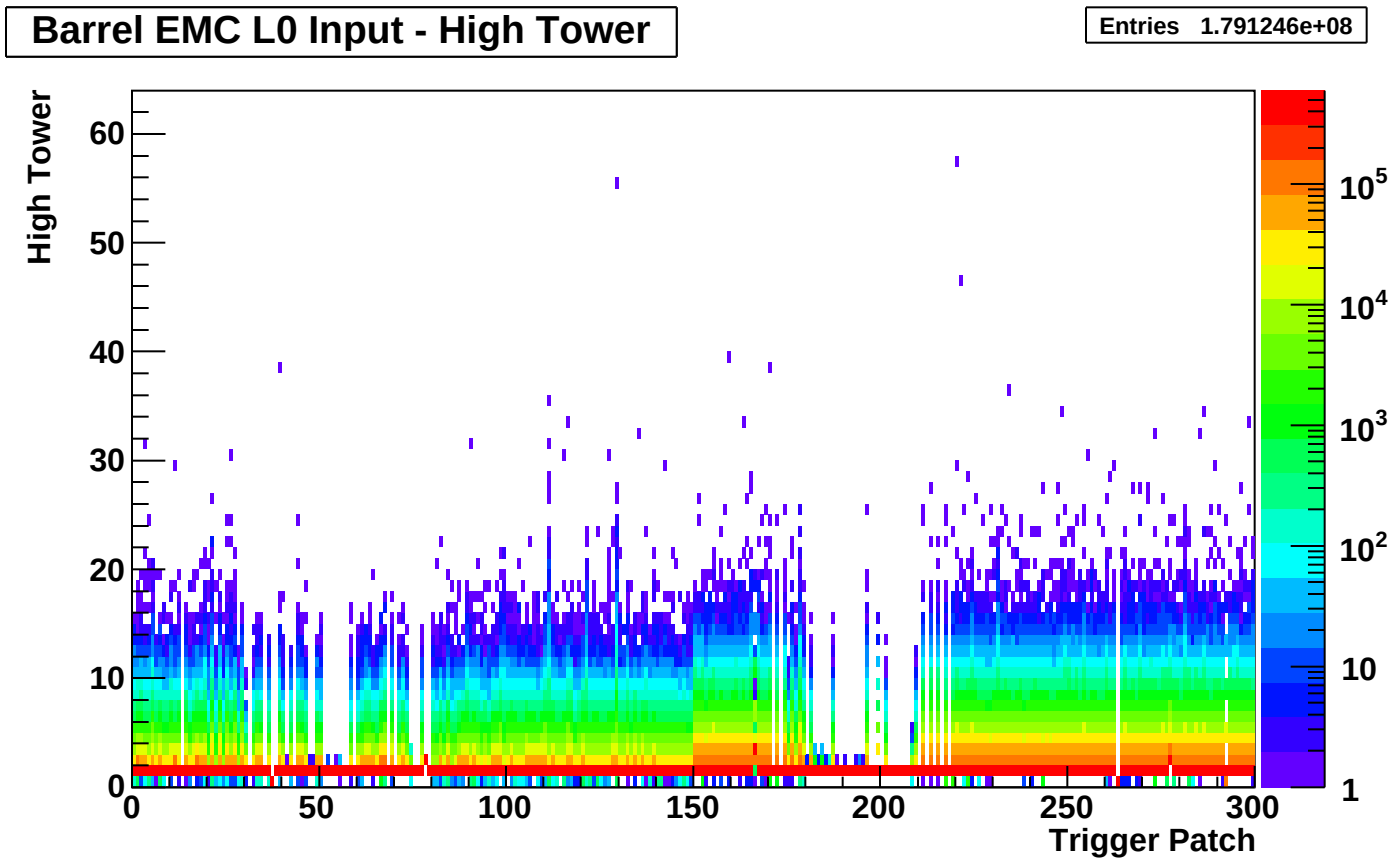

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

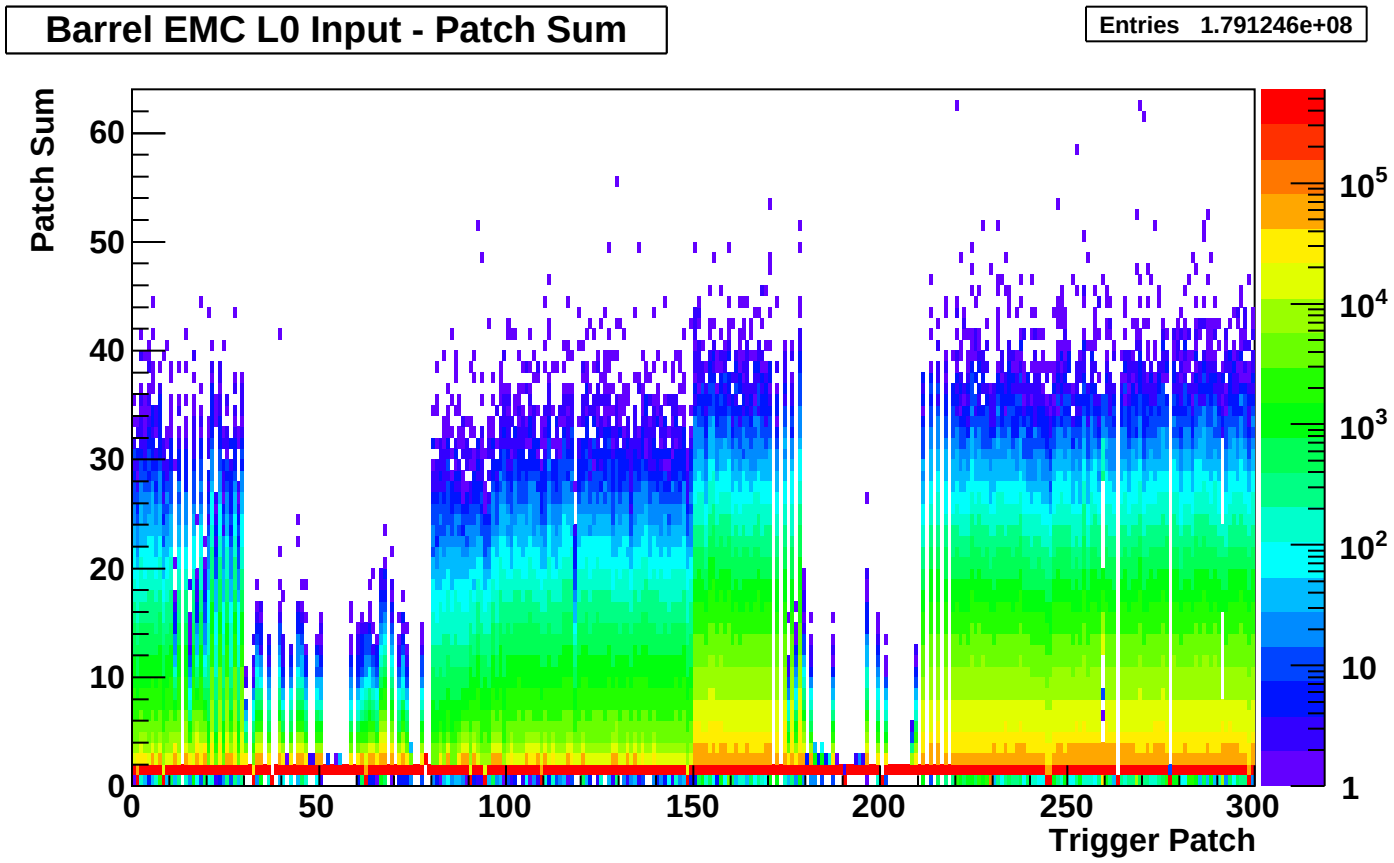

BunchId7Bit (BHT3)
Entries 51
1
0
20
40
60
80
100
120

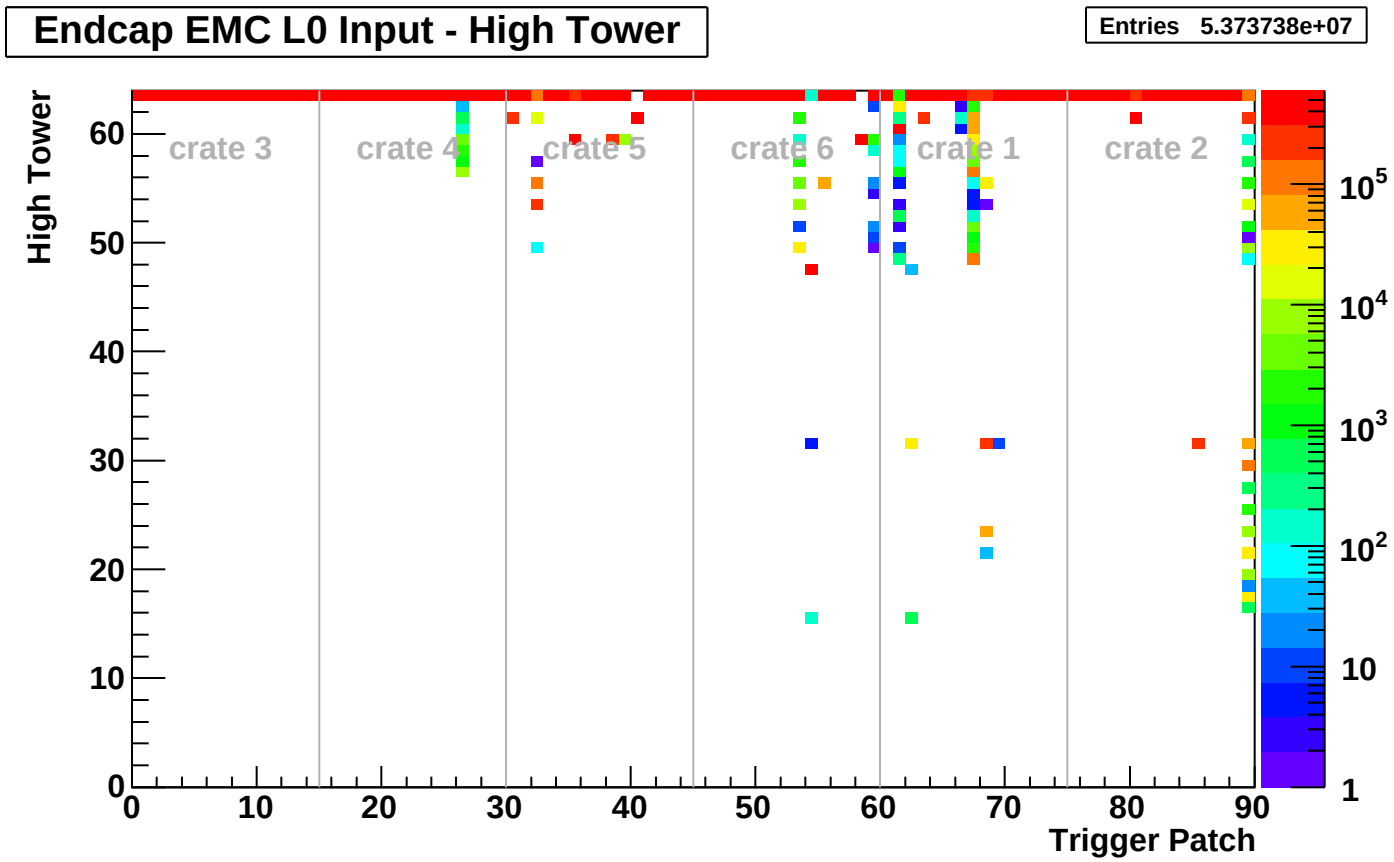

BunchId7Bit (all events)
Entries 597082
10^4
10^3
10^2
10
0
20
40
60
80
100
120

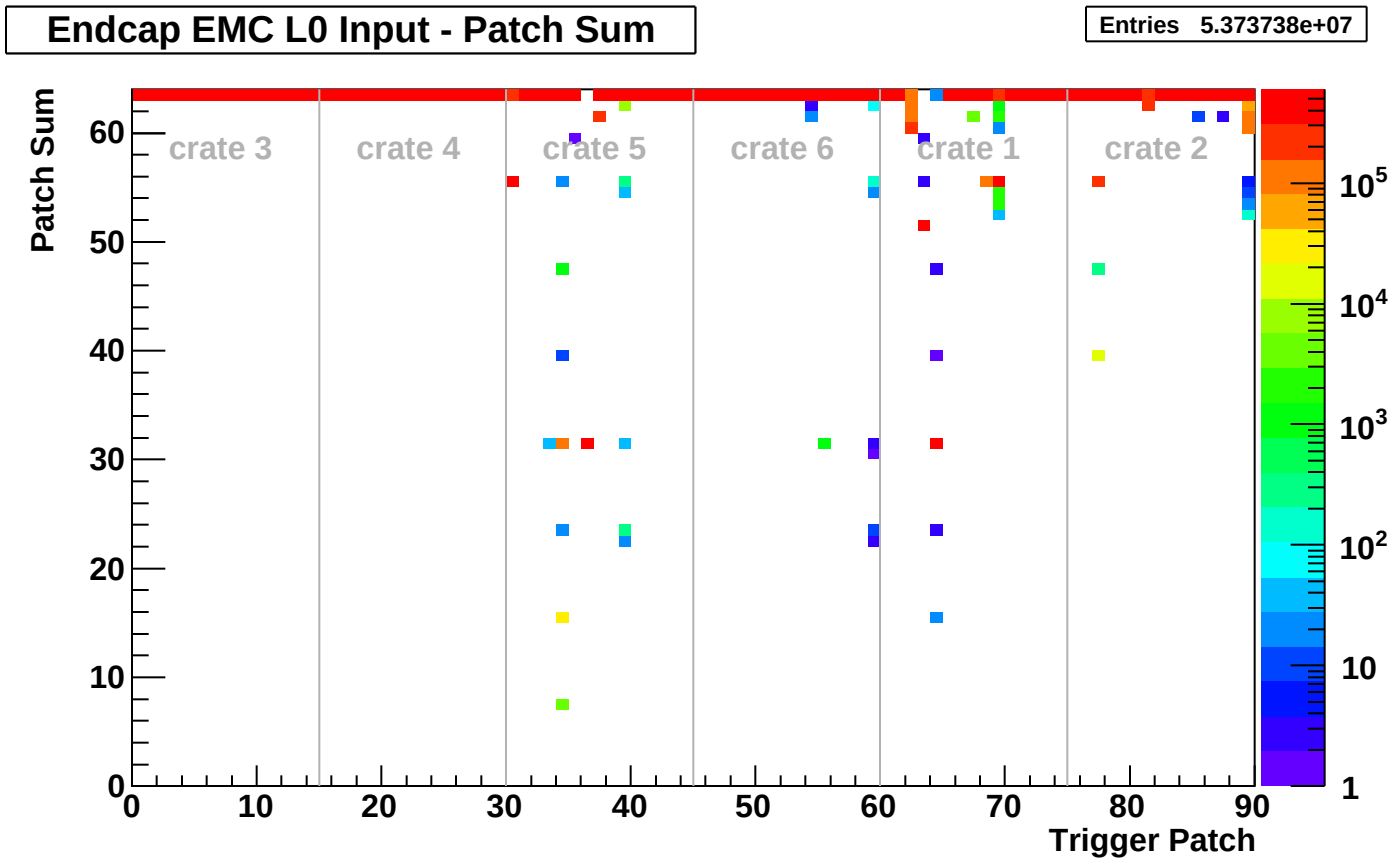

BunchId7Bit (JP1)
Entries 597082
10^4
10^3
10^2
10
0
20
40
60
80
100
120

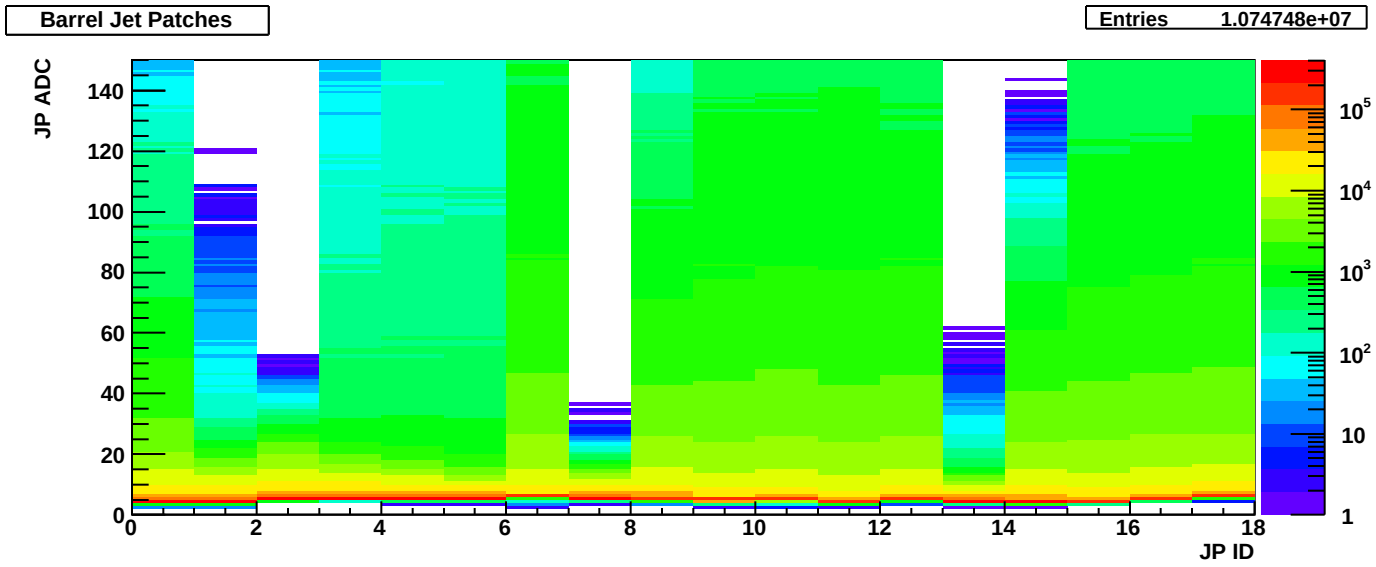

BunchId7Bit (JP2||AJP)
Entries 597082
10^4
10^3
10^2
10
0
20
40
60
80
100
120

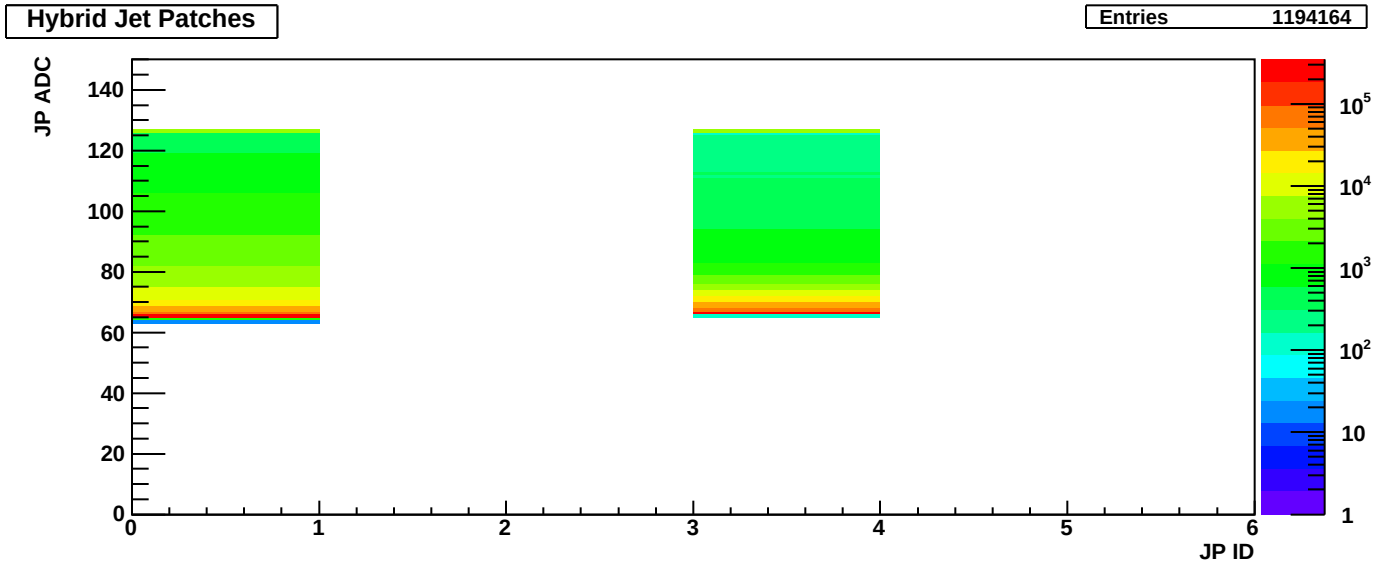

Barrel EMC L0 Input - High Tower
Entries 1.791246e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

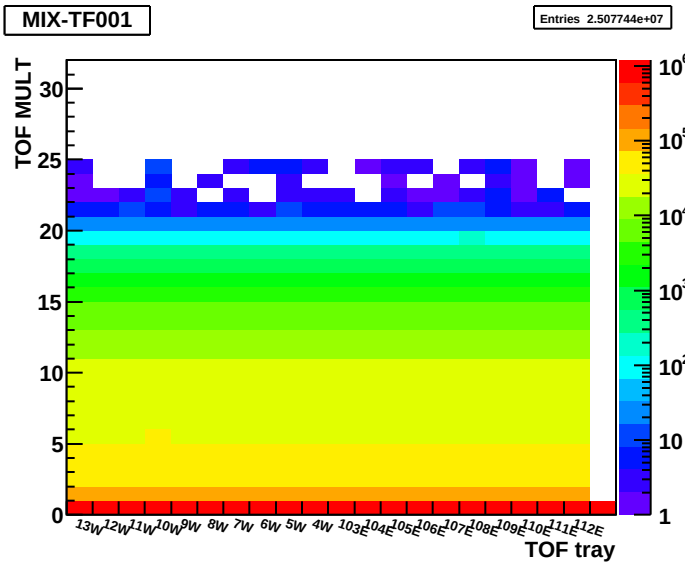

Barrel EMC L0 Input - Patch Sum
Entries 1.791246e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

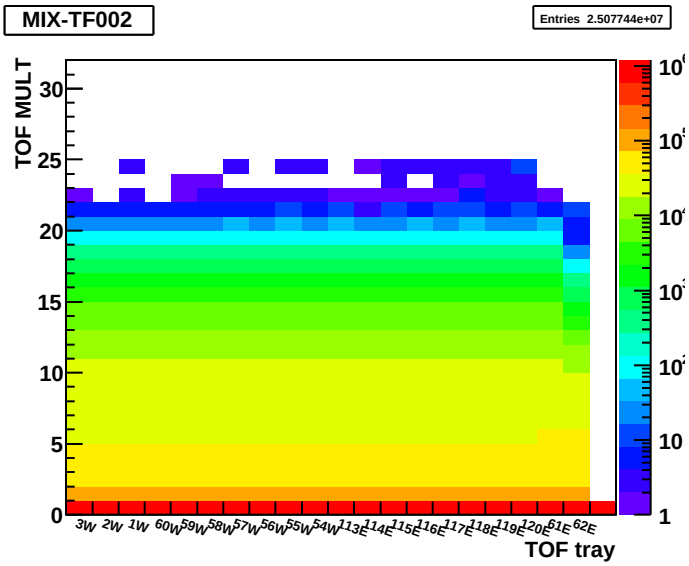

Endcap EMC L0 Input - High Tower
Entries 5.373738e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

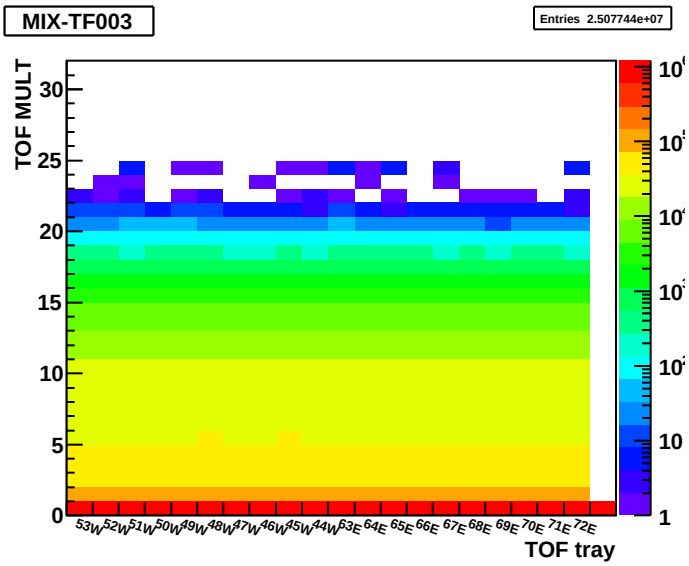

Endcap EMC L0 Input - Patch Sum
Entries 5.373738e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

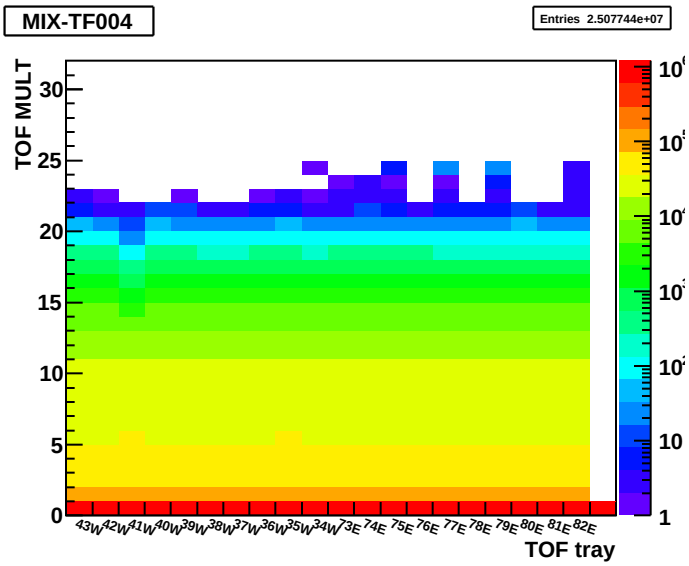

Barrel Jet Patches
Entries 1.074748e+07
JP ADC
JP ID

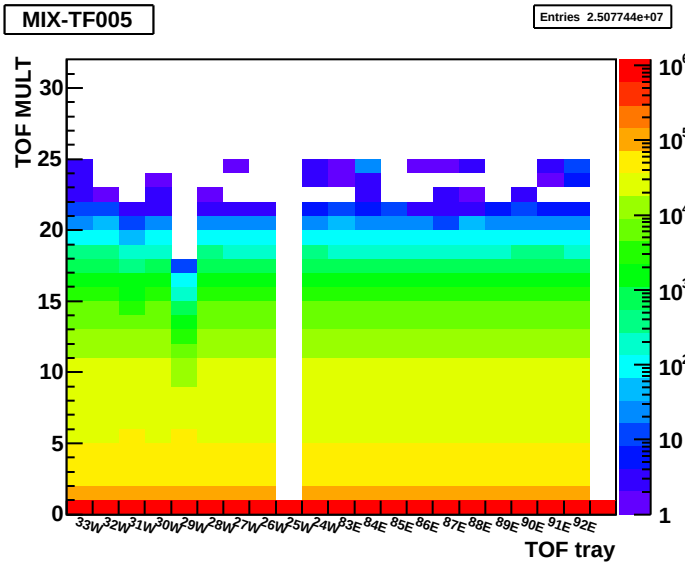

Hybrid Jet Patches
Entries 1194164
JP ADC
JP ID

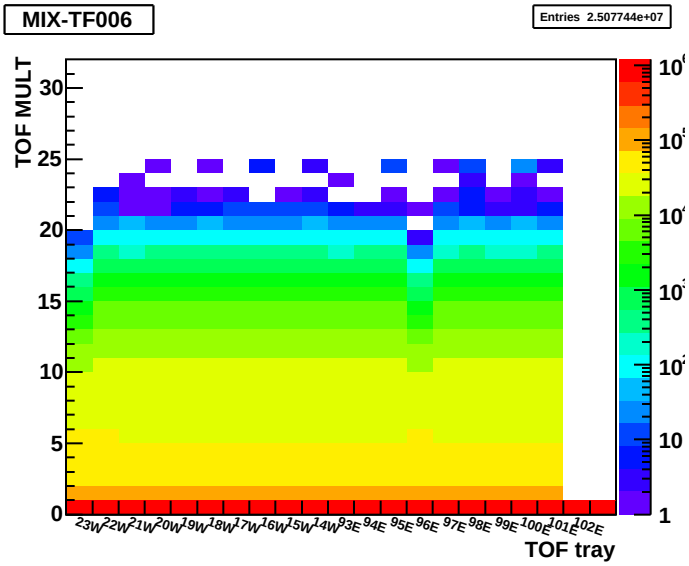

MIX-TF001
Entries 2.507744e+07
TOF MULT
TOF tray

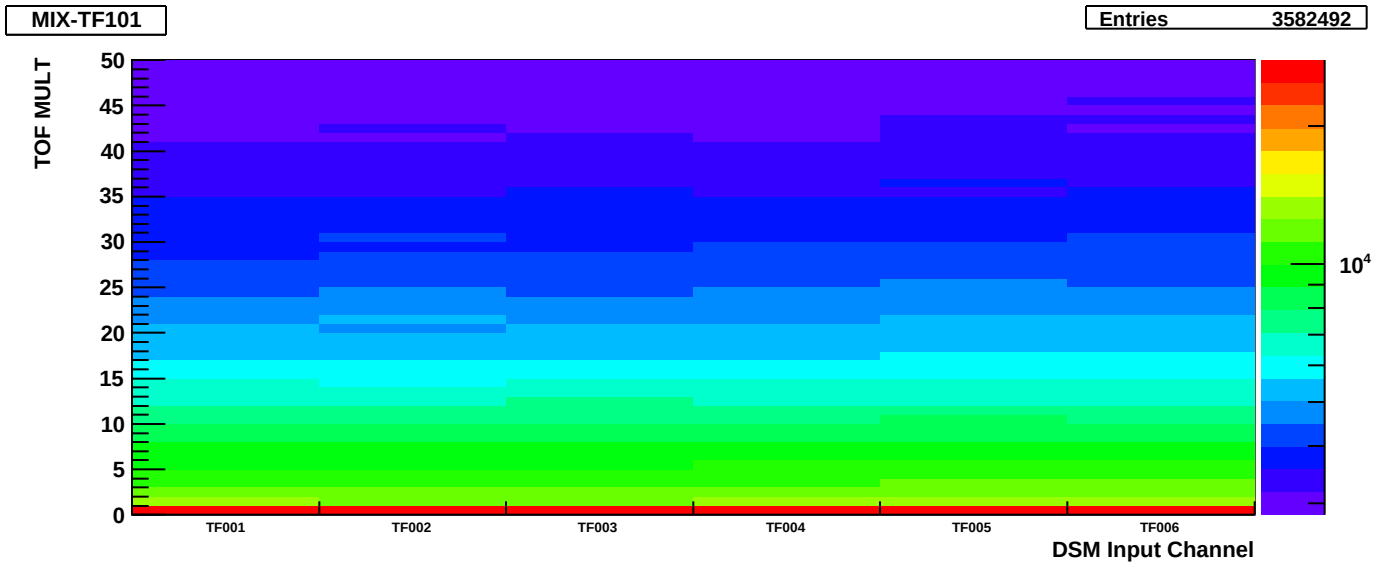

MIX-TF002
Entries 2.507744e+07
TOF MULT
TOF tray

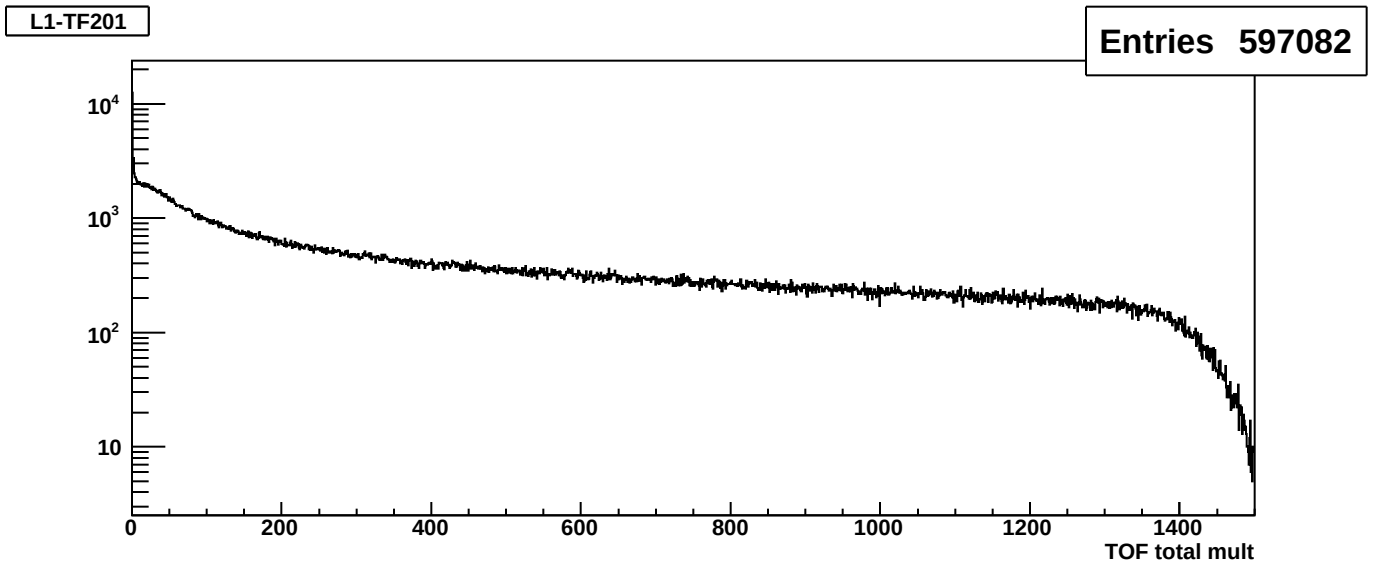

MIX-TF003
Entries 2.507744e+07
TOF MULT
TOF tray

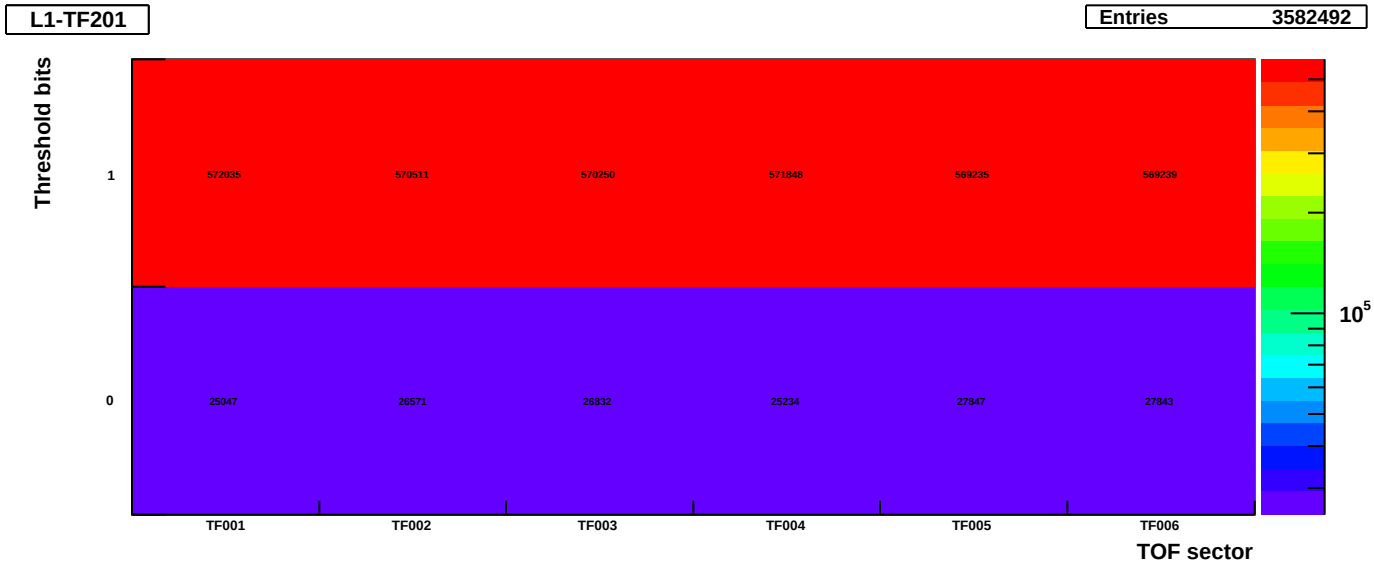

MIX-TF004
Entries 2.507744e+07
TOF MULT
TOF tray

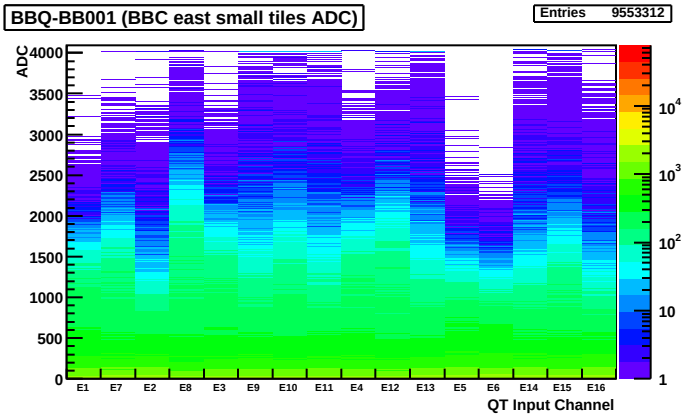

MIX-TF005
Entries 2.507744e+07
TOF MULT
TOF tray

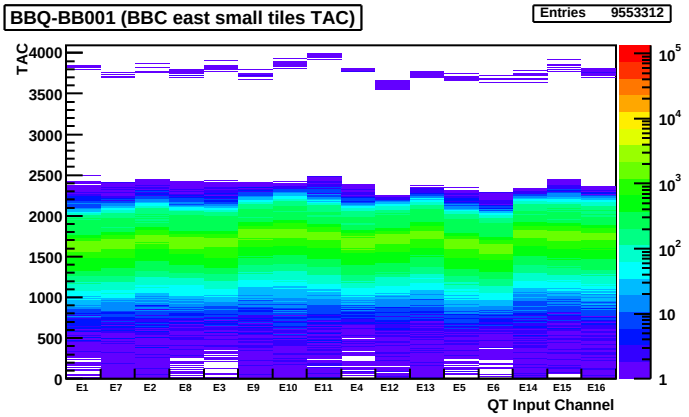

MIX-TF006
Entries 2.507744e+07
TOF MULT
TOF tray

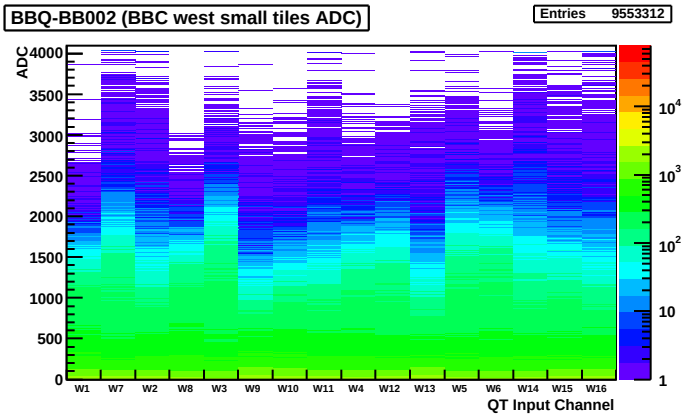

MIX-TF101
Entries 3582492
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

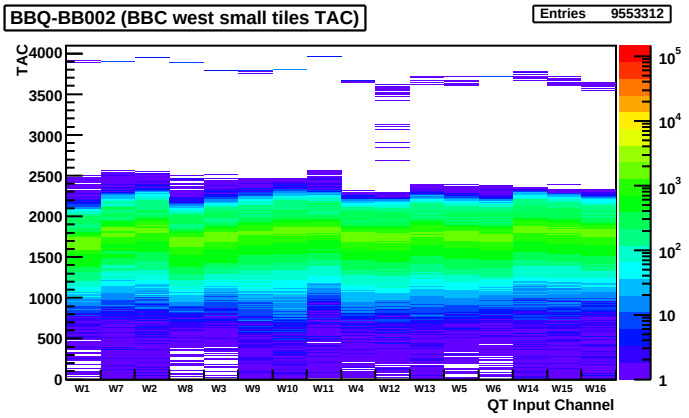

L1-TF201
Entries 597082
10^4
10^3
10^2
10
0
200
400
600
800
1000
1200
1400
TOF total mult

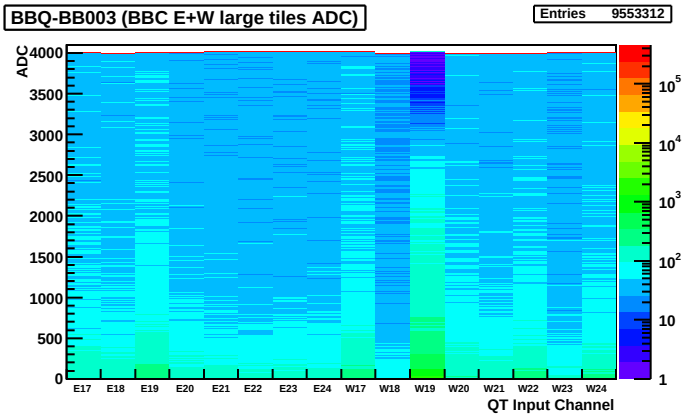

L1-TF201
Entries 3582492
Threshold bits
1
0
572035
570511
570250
571848
569235
569239
25047
26571
26832
25234
27847
27843
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

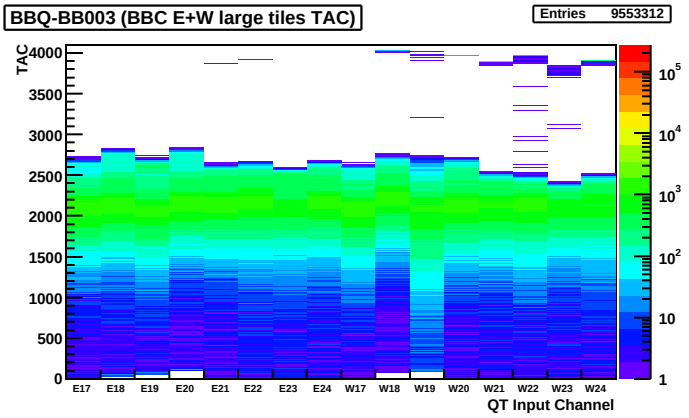

BBQ-BB001 (BBC east small tiles ADC)
Entries 9553312
ADC
QT Input Channel

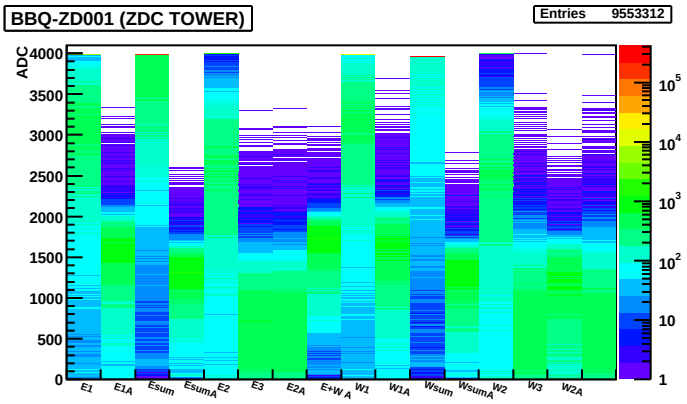

BBQ-BB001 (BBC east small tiles TAC)
Entries 9553312
TAC
QT Input Channel

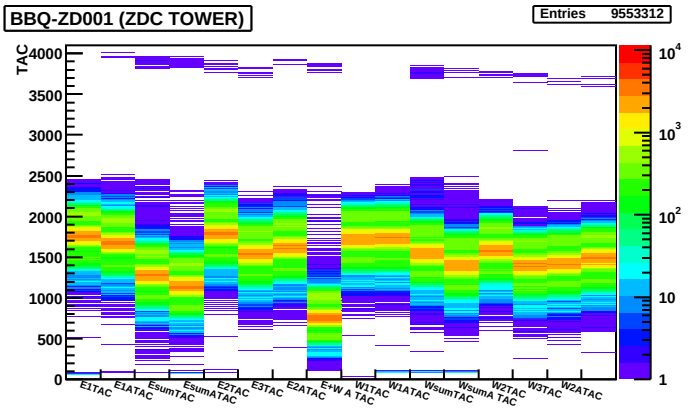

BBQ-BB002 (BBC west small tiles ADC)
Entries 9553312
ADC
QT Input Channel

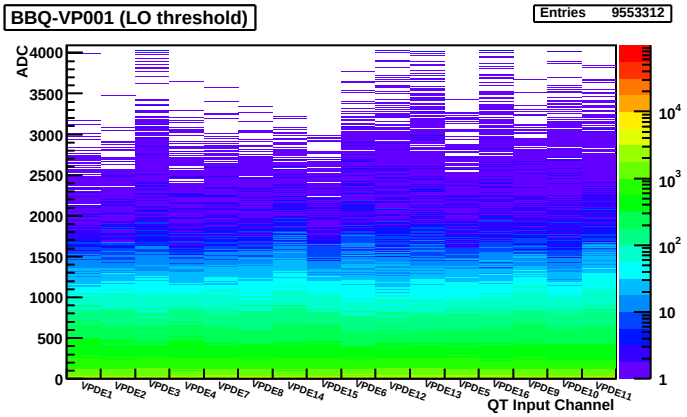

BBQ-BB002 (BBC west small tiles TAC)
Entries 9553312
TAC
QT Input Channel

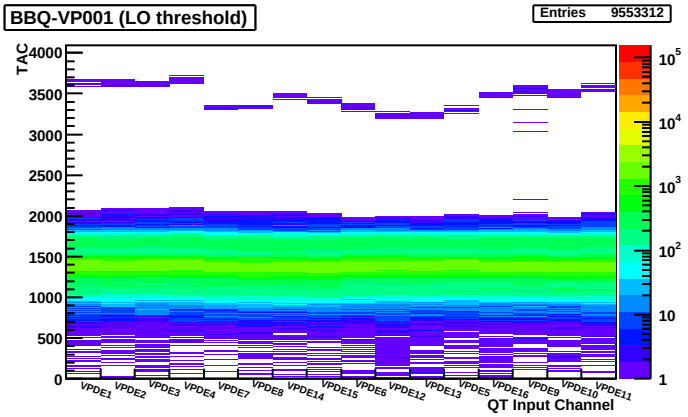

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 9553312
ADC
QT Input Channel

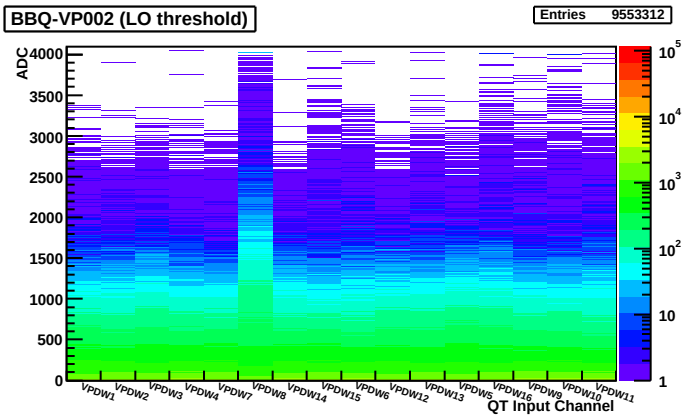

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 9553312
TAC
QT Input Channel

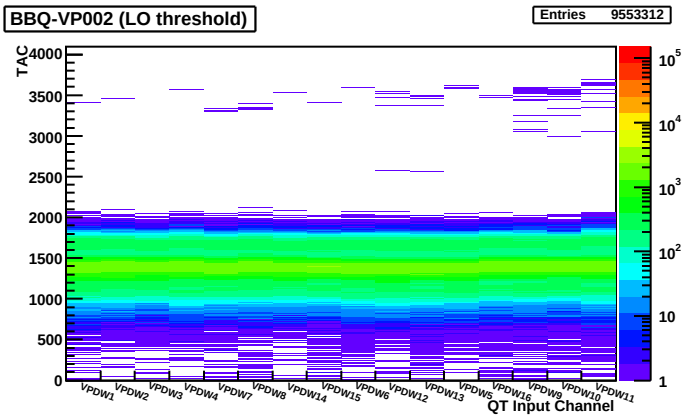

BBQ-ZD001 (ZDC TOWER)
Entries 9553312
ADC

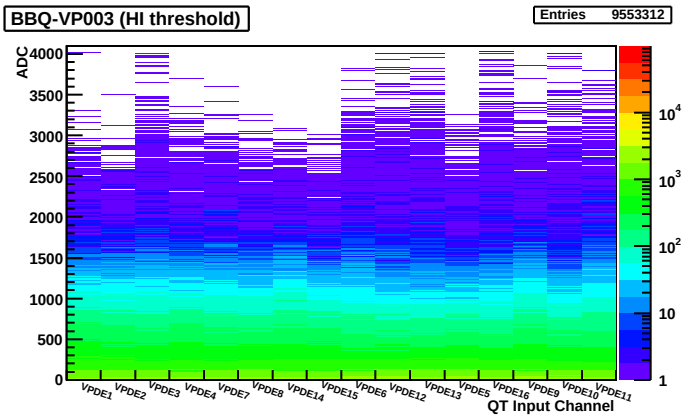

BBQ-ZD001 (ZDC TOWER)
Entries 9553312
TAC

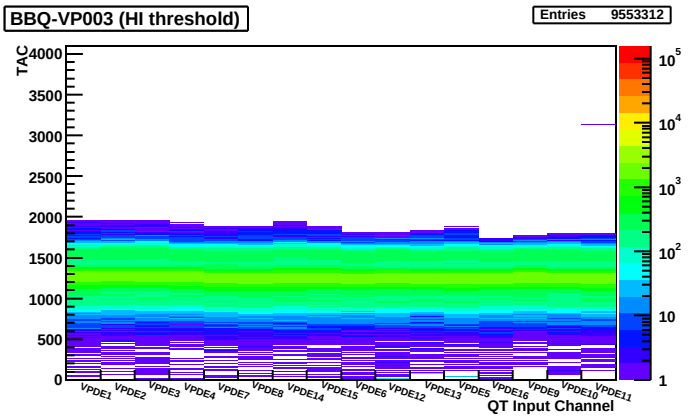

BBQ-VP001 (LO threshold)
Entries 9553312
ADC
QT Input Channel

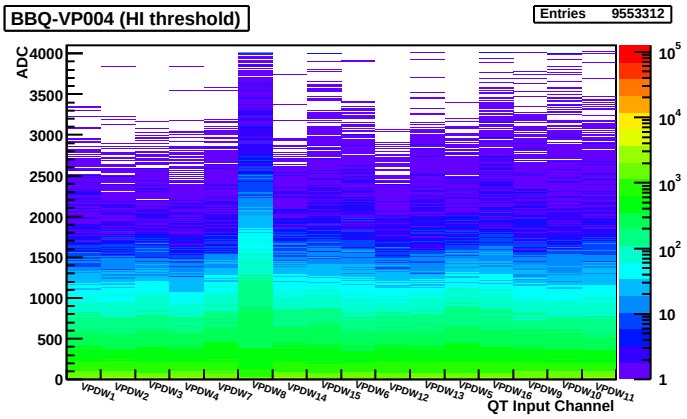

BBQ-VP001 (LO threshold)
Entries 9553312
TAC
QT Input Channel

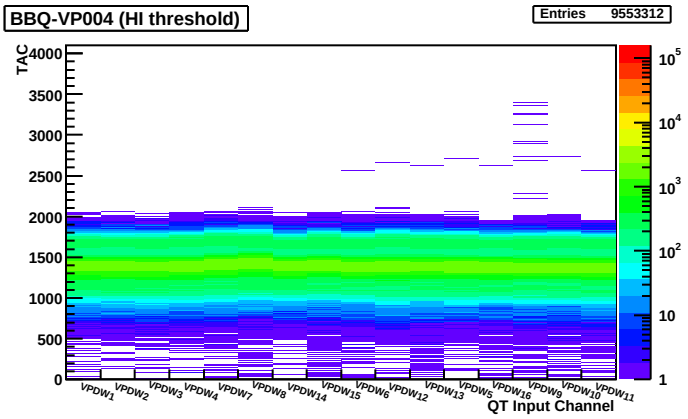

BBQ-VP002 (LO threshold)
Entries 9553312
ADC
QT Input Channel

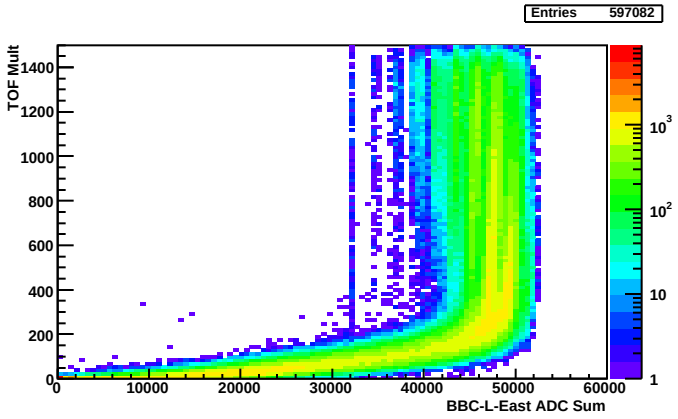

BBQ-VP002 (LO threshold)
Entries 9553312
TAC
QT Input Channel

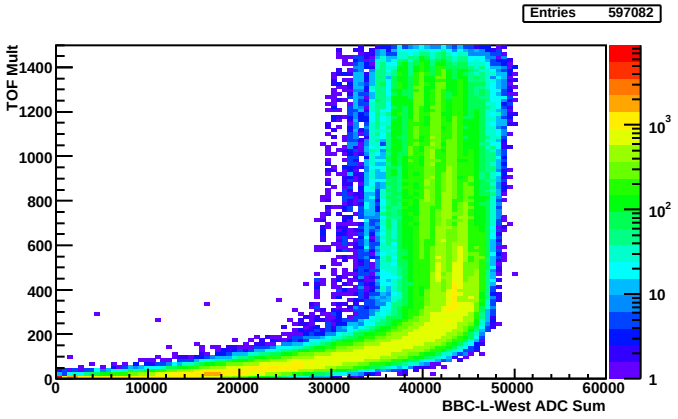

BBQ-VP003 (HI threshold)
Entries 9553312
ADC
QT Input Channel

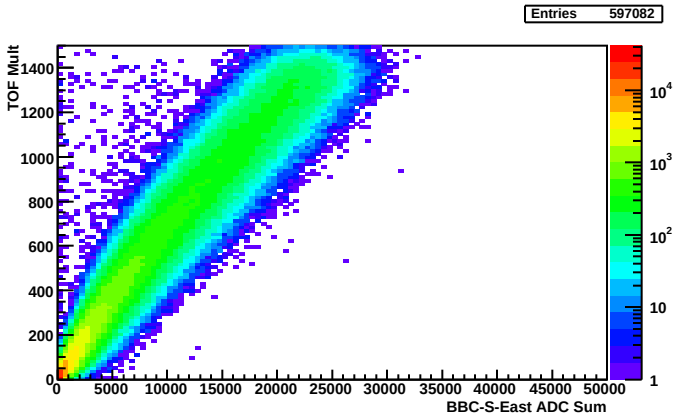

BBQ-VP003 (HI threshold)
Entries 9553312
TAC
QT Input Channel

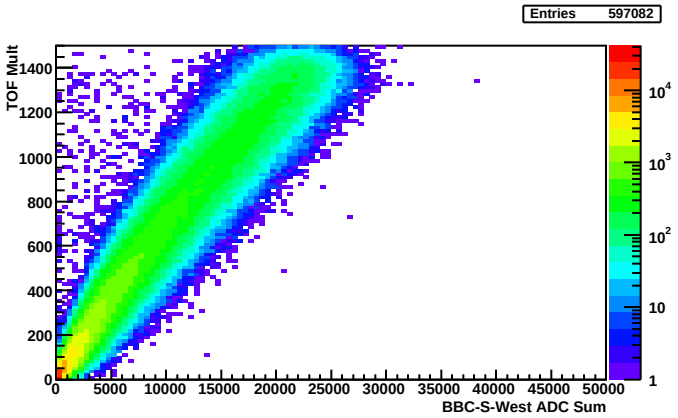

BBQ-VP004 (HI threshold)
Entries 9553312
ADC
QT Input Channel

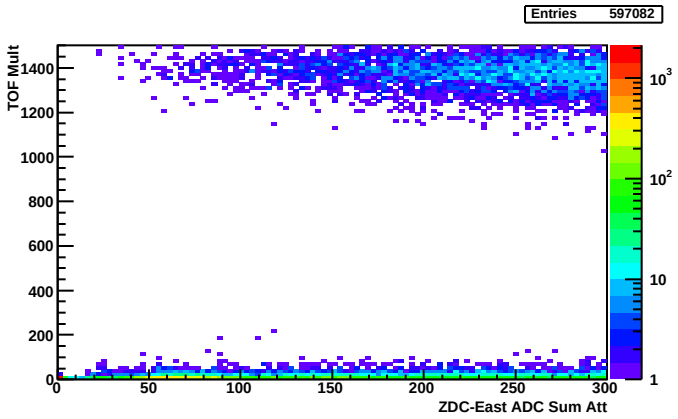

BBQ-VP004 (HI threshold)
Entries 9553312
TAC
QT Input Channel

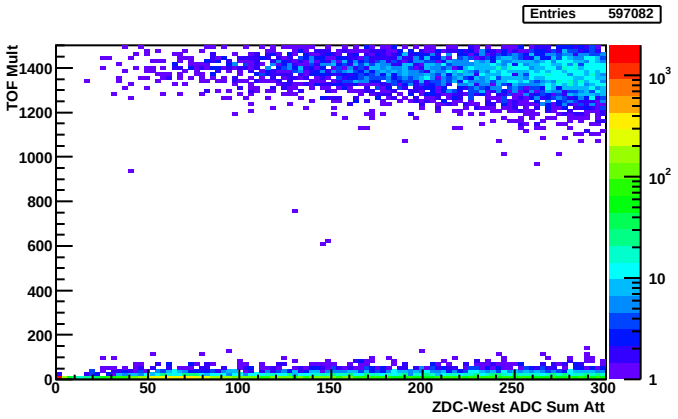

Entries 597082
TOF Mult
BBC-L-East ADC Sum

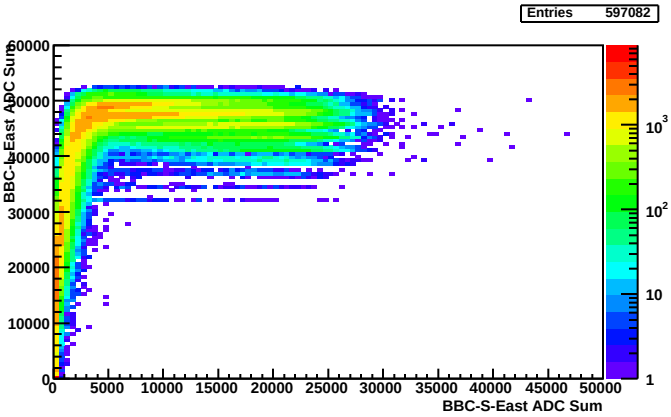

Entries 597082
TOF Mult
BBC-L-West ADC Sum

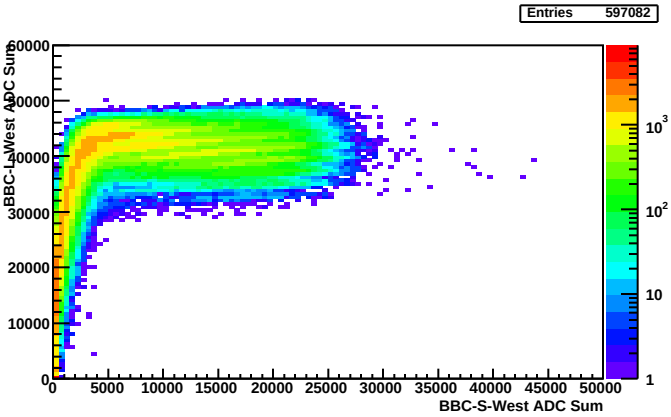

Entries 597082
TOF Mult
BBC-S-East ADC Sum

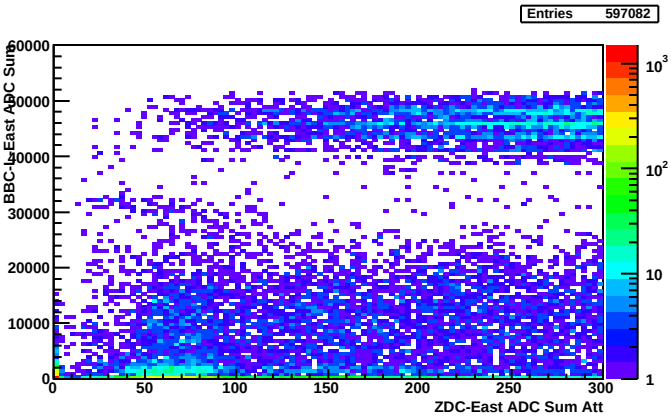

Entries 597082
TOF Mult
BBC-S-West ADC Sum

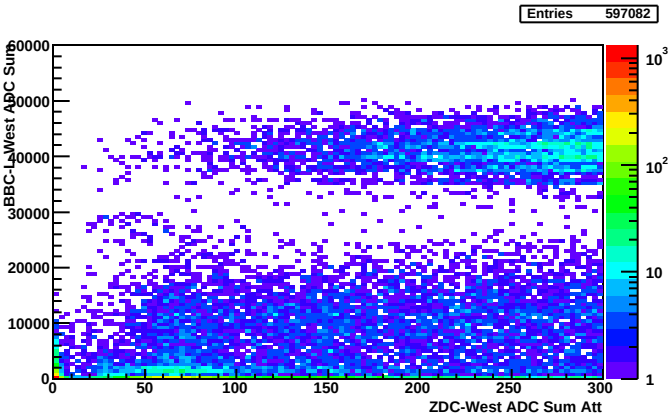

Entries 597082
TOF Mult
ZDC-East ADC Sum Att

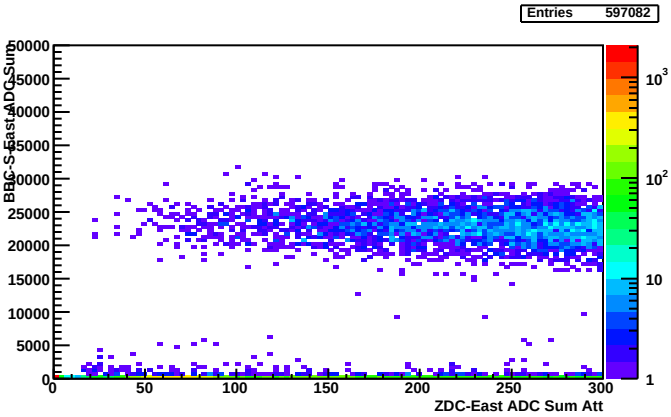

Entries 597082
TOF Mult
ZDC-West ADC Sum Att

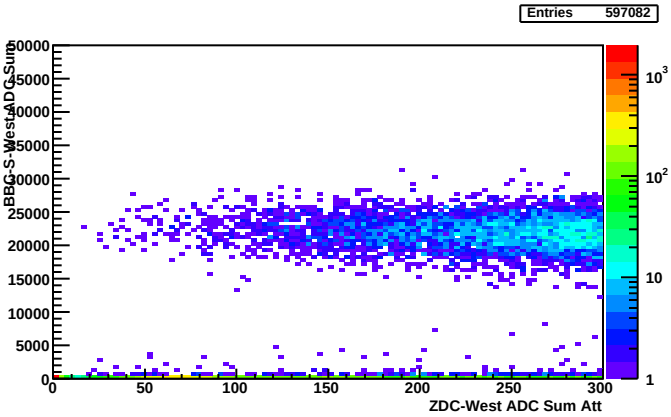

Entries 597082
BBC-L-East ADC Sum
BBC-S-East ADC Sum

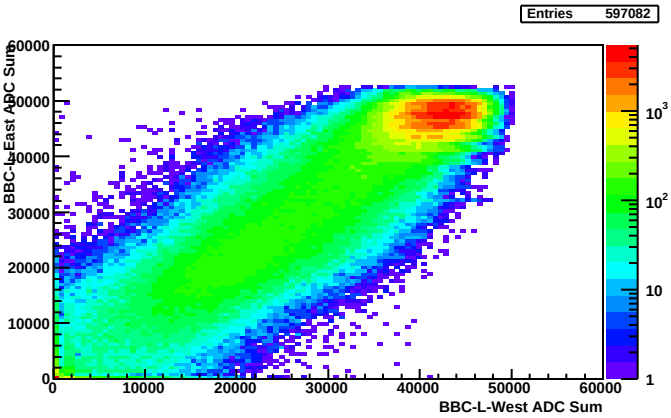

Entries 597082
BBC-L-West ADC Sum
BBC-S-West ADC Sum

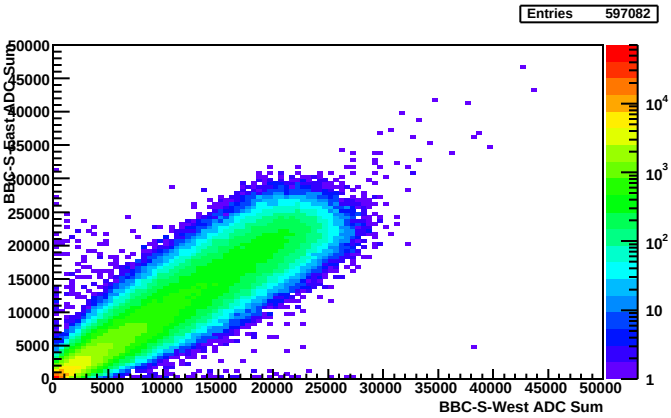

Entries 597082
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

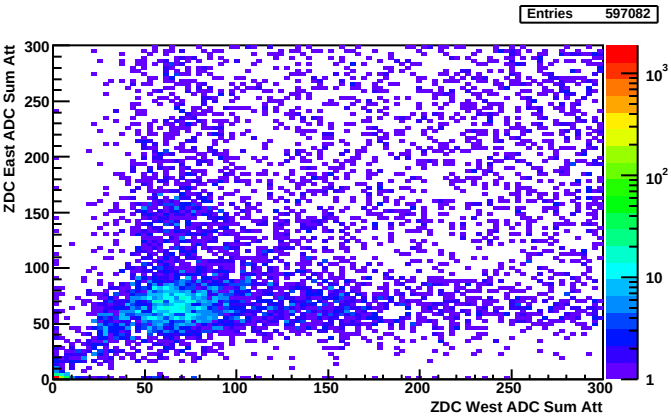

Entries 597082
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

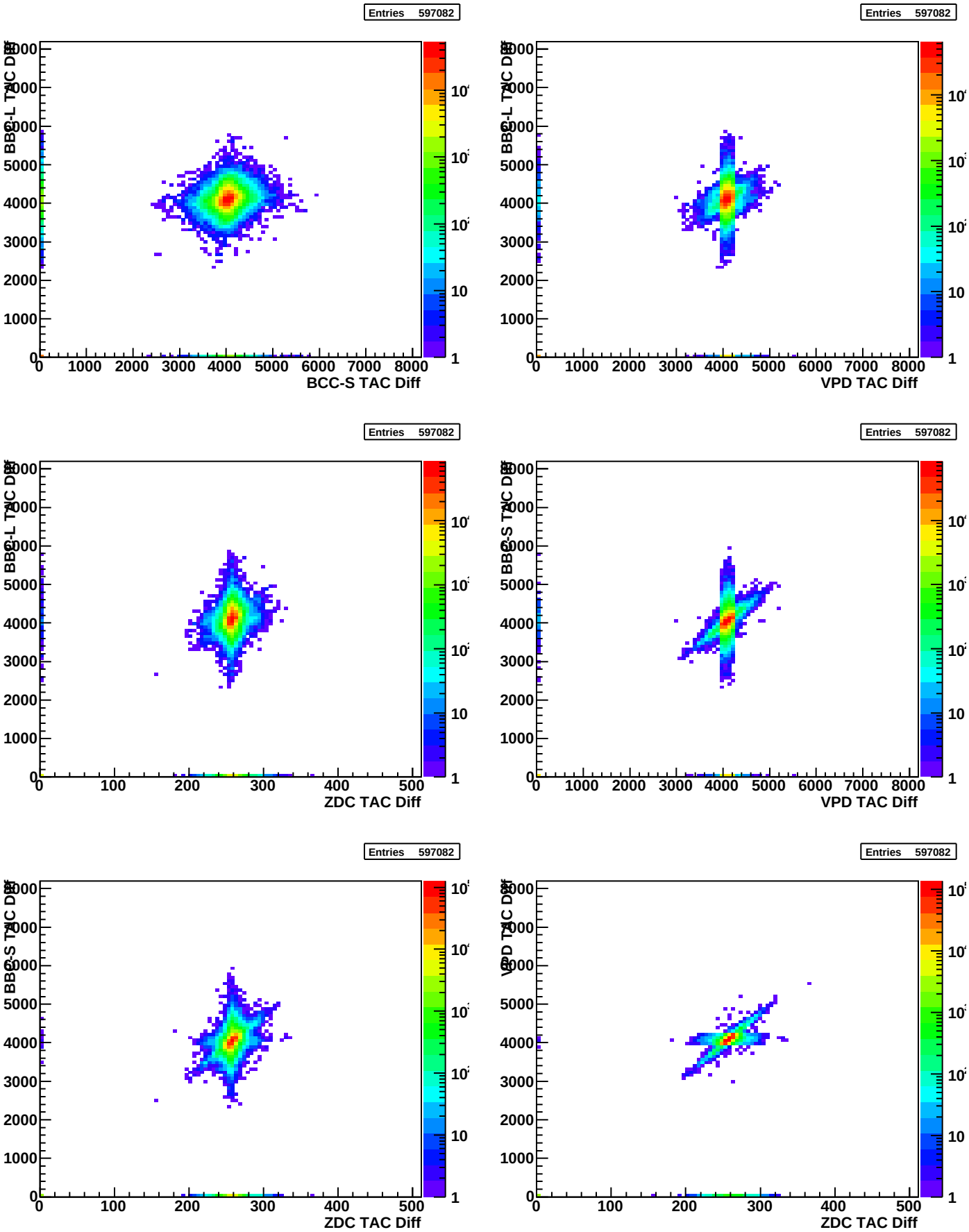

Entries 597082
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

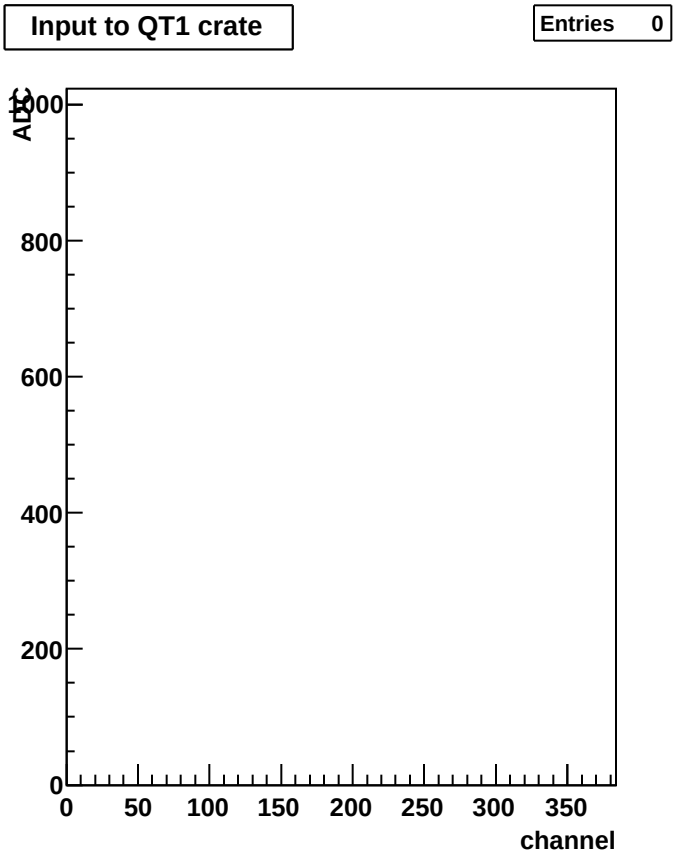

Entries 597082
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

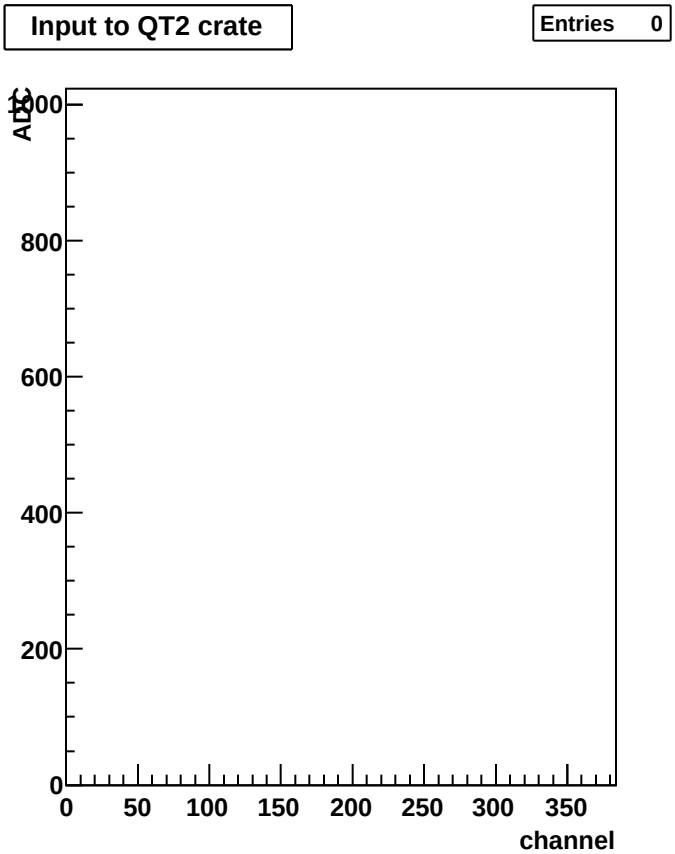

Entries 597082
BBC-L-East ADC Sum
BBC-L-West ADC Sum

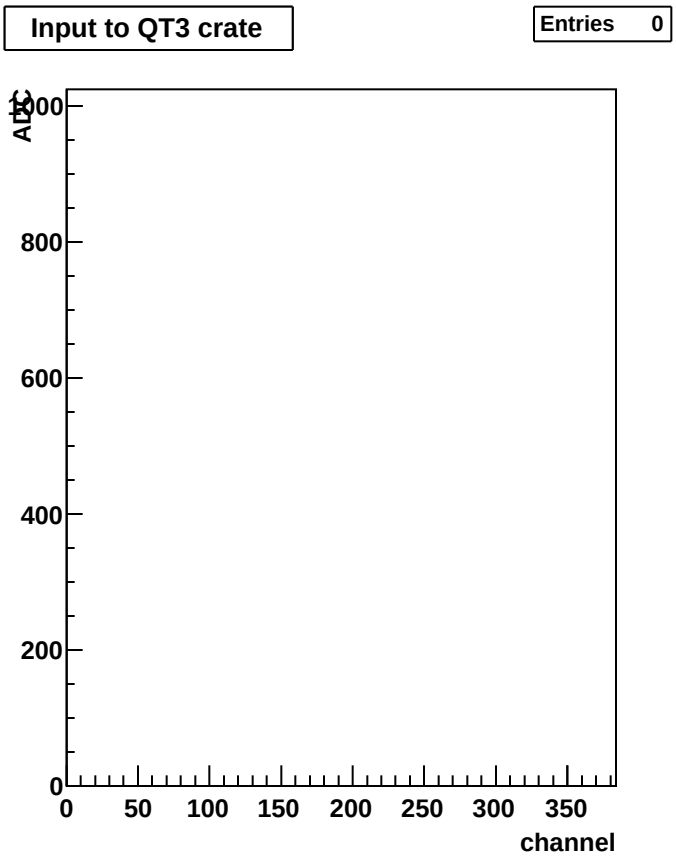

Entries 597082
BBC-S-East ADC Sum
BBC-S-West ADC Sum

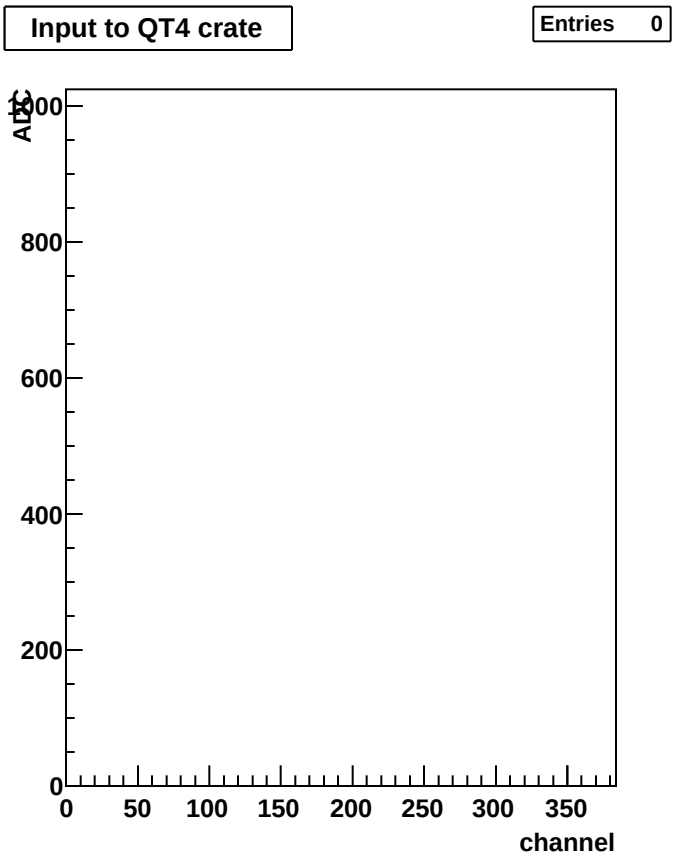

Entries 597082
ZDC East ADC Sum Att
ZDC West ADC Sum Att

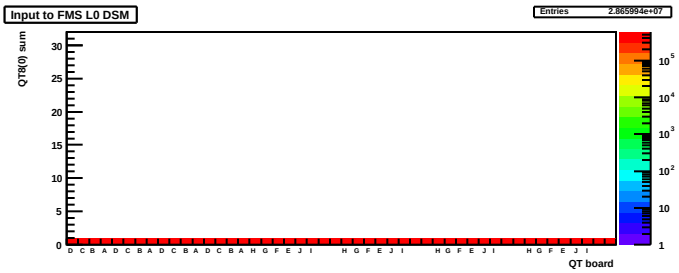

Entries 597082
BBC-L TAC Diff
BCC-S TAC Diff
Entries 597082
BBC-L TAC Diff
VPD TAC Diff
Entries 597082
BBC-L TAC Diff
ZDC TAC Diff
Entries 597082
BBC-S TAC Diff
VPD TAC Diff
Entries 597082
BBC-S TAC Diff
ZDC TAC Diff
Entries 597082
VPD TAC Diff
ZDC TAC Diff

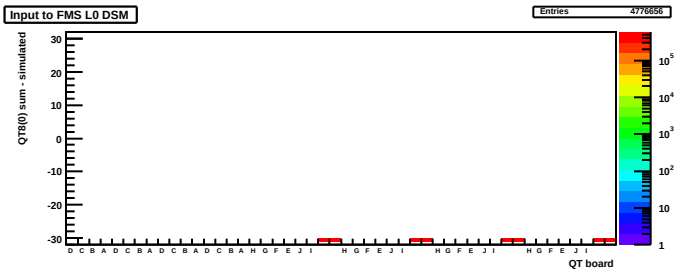

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

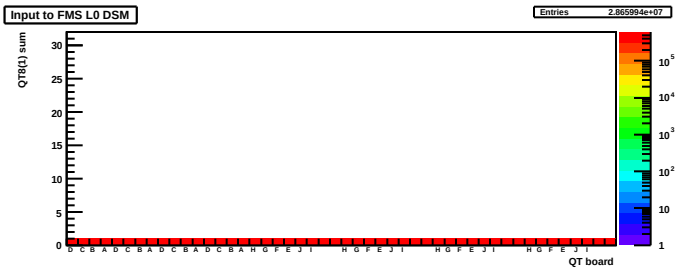

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

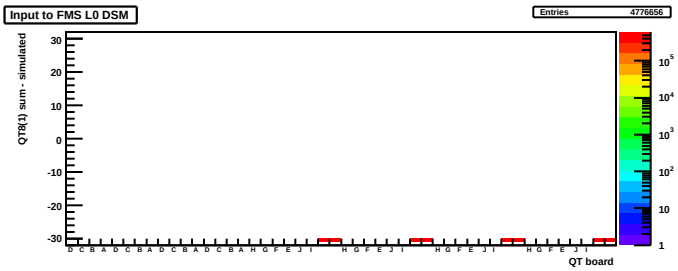

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

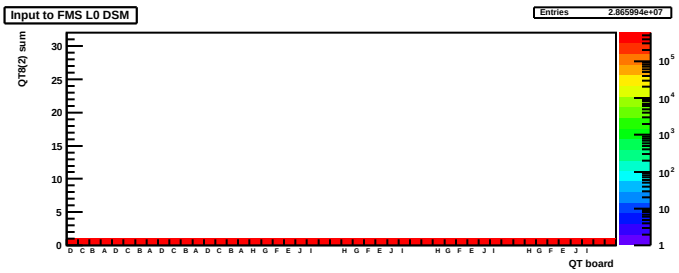

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

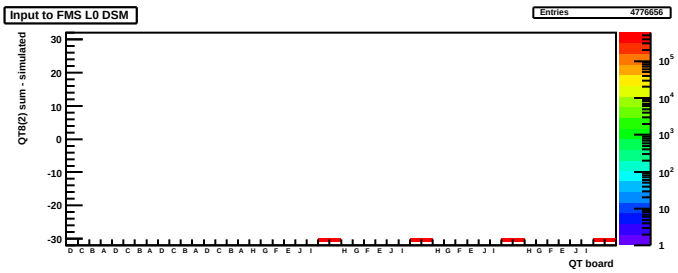

Input to FMS L0 DSM
Entries 2.865994e+07
QT8(0) sum
QT board

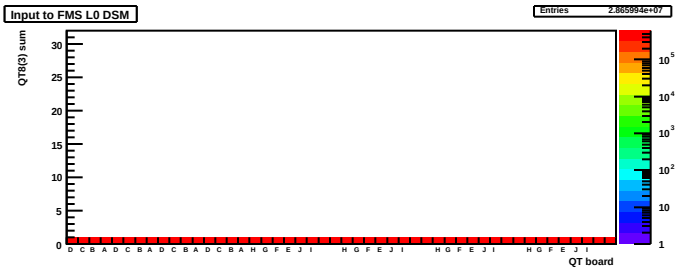

Input to FMS L0 DSM
Entries 4776656
QT8(0) sum - simulated
QT board

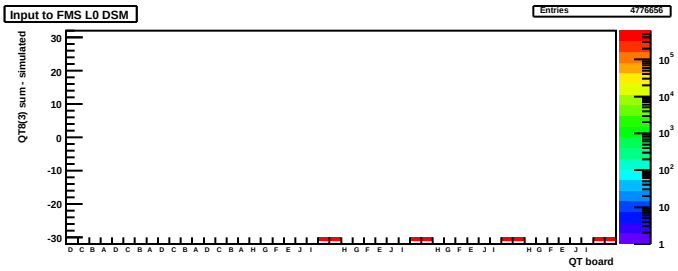

Input to FMS L0 DSM
Entries 2.865994e+07
QT8(1) sum
QT board

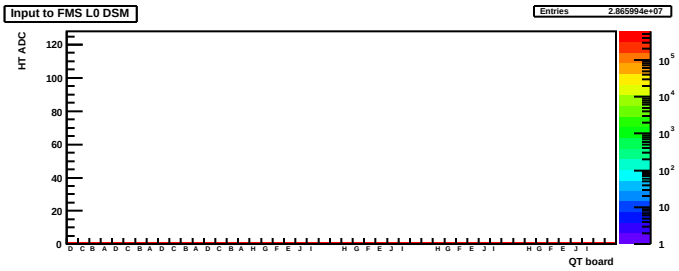

Input to FMS L0 DSM
Entries 4776656
QT8(1) sum - simulated
QT board

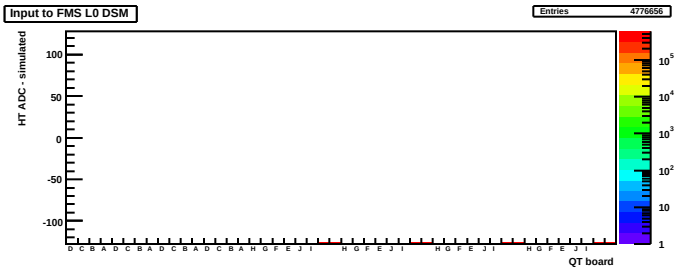

Input to FMS L0 DSM
Entries 2.865994e+07
QT8(2) sum
QT board

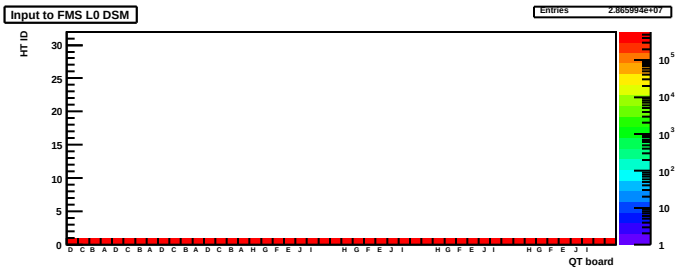

Input to FMS L0 DSM
Entries 4776656
QT8(2) sum - simulated
QT board

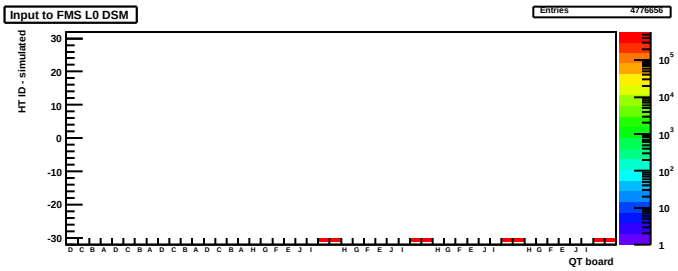

Input to FMS L0 DSM
Entries 2.865994e+07
QT8(3) sum
QT board

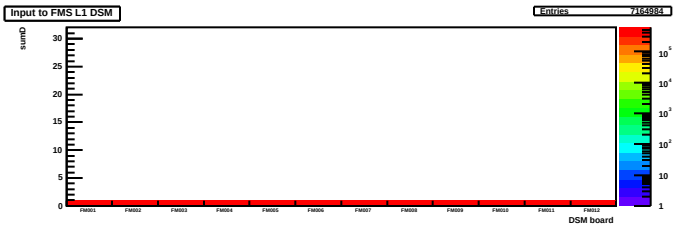

Input to FMS L0 DSM
Entries 4776656
QT8(3) sum - simulated
QT board

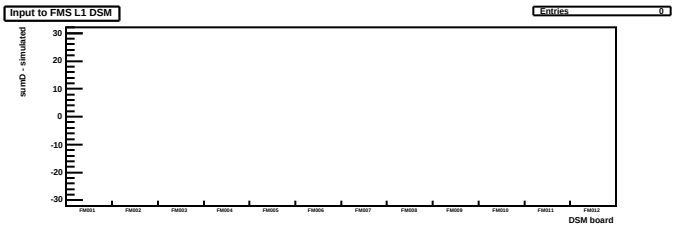

Input to FMS L0 DSM
Entries 2.865994e+07
HT ADC
QT board

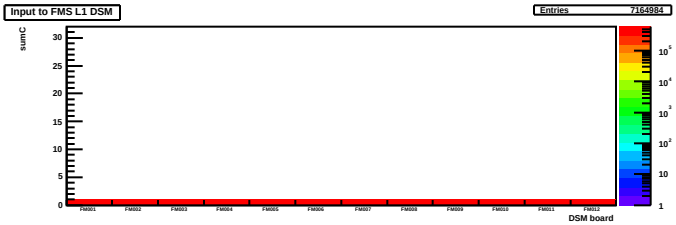

Input to FMS L0 DSM
Entries 4776656
HT ADC - simulated
QT board

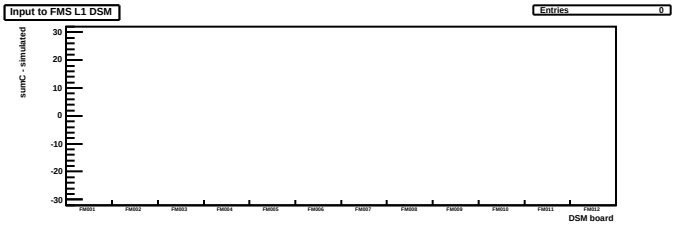

Input to FMS L0 DSM
Entries 2.865994e+07
HT ID
QT board

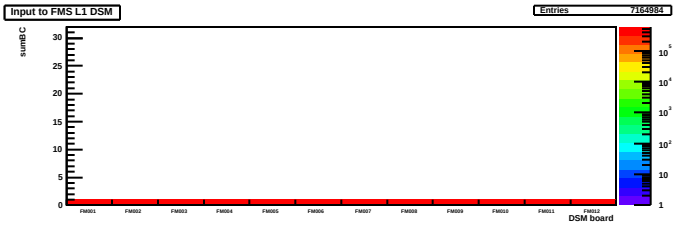

Input to FMS L0 DSM
Entries 4776656
HT ID - simulated
QT board

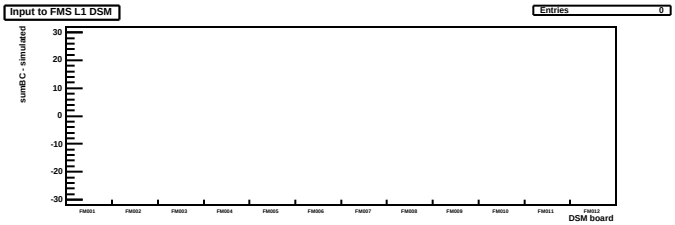

Input to FMS L1 DSM
Entries 7164984
sumD
DSM board

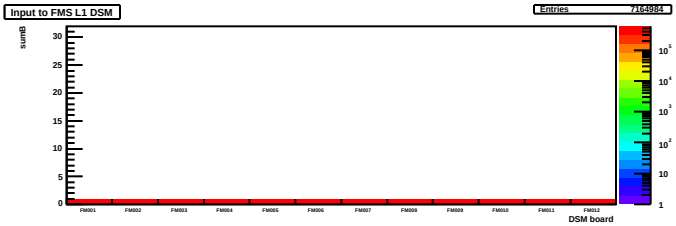

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

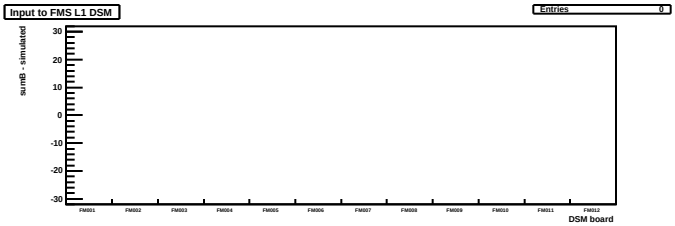

Input to FMS L1 DSM
Entries 7164984
sumC
DSM board

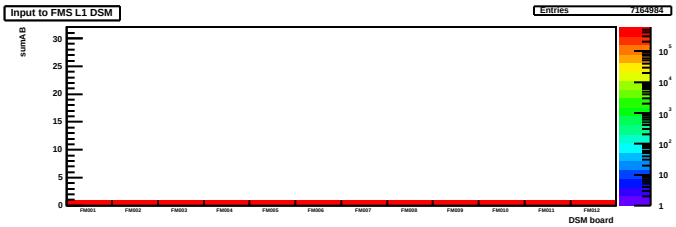

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

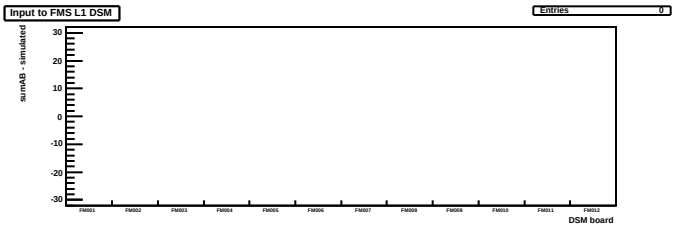

Input to FMS L1 DSM
Entries 7164984
sumBC
DSM board

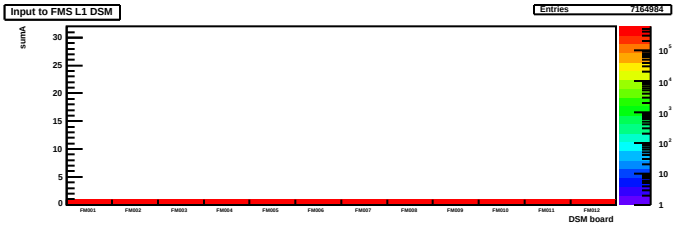

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

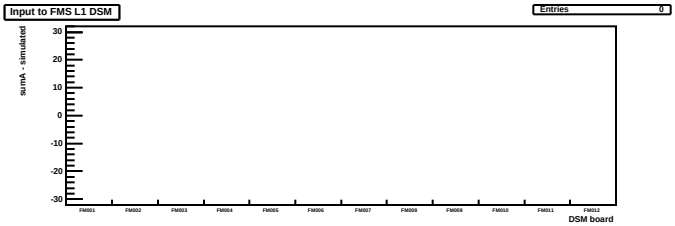

Input to FMS L1 DSM
Entries 7164984
sumB
DSM board

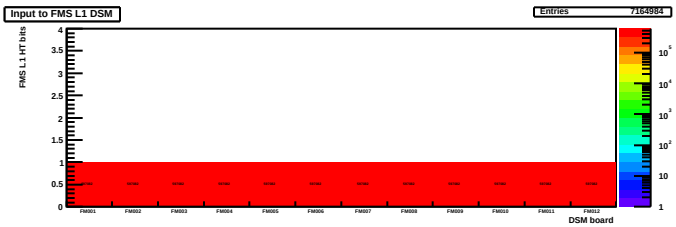

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

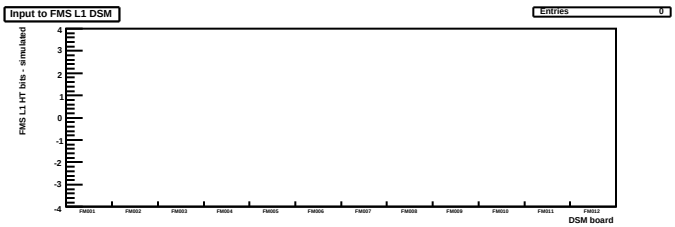

Input to FMS L1 DSM
Entries 7164984
sumAB
DSM board

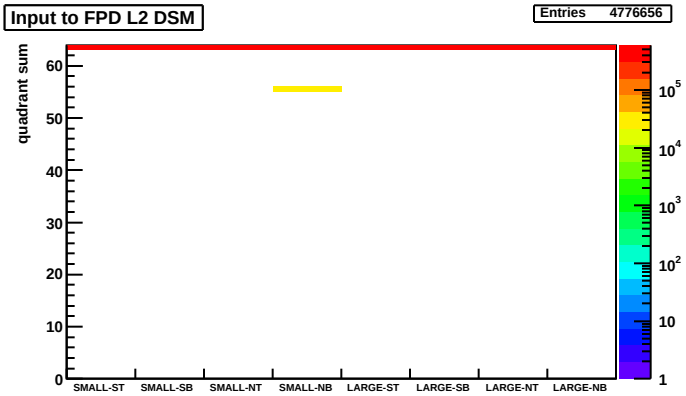

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

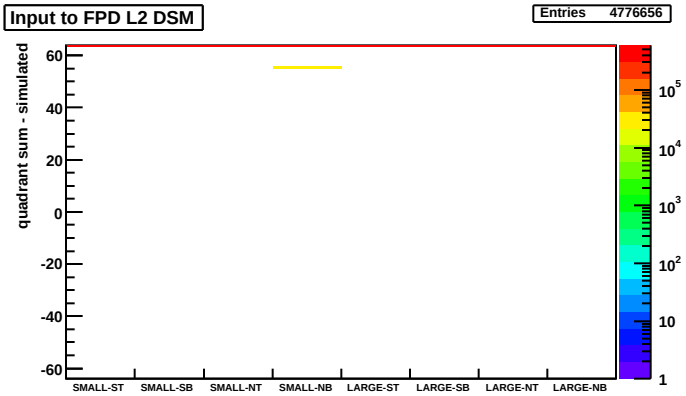

Input to FMS L1 DSM
Entries 7164984
sumA
DSM board

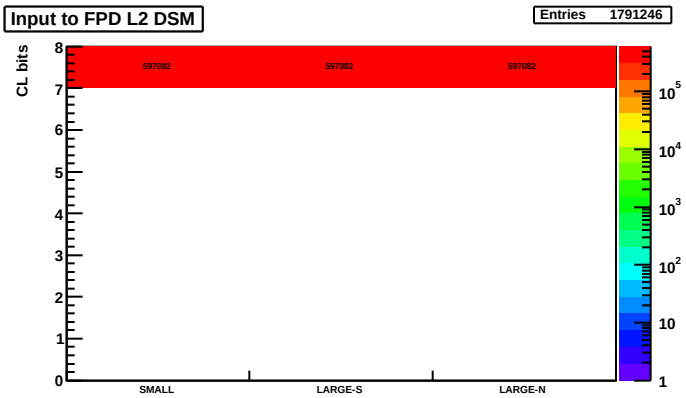

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

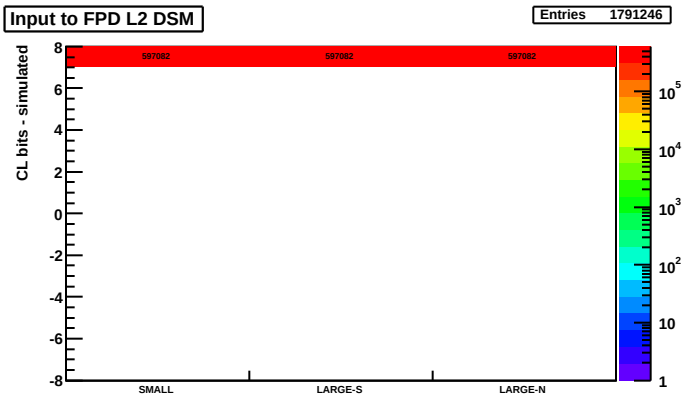

Input to FMS L1 DSM
Entries 7164984
FMS L1 HT bits
DSM board

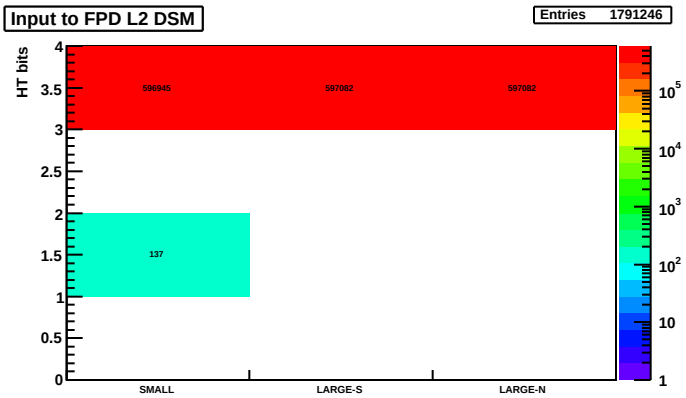

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

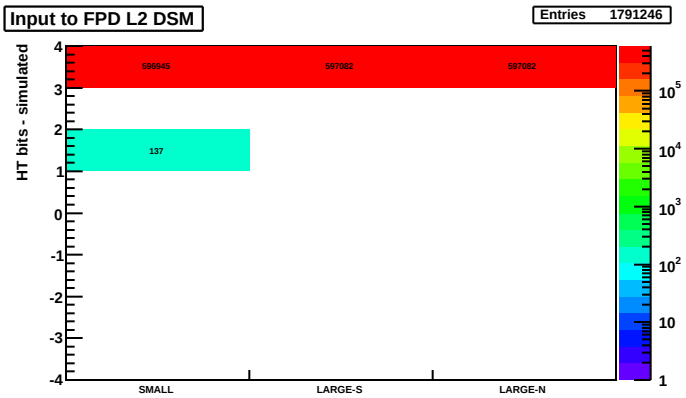

Input to FPD L2 DSM
Entries 4776656
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

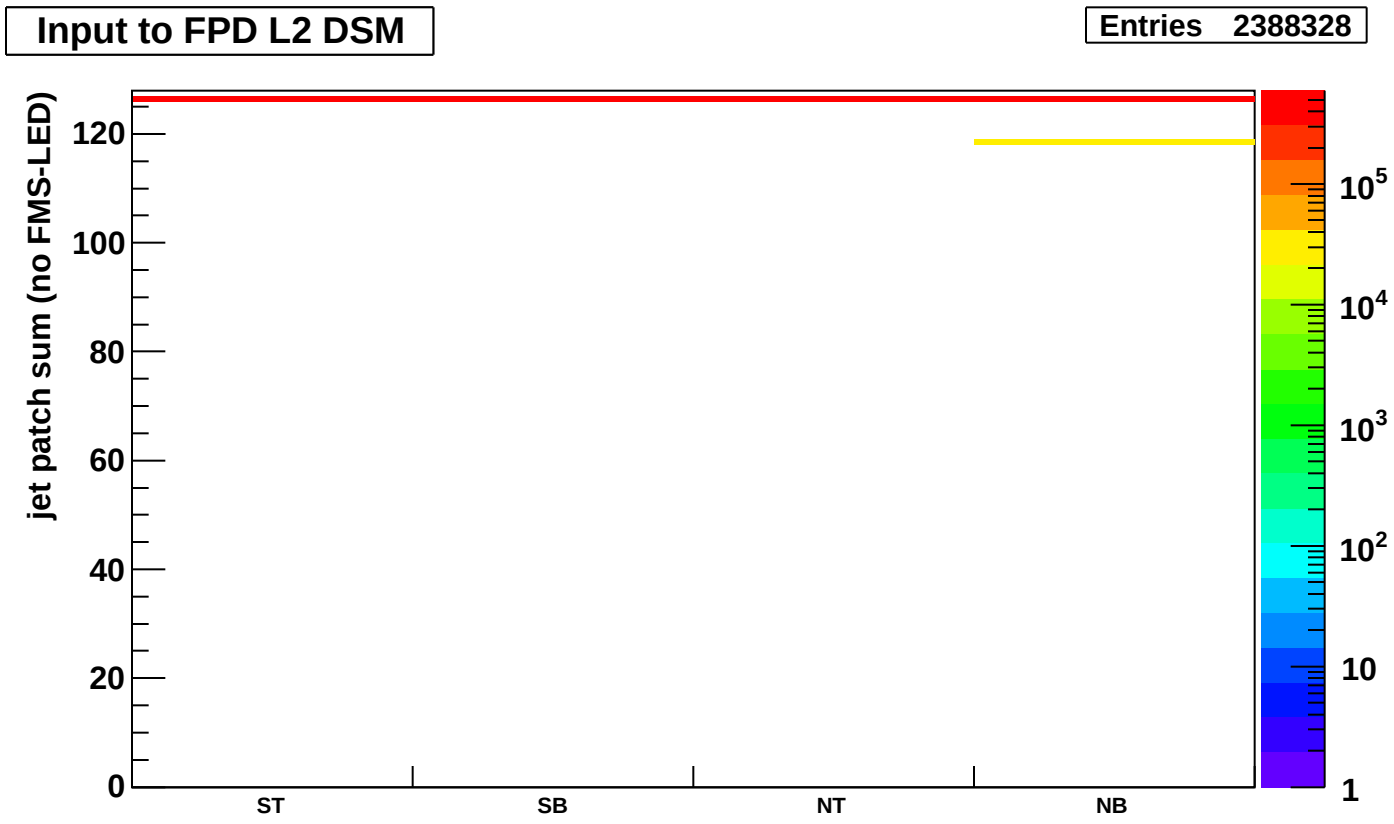

Input to FPD L2 DSM
Entries 4776656
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

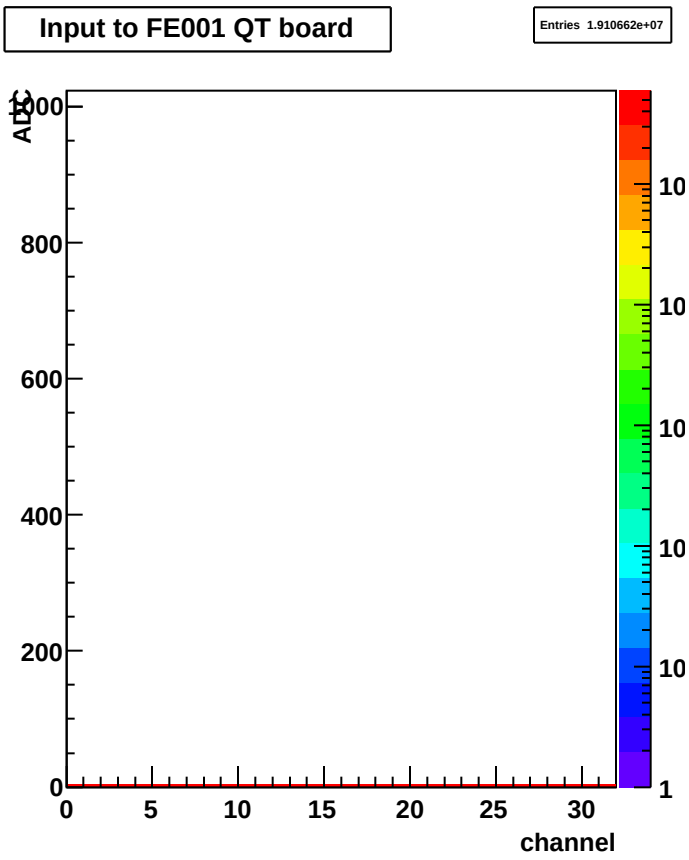

Input to FPD L2 DSM
Entries 1791246
CL bits
597082
597082
597082
SMALL
LARGE-S
LARGE-N

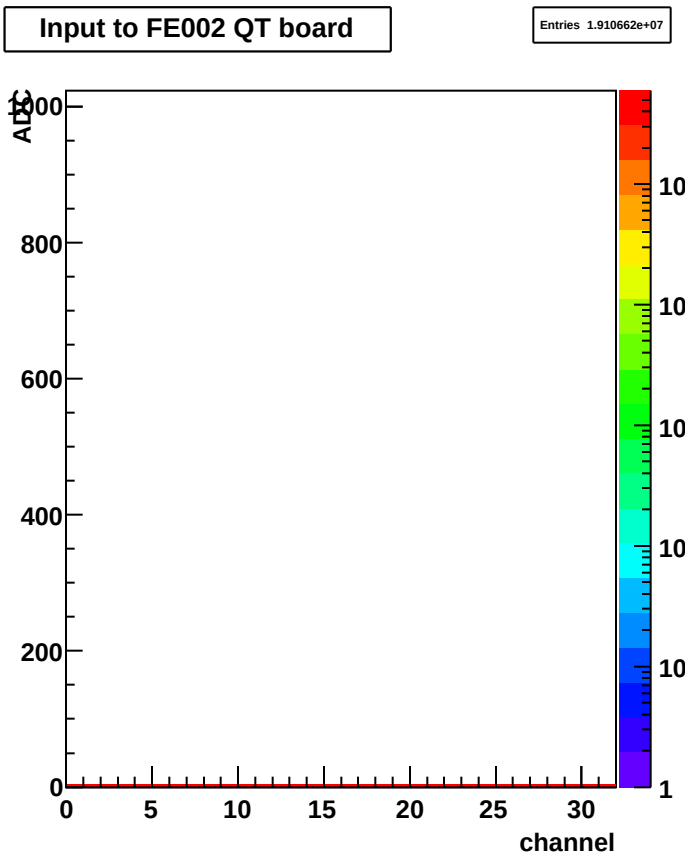

Input to FPD L2 DSM
Entries 1791246
CL bits - simulated
597082
597082
597082
SMALL
LARGE-S
LARGE-N

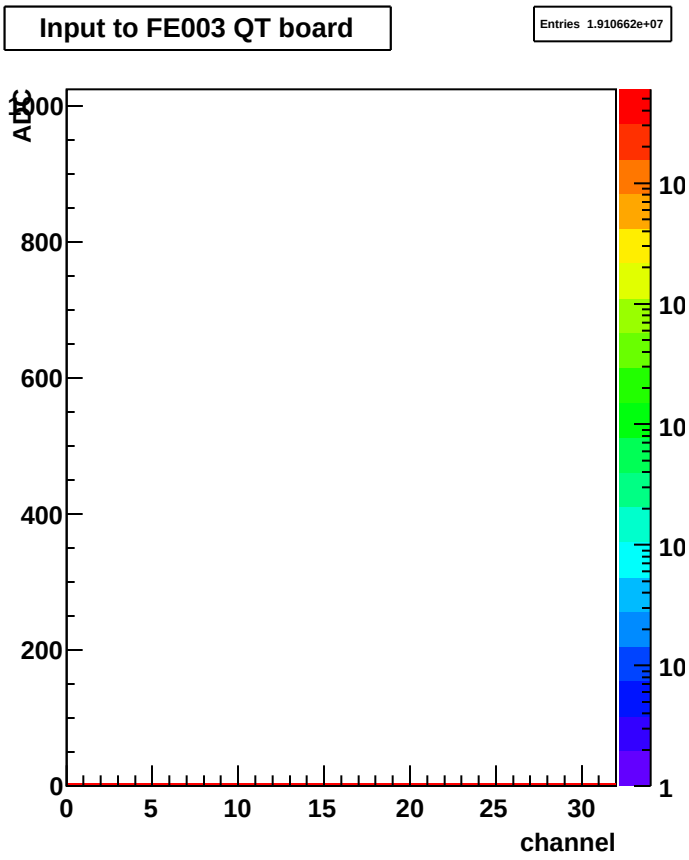

Input to FPD L2 DSM
Entries 1791246
HT bits
596945
597082
597082
137
SMALL
LARGE-S
LARGE-N

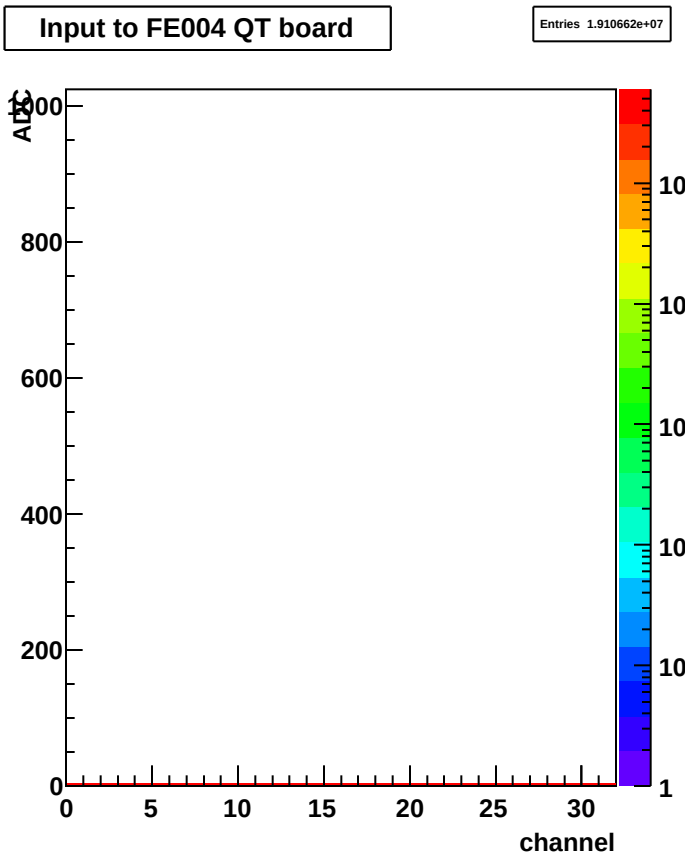

Input to FPD L2 DSM
Entries 1791246
HT bits - simulated
596945
597082
597082
137
SMALL
LARGE-S
LARGE-N


Input to FPD L2 DSM
Entries 2388328
jet patch sum (no FMS-LED)
120
100
80
60
40
20
0
ST
SB
NT
NB
10^5
10^4
10^3
10^2
10
1

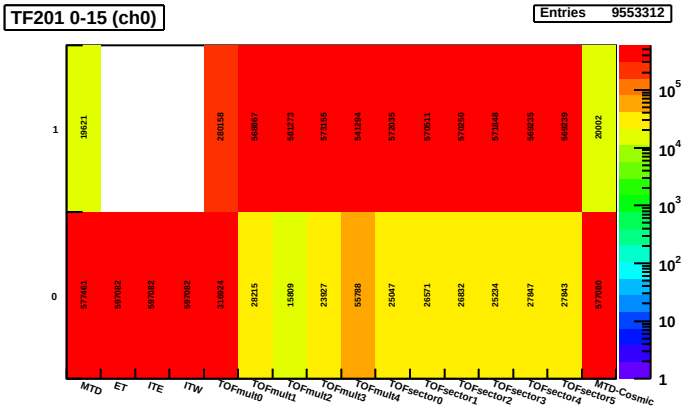

Input to FE001 QT board
Entries 1.910662e+07
ADC
channel


Input to FE002 QT board
Entries 1.910662e+07
ADC
channel

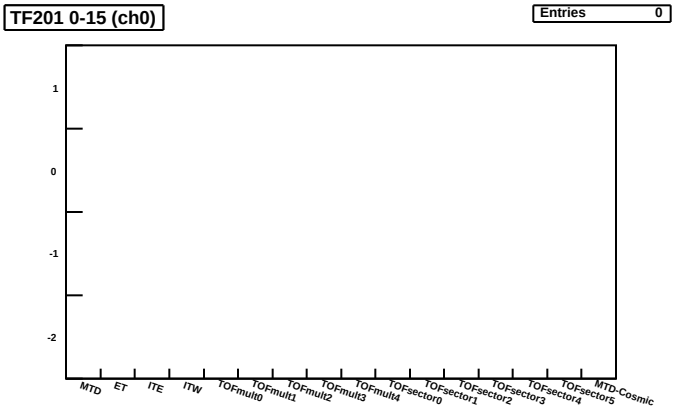

Input to FE003 QT board
Entries 1.910662e+07
ADC
channel


Input to FE004 QT board
Entries 1.910662e+07
ADC
channel

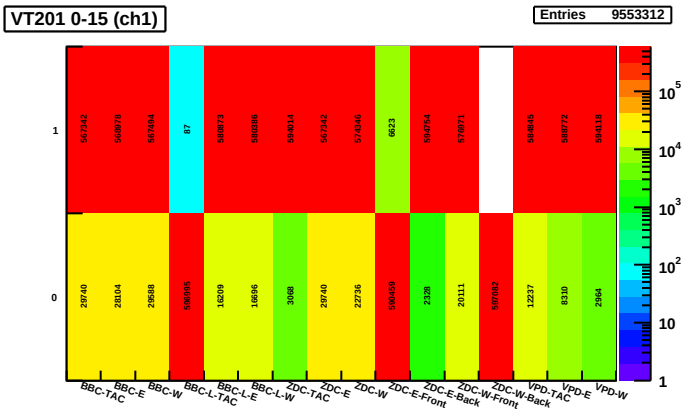
TF201 0-15 (ch0)



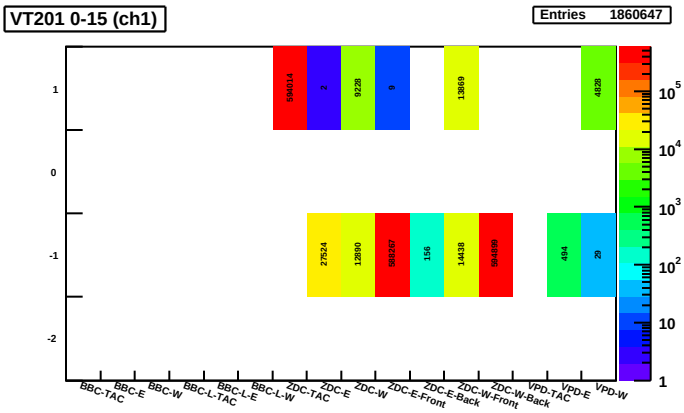
TF201 0-15 (ch0)



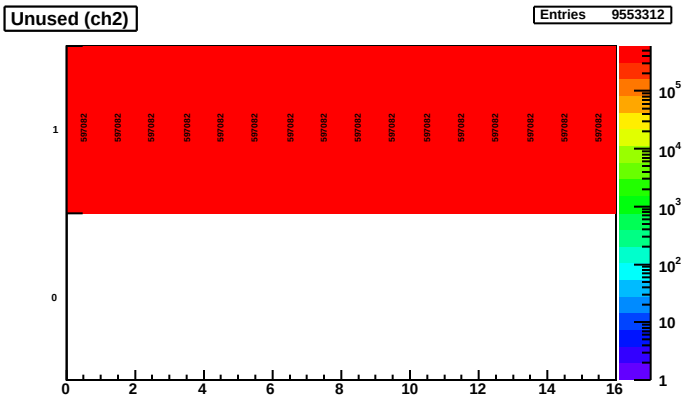
VT201 0-15 (ch1)

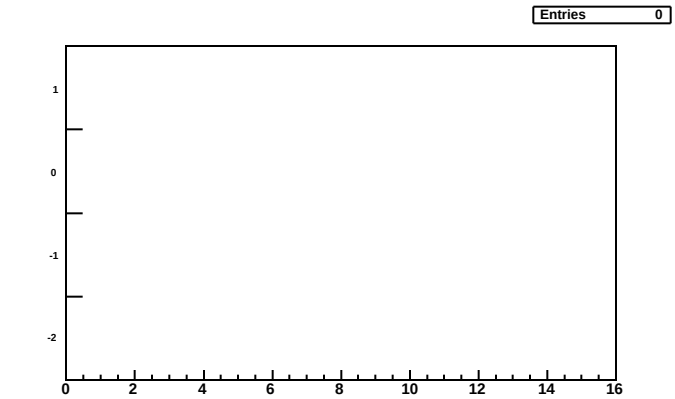


VT201 0-15 (ch1)

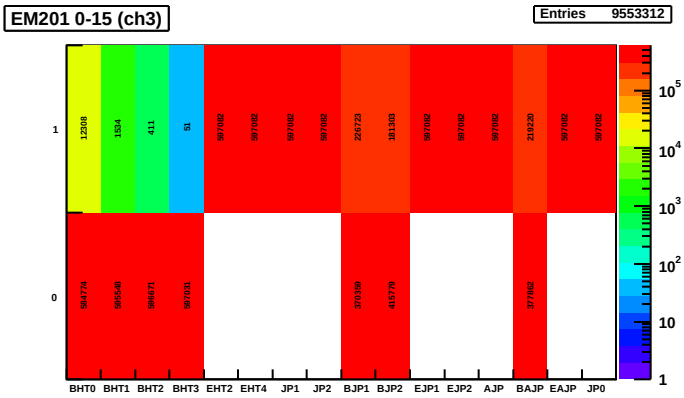


Unused (ch2)

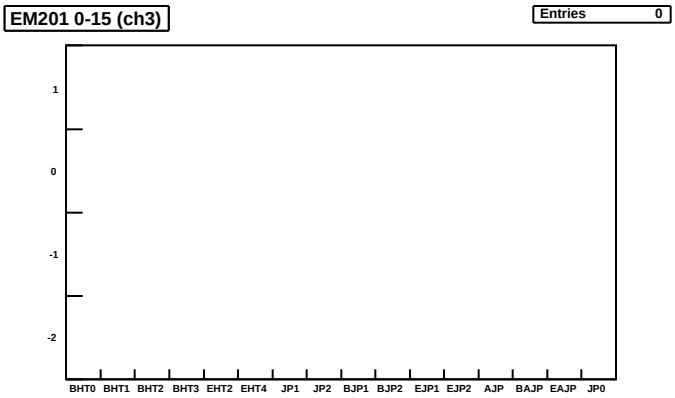




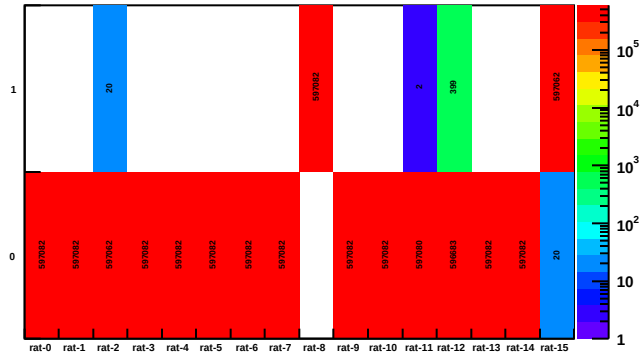
EM201 0-15 (ch3)

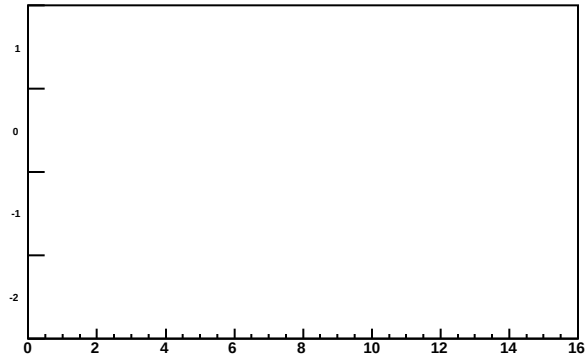


EM201 0-15 (ch3)

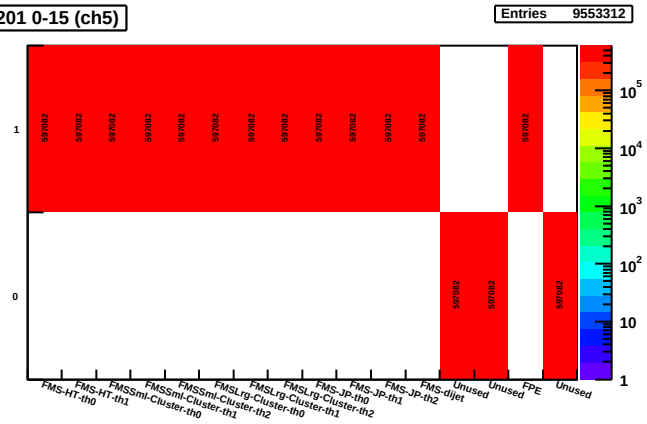


RAT board (ch4)

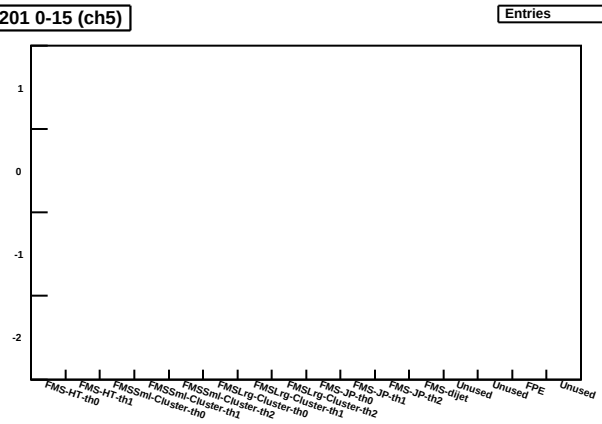




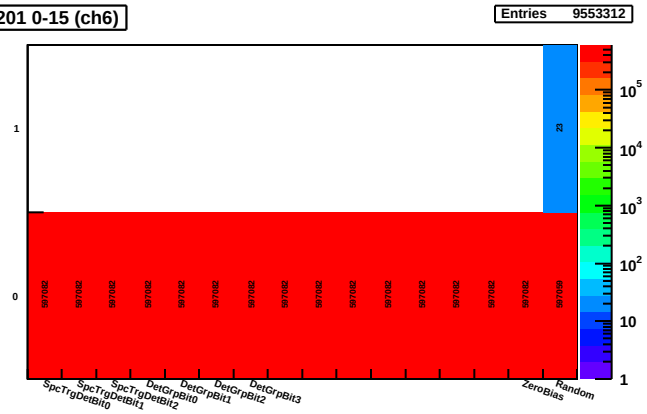
FP201 0-15 (ch5)

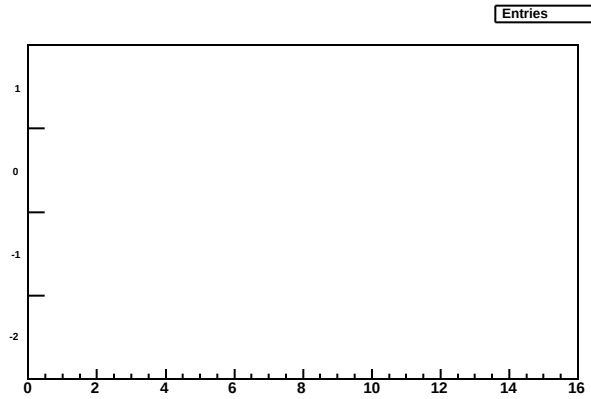
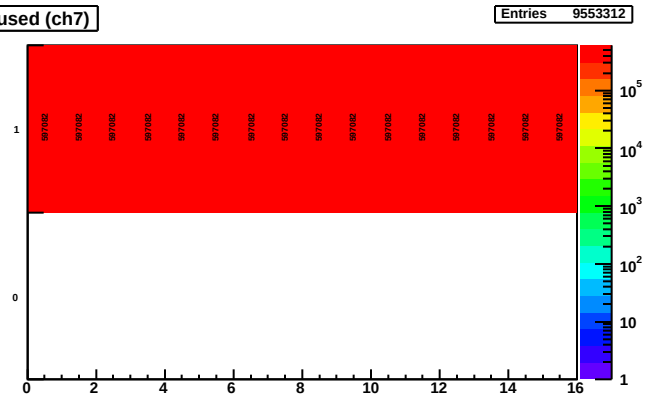
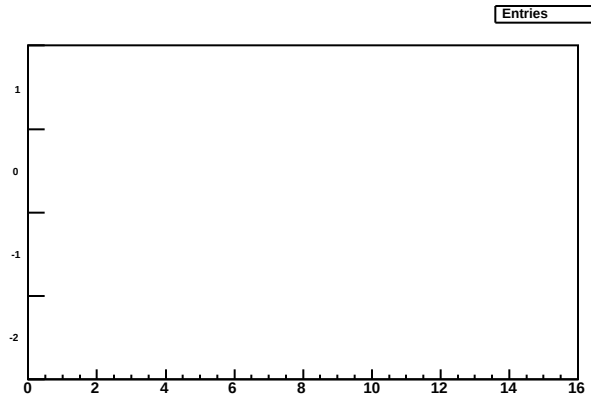


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)