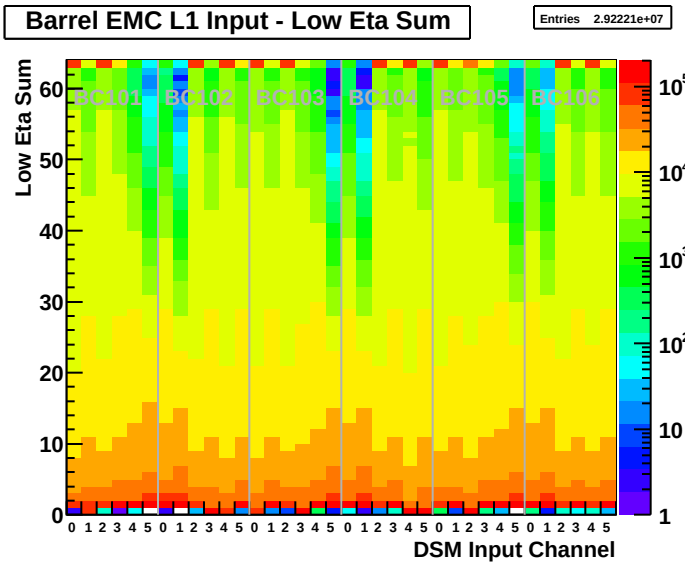
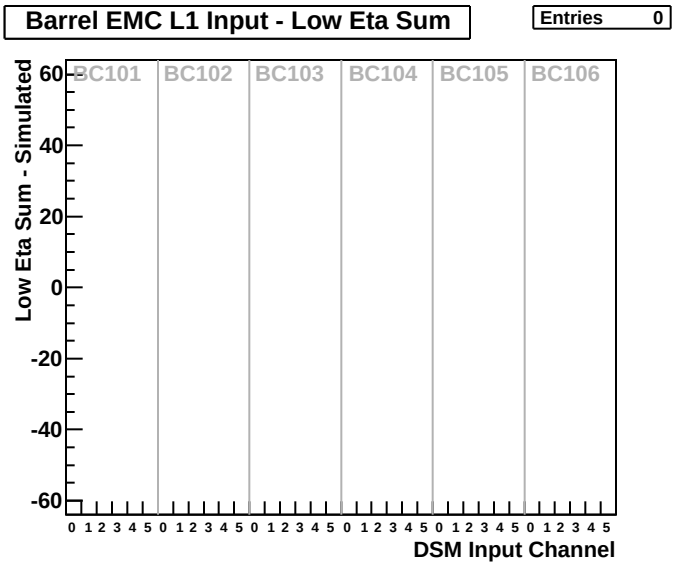
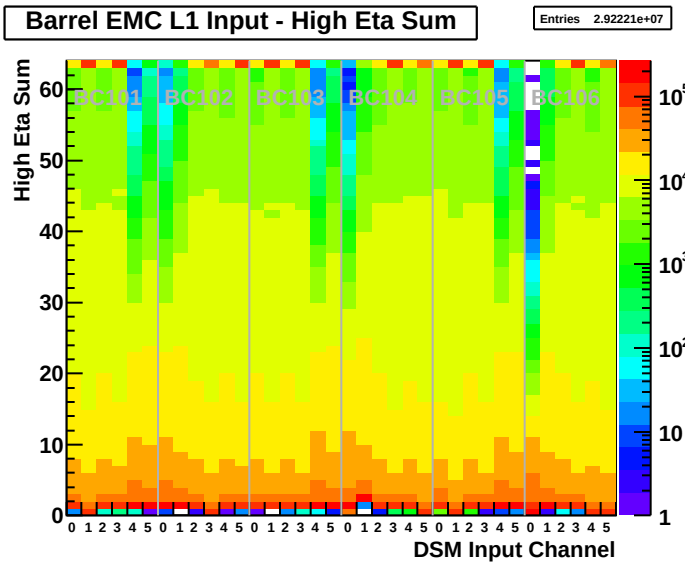
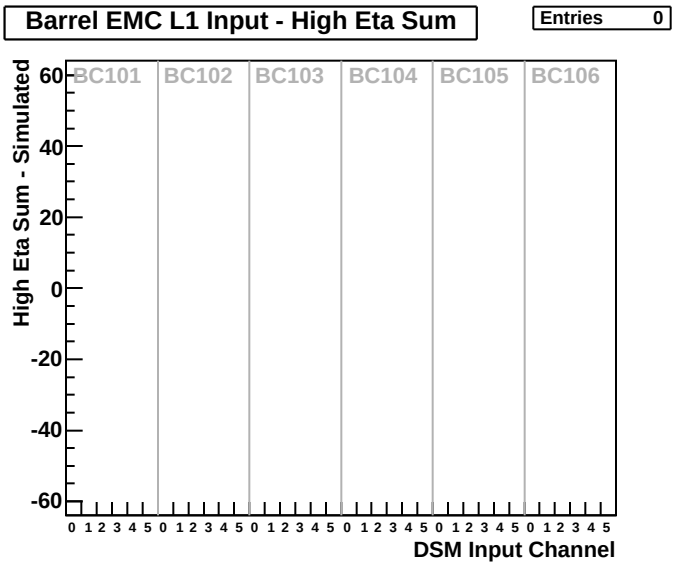
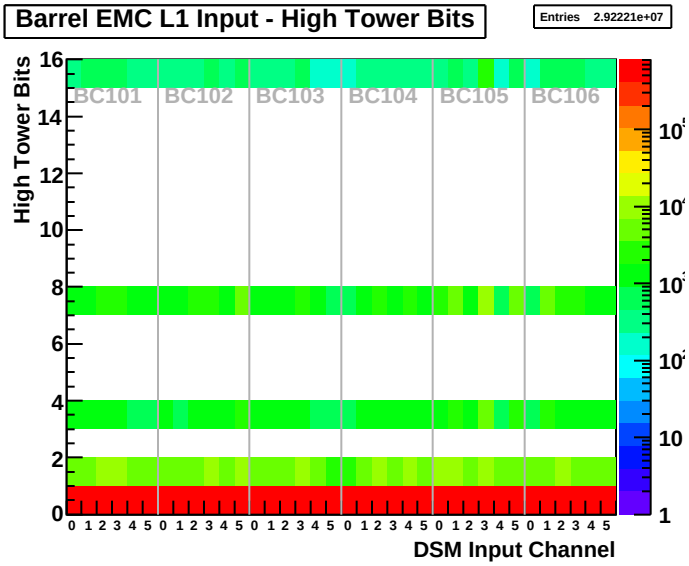
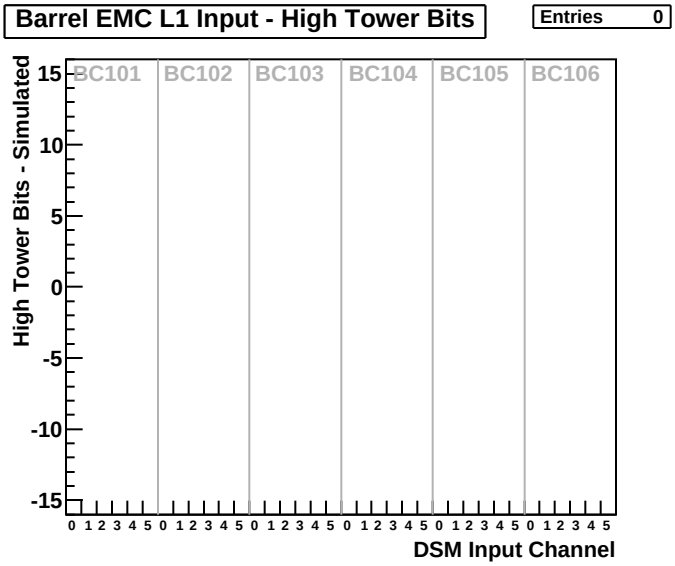

Barrel EMC L1 Input - Low Eta Sum
Entries 2.92221e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

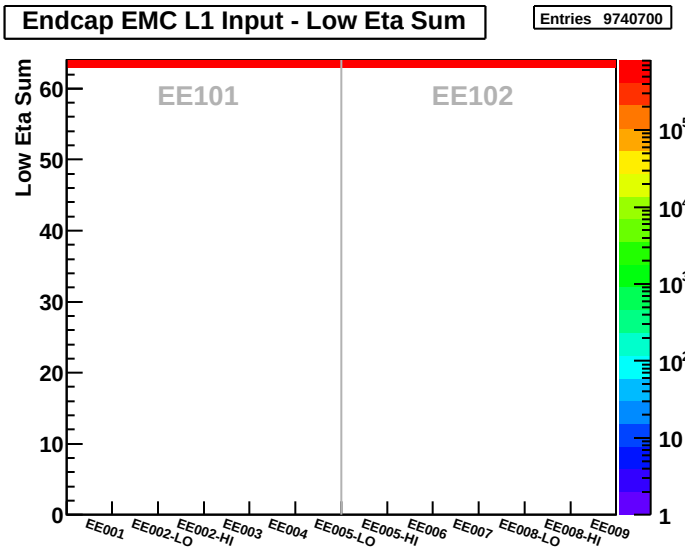

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

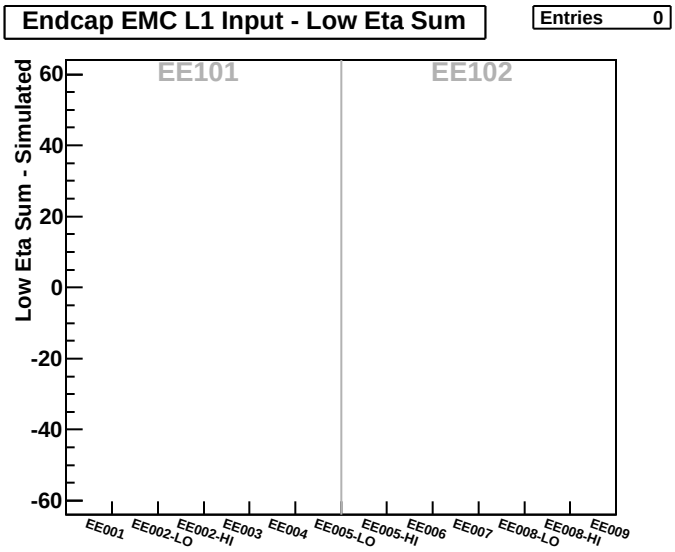

Barrel EMC L1 Input - High Eta Sum
Entries 2.92221e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

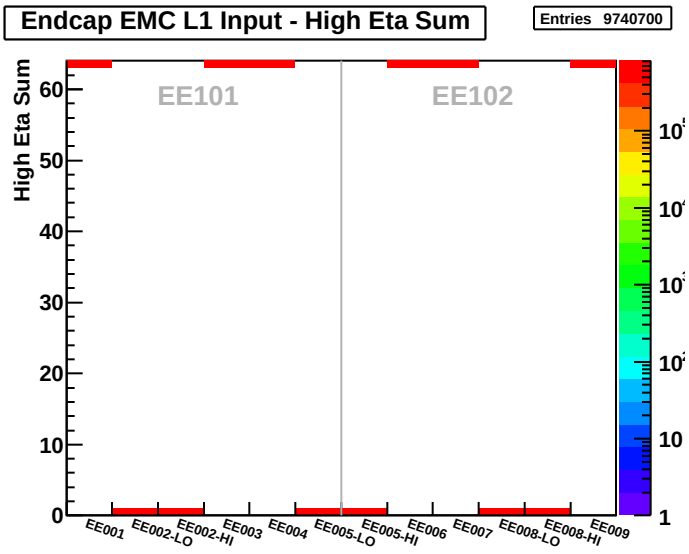

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

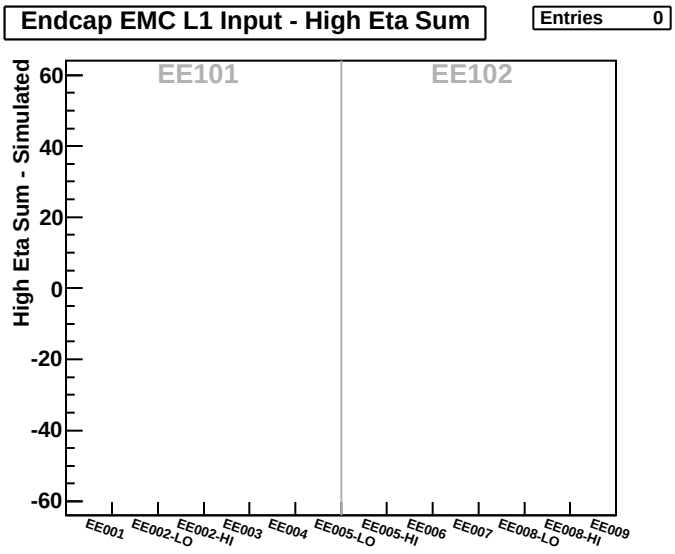

Barrel EMC L1 Input - High Tower Bits
Entries 2.92221e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

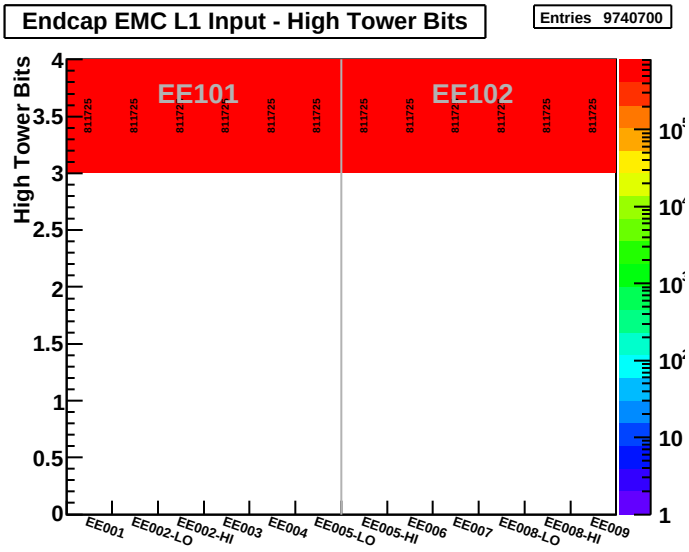

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

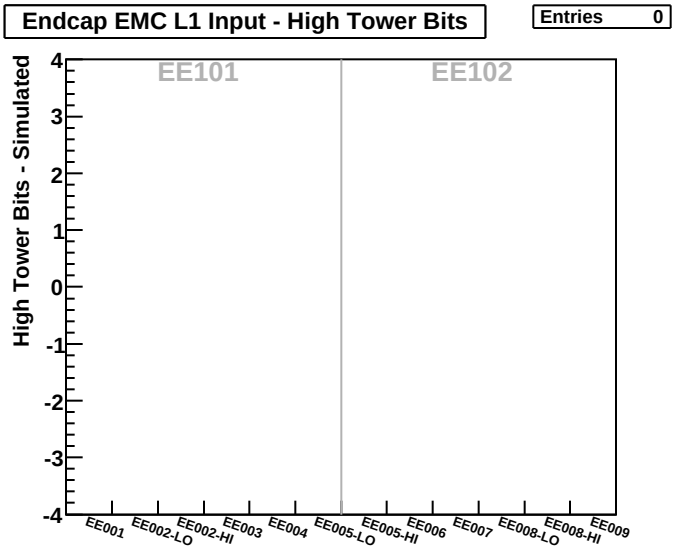

Endcap EMC L1 Input - Low Eta Sum
Entries 9740700
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

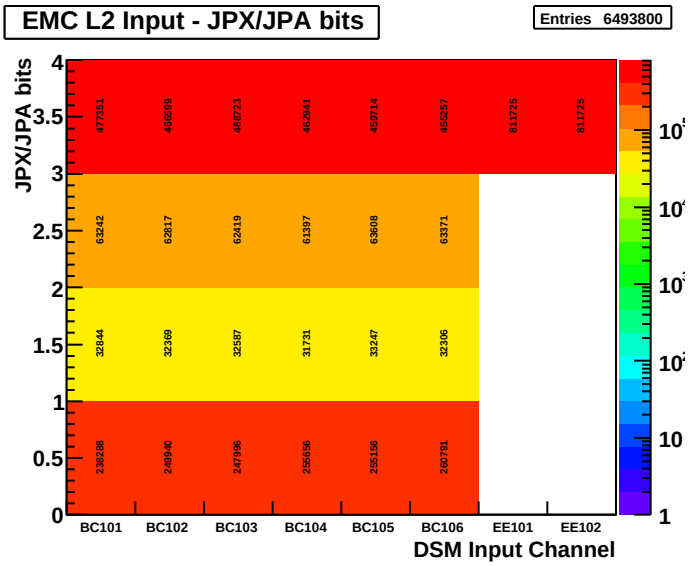

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

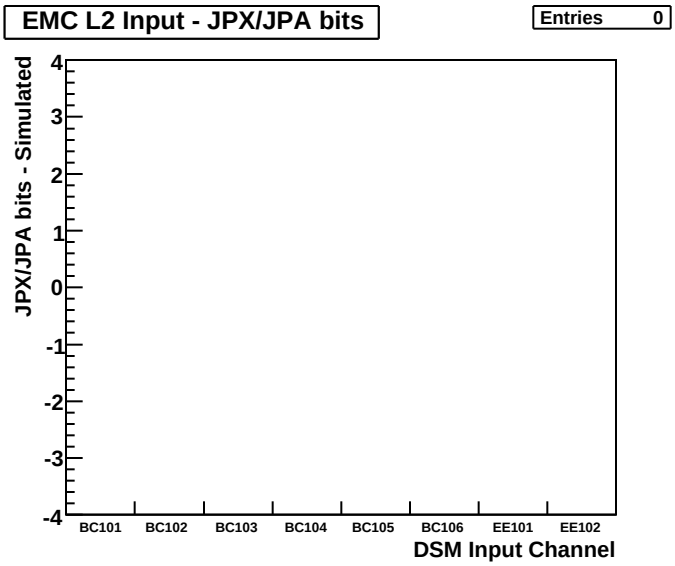

Endcap EMC L1 Input - High Eta Sum
Entries 9740700
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

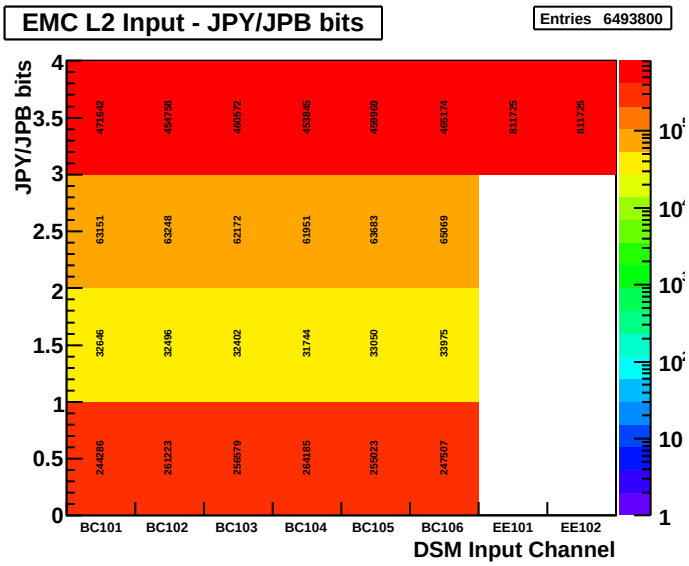

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

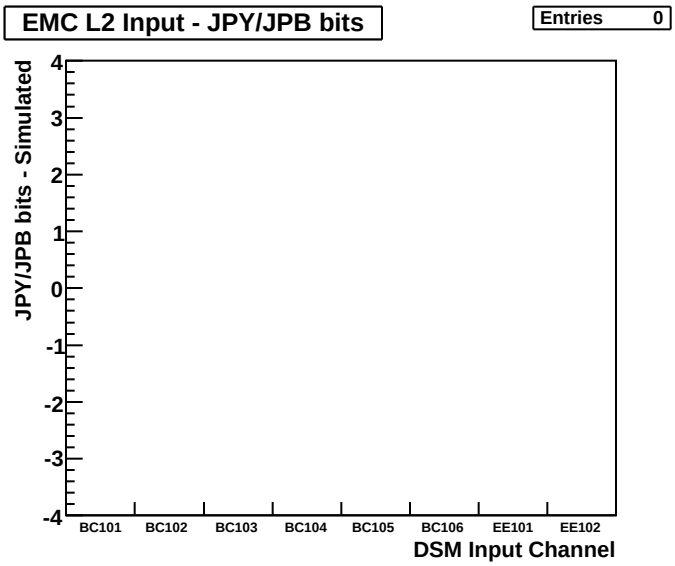

Endcap EMC L1 Input - High Tower Bits
Entries 9740700
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

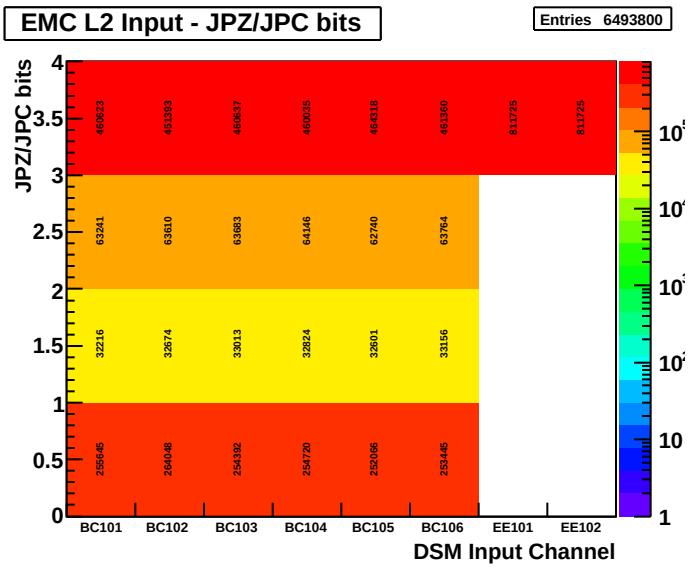

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

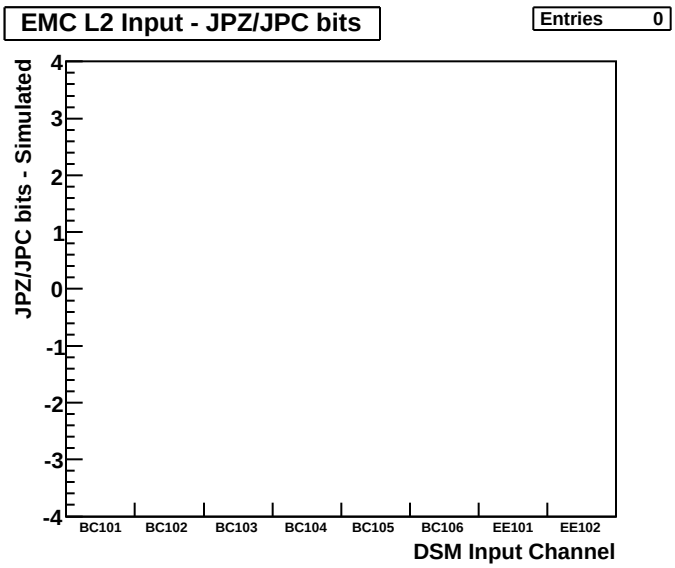

EMC L2 Input - JPX/JPA bits
Entries 6493800
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
477351 466699 458723 452841 459714 455257 811725 811725
63242 62817 62419 61397 63608 63371
32844 32369 32567 31731 33247 32306
238288 249940 247996 255656 255156 260791

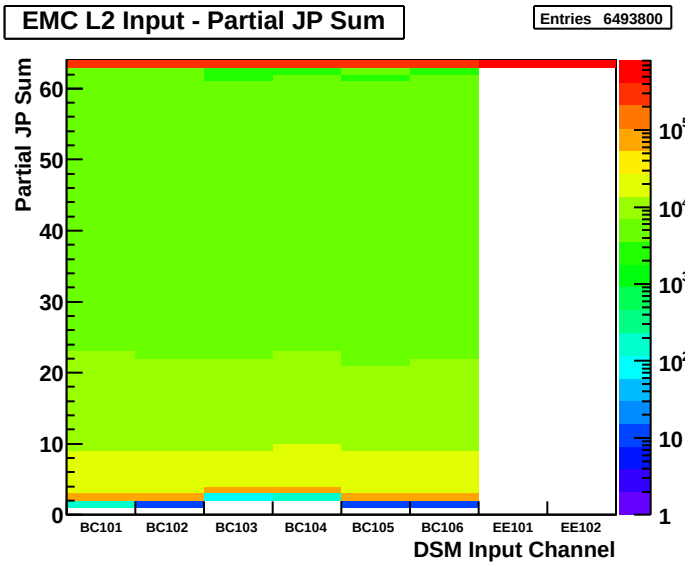

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

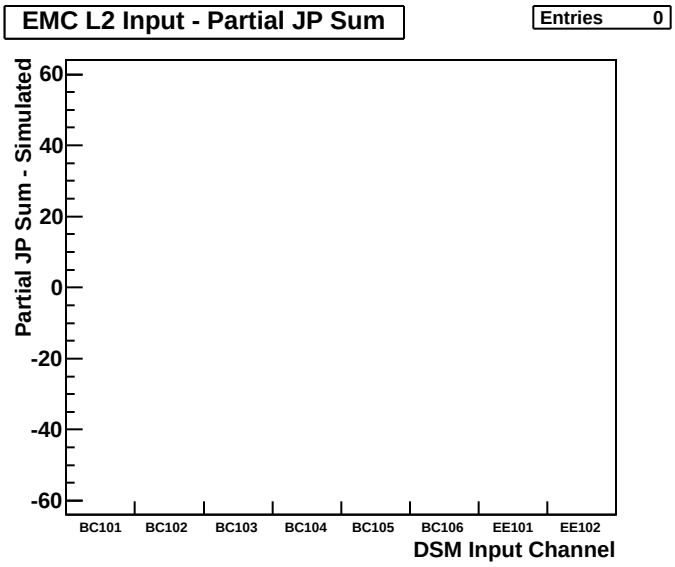

EMC L2 Input - JPY/JPB bits
Entries 6493800
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
471642 454758 460572 453845 459969 465174 811725 811725
63151 63248 62172 61951 63683 65069
32646 32496 32402 31744 33050 33975
244286 261223 256579 264185 255023 247507

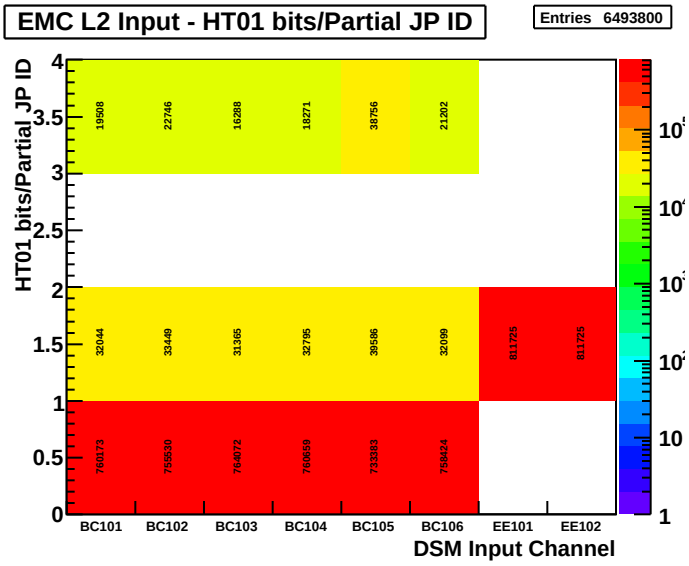

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

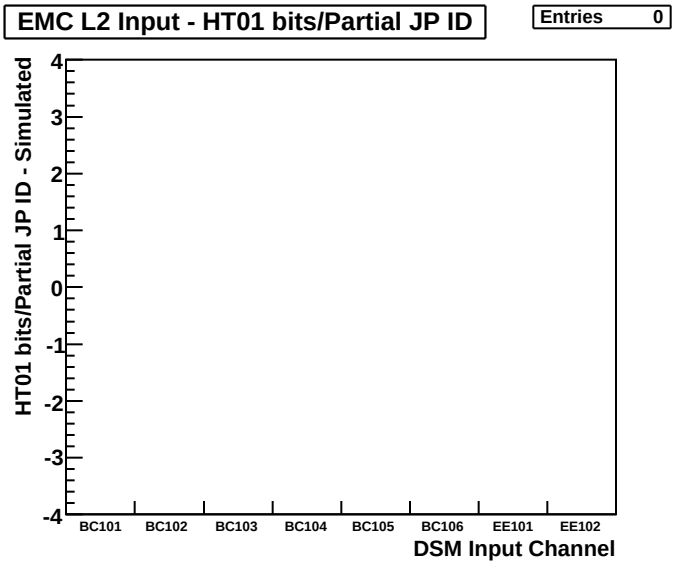

EMC L2 Input - JPZ/JPC bits
Entries 6493800
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
460623 451393 460637 460035 464318 461360 811725 811725
63241 63610 63683 64146 62740 63764
32216 32674 33013 32824 32601 33156
255645 264048 254392 254720 252066 253445

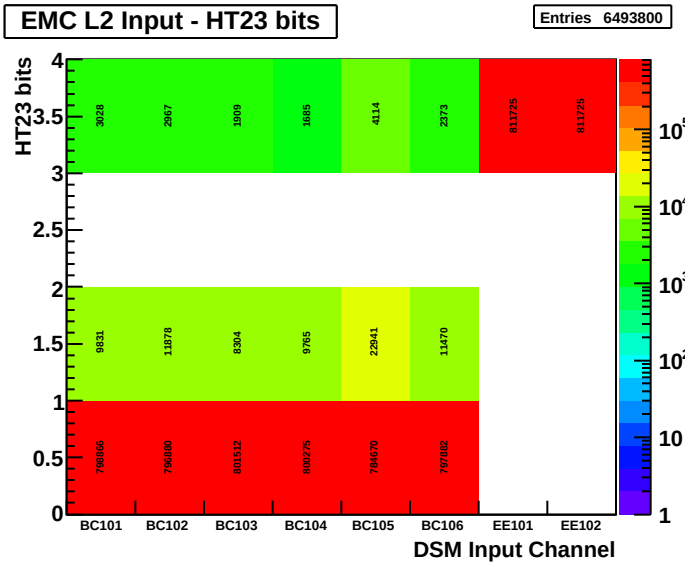

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

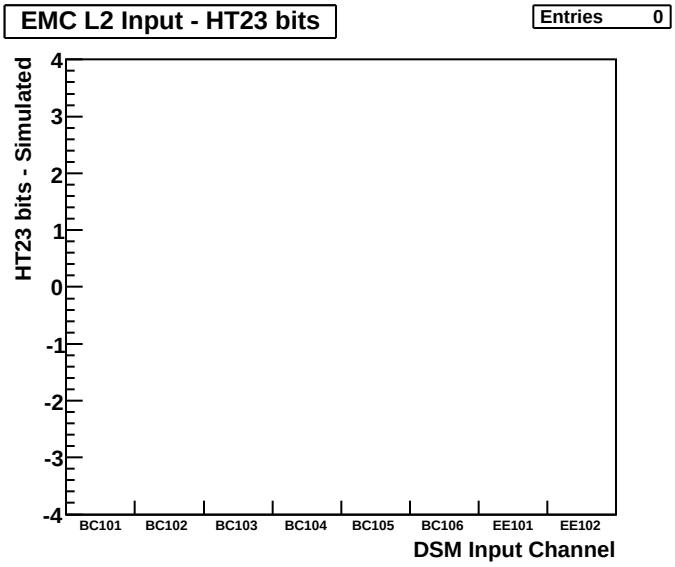

EMC L2 Input - Partial JP Sum
Entries 6493800
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

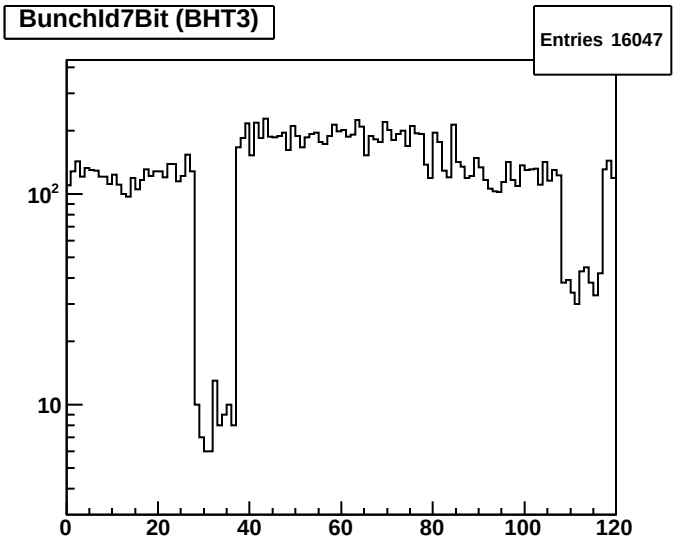

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

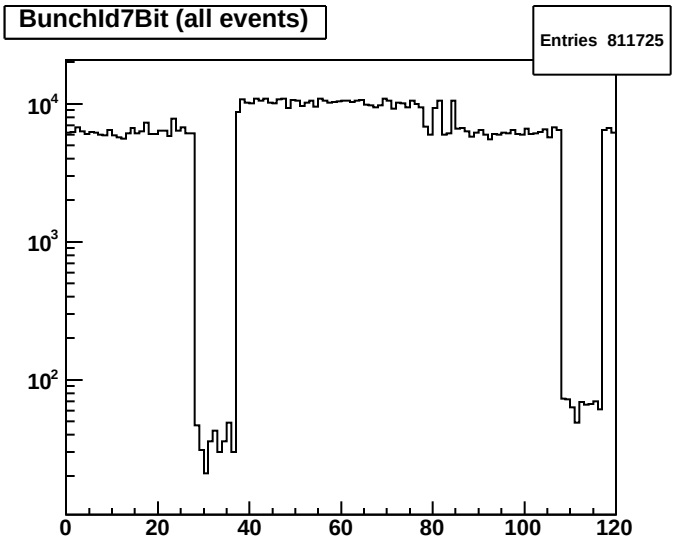

EMC L2 Input - HT01 bits/Partial JP ID
Entries 6493800
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

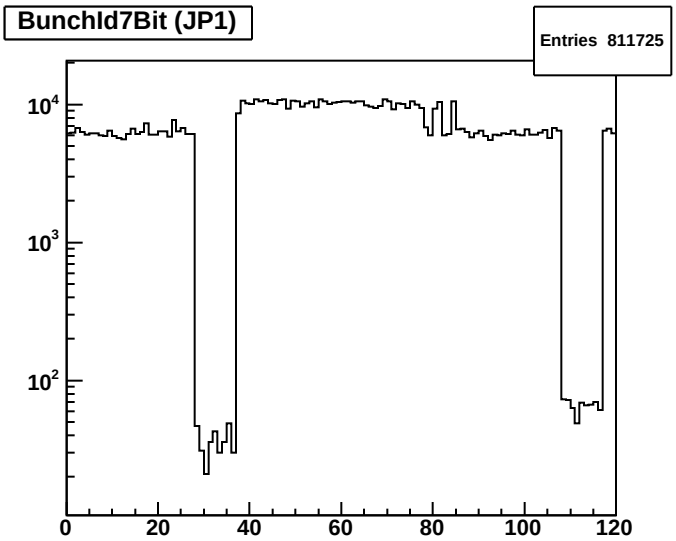

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

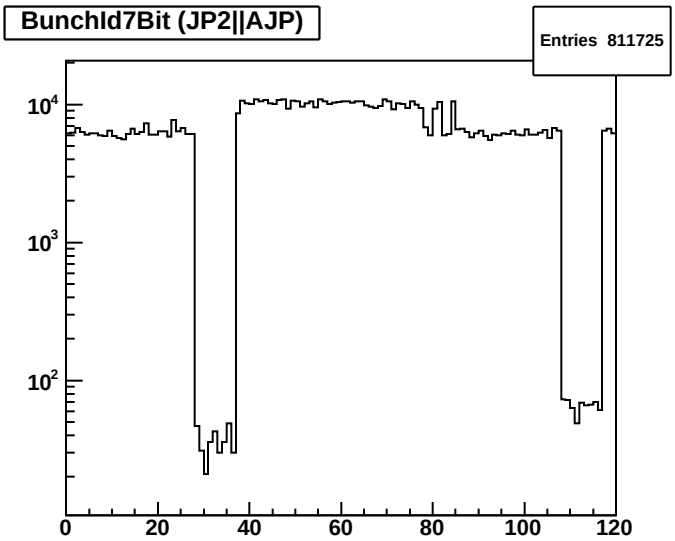

EMC L2 Input - HT23 bits
Entries 6493800
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

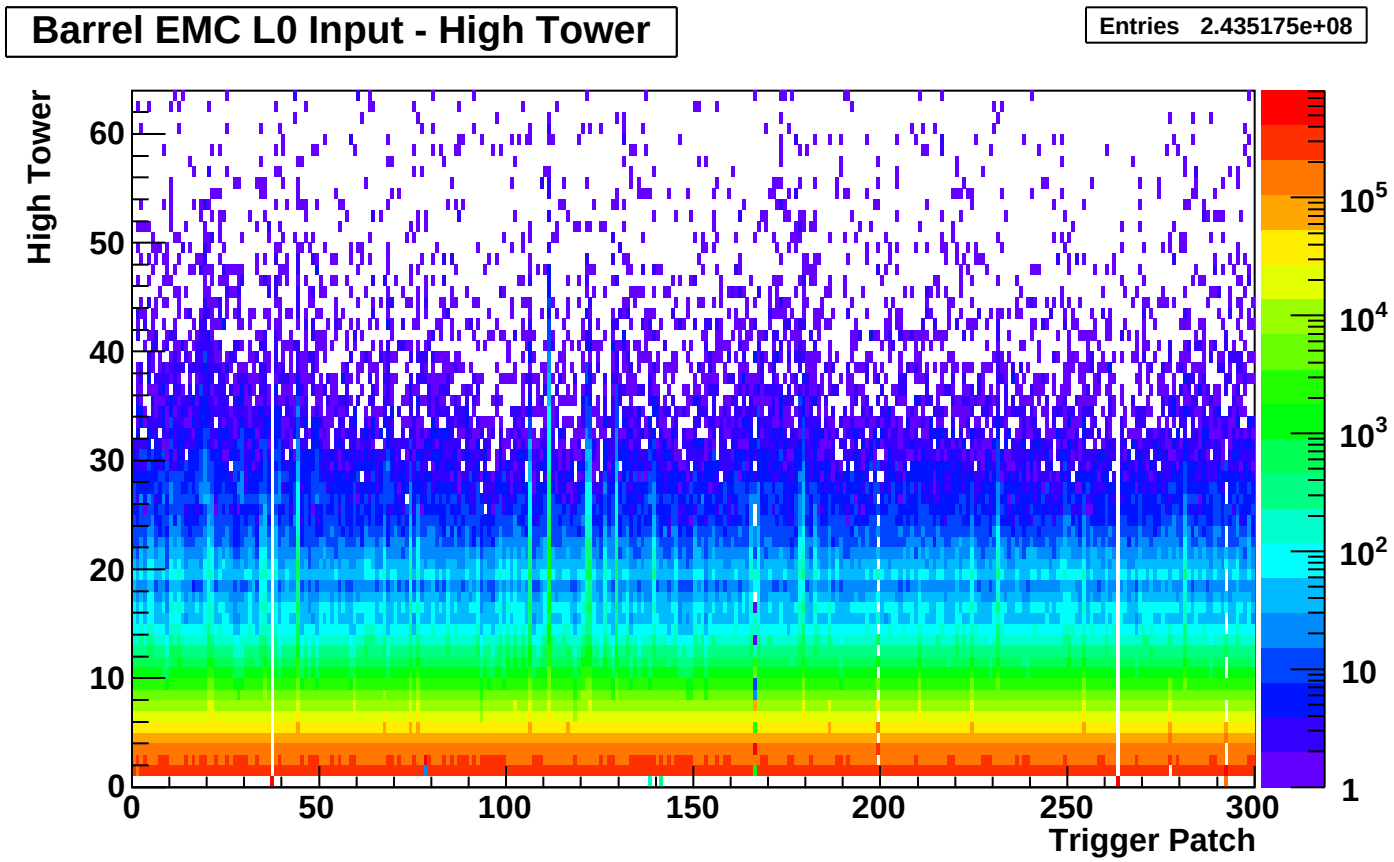

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

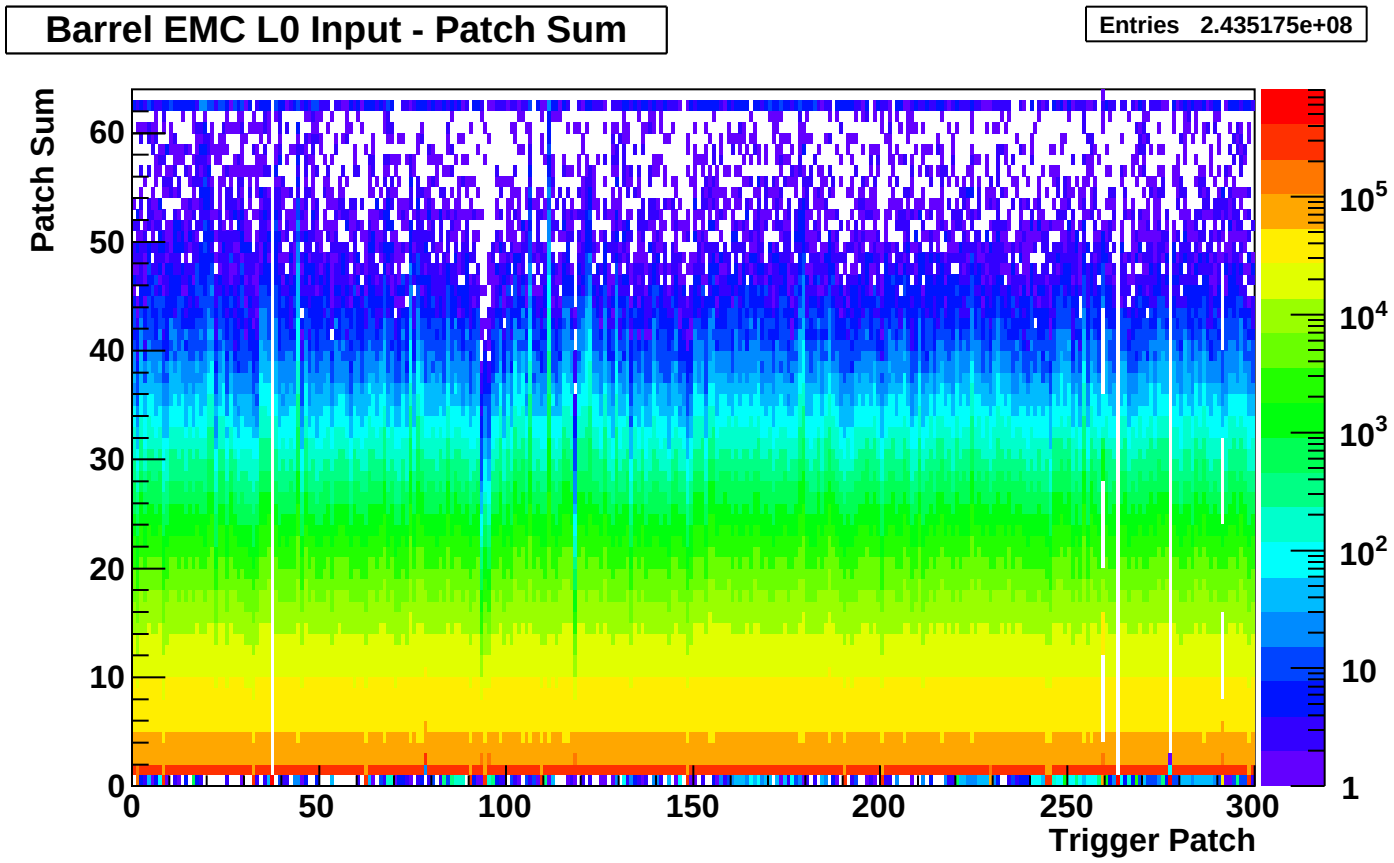

BunchId7Bit (BHT3)
Entries 16047
10^2
10
0
20
40
60
80
100
120

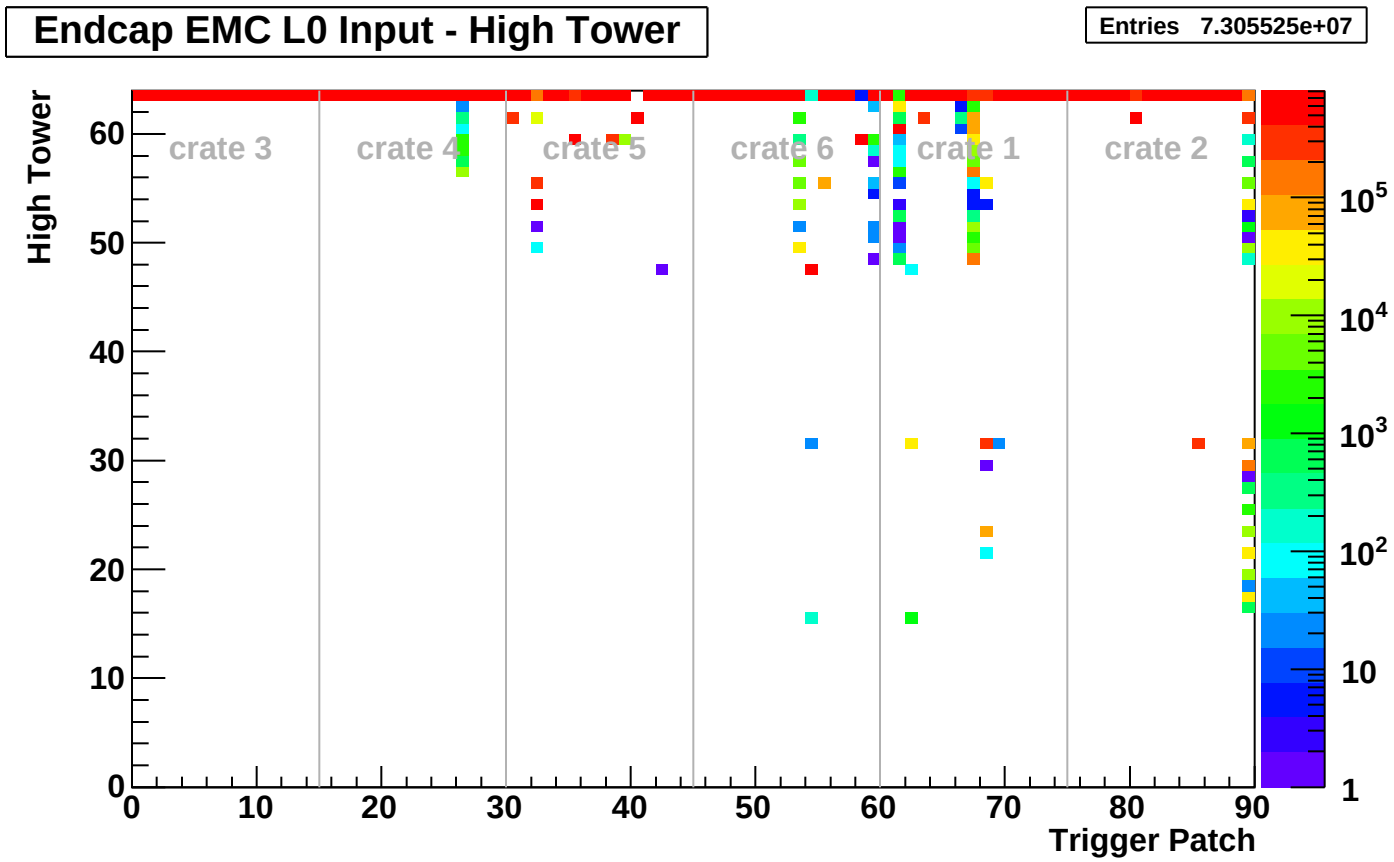

BunchId7Bit (all events)
Entries 811725
10^4
10^3
10^2
0
20
40
60
80
100
120

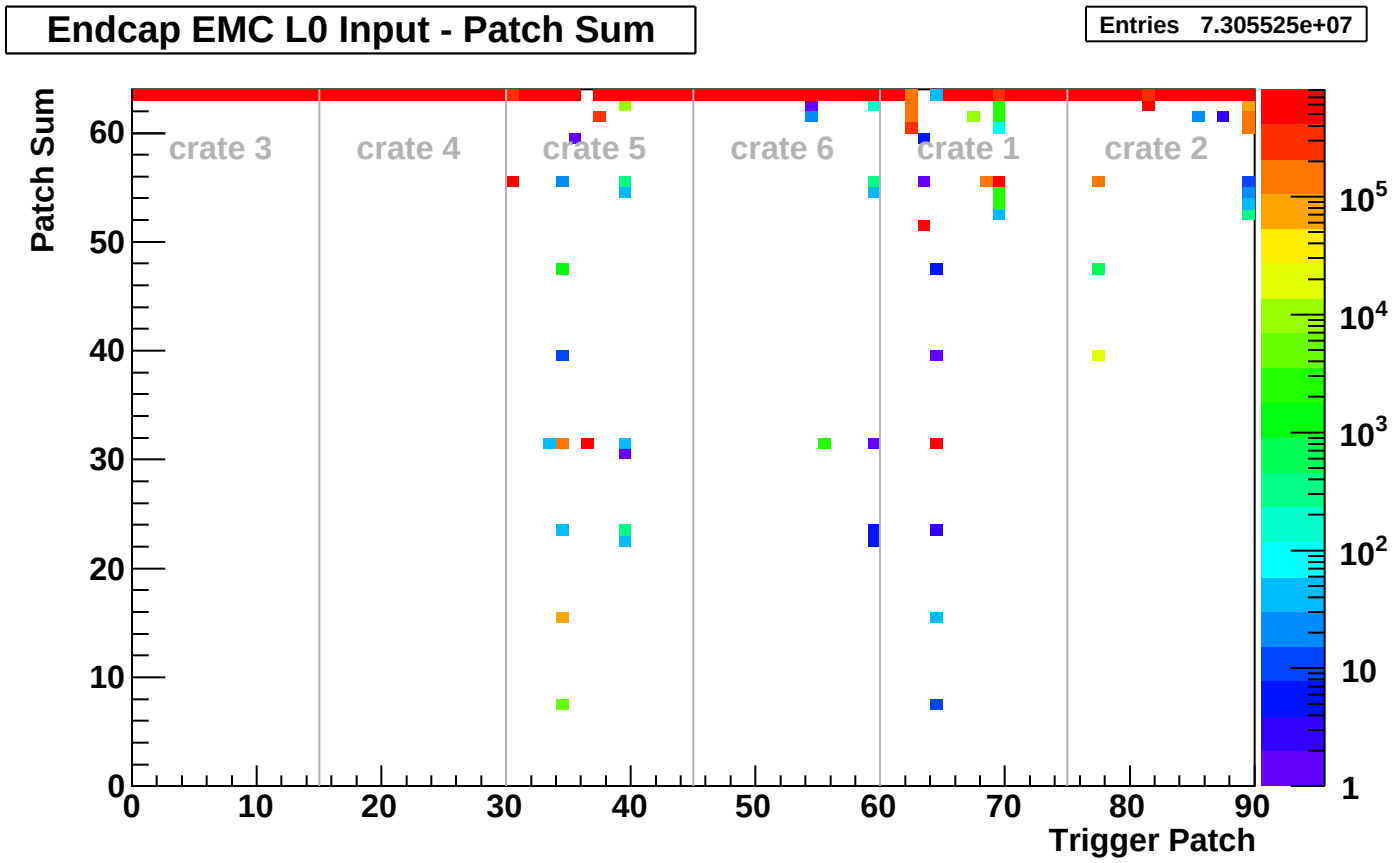

BunchId7Bit (JP1)
Entries 811725
10^4
10^3
10^2
0
20
40
60
80
100
120

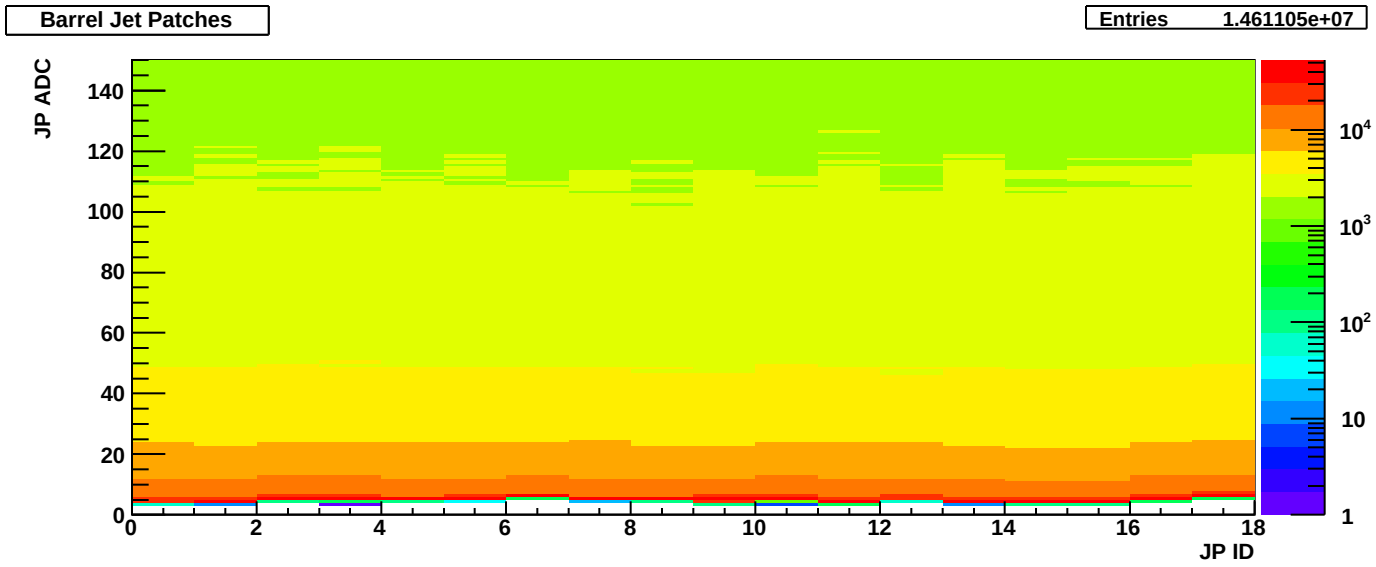

BunchId7Bit (JP2||AJP)
Entries 811725
10^4
10^3
10^2
0
20
40
60
80
100
120

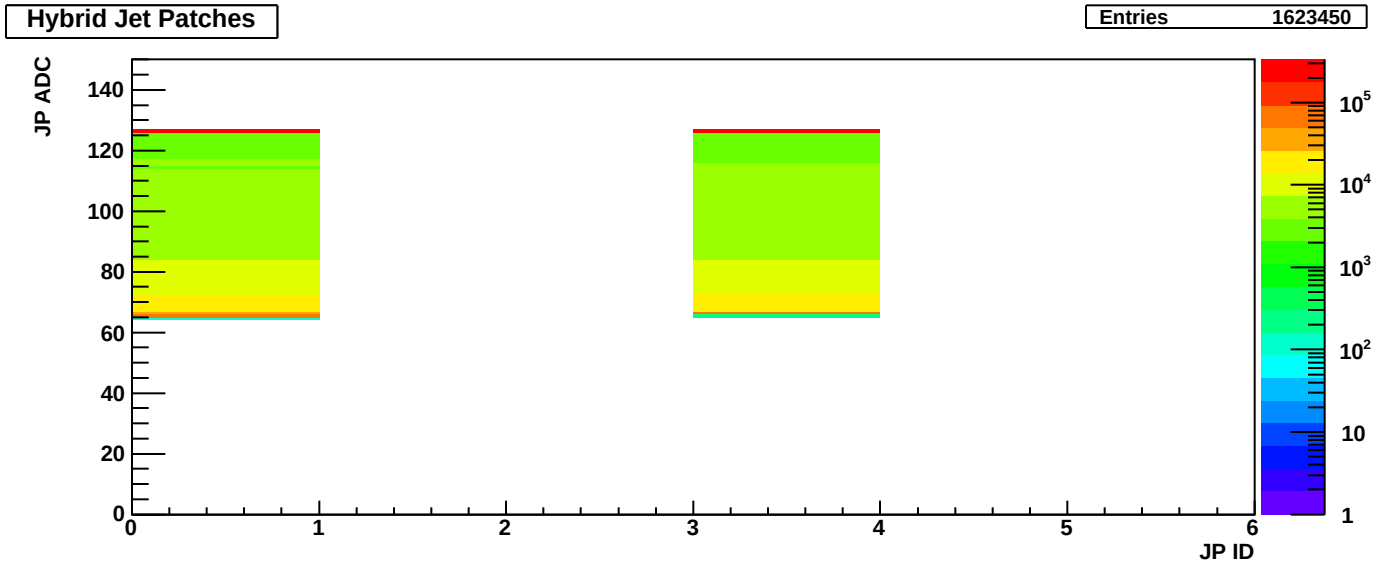

Barrel EMC L0 Input - High Tower
Entries 2.435175e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

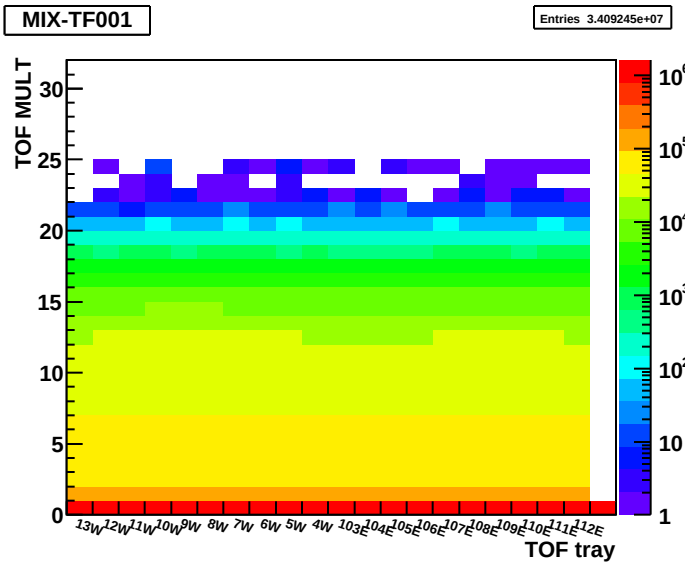

Barrel EMC L0 Input - Patch Sum
Entries 2.435175e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

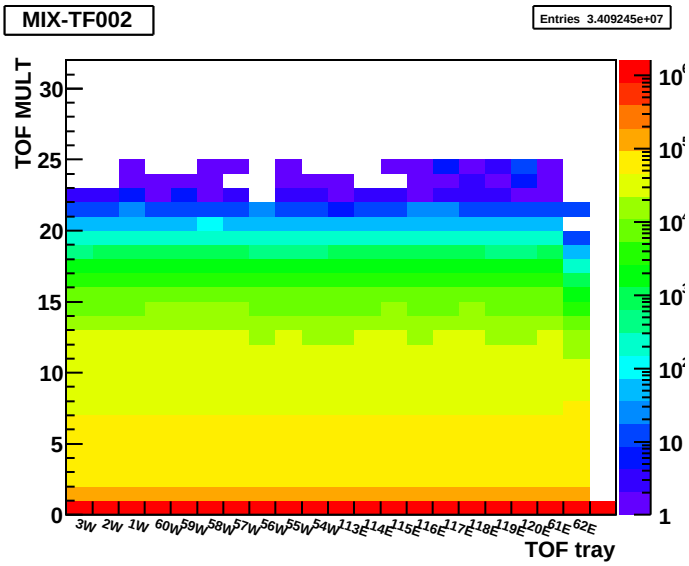

Endcap EMC L0 Input - High Tower
Entries 7.305525e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

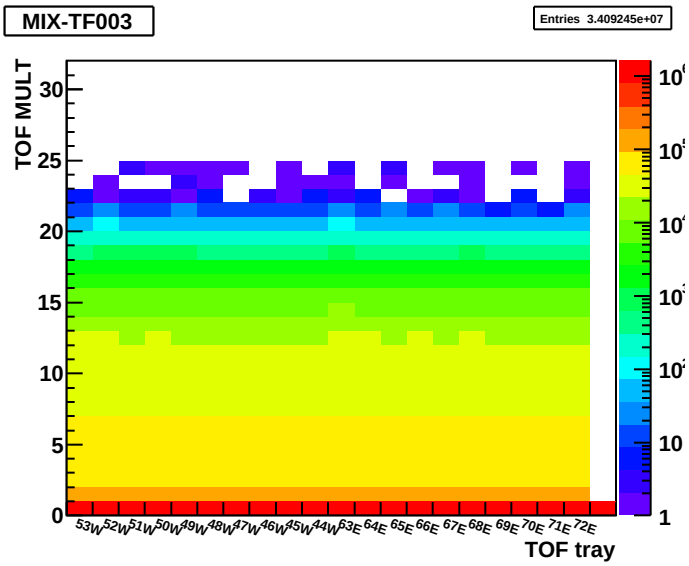

Endcap EMC L0 Input - Patch Sum
Entries 7.305525e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

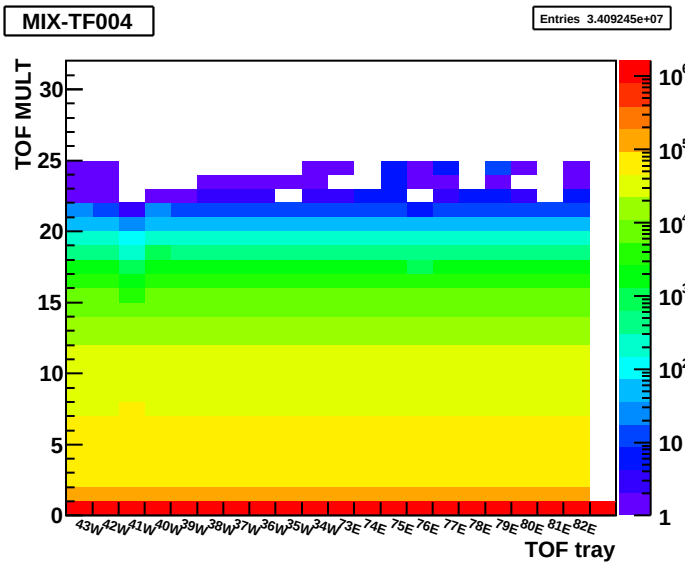

Barrel Jet Patches
Entries 1.461105e+07
JP ADC
JP ID

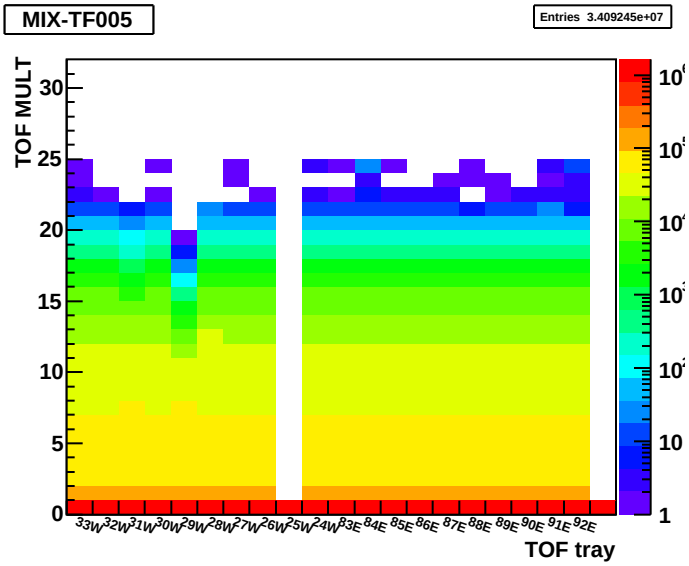

Hybrid Jet Patches
Entries 1623450
JP ADC
JP ID

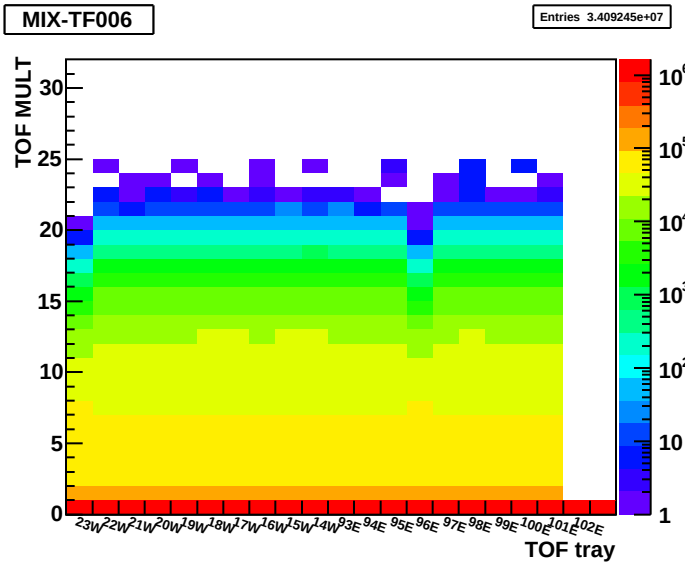

MIX-TF001
Entries 3.409245e+07
TOF MULT
TOF tray

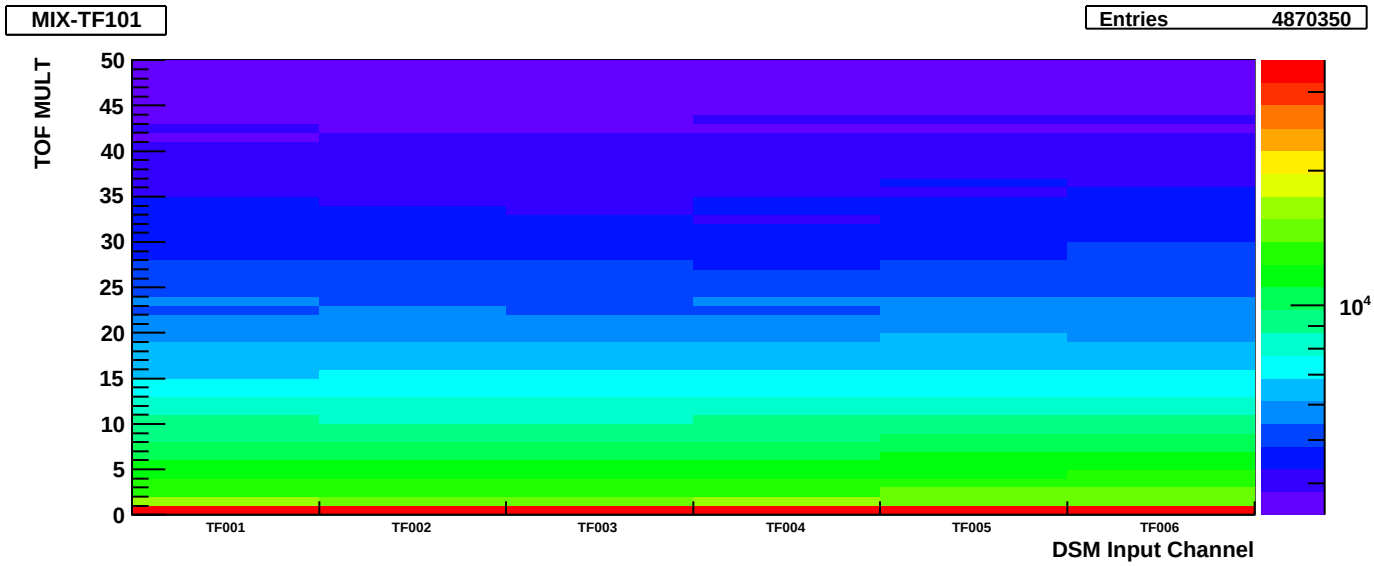

MIX-TF002
Entries 3.409245e+07
TOF MULT
TOF tray

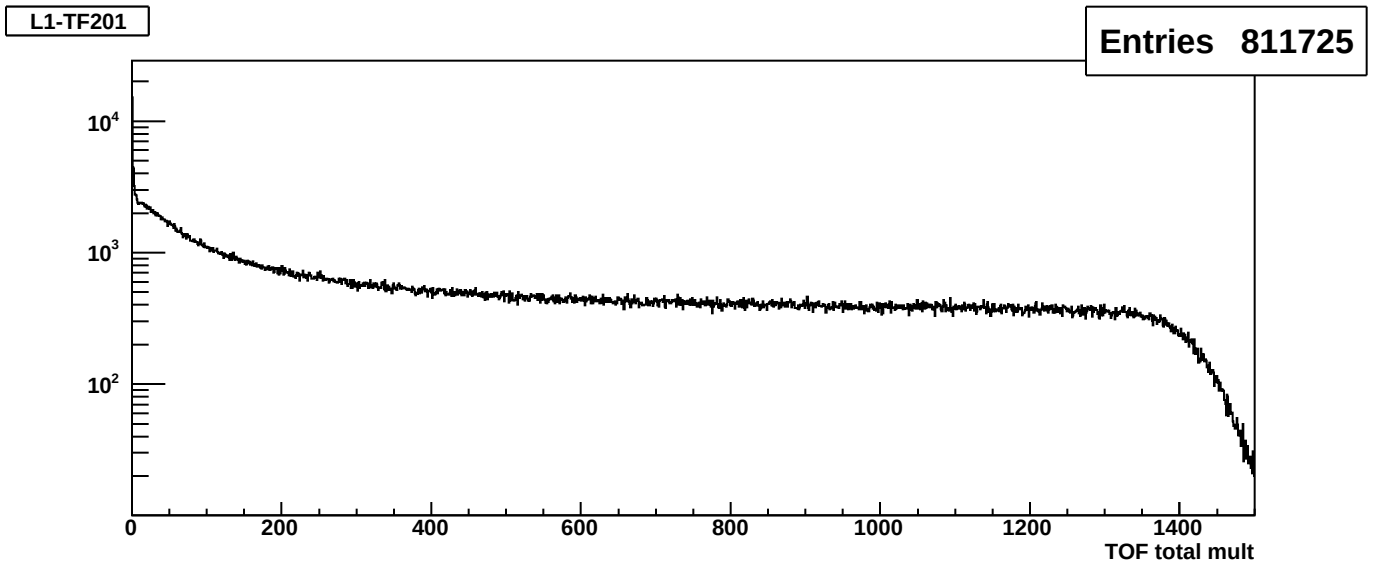

MIX-TF003
Entries 3.409245e+07
TOF MULT
TOF tray

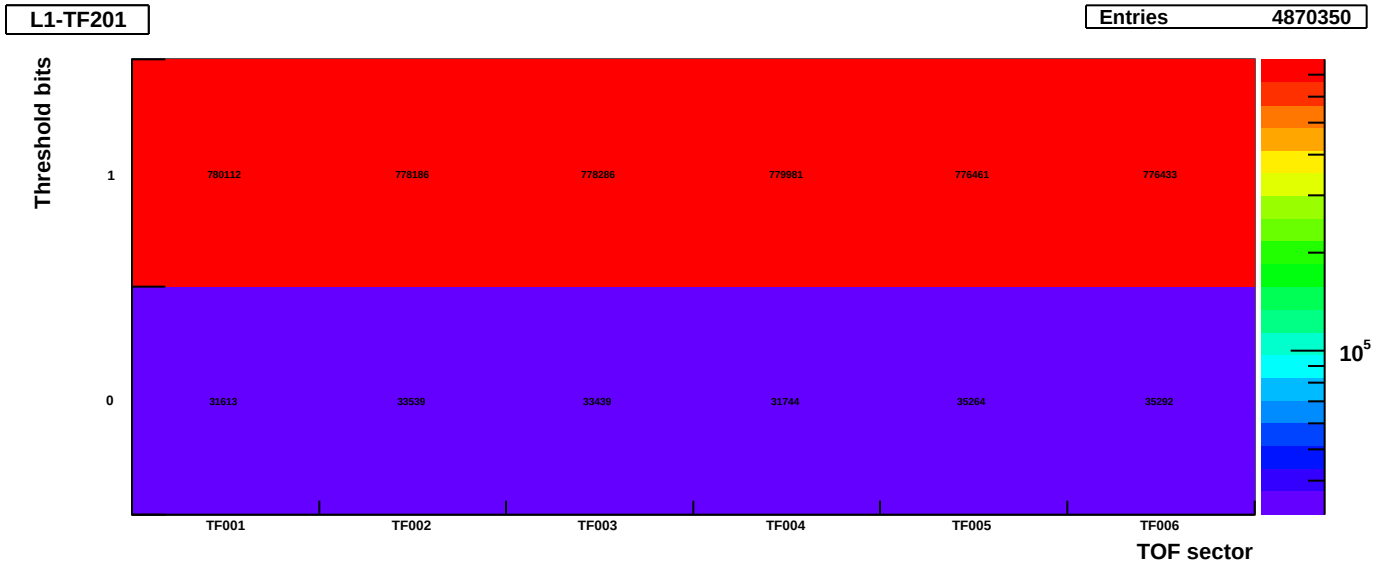

MIX-TF004
Entries 3.409245e+07
TOF MULT
TOF tray

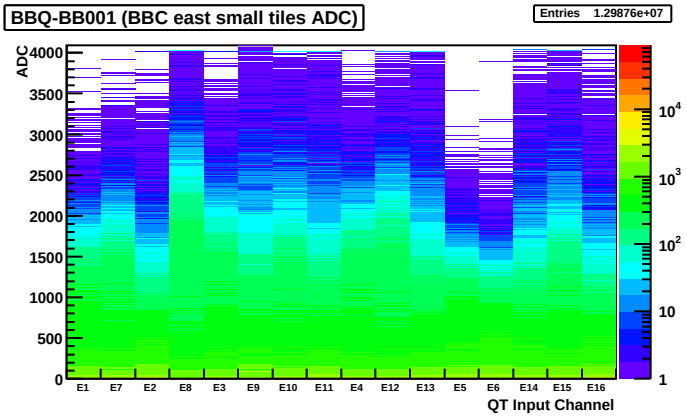

MIX-TF005
Entries 3.409245e+07
TOF MULT
TOF tray

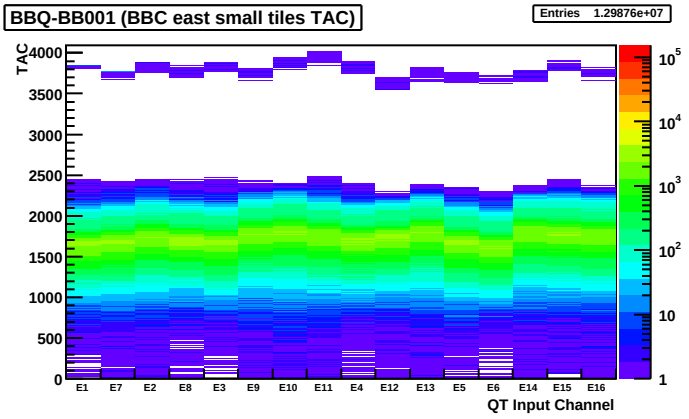

MIX-TF006
Entries 3.409245e+07
TOF MULT
TOF tray

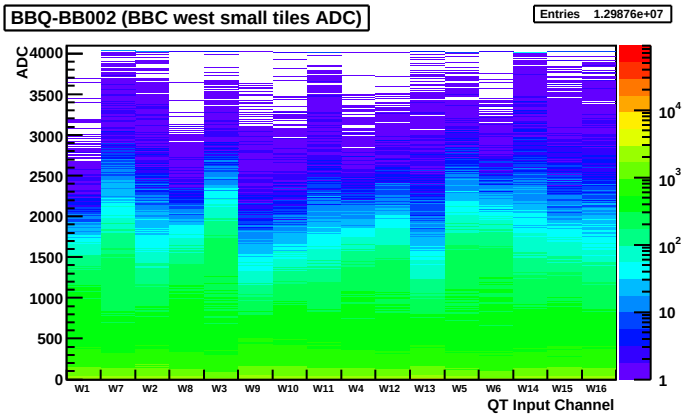

MIX-TF101
Entries 4870350
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

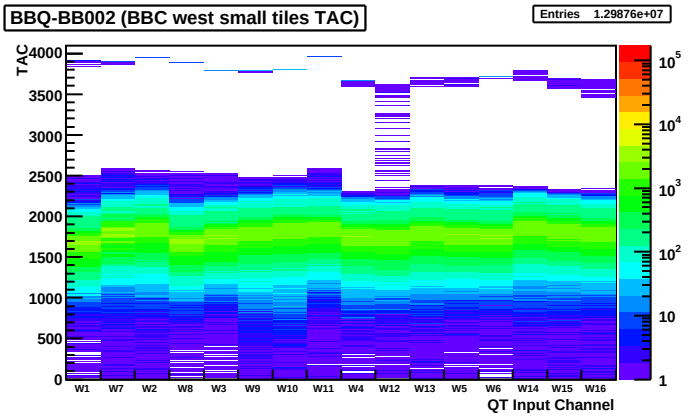

L1-TF201
Entries 811725
10^4
10^3
10^2
0
200
400
600
800
1000
1200
1400
TOF total mult

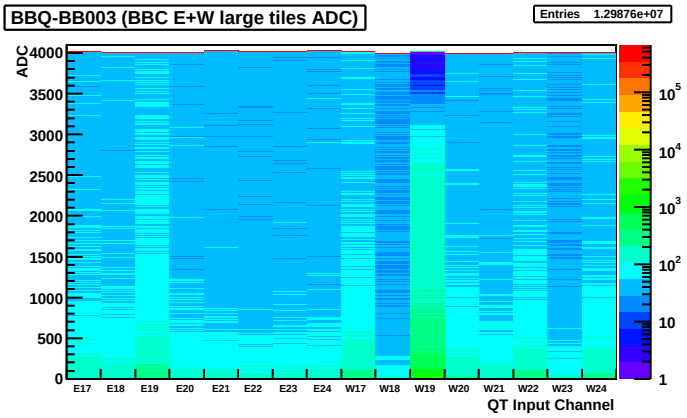

L1-TF201
Entries 4870350
Threshold bits
1
0
780112
778186
778286
779981
776461
776433
31613
33539
33439
31744
35264
35292
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

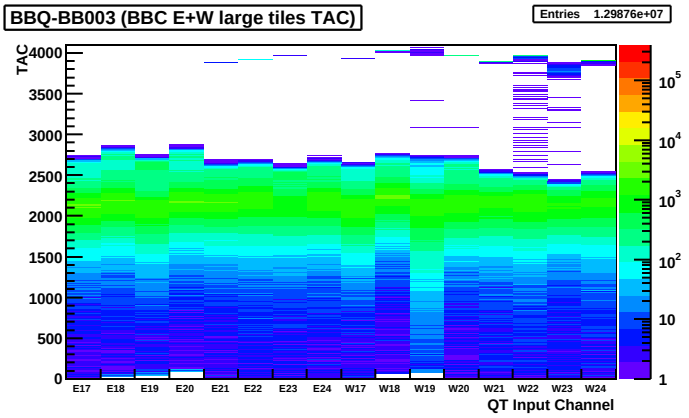

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.29876e+07
ADC
QT Input Channel

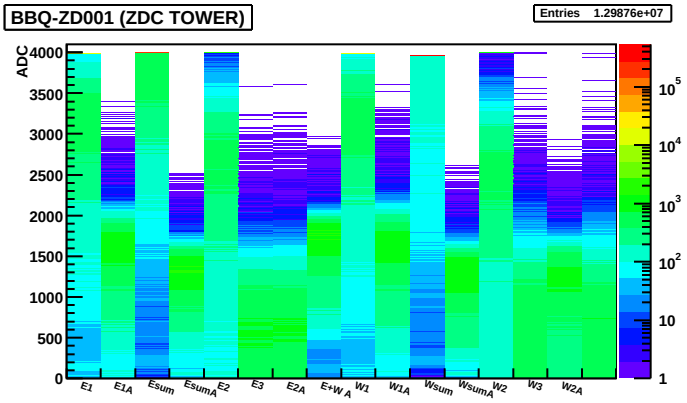

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.29876e+07
TAC
QT Input Channel

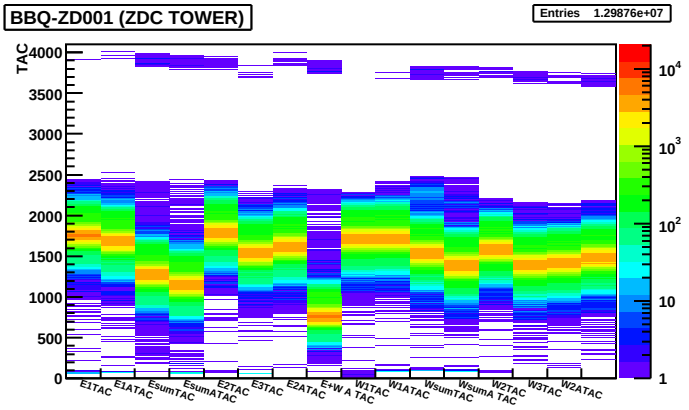

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.29876e+07
ADC
QT Input Channel

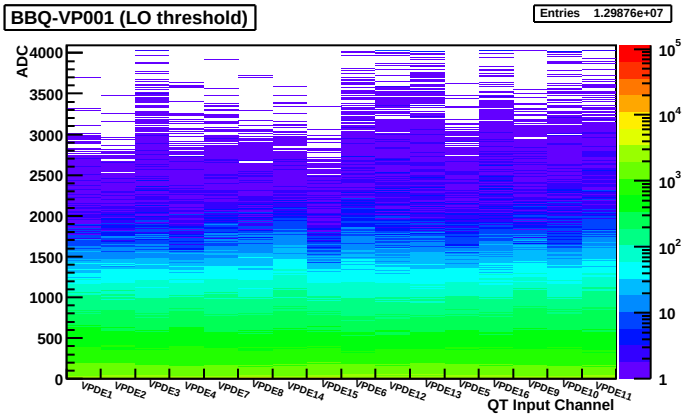

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.29876e+07
TAC
QT Input Channel

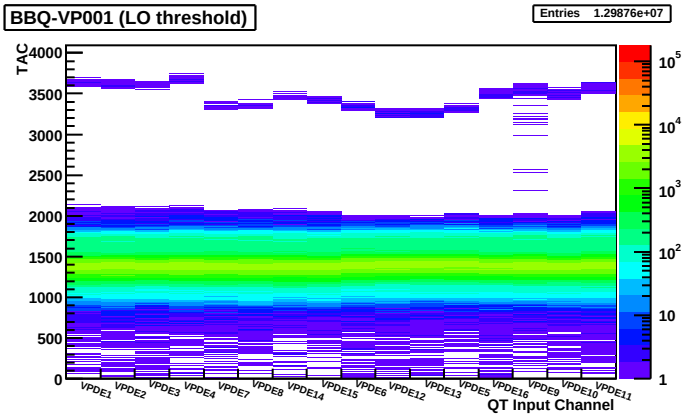

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.29876e+07
ADC
QT Input Channel

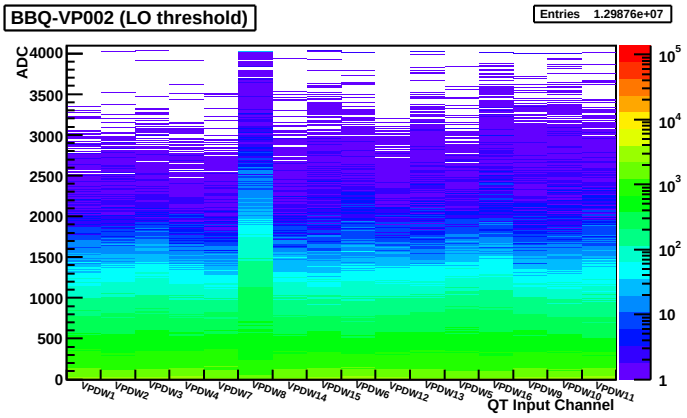

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.29876e+07
TAC
QT Input Channel

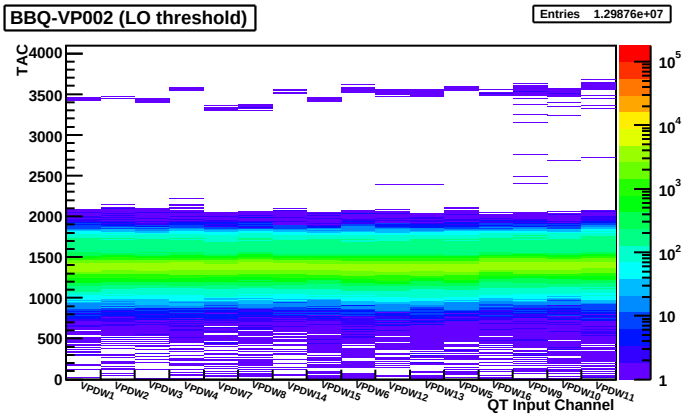

BBQ-ZD001 (ZDC TOWER)
Entries 1.29876e+07
ADC

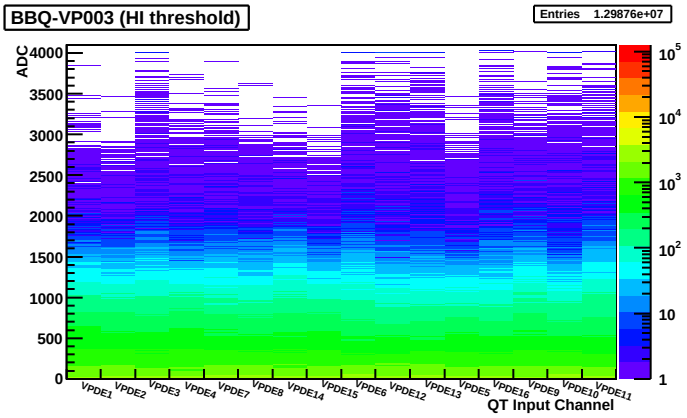

BBQ-ZD001 (ZDC TOWER)
Entries 1.29876e+07
TAC

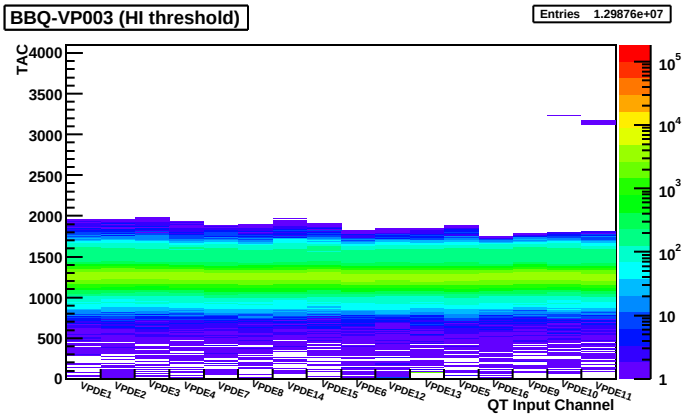

BBQ-VP001 (LO threshold)
Entries 1.29876e+07
ADC
QT Input Channel

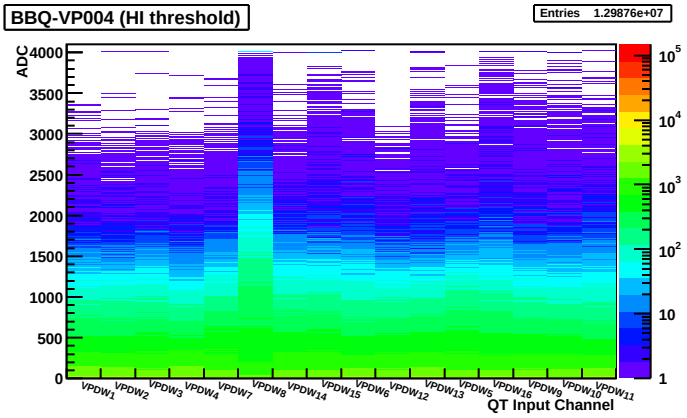

BBQ-VP001 (LO threshold)
Entries 1.29876e+07
TAC
QT Input Channel

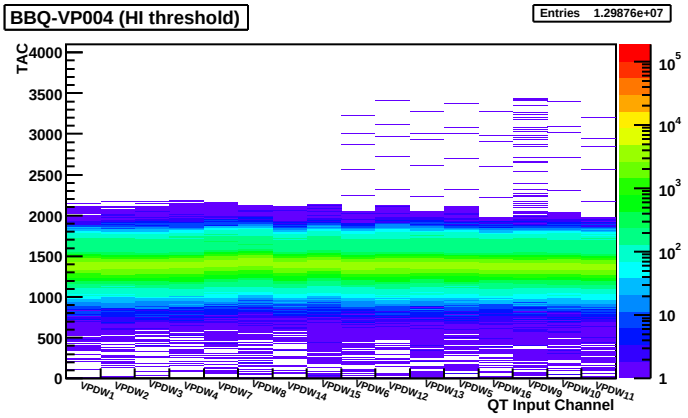

BBQ-VP002 (LO threshold)
Entries 1.29876e+07
ADC
QT Input Channel

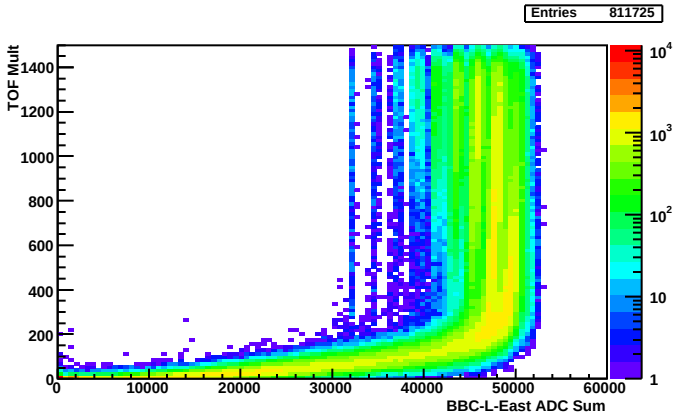

BBQ-VP002 (LO threshold)
Entries 1.29876e+07
TAC
QT Input Channel

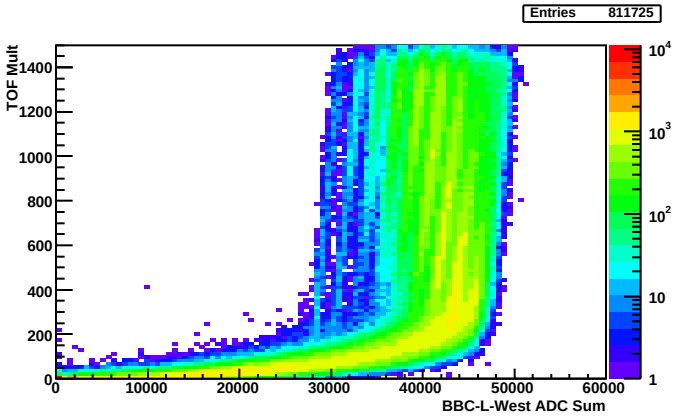

BBQ-VP003 (HI threshold)
Entries 1.29876e+07
ADC
QT Input Channel

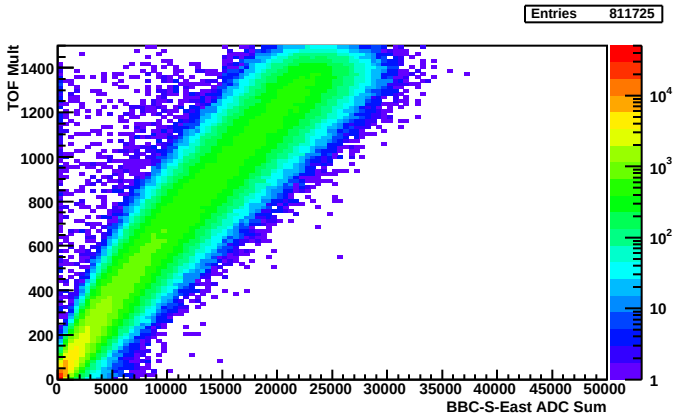

BBQ-VP003 (HI threshold)
Entries 1.29876e+07
TAC
QT Input Channel

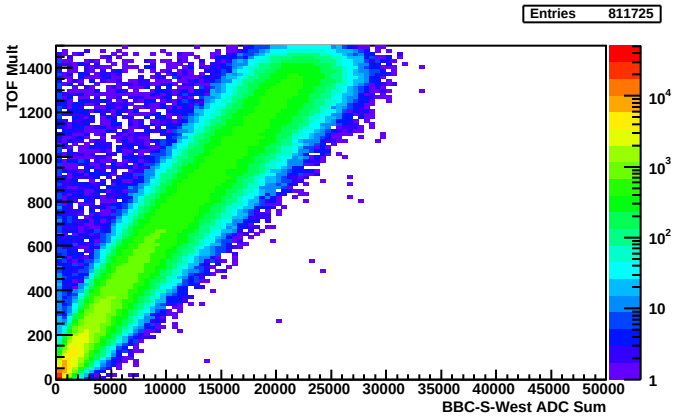

BBQ-VP004 (HI threshold)
Entries 1.29876e+07
ADC
QT Input Channel

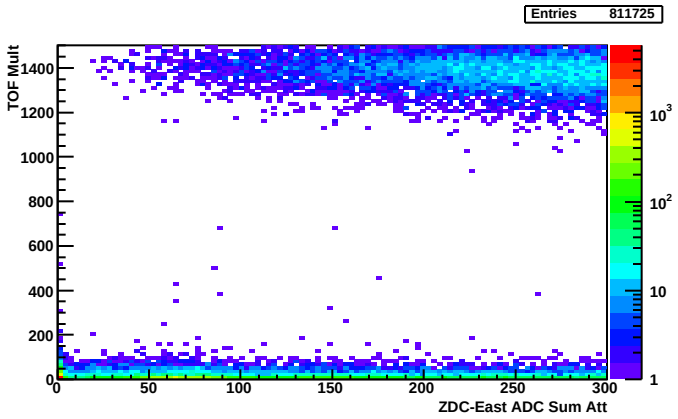

BBQ-VP004 (HI threshold)
Entries 1.29876e+07
TAC
QT Input Channel

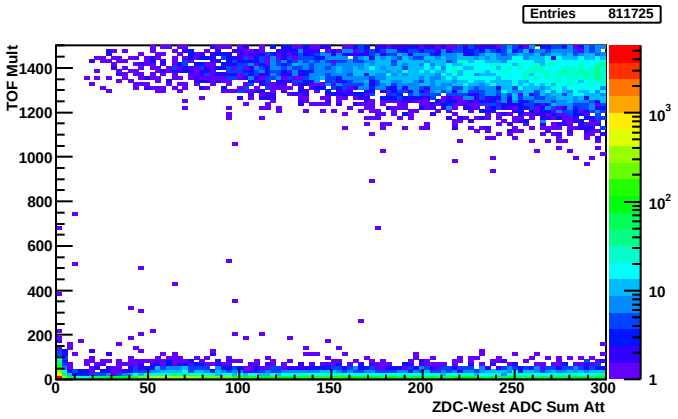

Entries 811725
TOF Mult
BBC-L-East ADC Sum

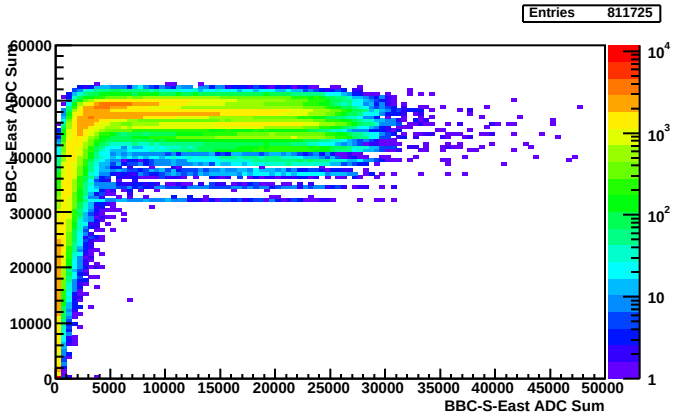

Entries 811725
TOF Mult
BBC-L-West ADC Sum

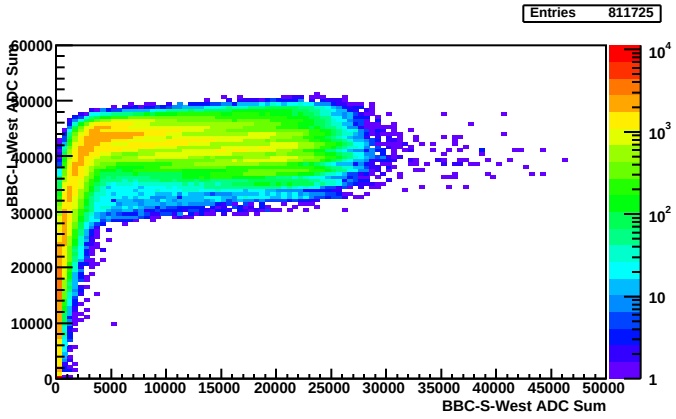

Entries 811725
TOF Mult
BBC-S-East ADC Sum

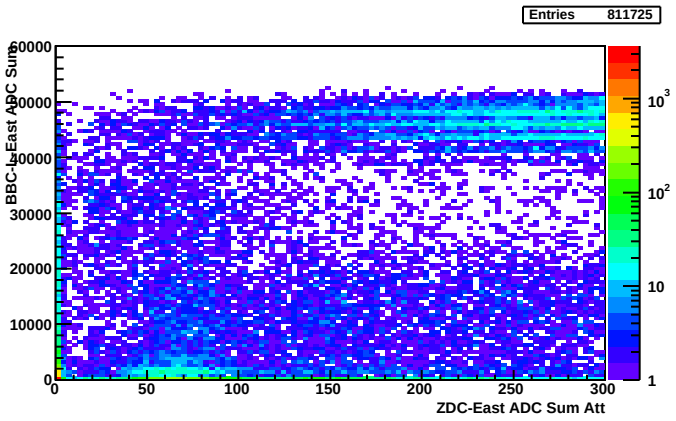

Entries 811725
TOF Mult
BBC-S-West ADC Sum

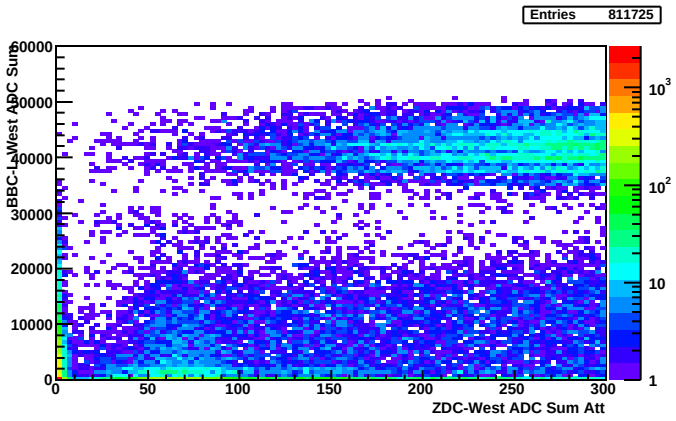

Entries 811725
TOF Mult
ZDC-East ADC Sum Att

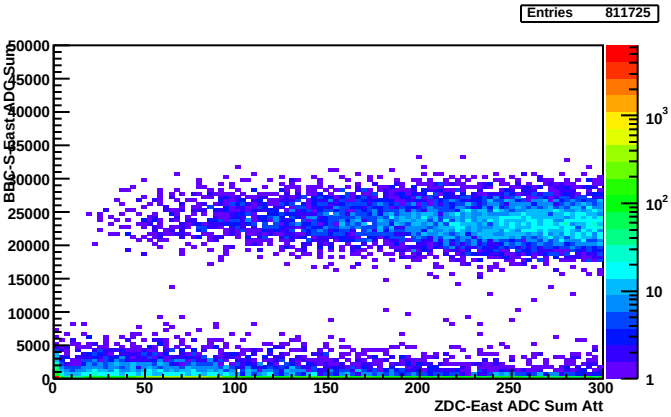

Entries 811725
TOF Mult
ZDC-West ADC Sum Att

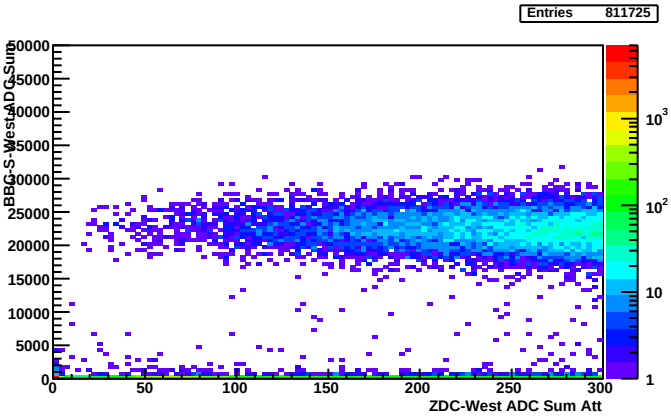

Entries 811725
BBC-L-East ADC Sum
BBC-S-East ADC Sum

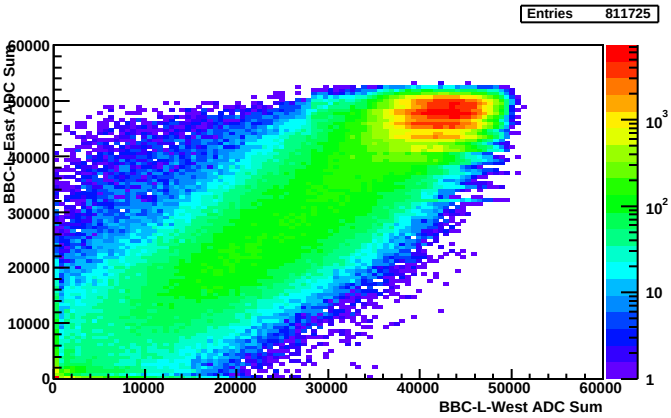

Entries 811725
BBC-L-West ADC Sum
BBC-S-West ADC Sum

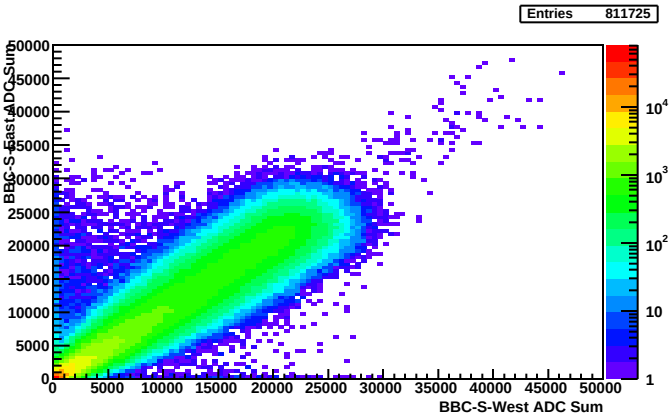

Entries 811725
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

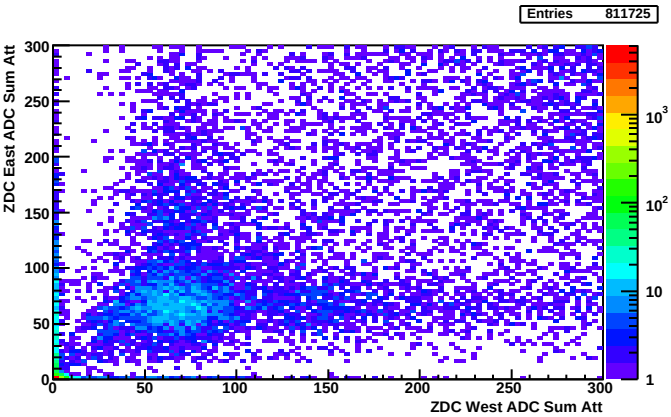

Entries 811725
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

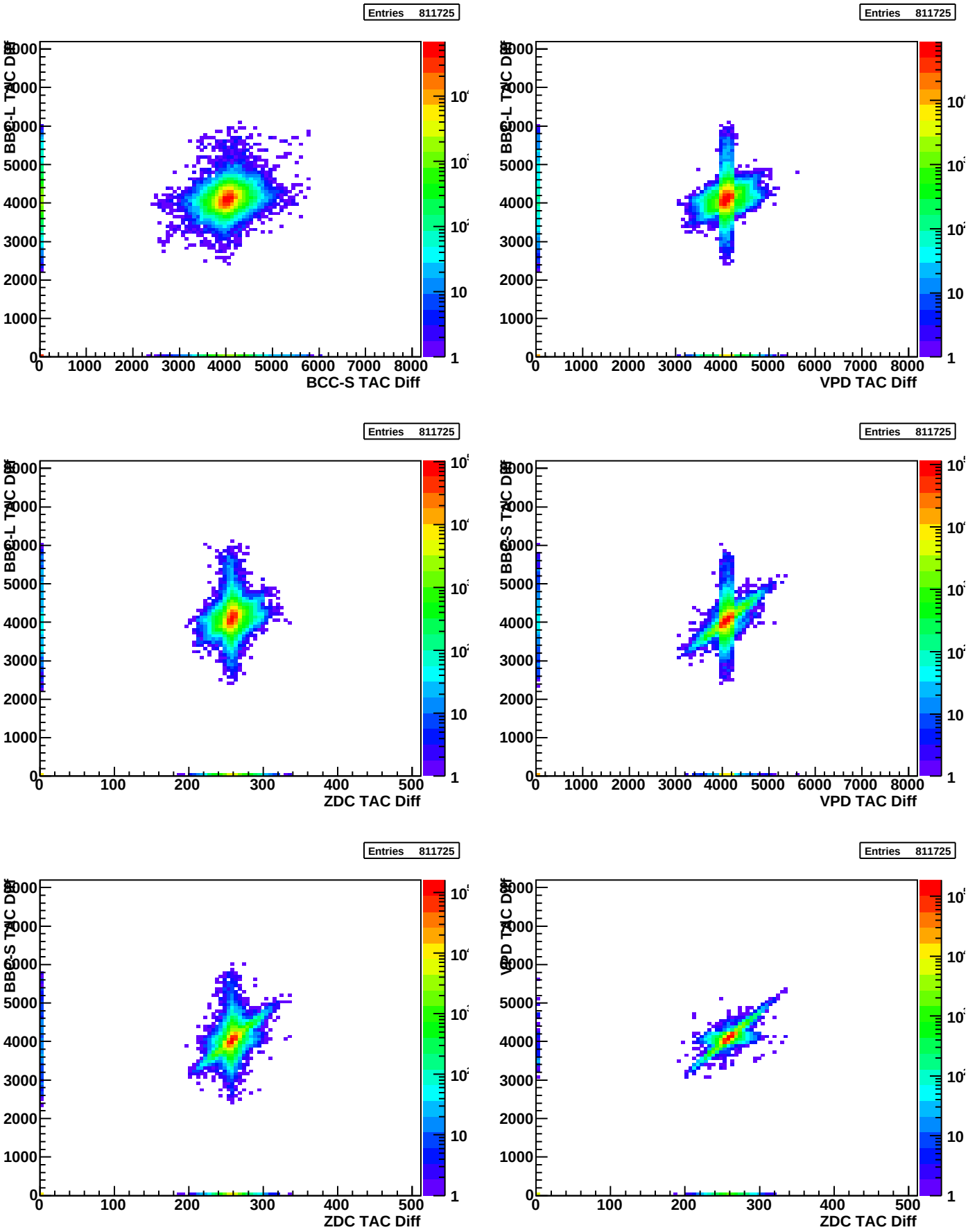

Entries 811725
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

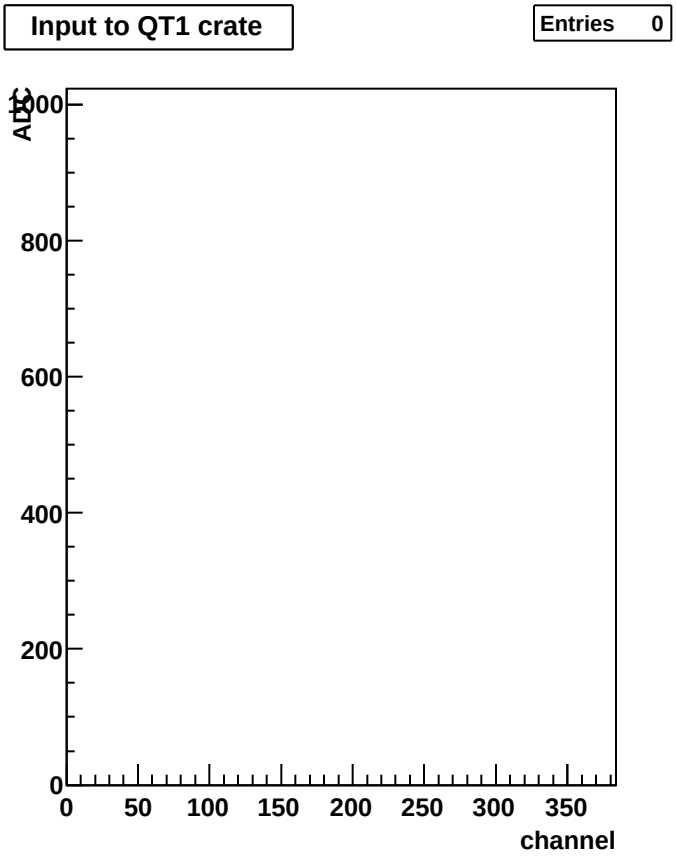

Entries 811725
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

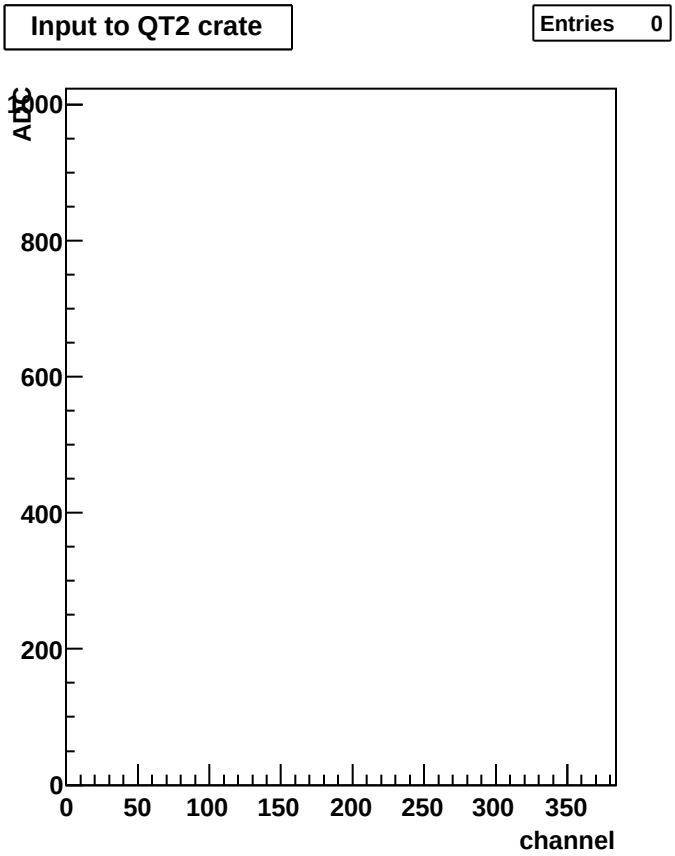

Entries 811725
BBC-L-East ADC Sum
BBC-L-West ADC Sum

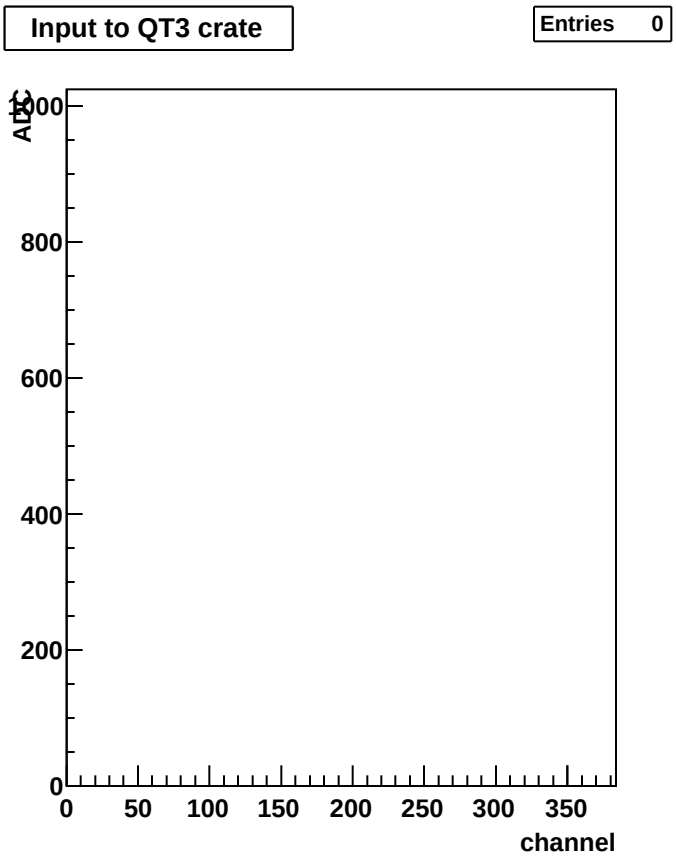

Entries 811725
BBC-S-East ADC Sum
BBC-S-West ADC Sum

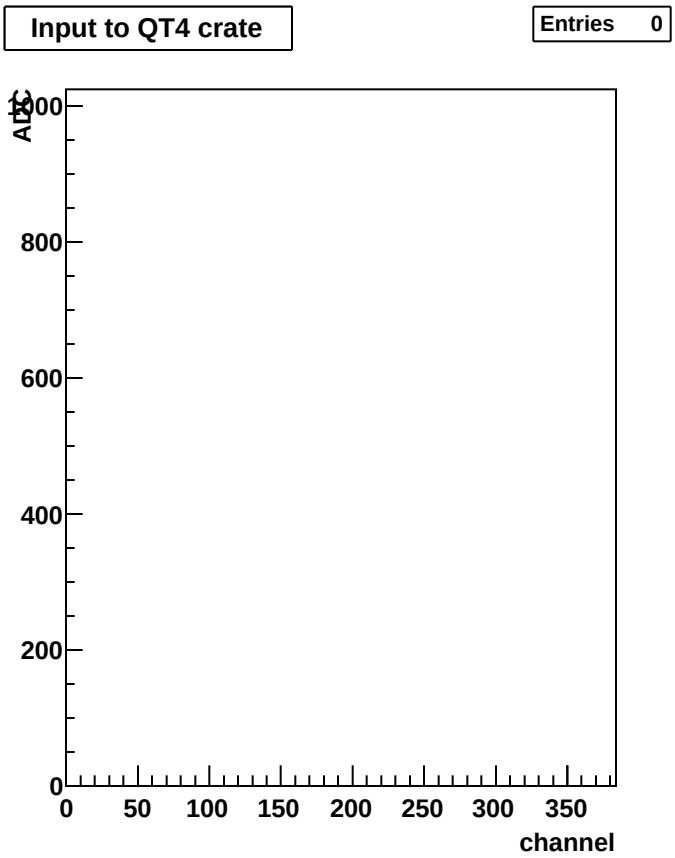

Entries 811725
ZDC East ADC Sum Att
ZDC West ADC Sum Att

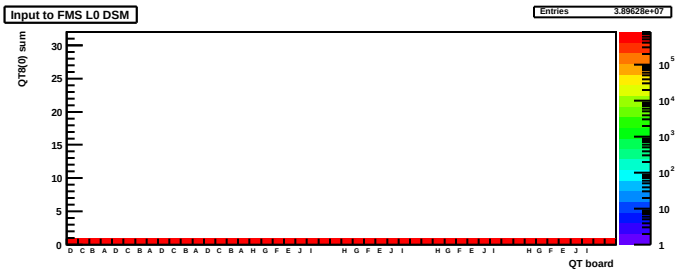

Entries 811725
BBC-L TAC Diff
BCC-S TAC Diff
Entries 811725
BBC-L TAC Diff
VPD TAC Diff
Entries 811725
BBC-L TAC Diff
ZDC TAC Diff
Entries 811725
BBC-S TAC Diff
VPD TAC Diff
Entries 811725
BBC-S TAC Diff
ZDC TAC Diff
Entries 811725
VPD TAC Diff
ZDC TAC Diff

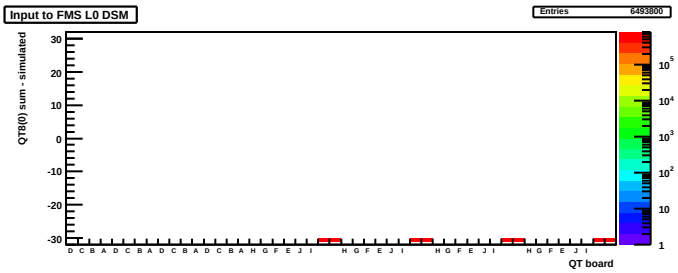

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

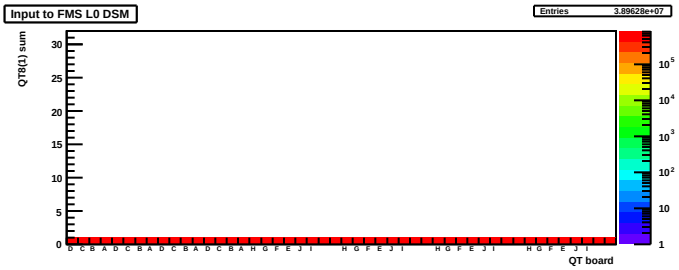

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

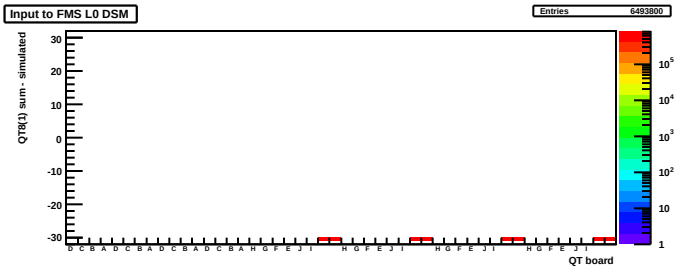

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

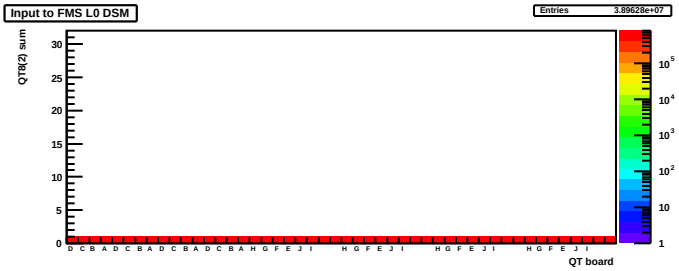

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

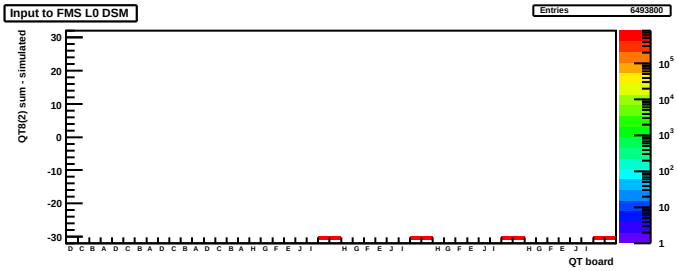

Input to FMS L0 DSM
Entries 3.89628e+07
QT8(0) sum
QT board

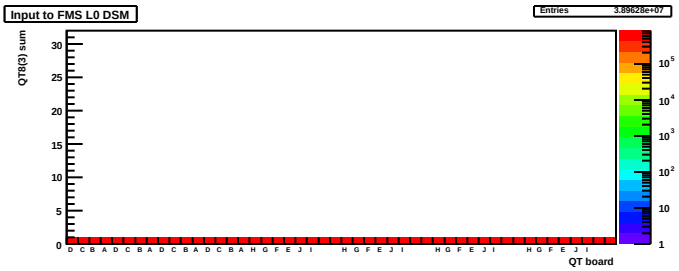

Input to FMS L0 DSM
Entries 6493800
QT8(0) sum - simulated
QT board

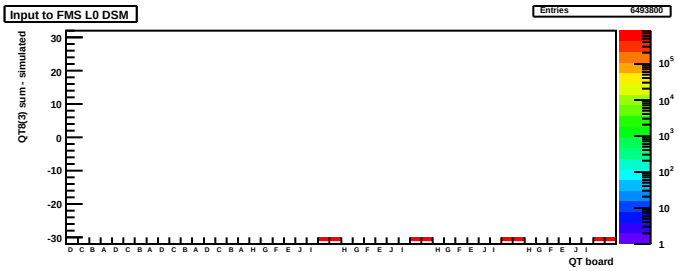

Input to FMS L0 DSM
Entries 3.89628e+07
QT8(1) sum
QT board

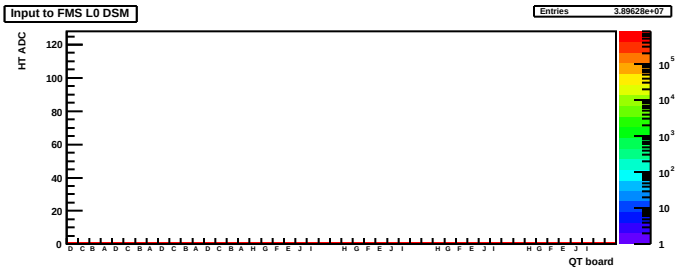

Input to FMS L0 DSM
Entries 6493800
QT8(1) sum - simulated
QT board

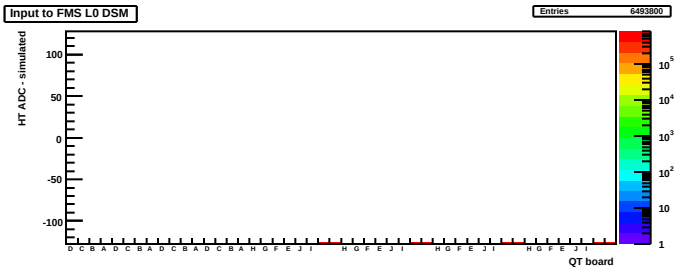

Input to FMS L0 DSM
Entries 3.89628e+07
QT8(2) sum
QT board

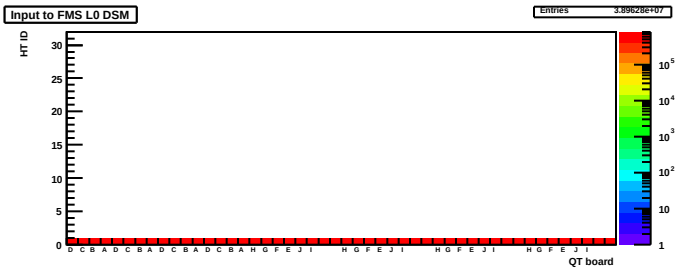

Input to FMS L0 DSM
Entries 6493800
QT8(2) sum - simulated
QT board

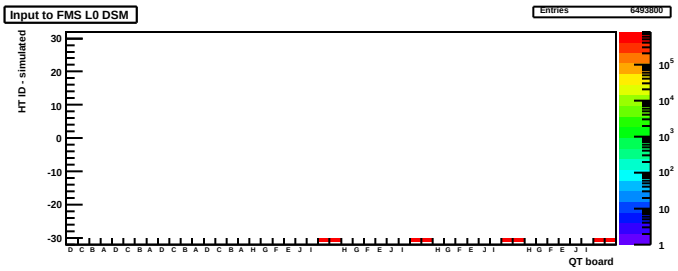

Input to FMS L0 DSM
Entries 3.89628e+07
QT8(3) sum
QT board

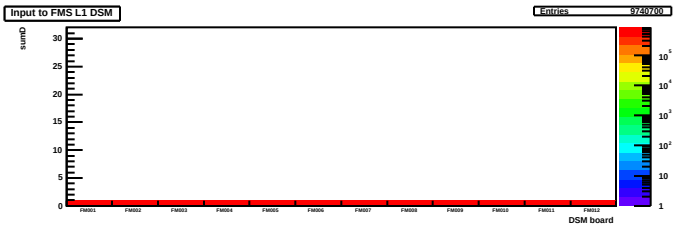

Input to FMS L0 DSM
Entries 6493800
QT8(3) sum - simulated
QT board

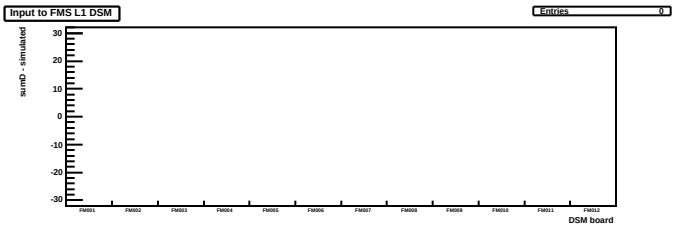

Input to FMS L0 DSM
Entries 3.89628e+07
HT ADC
QT board

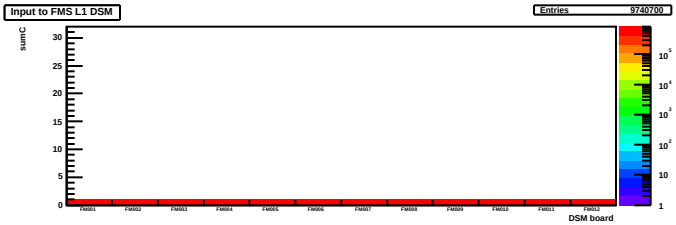

Input to FMS L0 DSM
Entries 6493800
HT ADC - simulated
QT board

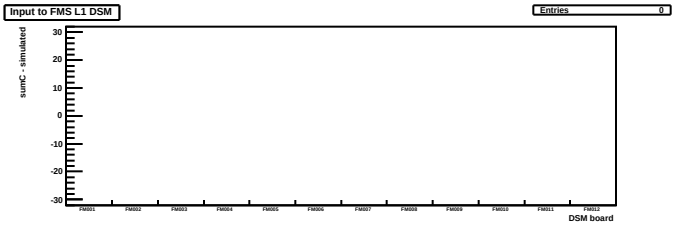

Input to FMS L0 DSM
Entries 3.89628e+07
HT ID
QT board

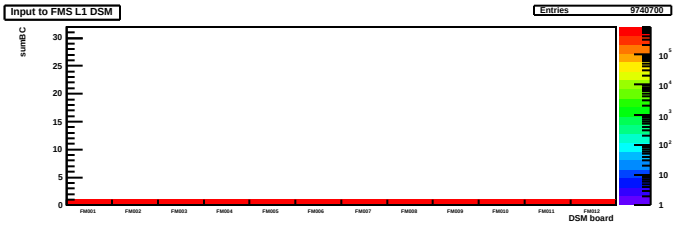

Input to FMS L0 DSM
Entries 6493800
HT ID - simulated
QT board

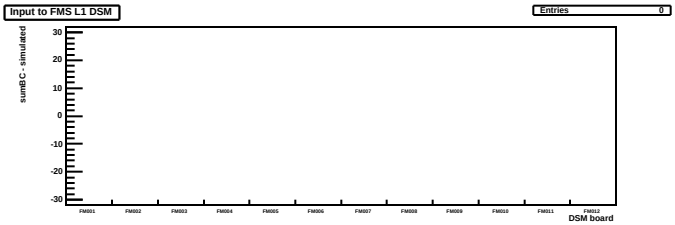

Input to FMS L1 DSM
Entries 9740700
sumD
DSM board

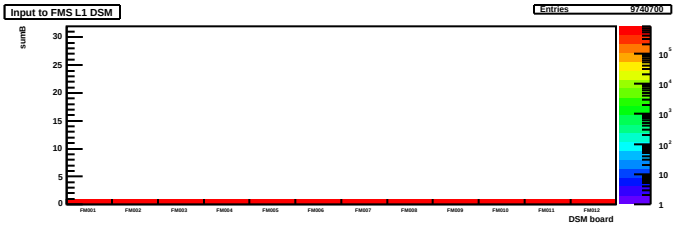

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

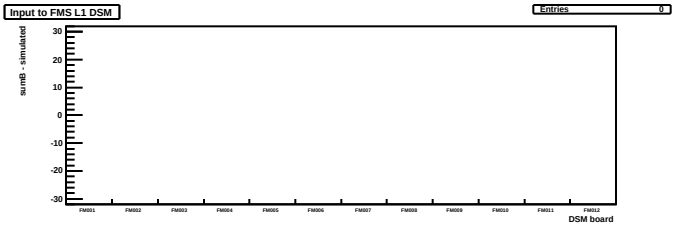

Input to FMS L1 DSM
Entries 9740700
sumC
DSM board

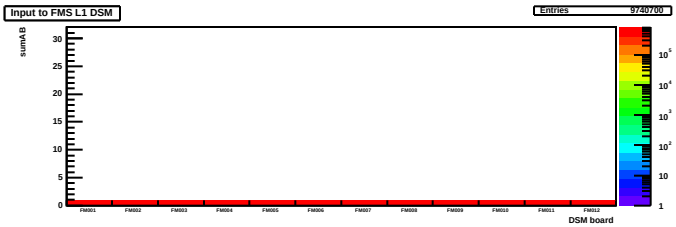

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

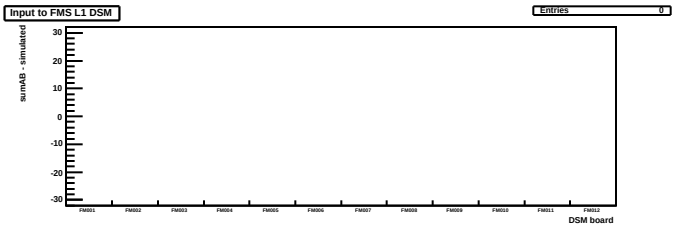

Input to FMS L1 DSM
Entries 9740700
sumBC
DSM board

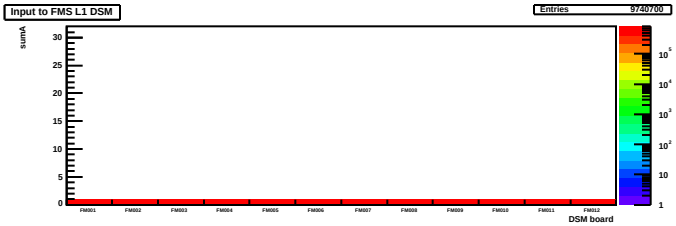

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

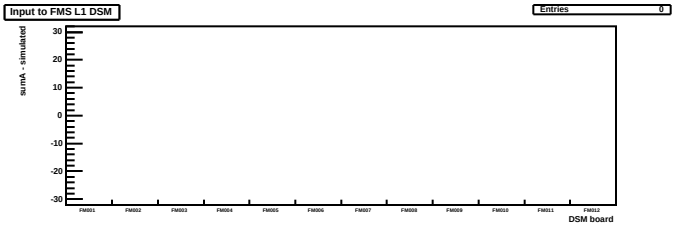

Input to FMS L1 DSM
Entries 9740700
sumB
DSM board

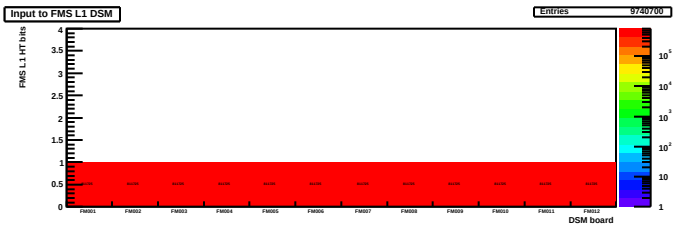

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

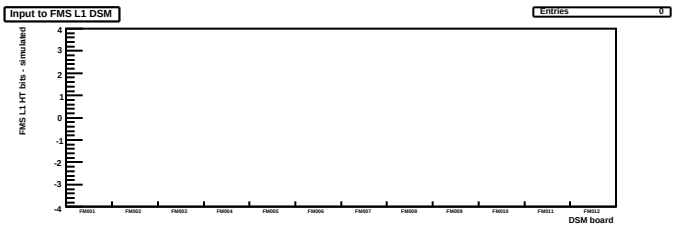

Input to FMS L1 DSM
Entries 9740700
sumAB
DSM board

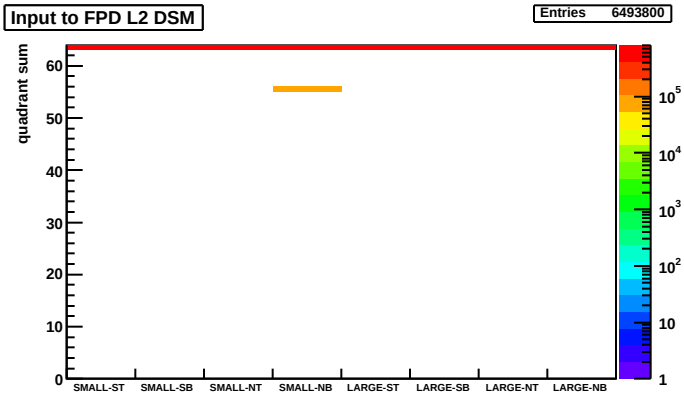

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

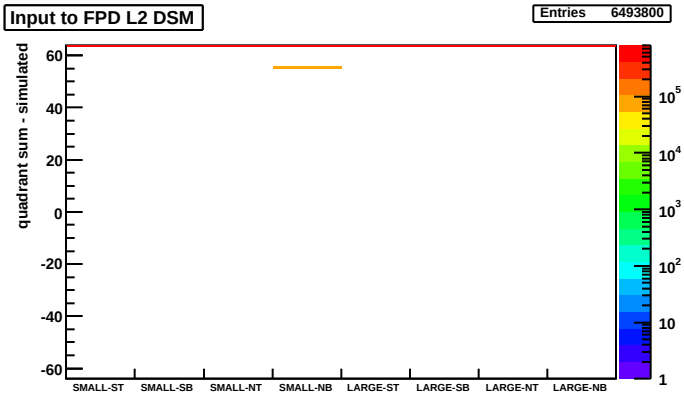

Input to FMS L1 DSM
Entries 9740700
sumA
DSM board

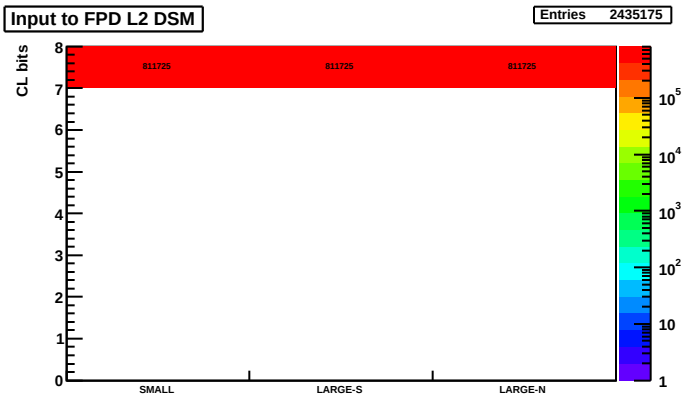

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

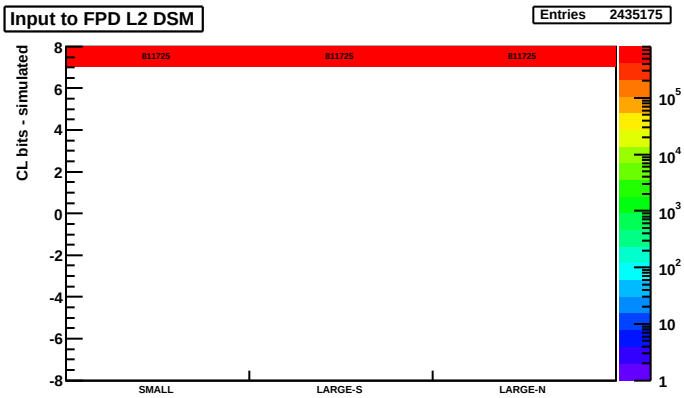

Input to FMS L1 DSM
Entries 9740700
FMS L1 HT bits
DSM board

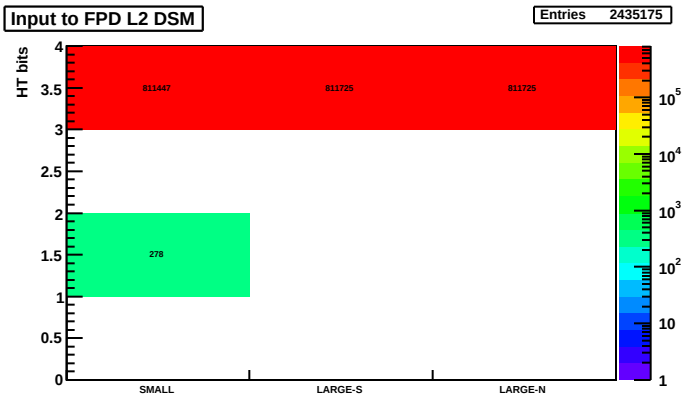

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

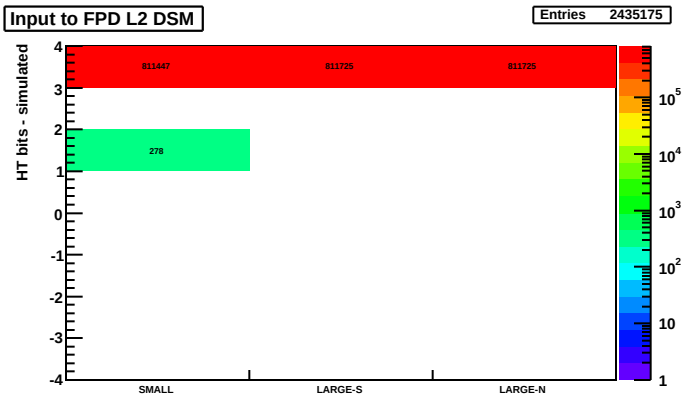

Input to FPD L2 DSM
Entries 6493800
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

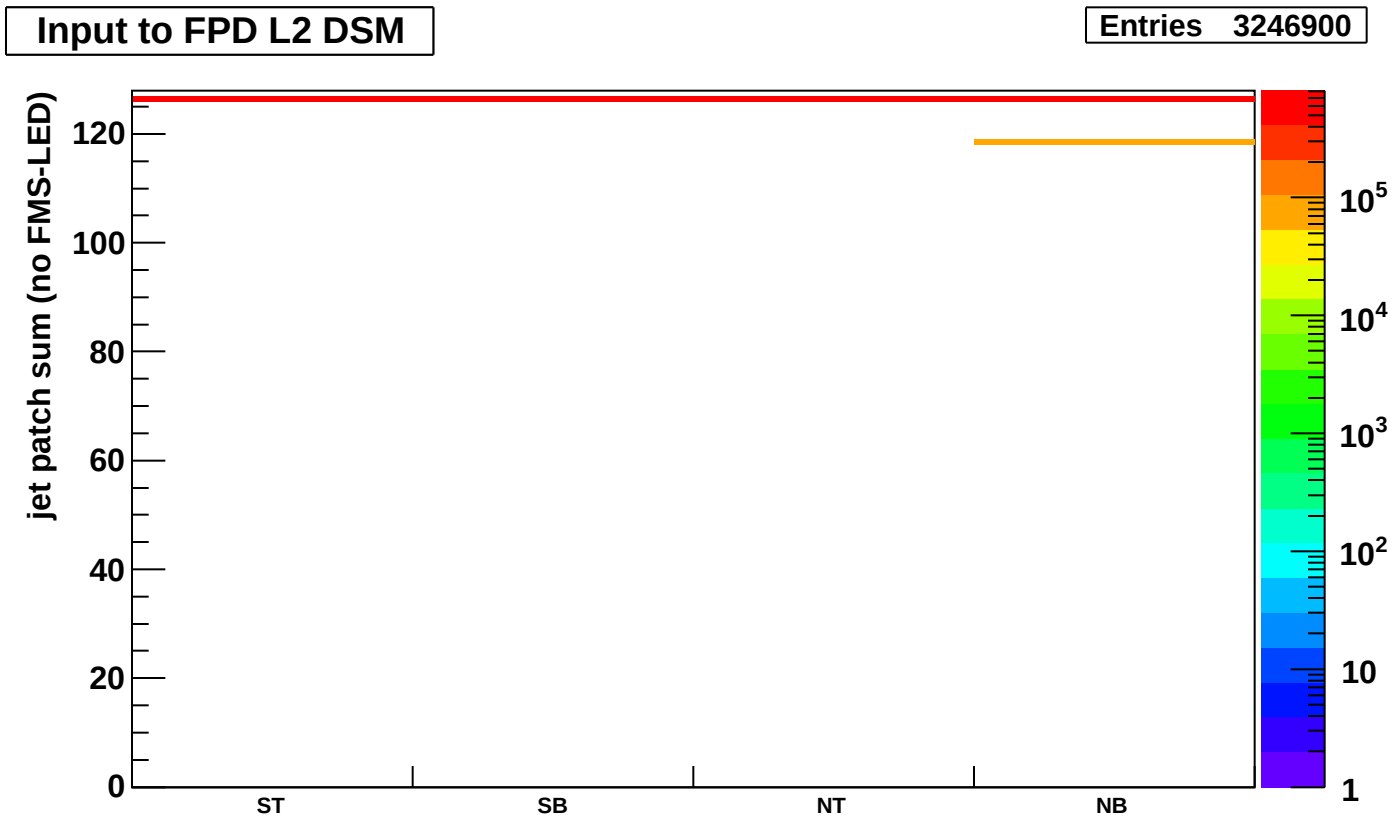

Input to FPD L2 DSM
Entries 6493800
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

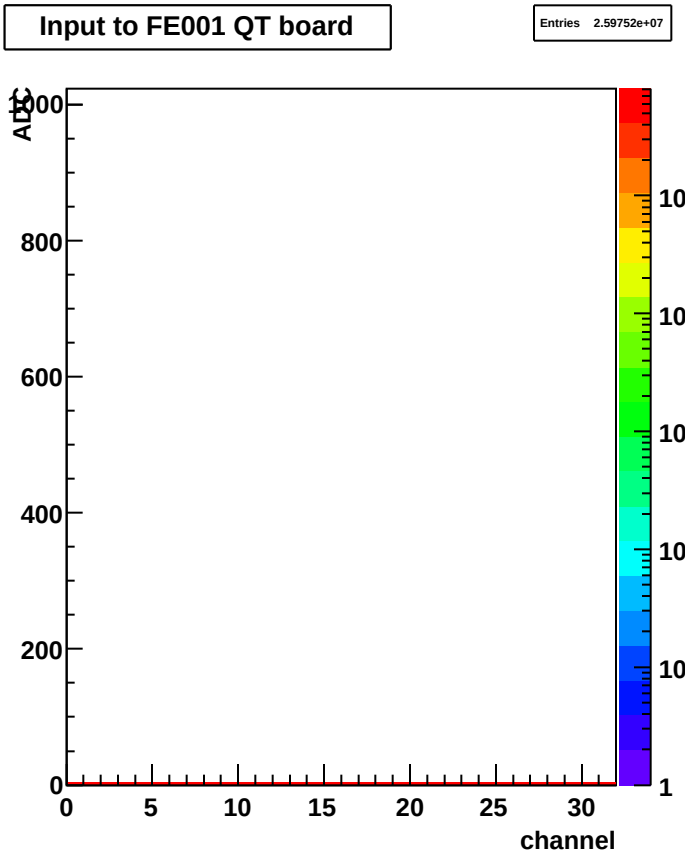

Input to FPD L2 DSM
Entries 2435175
CL bits
811725
811725
811725
SMALL
LARGE-S
LARGE-N

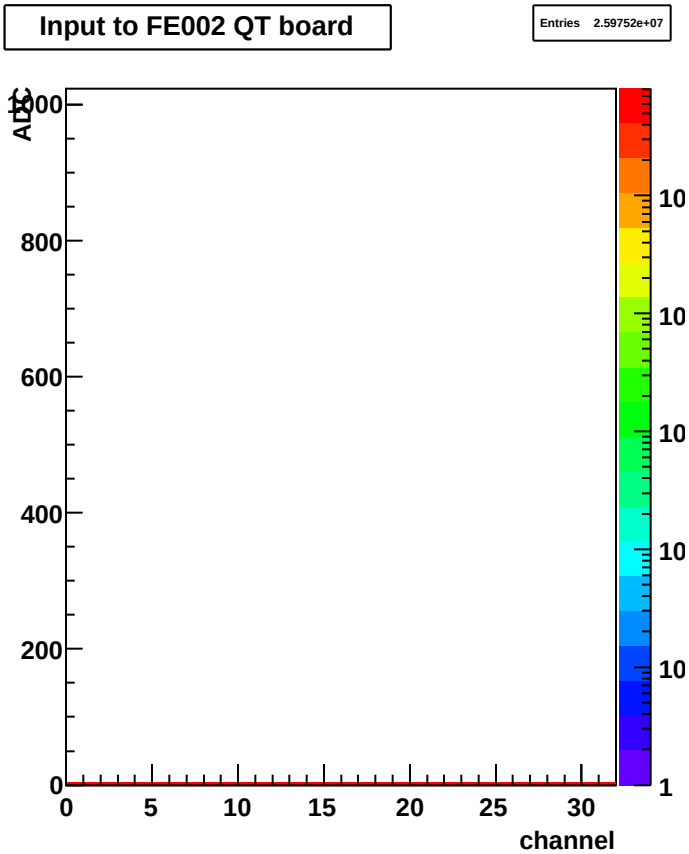

Input to FPD L2 DSM
Entries 2435175
CL bits - simulated
811725
811725
811725
SMALL
LARGE-S
LARGE-N

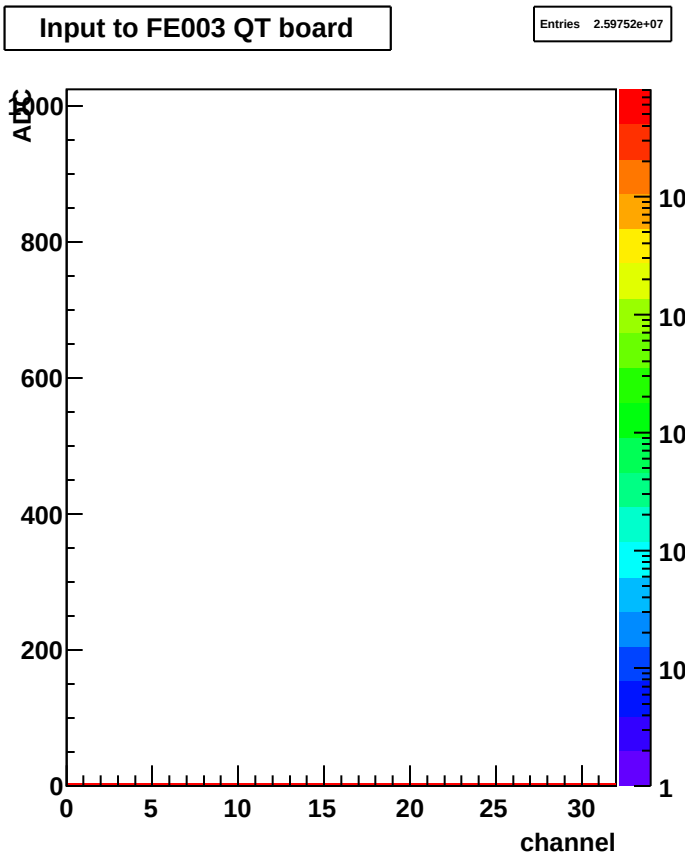

Input to FPD L2 DSM
Entries 2435175
HT bits
811447
811725
811725
278
SMALL
LARGE-S
LARGE-N

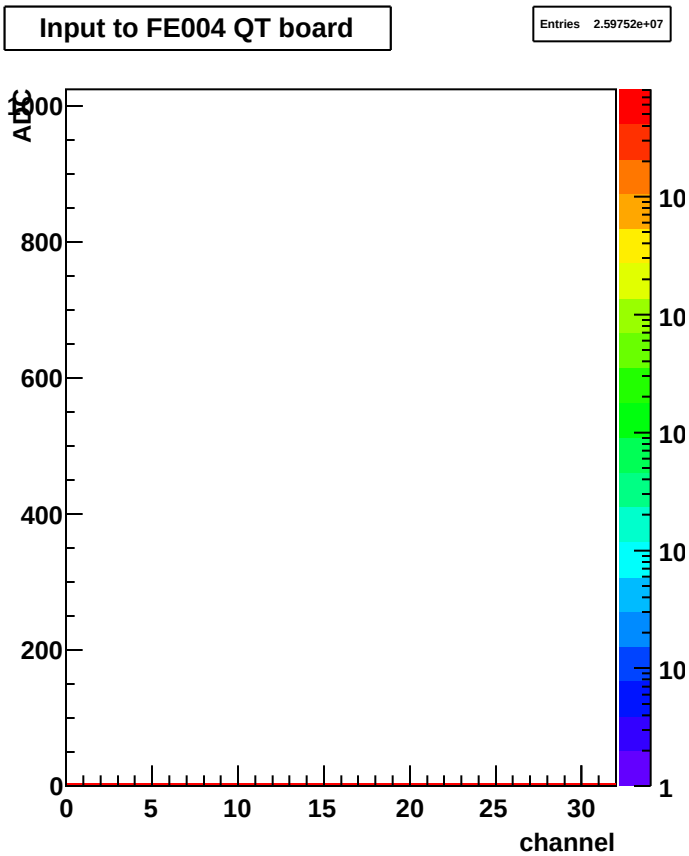

Input to FPD L2 DSM
Entries 2435175
HT bits - simulated
811447
811725
811725
278
SMALL
LARGE-S
LARGE-N

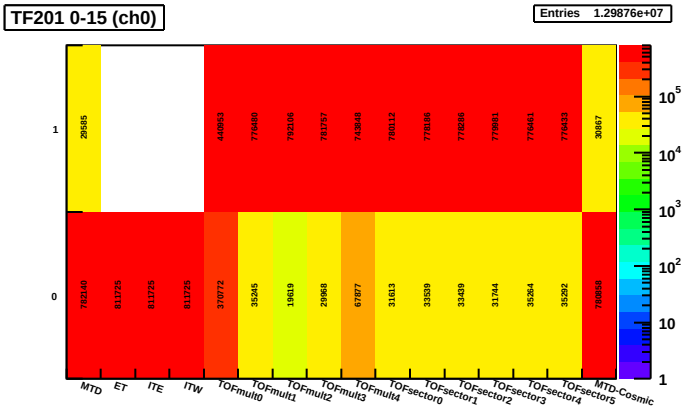

Input to FPD L2 DSM
Entries 3246900
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

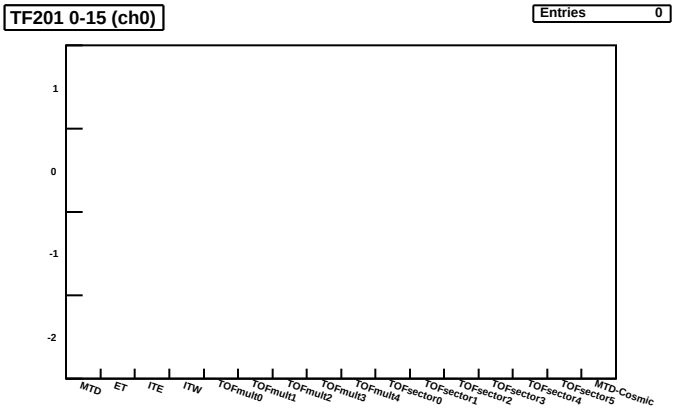

Input to FE001 QT board
Entries 2.59752e+07
ADC
channel

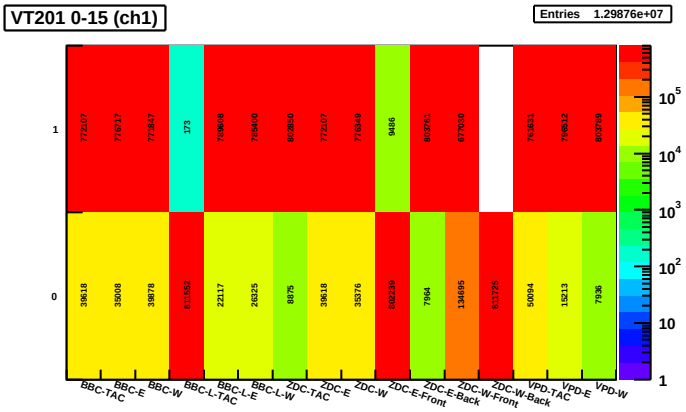

Input to FE002 QT board
Entries 2.59752e+07
ADC
channel

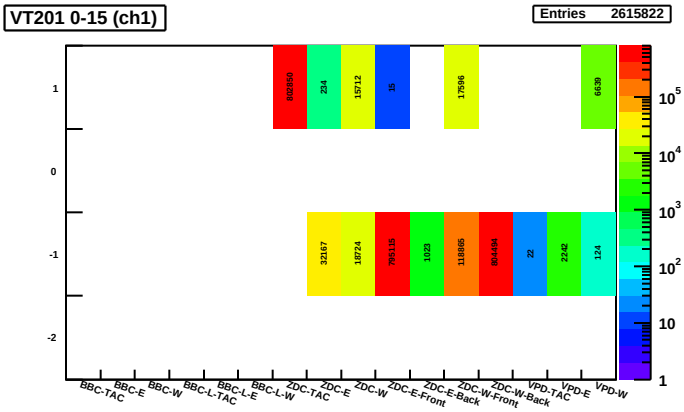

Input to FE003 QT board
Entries 2.59752e+07
ADC
channel

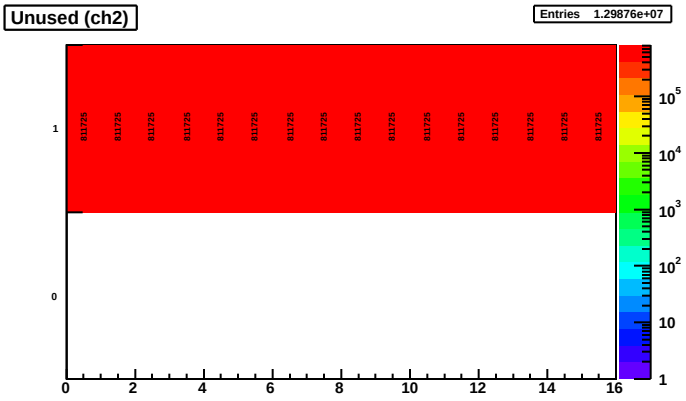

Input to FE004 QT board
Entries 2.59752e+07
ADC
channel

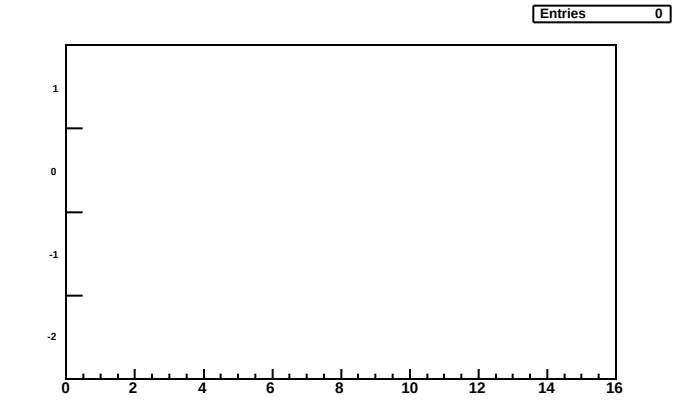

TF201 0-15 (ch0)
Entries 1.29876e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

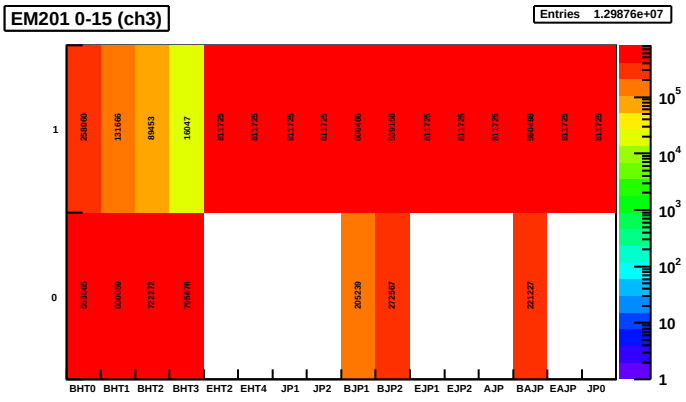

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

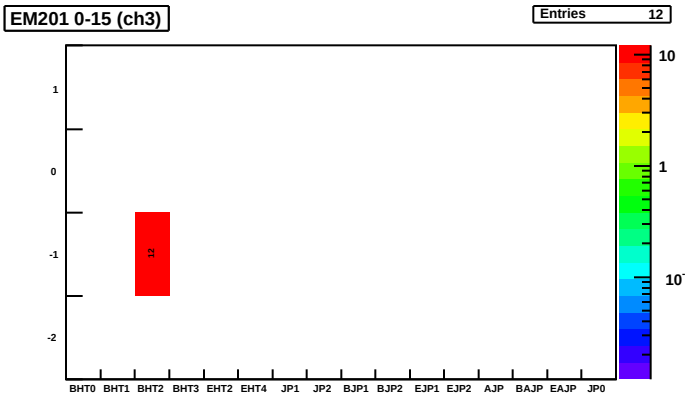

VT201 0-15 (ch1)
Entries 1.29876e+07
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 2615822
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

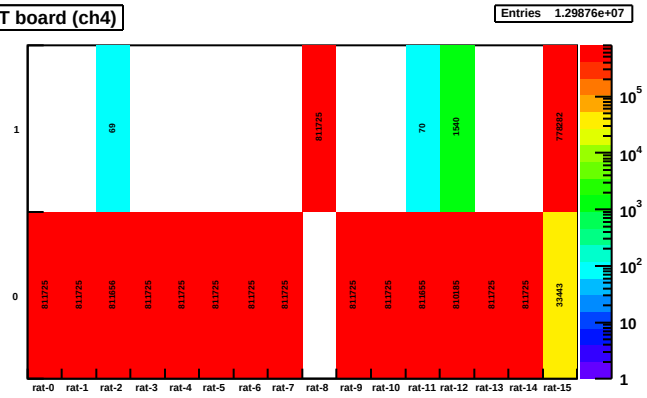

Unused (ch2)
Entries 1.29876e+07

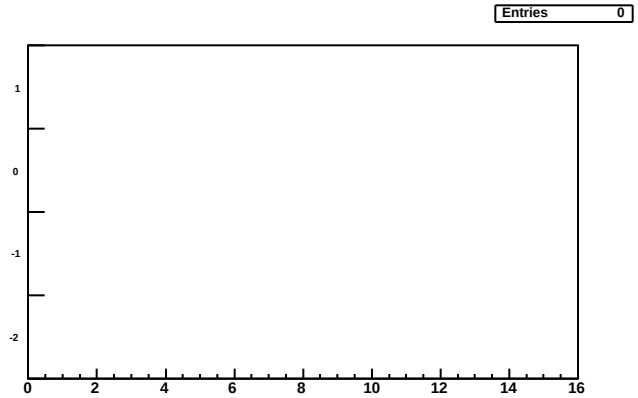

Entries 0


EM201 0-15 (ch3)
Entries 1.29876e+07
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

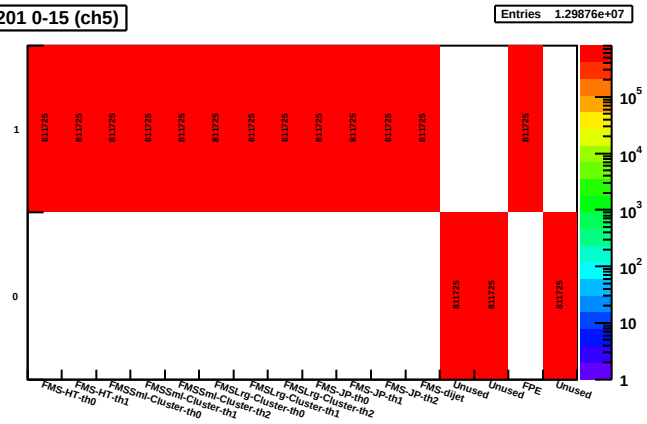

EM201 0-15 (ch3)
Entries 12
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

RAT board (ch4)

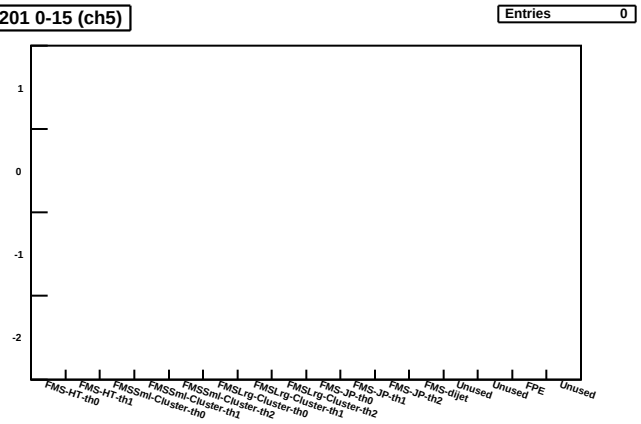




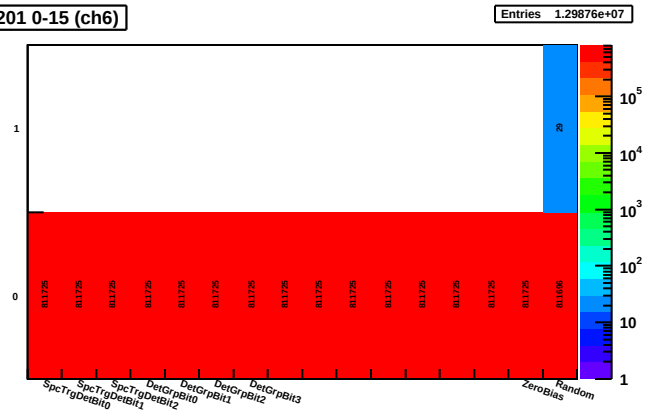
FP201 0-15 (ch5)

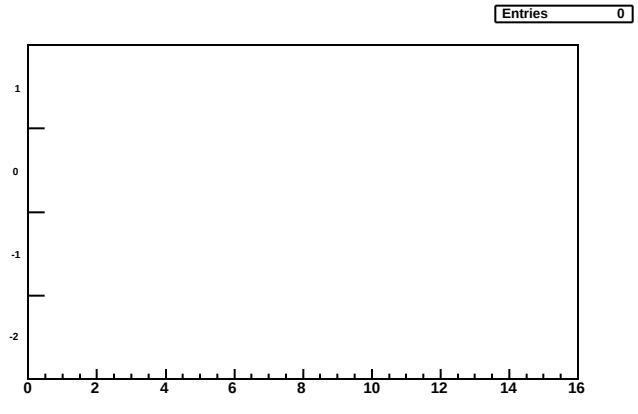
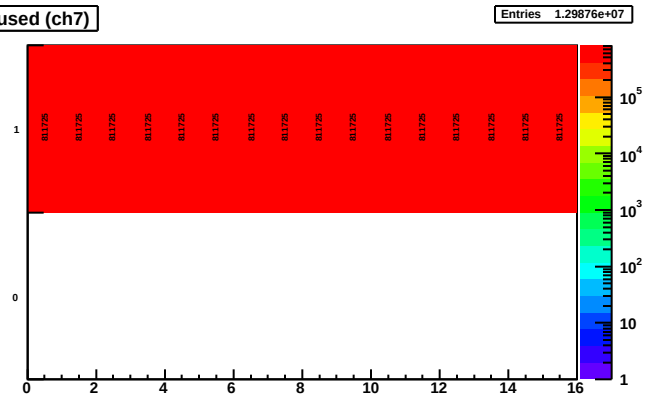
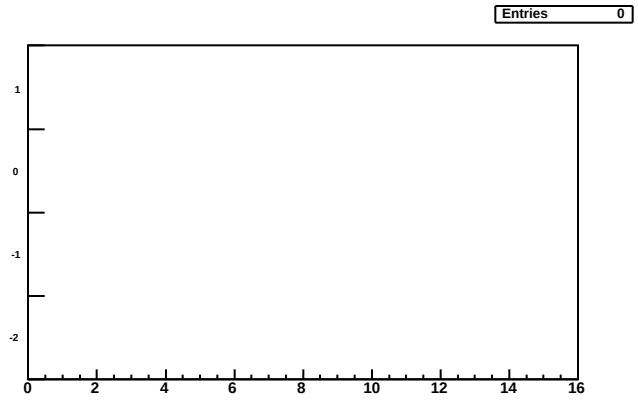


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)