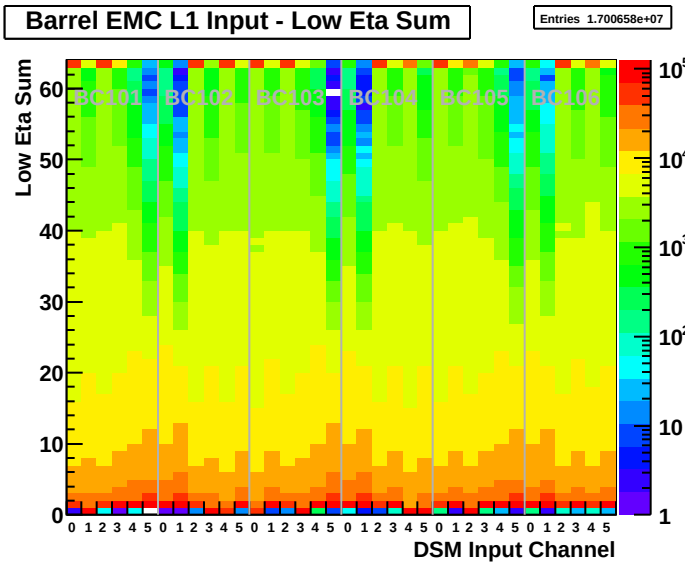
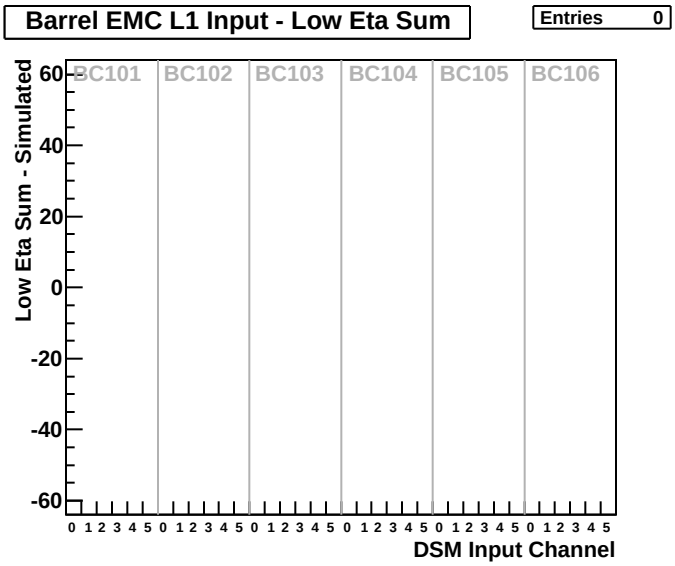
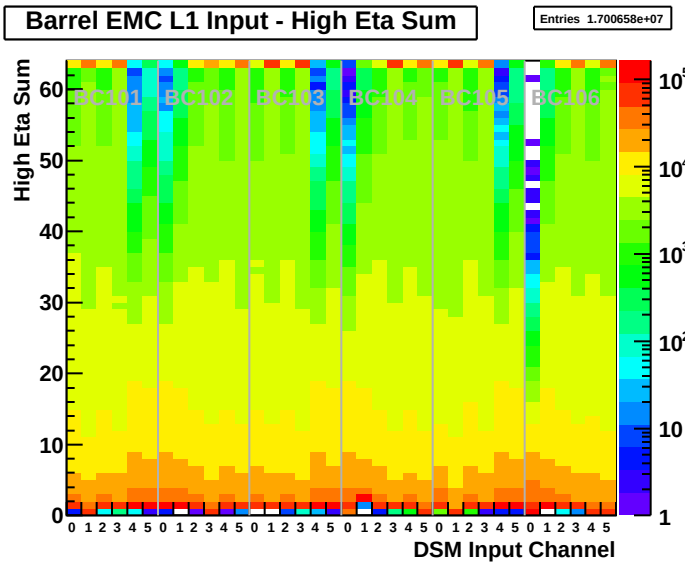
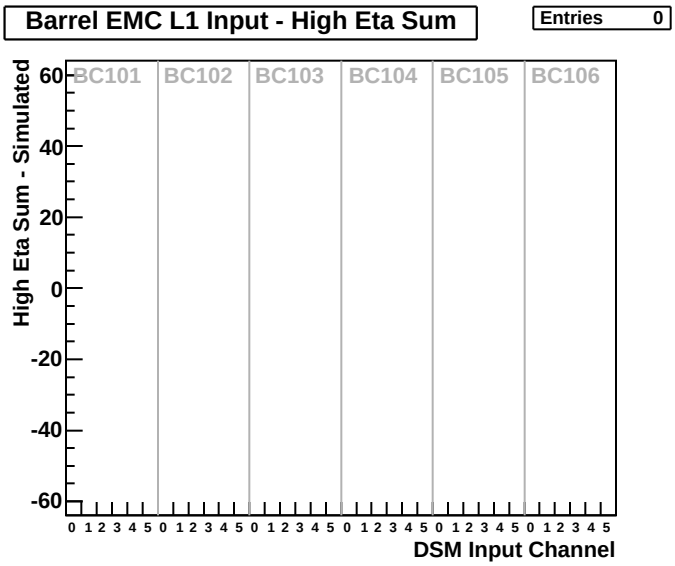
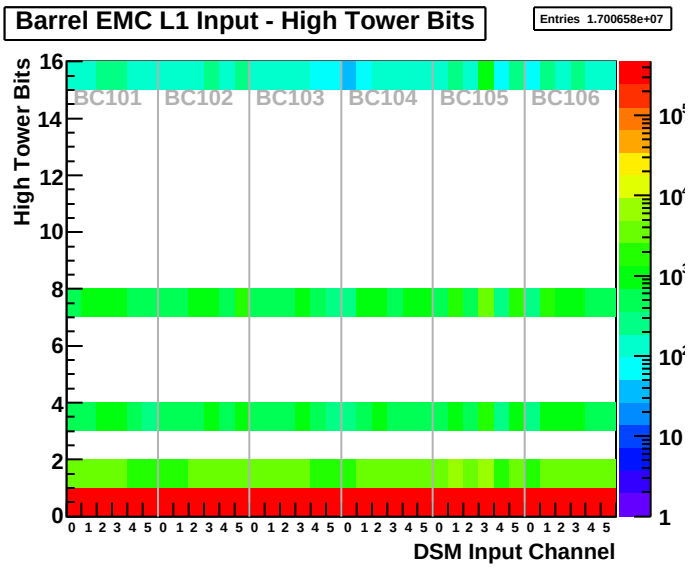
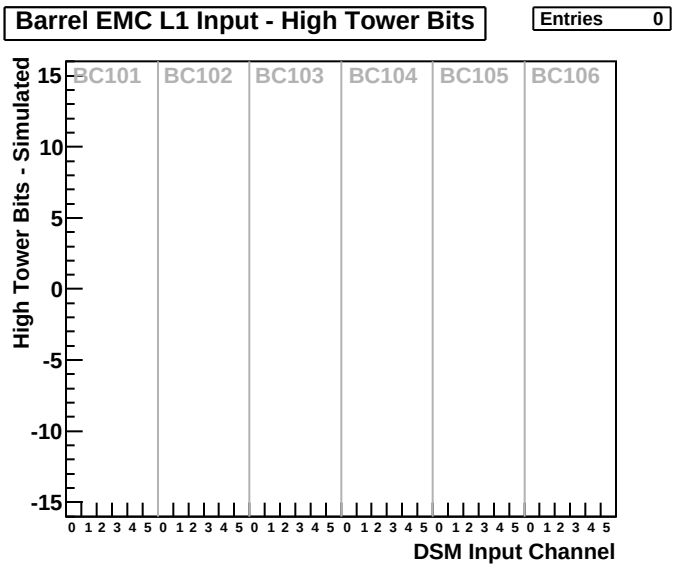

Barrel EMC L1 Input - Low Eta Sum
Entries 1.700658e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

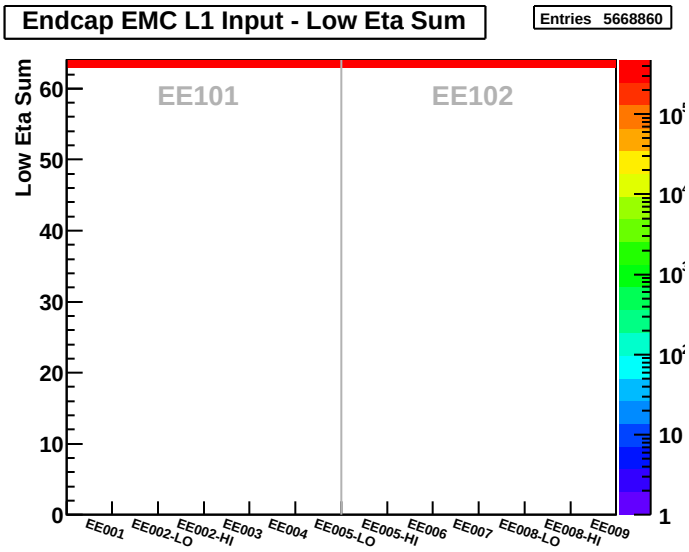

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

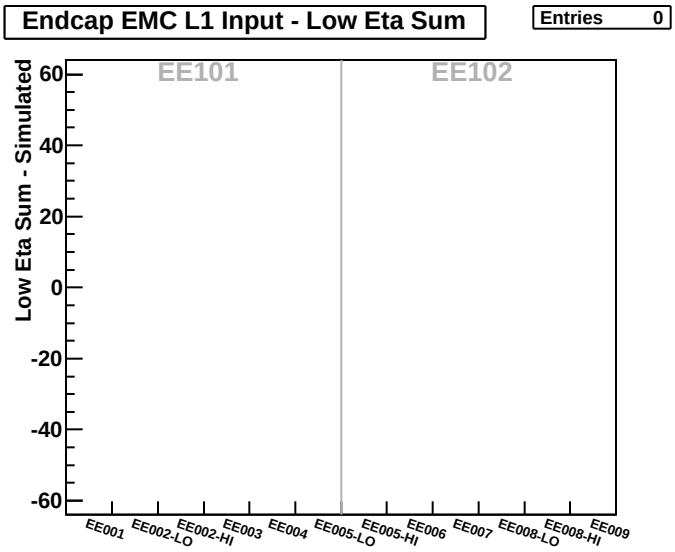

Barrel EMC L1 Input - High Eta Sum
Entries 1.700658e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

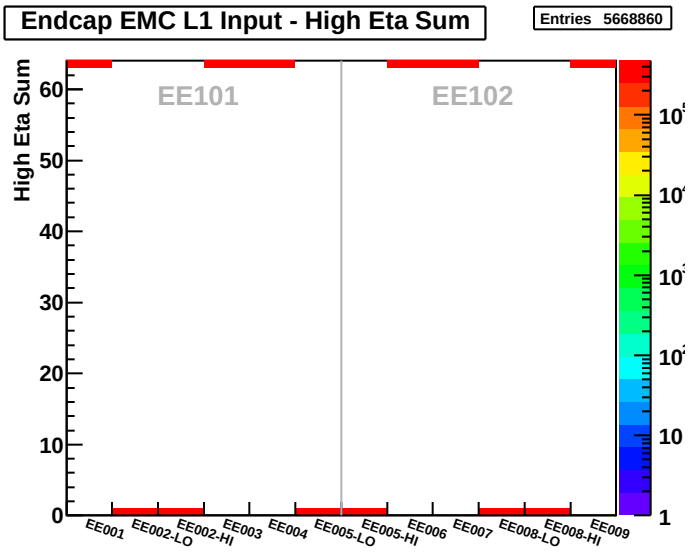

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

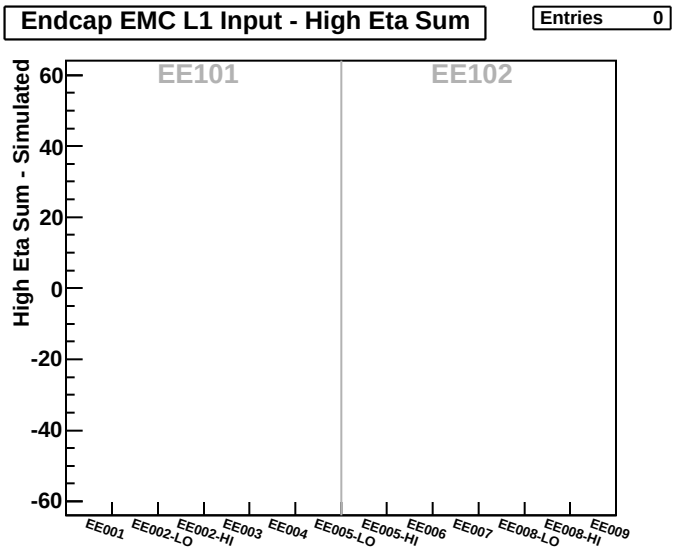

Barrel EMC L1 Input - High Tower Bits
Entries 1.700658e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

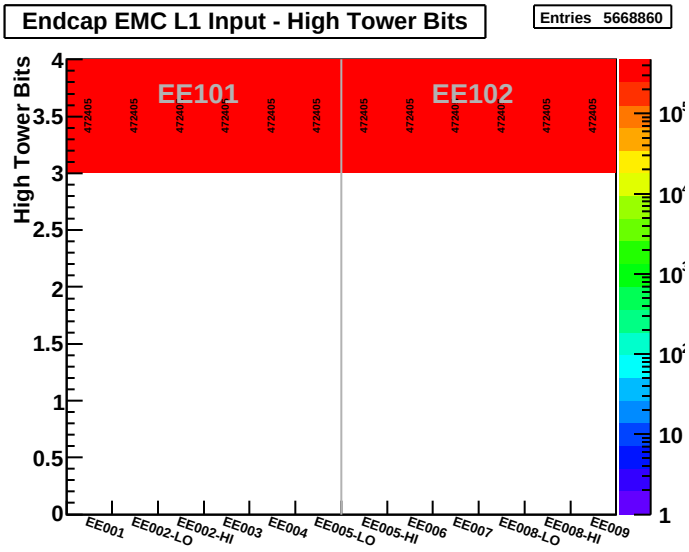

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

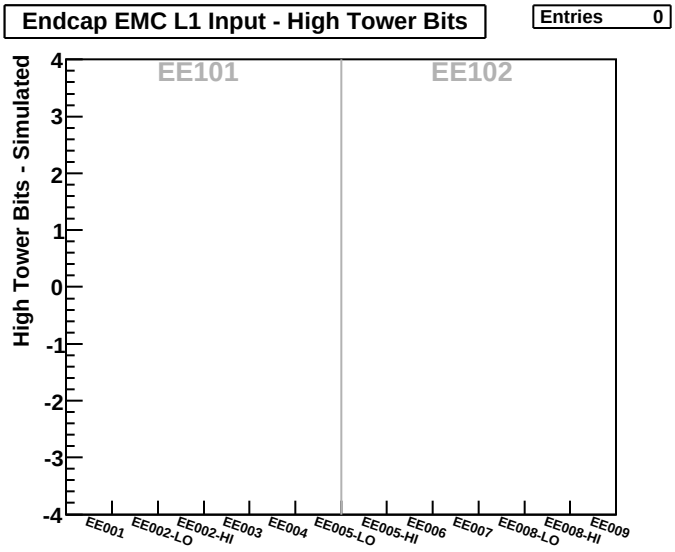

Endcap EMC L1 Input - Low Eta Sum
Entries 5668860
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

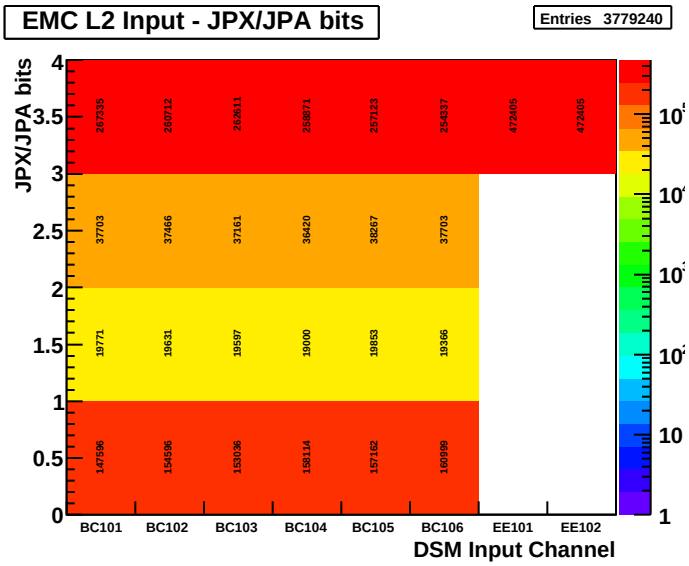

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

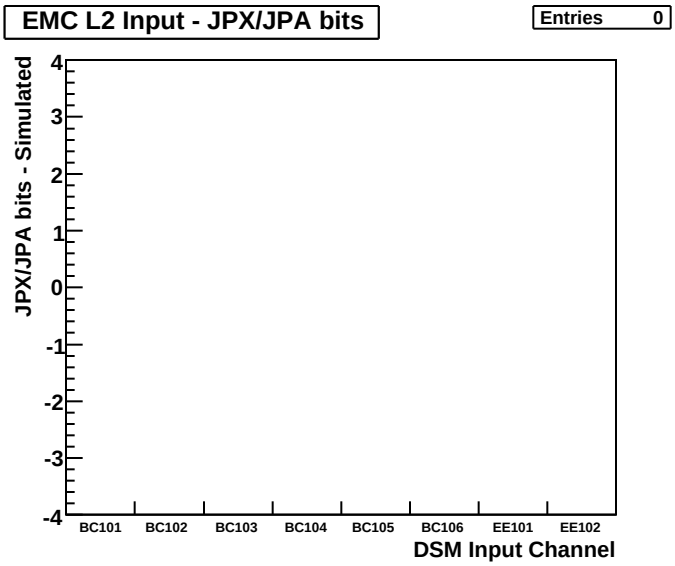

Endcap EMC L1 Input - High Eta Sum
Entries 5668860
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

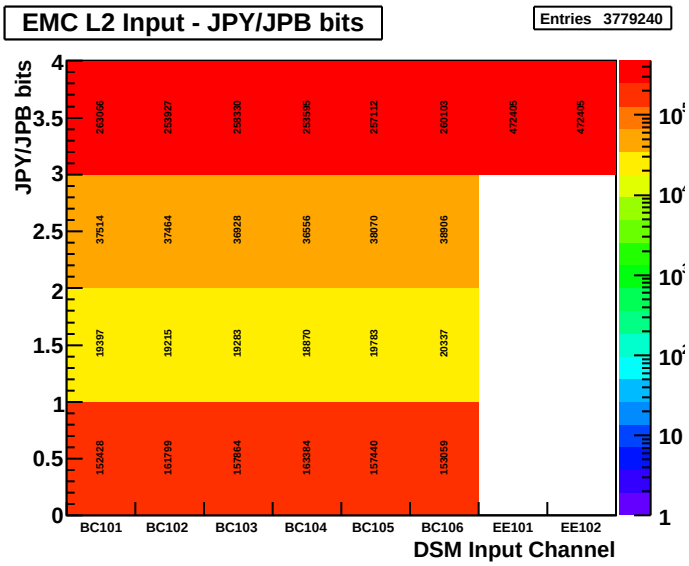

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

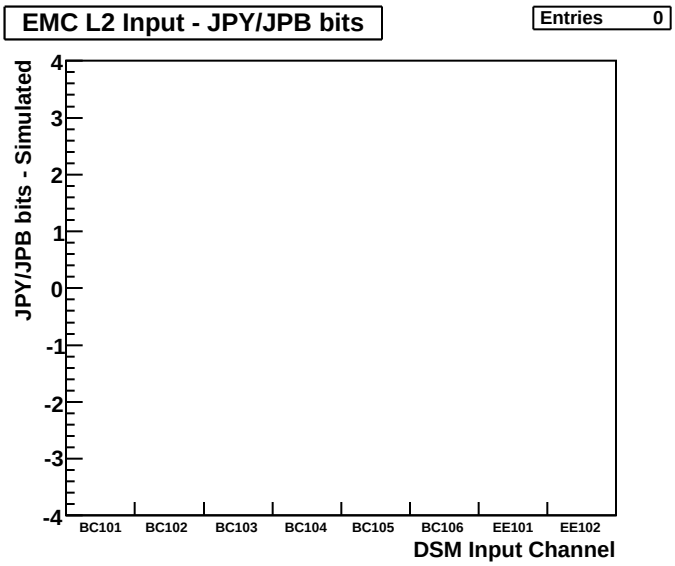

Endcap EMC L1 Input - High Tower Bits
Entries 5668860
High Tower Bits
EE101
EE102
472405
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

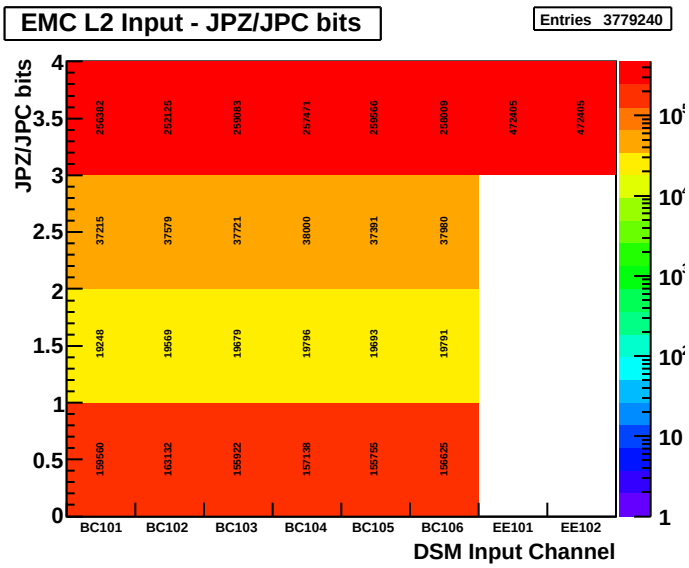

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

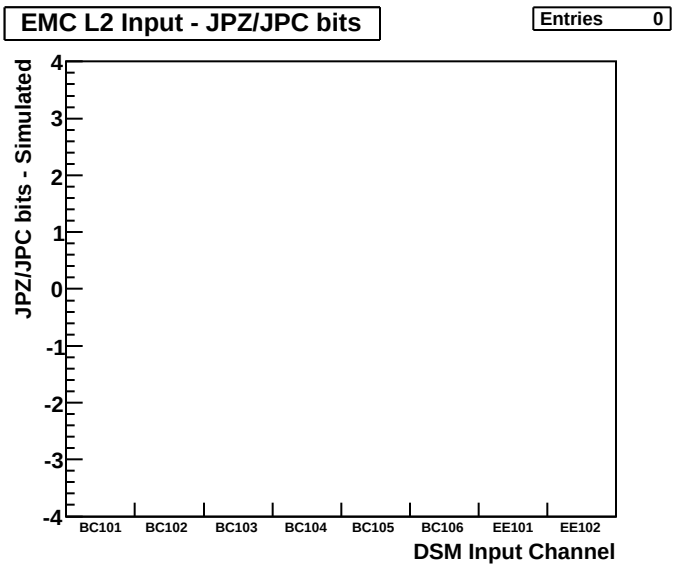

EMC L2 Input - JPX/JPA bits
Entries 3779240
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
267335 260712 263611 258871 257123 254337 472405 472405
37703 37466 37161 36420 38267 37703
19771 19631 19597 19000 19853 19366
147596 154596 153036 158114 157162 160999

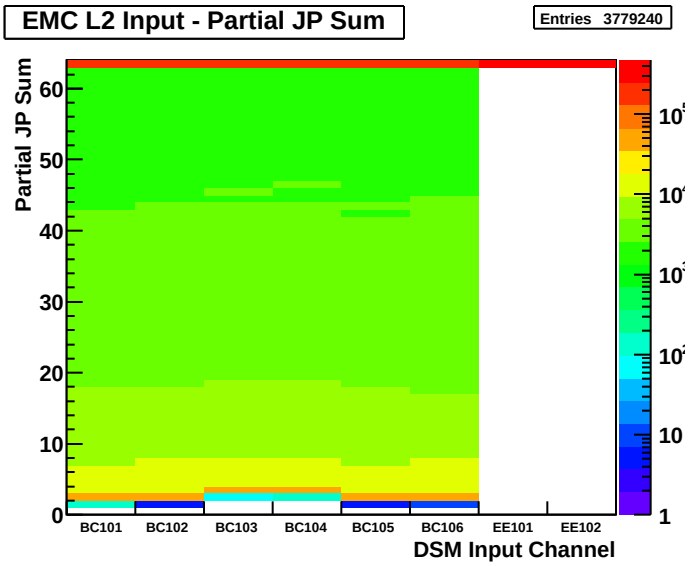

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

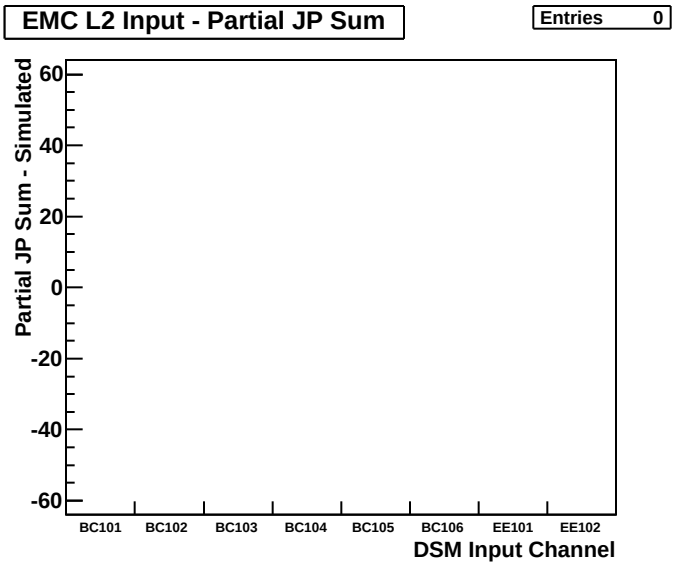

EMC L2 Input - JPY/JPB bits
Entries 3779240
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
263066 253927 258330 253595 257112 260103 472405 472405
37514 37464 36928 36556 38070 38906
19397 19215 19283 18870 19783 20337
152428 161799 157864 163384 157440 153059

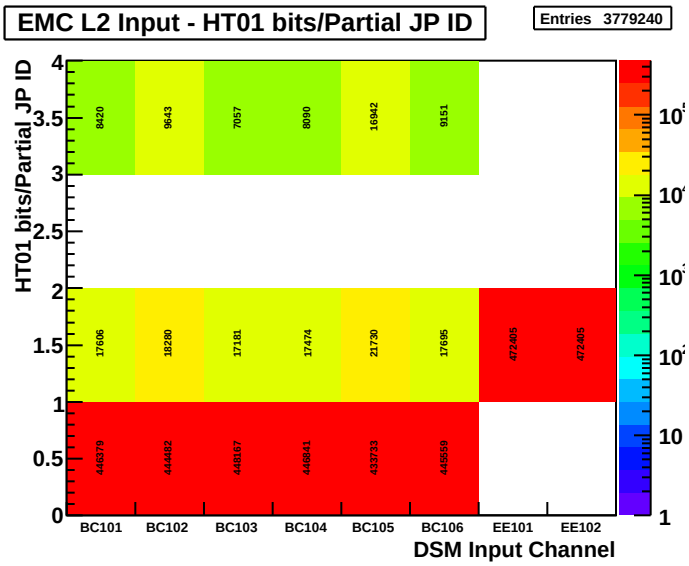

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

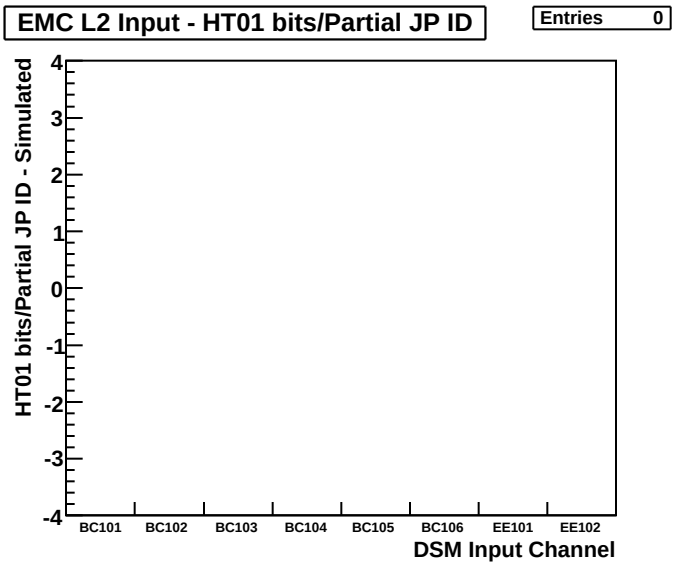

EMC L2 Input - JPZ/JPC bits
Entries 3779240
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
258382 252125 250083 257471 259566 258009 472405 472405
37215 37579 37721 38000 37391 37980
19248 19569 19679 19796 19693 19791
159560 163132 155922 157138 155755 156625

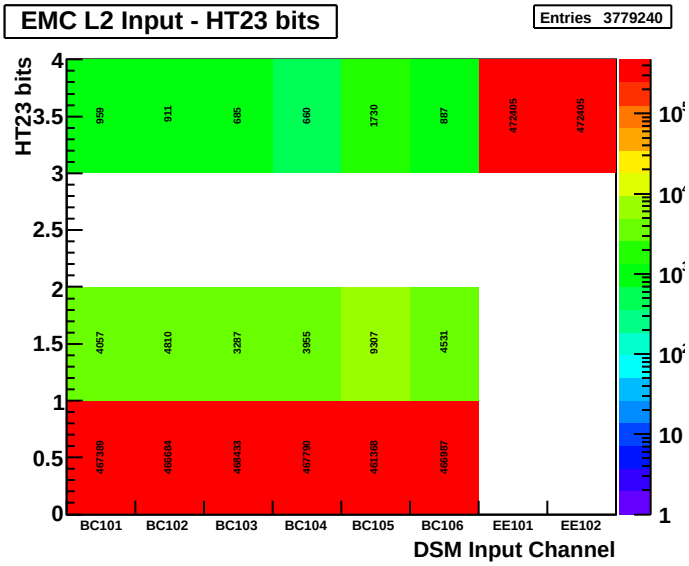

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

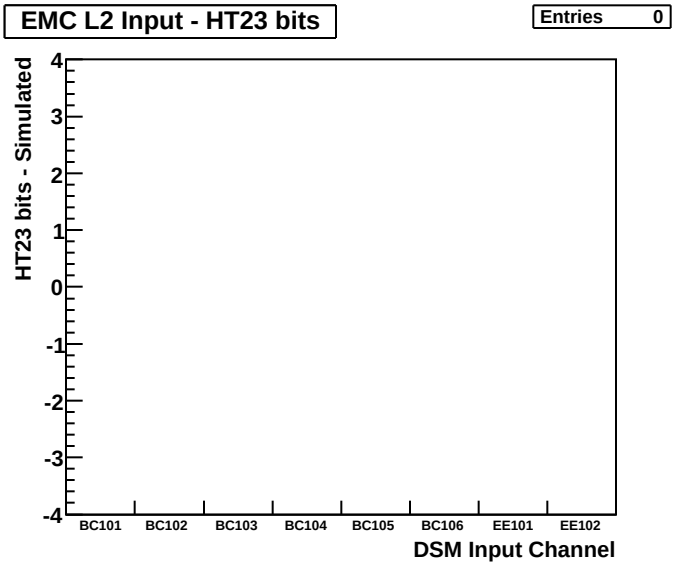

EMC L2 Input - Partial JP Sum
Entries 3779240
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

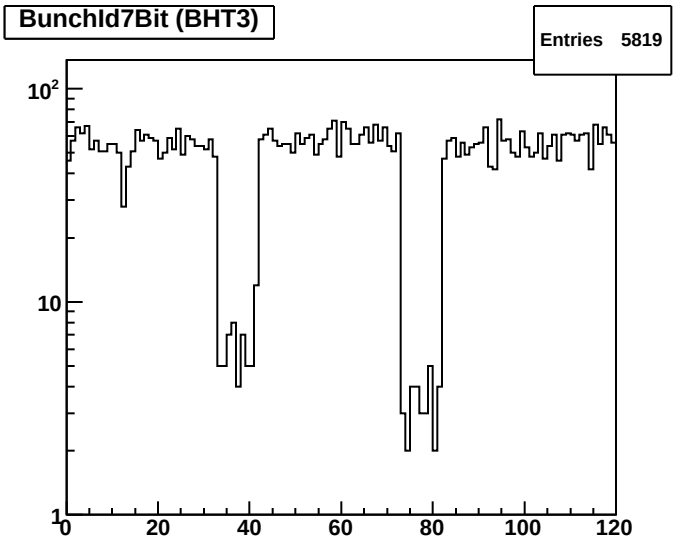

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

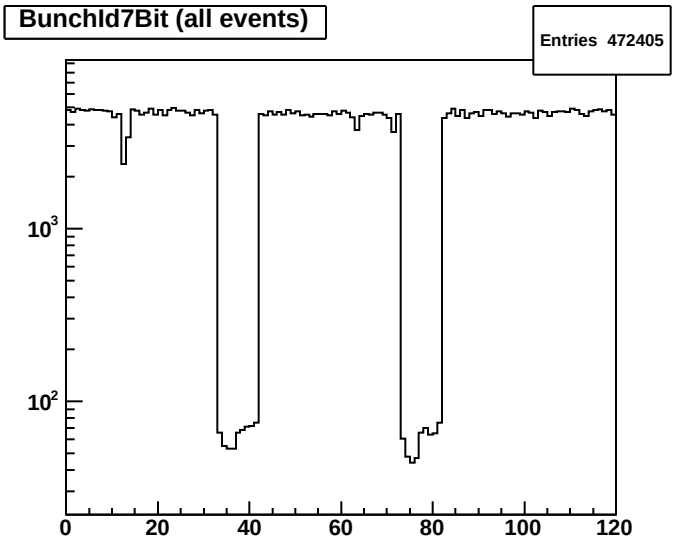

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3779240
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

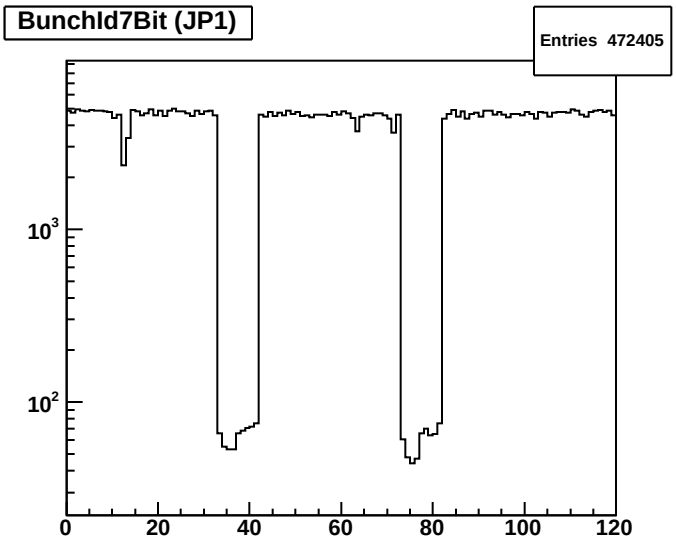

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

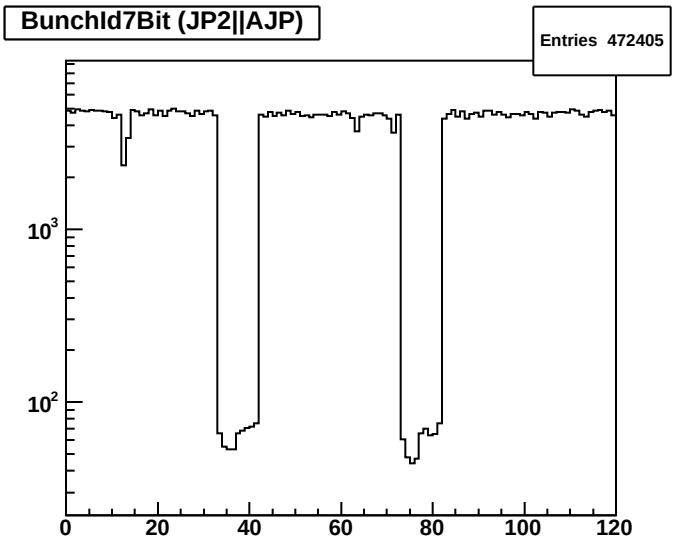

EMC L2 Input - HT23 bits
Entries 3779240
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

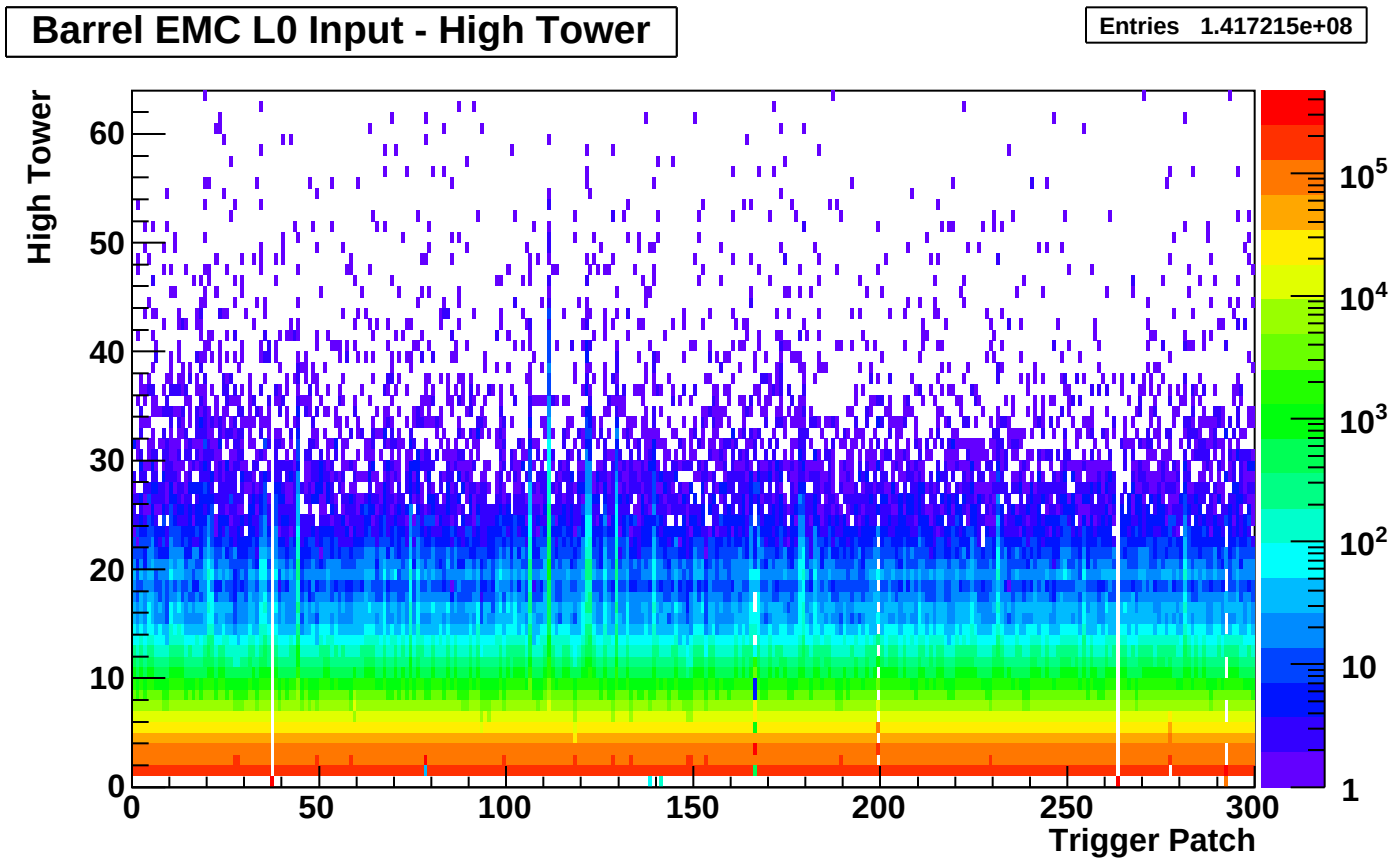

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

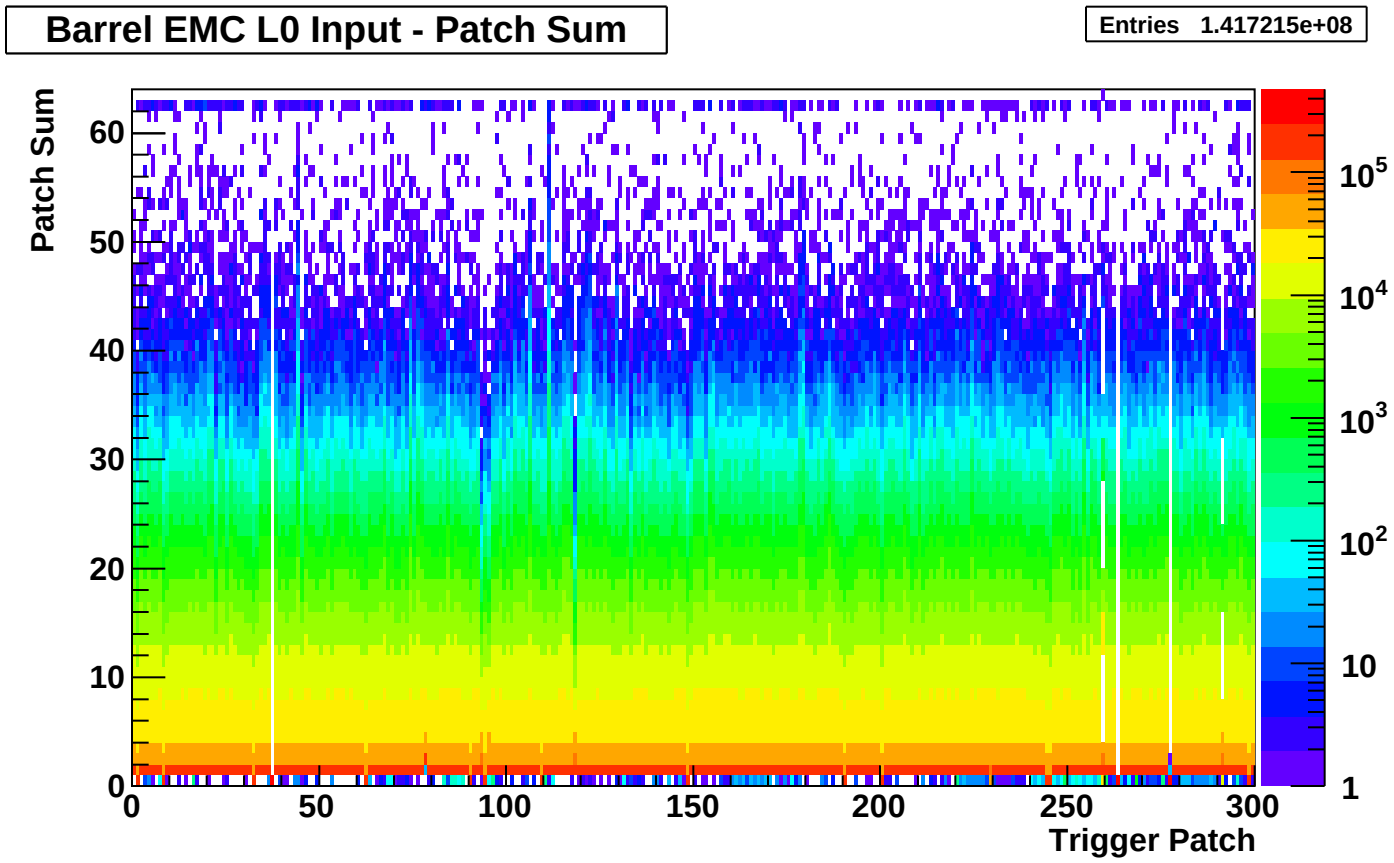

BunchId7Bit (BHT3)
Entries 5819
10^2
10
1
0
20
40
60
80
100
120

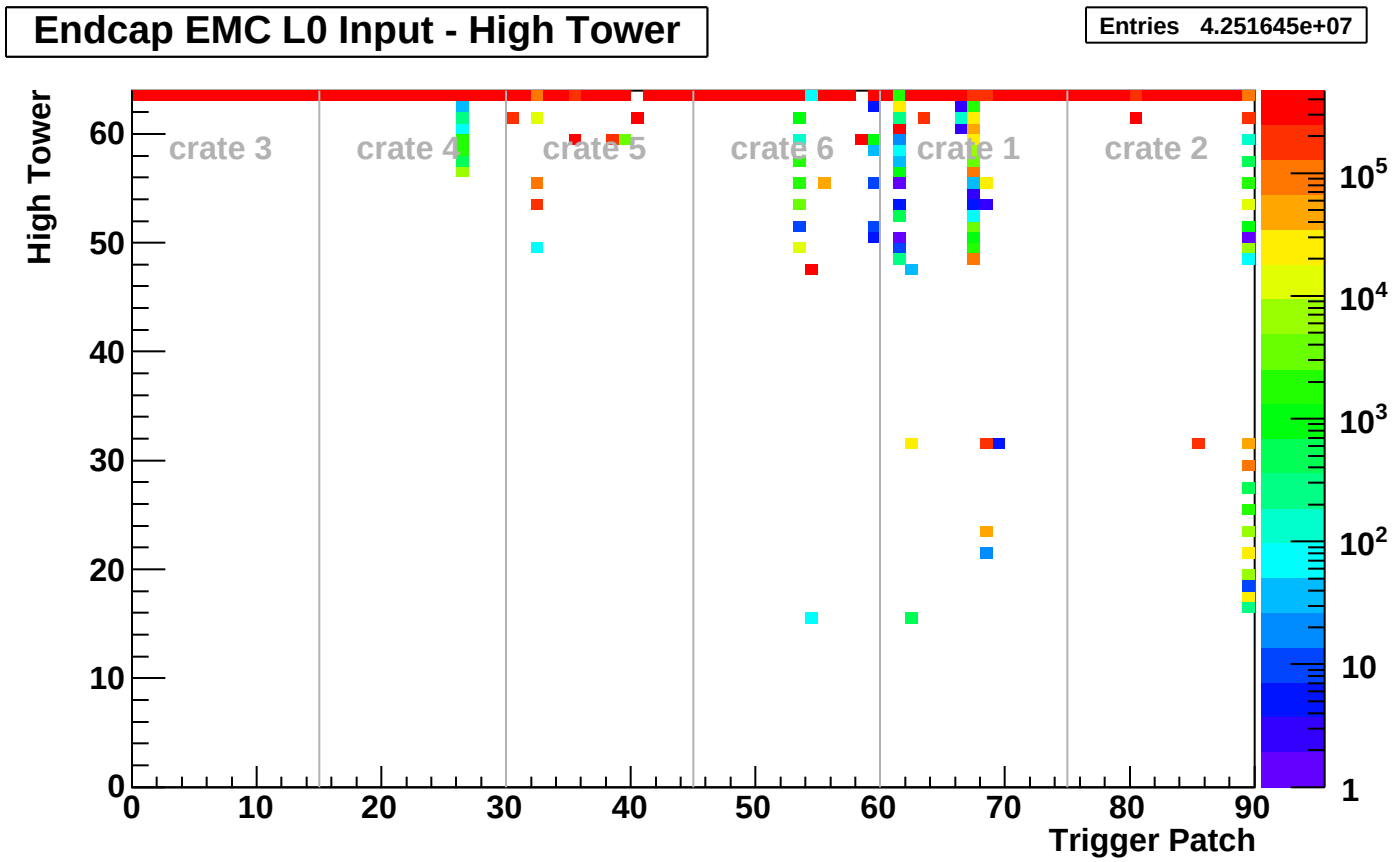

BunchId7Bit (all events)
Entries 472405
10^3
10^2
0
20
40
60
80
100
120

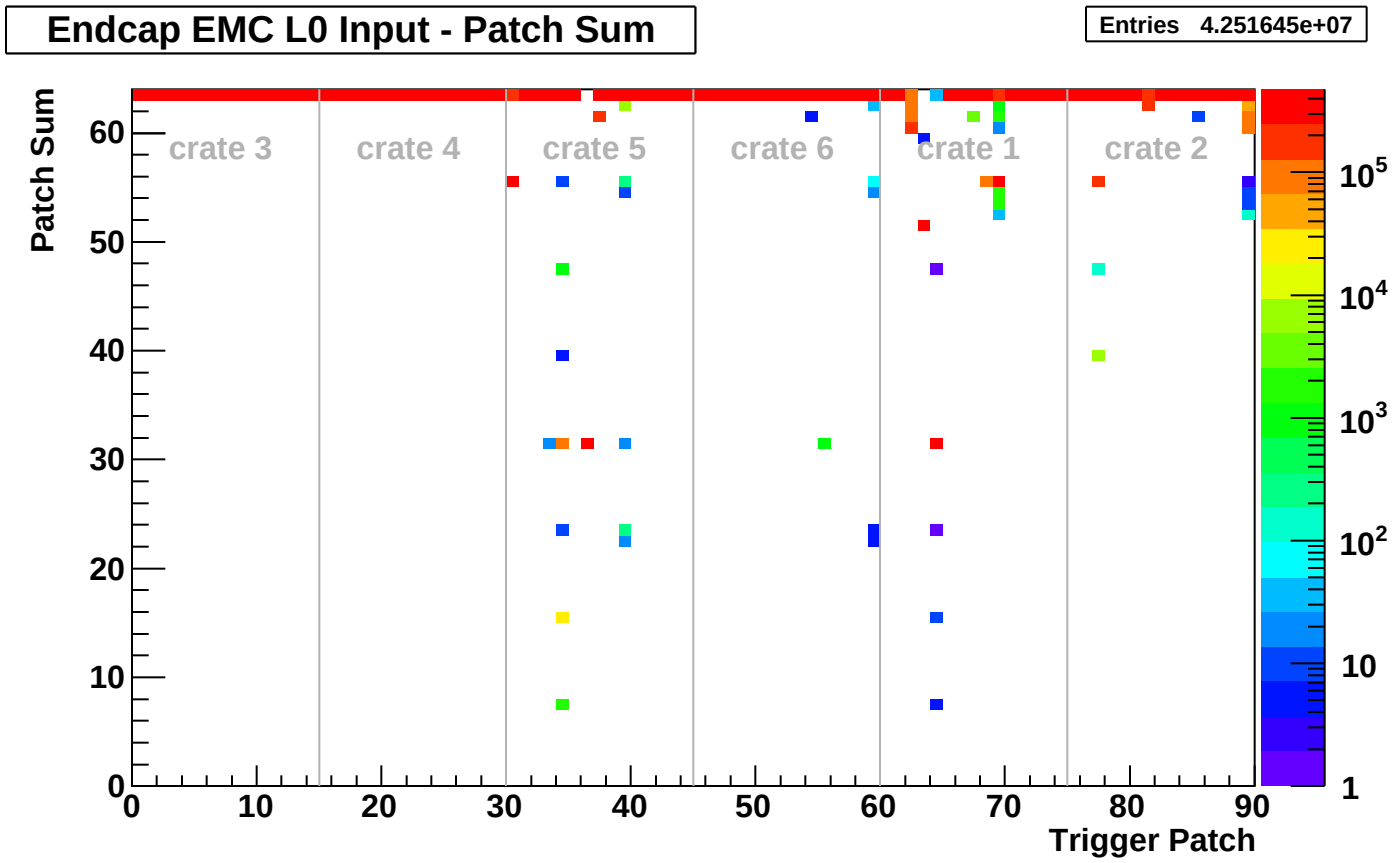

BunchId7Bit (JP1)
Entries 472405
10^3
10^2
0
20
40
60
80
100
120

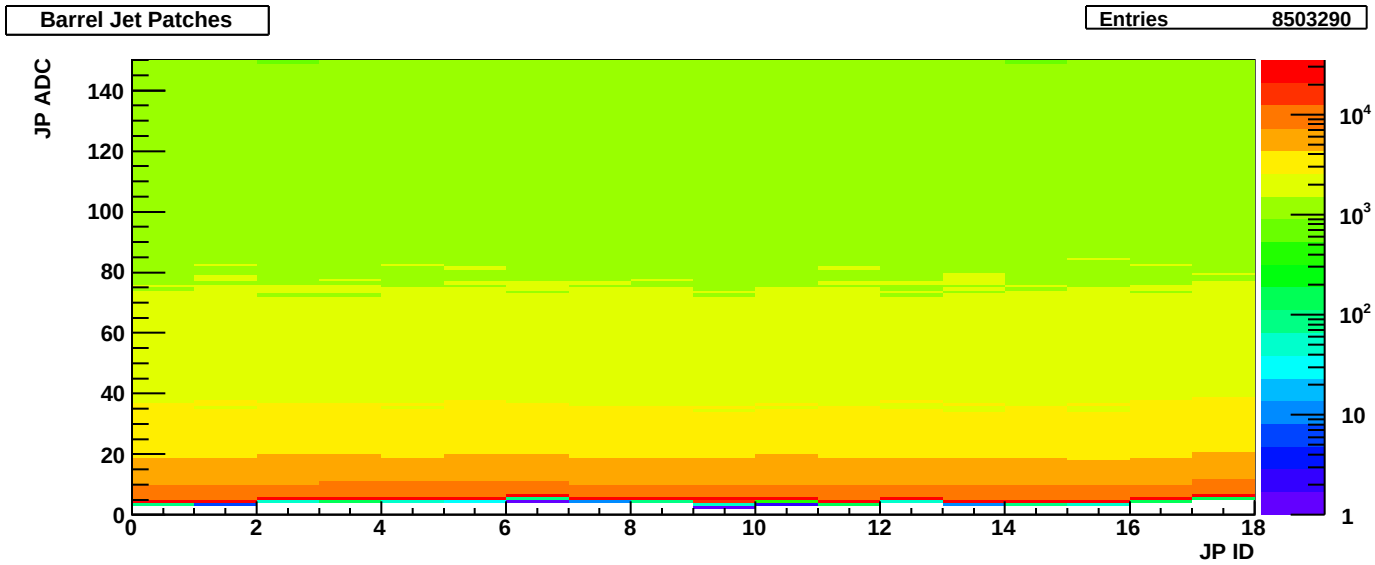

BunchId7Bit (JP2||AJP)
Entries 472405
10^3
10^2
0
20
40
60
80
100
120

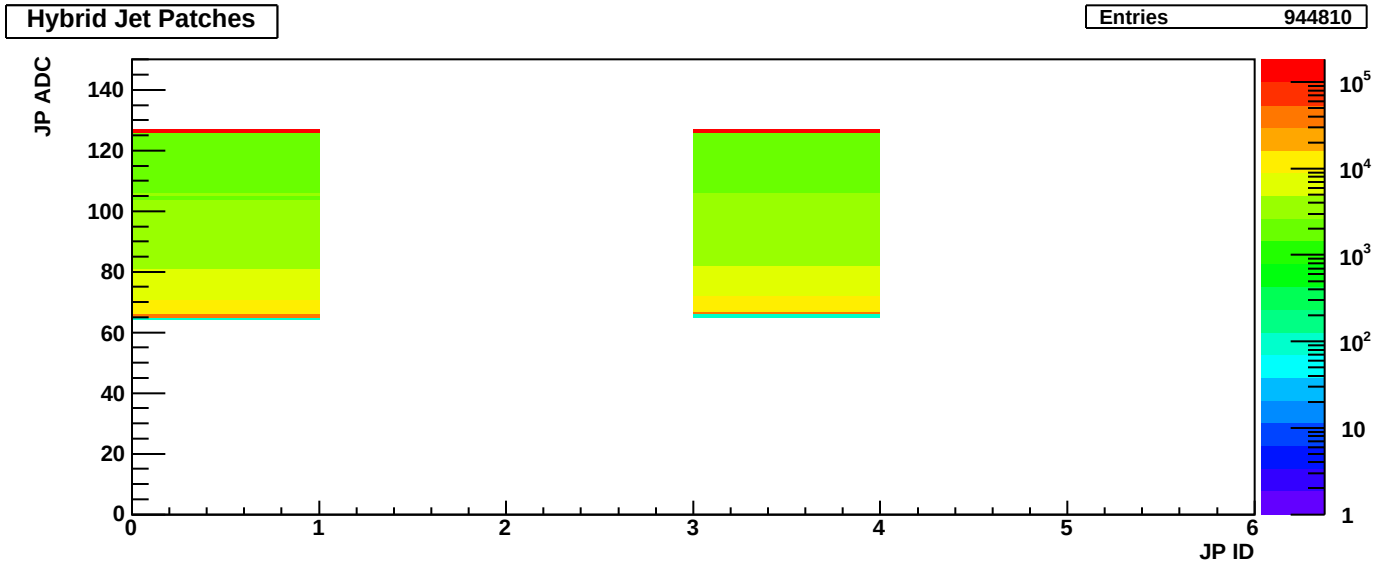

Barrel EMC L0 Input - High Tower
Entries 1.417215e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

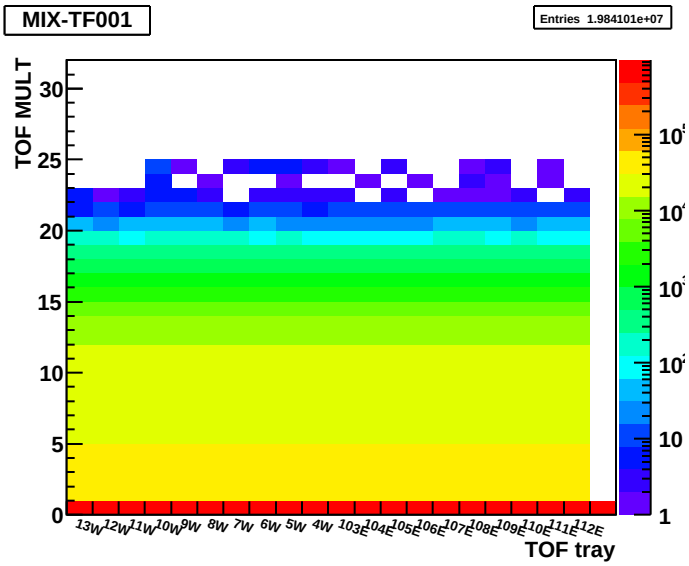

Barrel EMC L0 Input - Patch Sum
Entries 1.417215e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

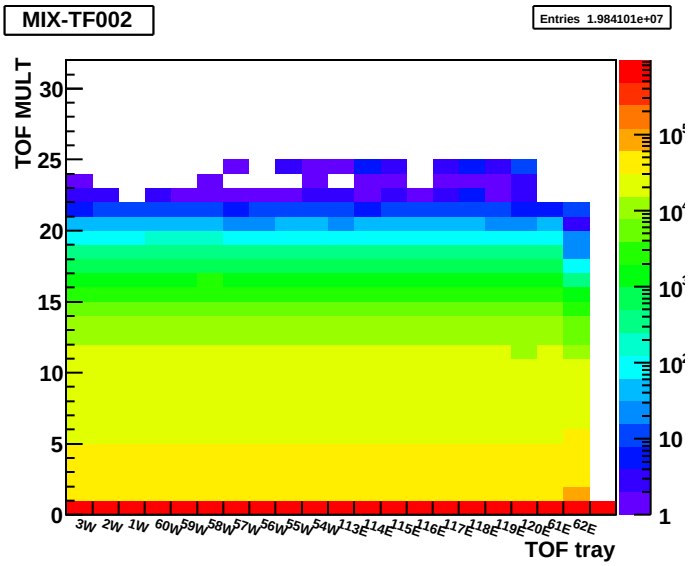

Endcap EMC L0 Input - High Tower
Entries 4.251645e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

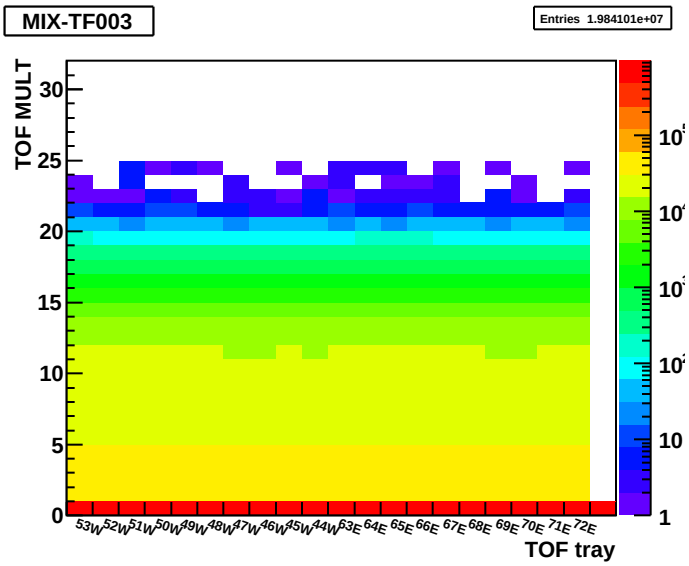

Endcap EMC L0 Input - Patch Sum
Entries 4.251645e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

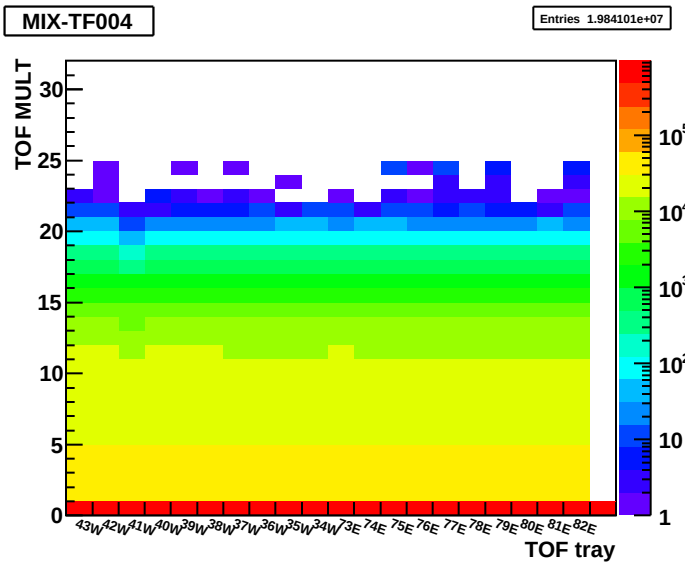

Barrel Jet Patches
Entries 8503290
JP ADC
JP ID

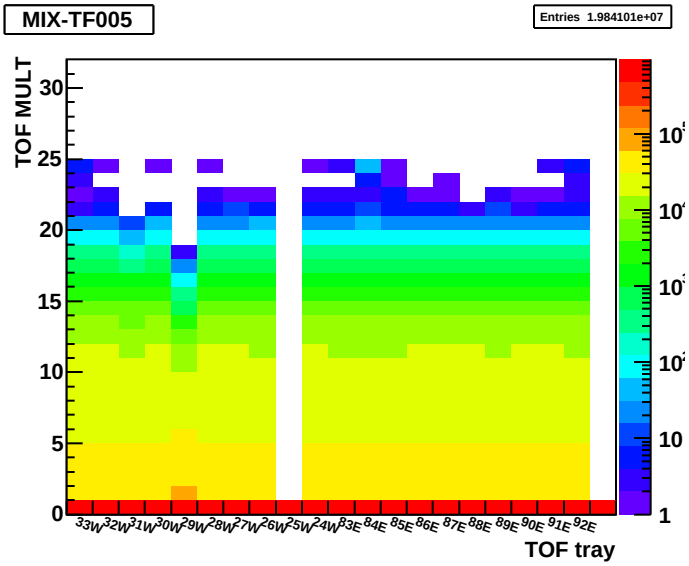

Hybrid Jet Patches
Entries 944810
JP ADC
JP ID

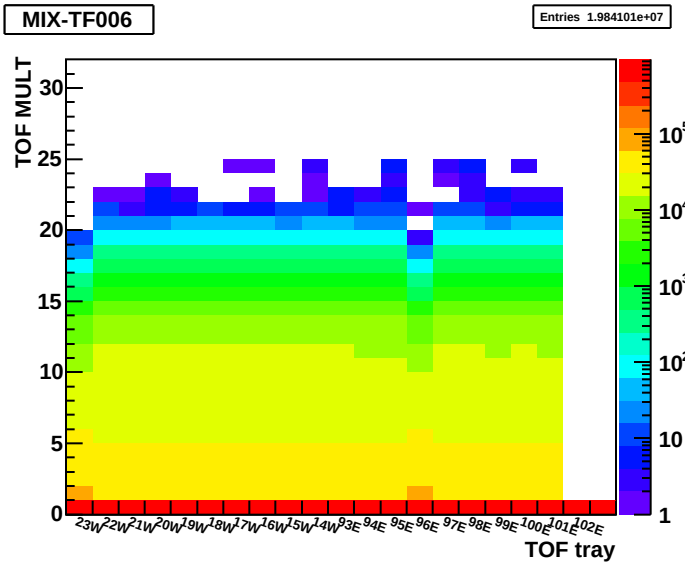

MIX-TF001
Entries 1.984101e+07
TOF MULT
TOF tray

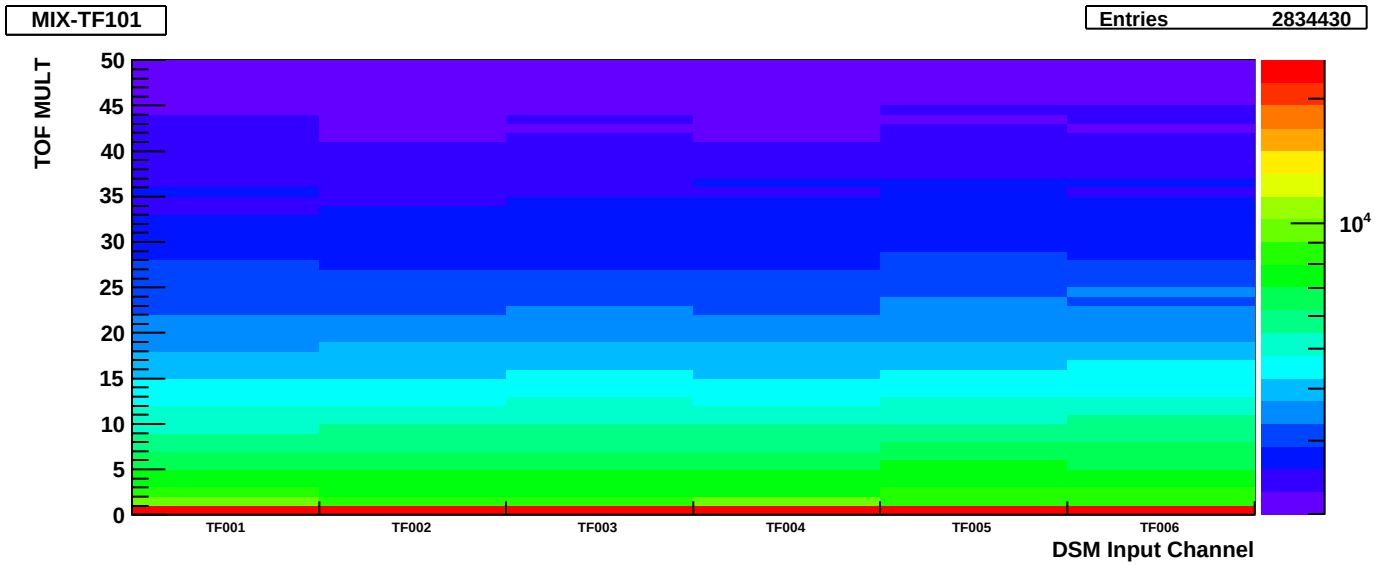

MIX-TF002
Entries 1.984101e+07
TOF MULT
TOF tray

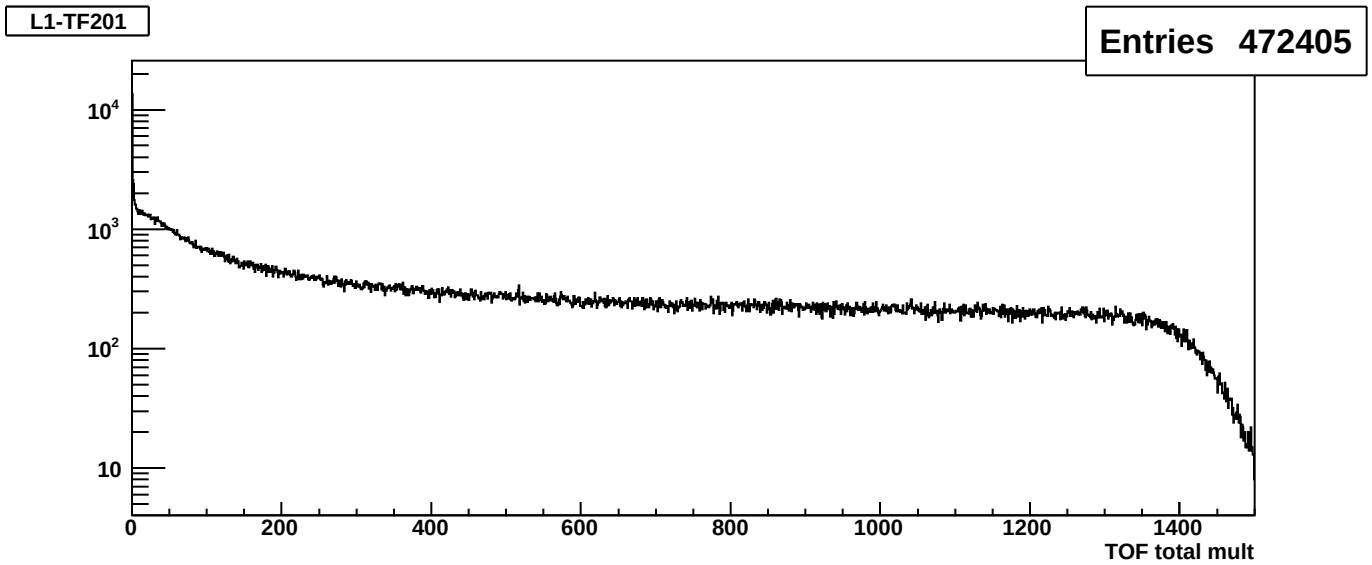

MIX-TF003
Entries 1.984101e+07
TOF MULT
TOF tray

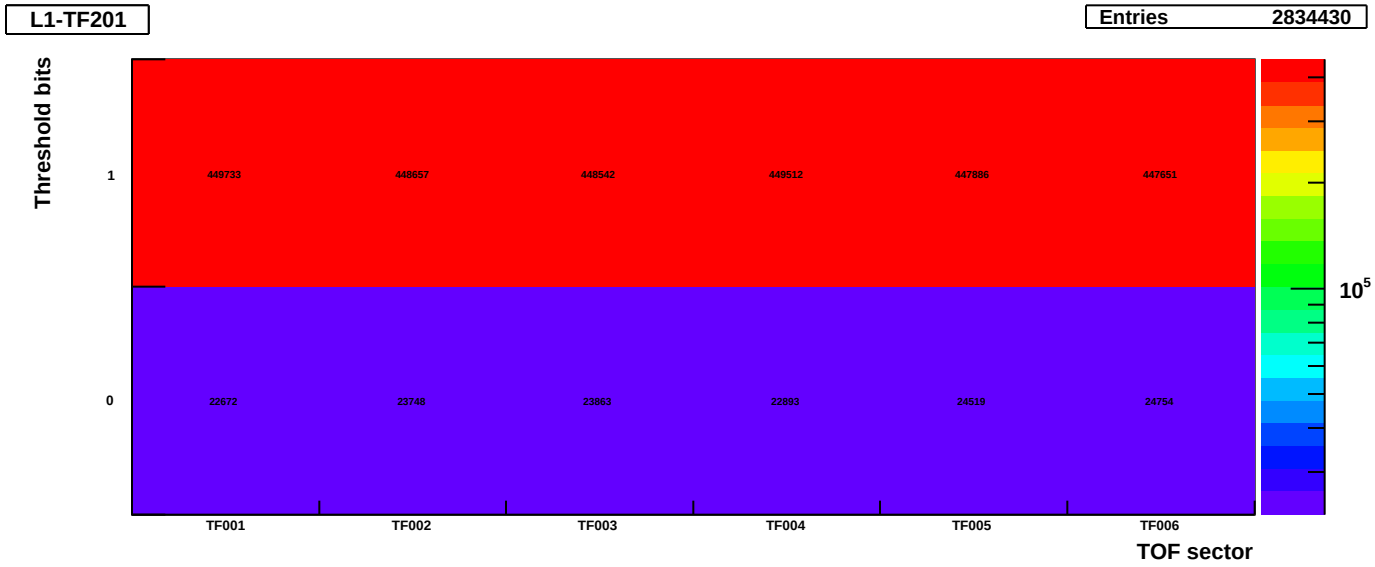

MIX-TF004
Entries 1.984101e+07
TOF MULT
TOF tray

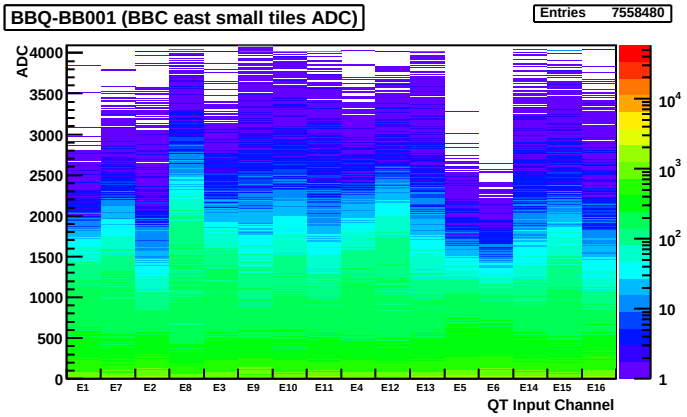

MIX-TF005
Entries 1.984101e+07
TOF MULT
TOF tray

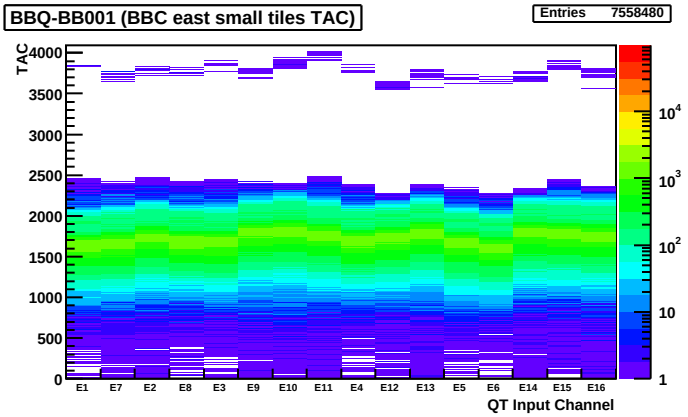

MIX-TF006
Entries 1.984101e+07
TOF MULT
TOF tray

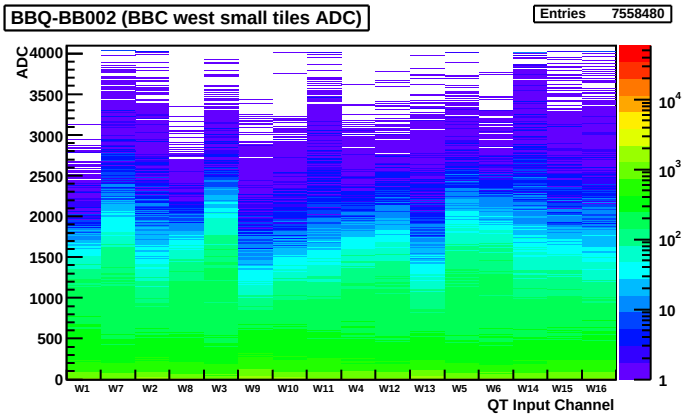

MIX-TF101
Entries 2834430
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

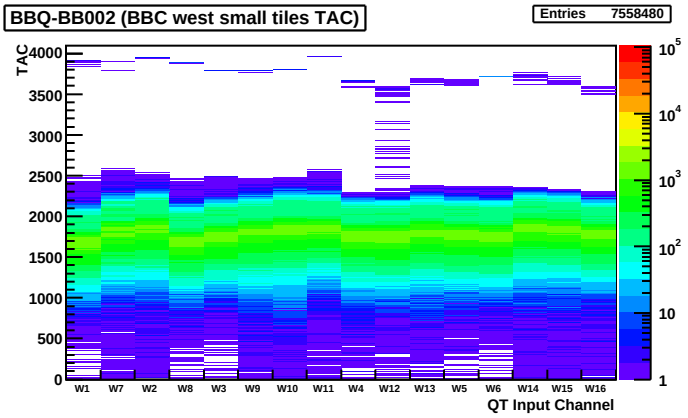

L1-TF201
Entries 472405
10^4
10^3
10^2
10
0
200
400
600
800
1000
1200
1400
TOF total mult

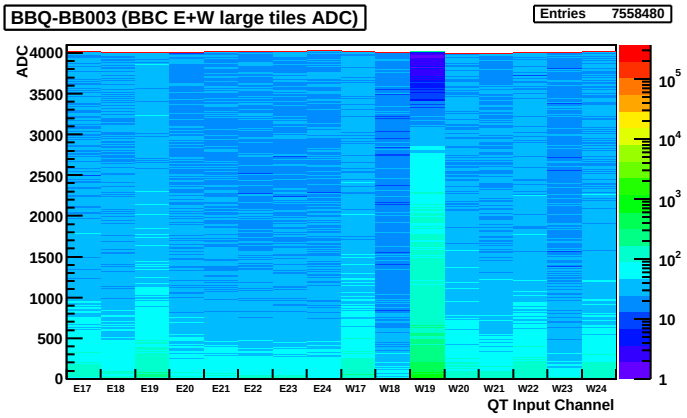

L1-TF201
Entries 2834430
Threshold bits
1
0
449733
448657
448542
449512
447886
447651
22672
23748
23863
22893
24519
24754
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

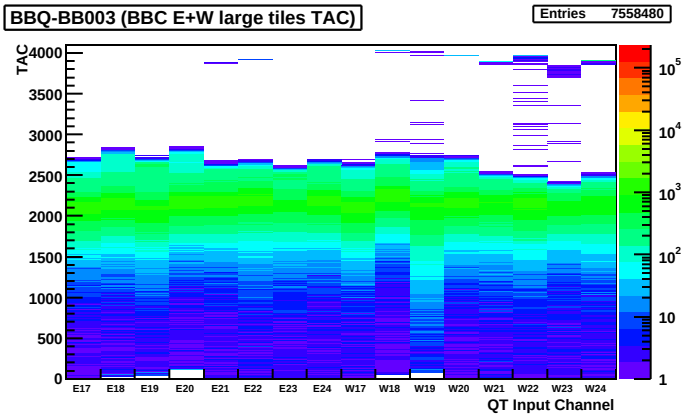

BBQ-BB001 (BBC east small tiles ADC)
Entries 7558480
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

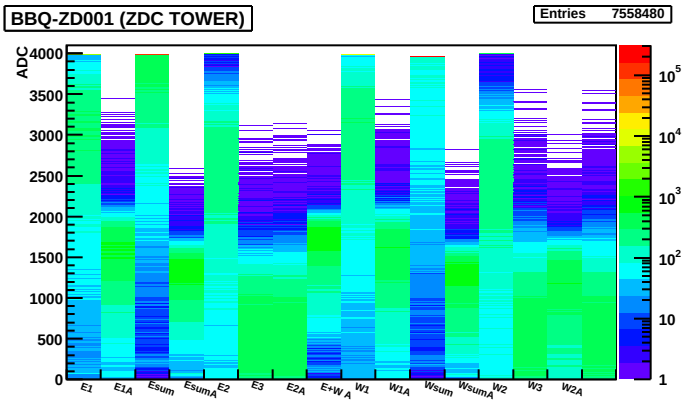

BBQ-BB001 (BBC east small tiles TAC)
Entries 7558480
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

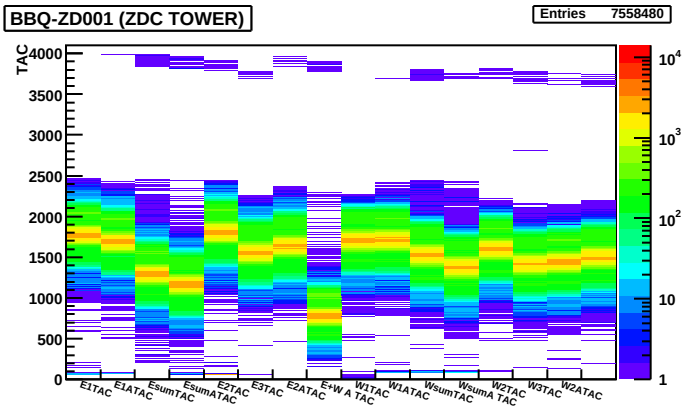

BBQ-BB002 (BBC west small tiles ADC)
Entries 7558480
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

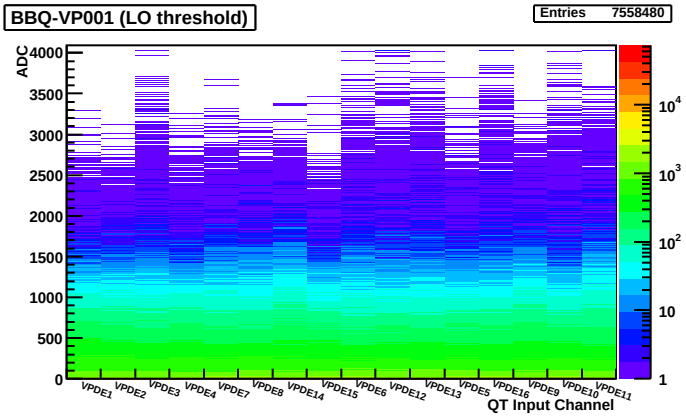

BBQ-BB002 (BBC west small tiles TAC)
Entries 7558480
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

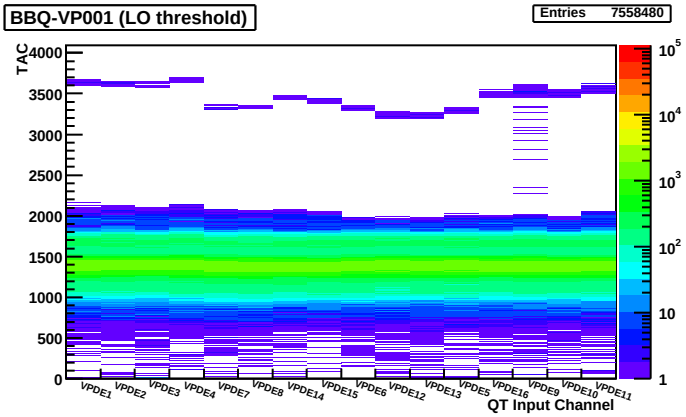

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 7558480
ADC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

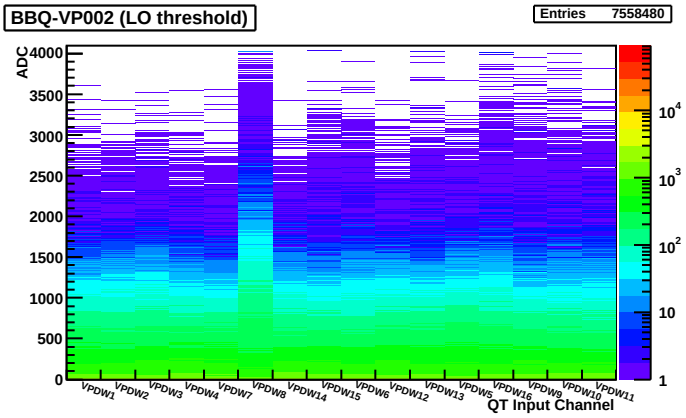

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 7558480
TAC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

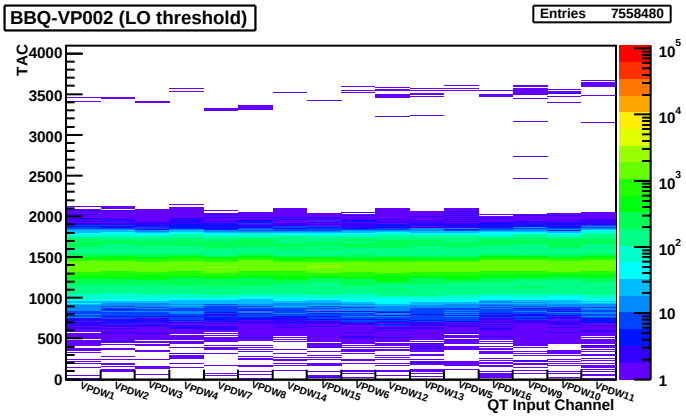

BBQ-ZD001 (ZDC TOWER)
Entries 7558480
ADC
E1 E1A Esum EsumA E2 E3 E2A E+W A W1 W1A Wsum WsumA W2 W3 W2A

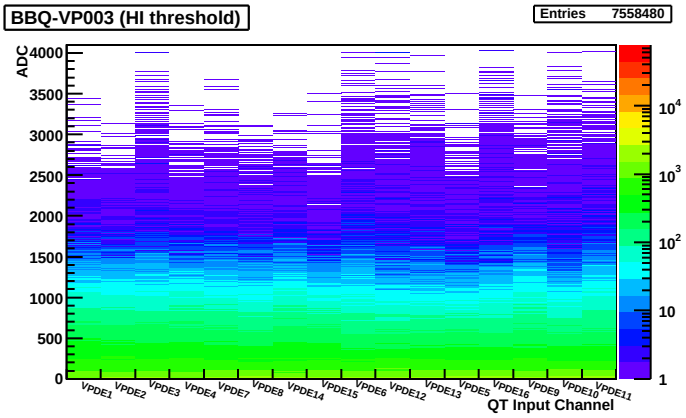

BBQ-ZD001 (ZDC TOWER)
Entries 7558480
TAC
E1TAC E1ATAC EsumTAC EsumATAC E2TAC E3TAC E2ATAC E+W A TAC W1TAC W1ATAC WsumTAC WsumA TAC W2TAC W3TAC W2ATAC

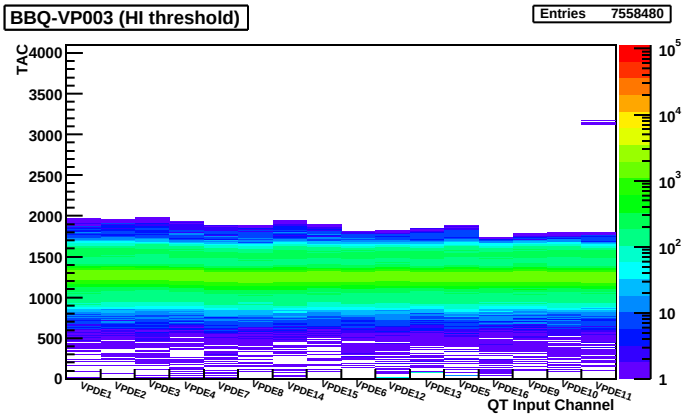

BBQ-VP001 (LO threshold)
Entries 7558480
ADC
QT Input Channel

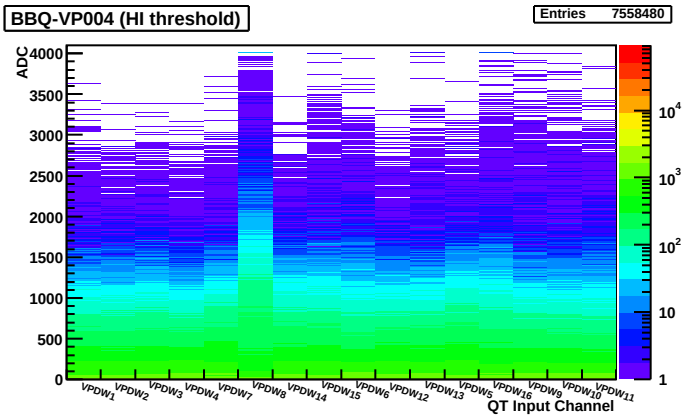

BBQ-VP001 (LO threshold)
Entries 7558480
TAC
QT Input Channel

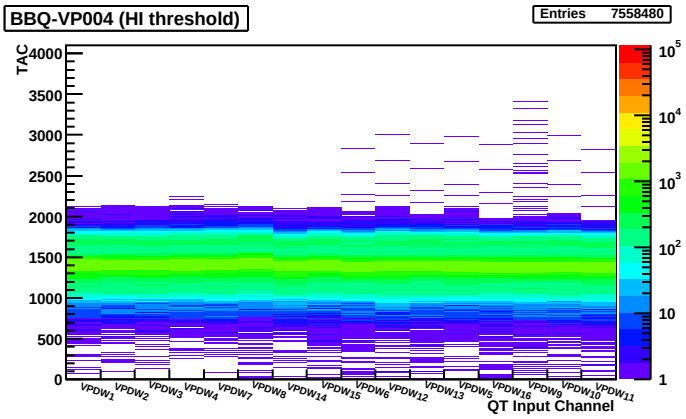

BBQ-VP002 (LO threshold)
Entries 7558480
ADC
QT Input Channel

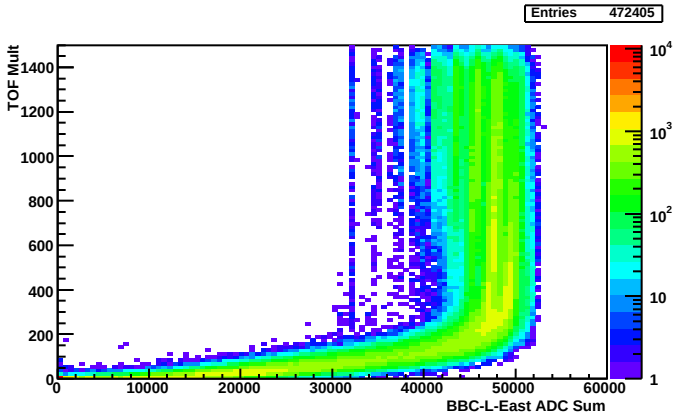

BBQ-VP002 (LO threshold)
Entries 7558480
TAC
QT Input Channel

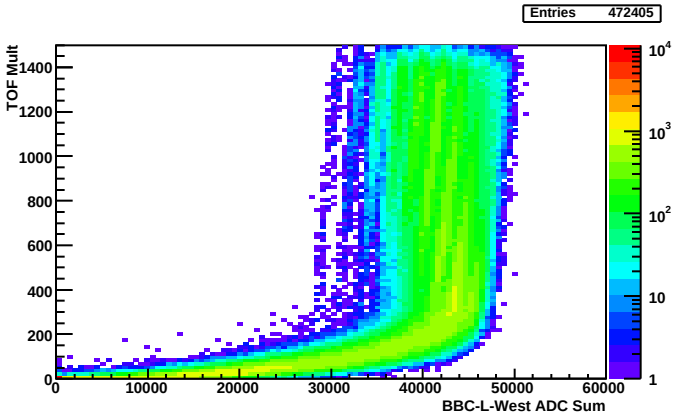

BBQ-VP003 (HI threshold)
Entries 7558480
ADC
QT Input Channel

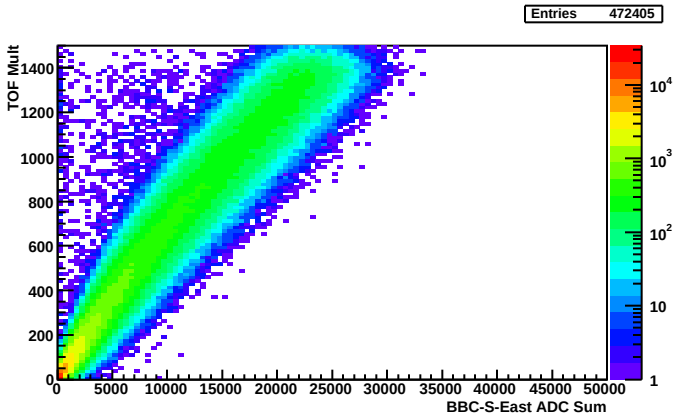

BBQ-VP003 (HI threshold)
Entries 7558480
TAC
QT Input Channel

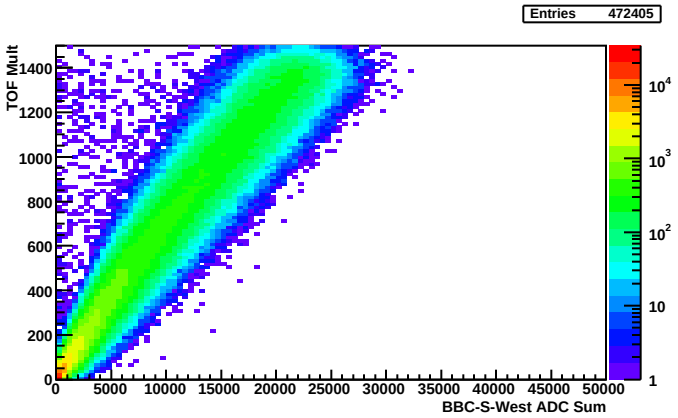

BBQ-VP004 (HI threshold)
Entries 7558480
ADC
QT Input Channel

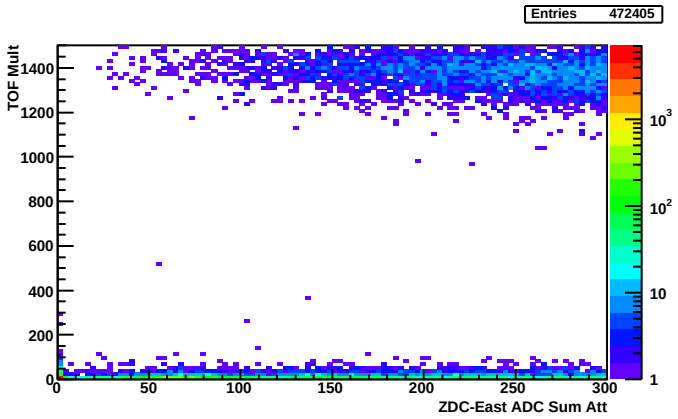

BBQ-VP004 (HI threshold)
Entries 7558480
TAC
QT Input Channel

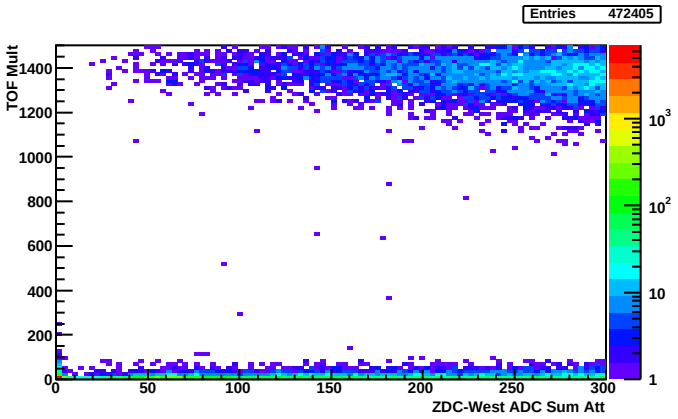

Entries 472405
TOF Mult
BBC-L-East ADC Sum

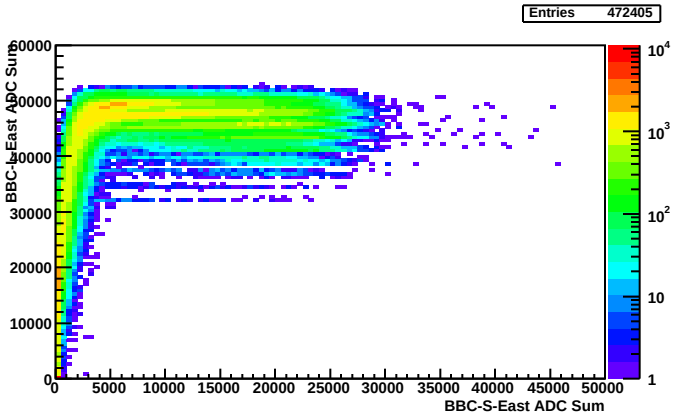

Entries 472405
TOF Mult
BBC-L-West ADC Sum

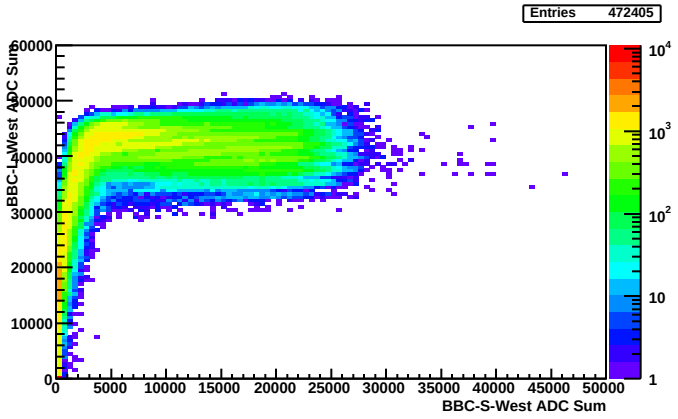

Entries 472405
TOF Mult
BBC-S-East ADC Sum

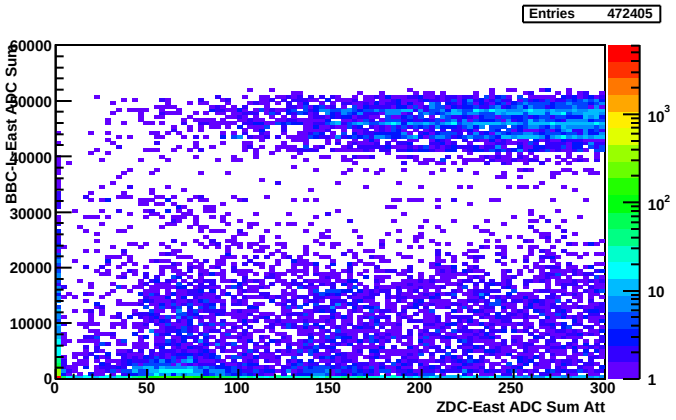

Entries 472405
TOF Mult
BBC-S-West ADC Sum

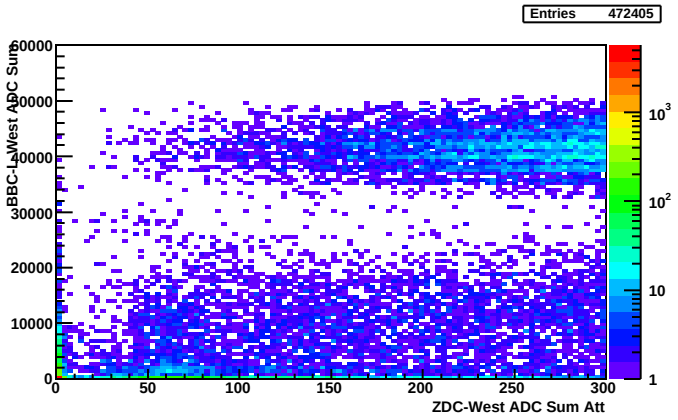

Entries 472405
TOF Mult
ZDC-East ADC Sum Att

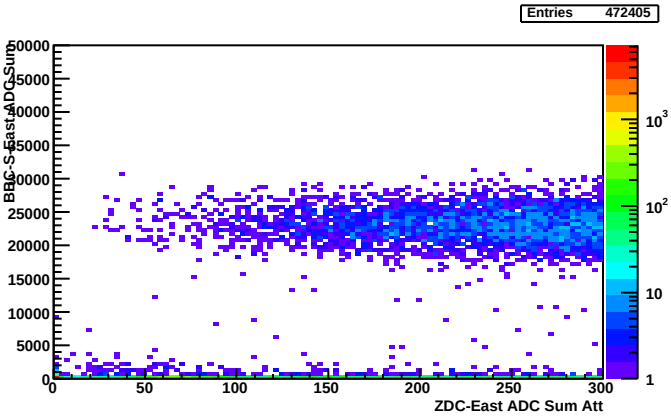

Entries 472405
TOF Mult
ZDC-West ADC Sum Att

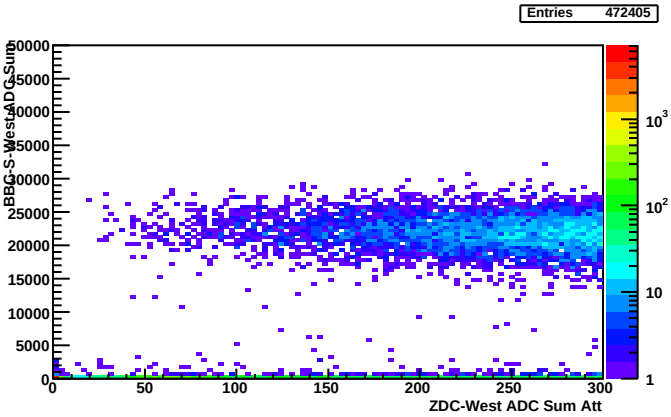

Entries 472405
BBC-L-East ADC Sum
BBC-S-East ADC Sum

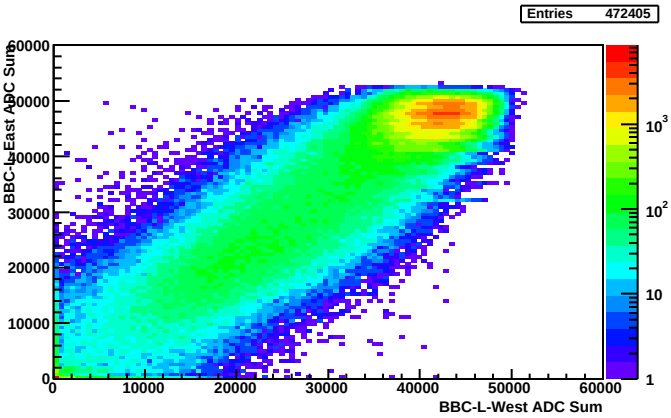

Entries 472405
BBC-L-West ADC Sum
BBC-S-West ADC Sum

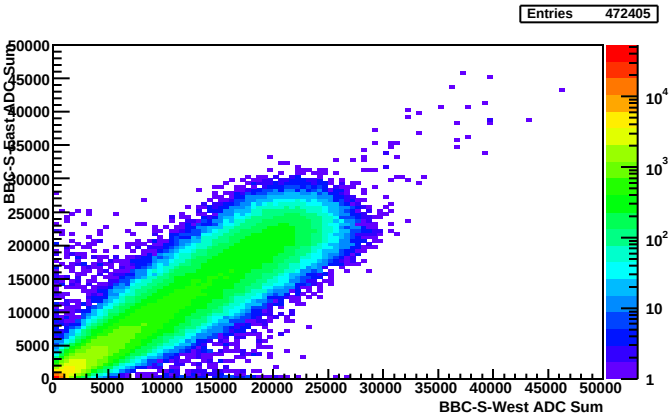

Entries 472405
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

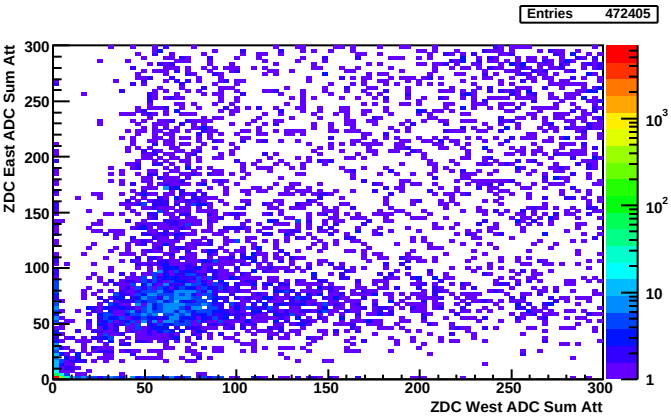

Entries 472405
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

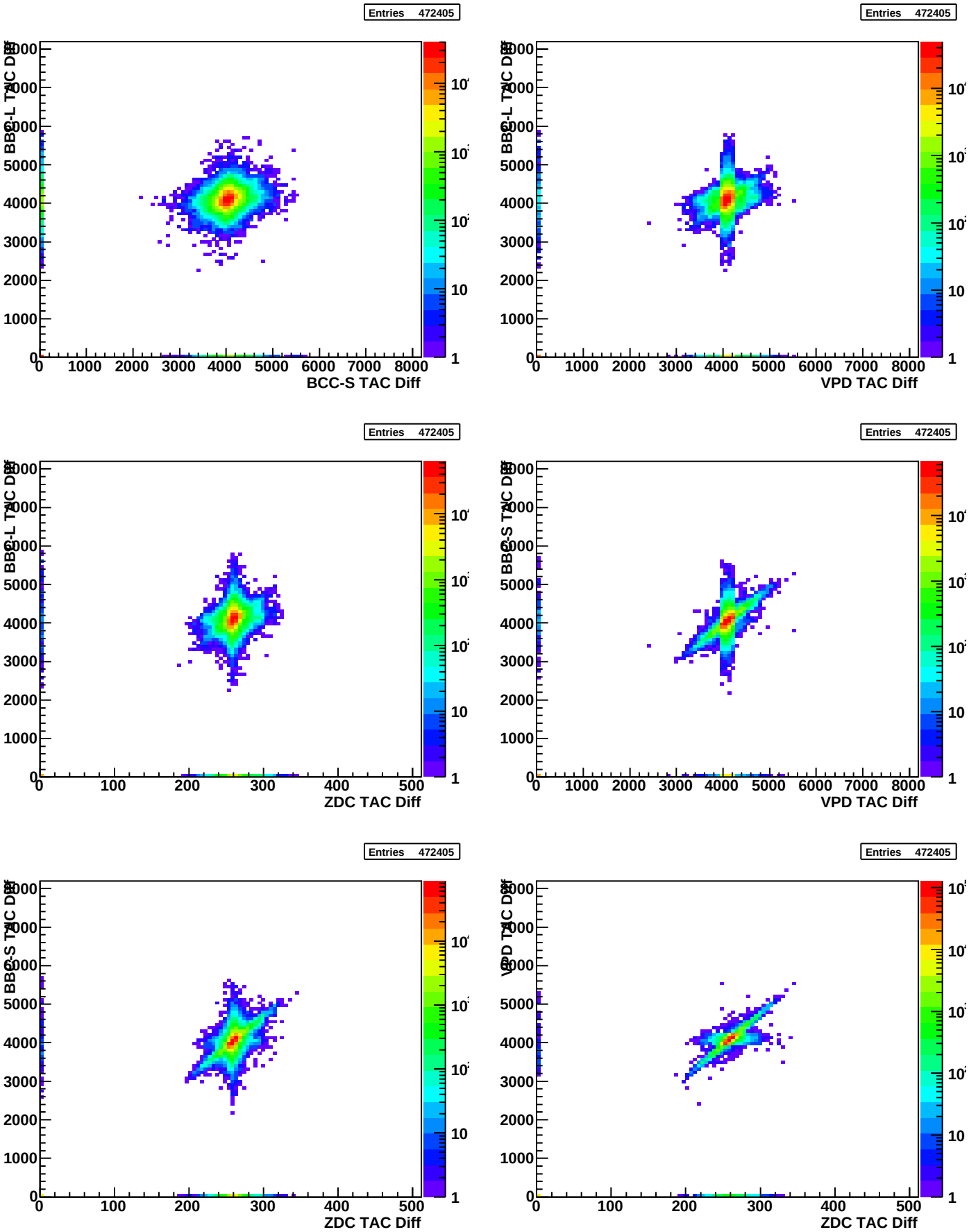

Entries 472405
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

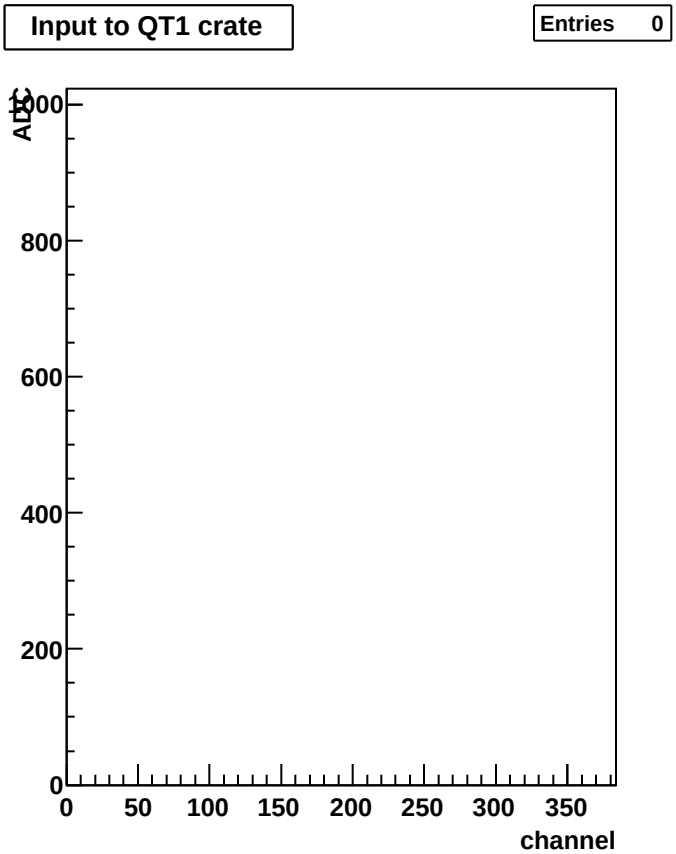

Entries 472405
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

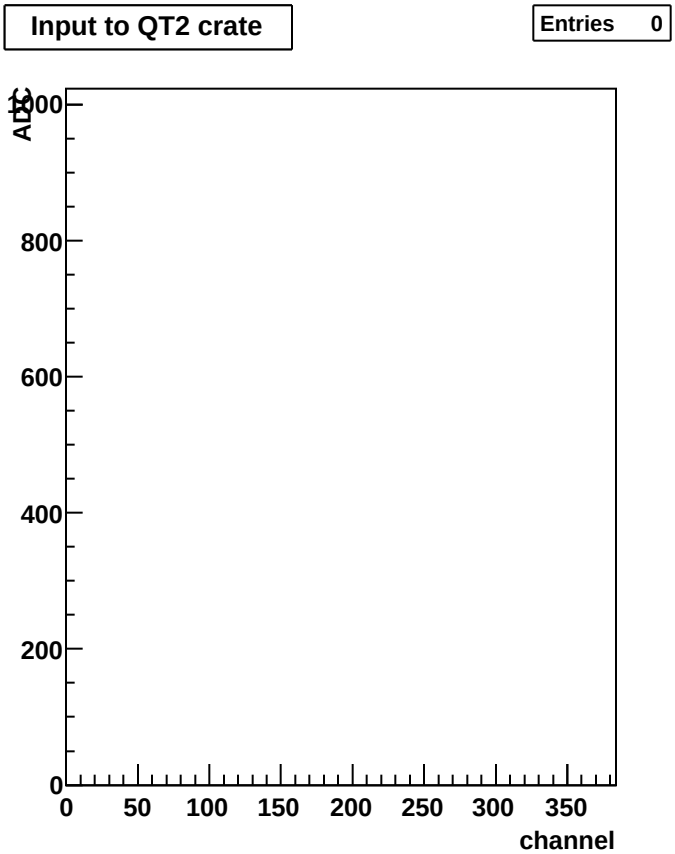

Entries 472405
BBC-L-East ADC Sum
BBC-L-West ADC Sum

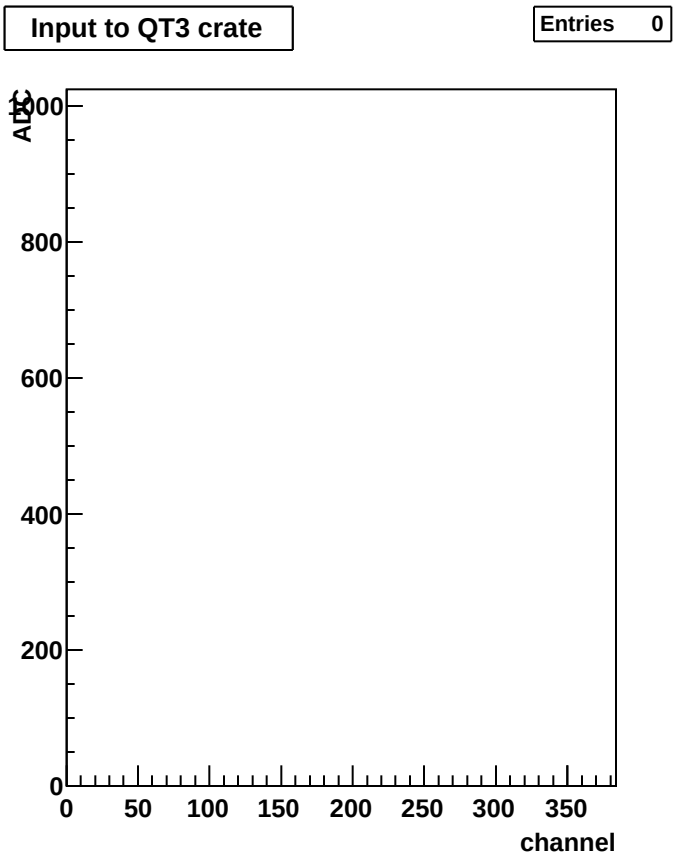

Entries 472405
BBC-S-East ADC Sum
BBC-S-West ADC Sum

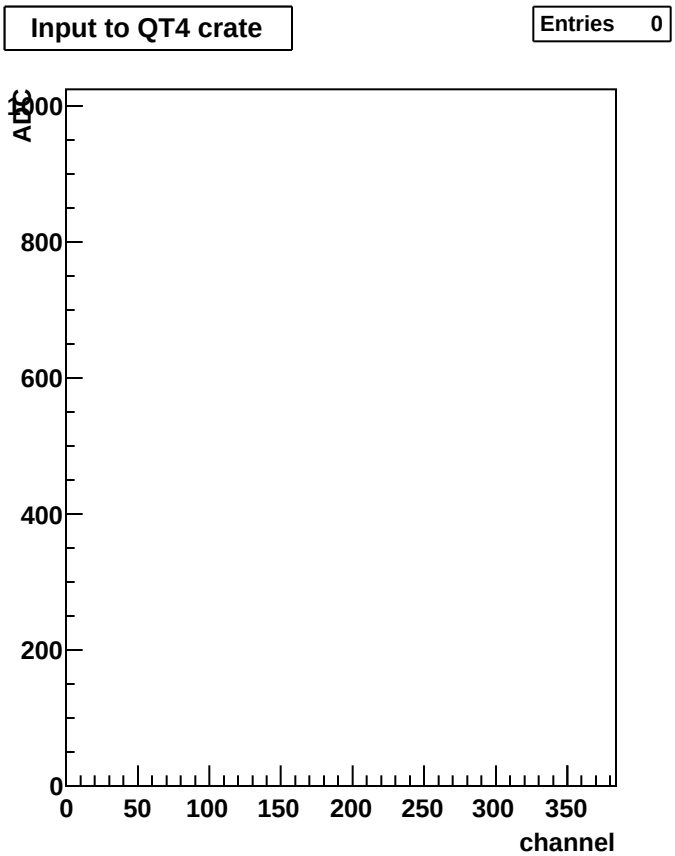

Entries 472405
ZDC East ADC Sum Att
ZDC West ADC Sum Att

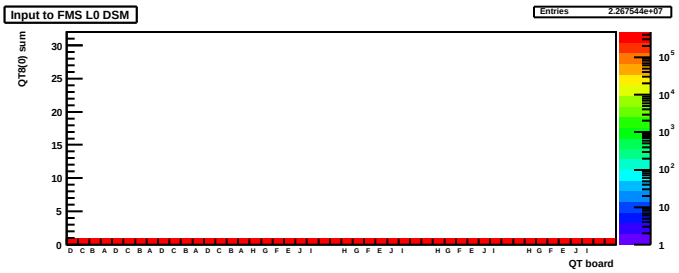

Entries 472405
BBC-L TAC Diff
BCC-S TAC Diff
Entries 472405
BBC-L TAC Diff
VPD TAC Diff
Entries 472405
BBC-L TAC Diff
ZDC TAC Diff
Entries 472405
BBC-S TAC Diff
VPD TAC Diff
Entries 472405
BBC-S TAC Diff
ZDC TAC Diff
Entries 472405
VPD TAC Diff
ZDC TAC Diff

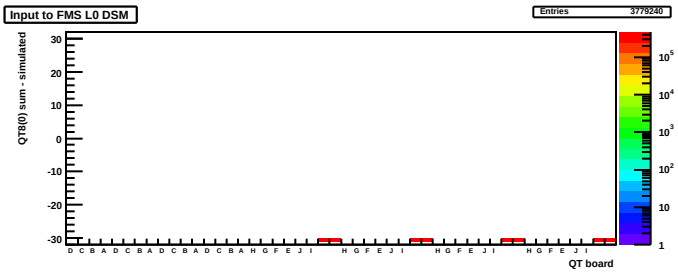

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

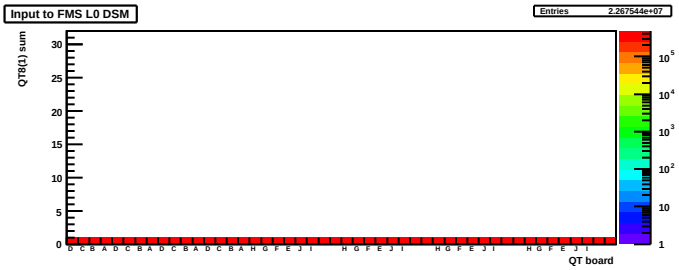

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

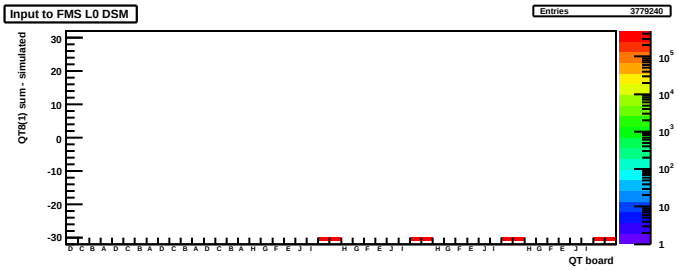

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

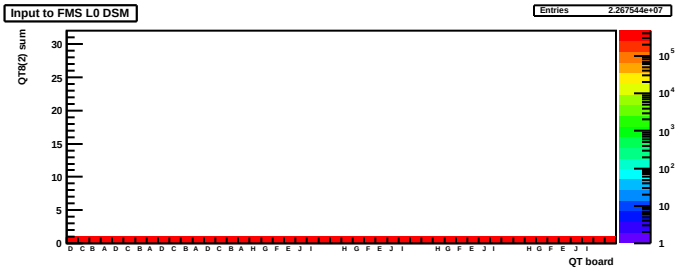

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

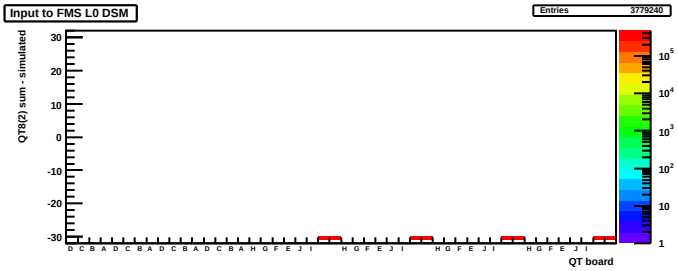

Input to FMS L0 DSM
Entries 2.267544e+07
QT8(0) sum
QT board

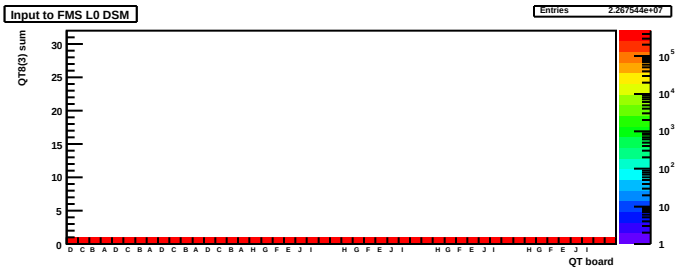

Input to FMS L0 DSM
Entries 3779240
QT8(0) sum - simulated
QT board

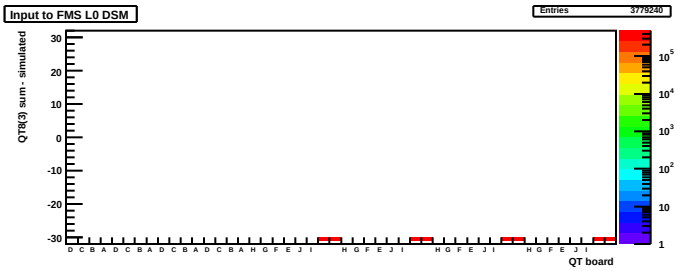

Input to FMS L0 DSM
Entries 2.267544e+07
QT8(1) sum
QT board

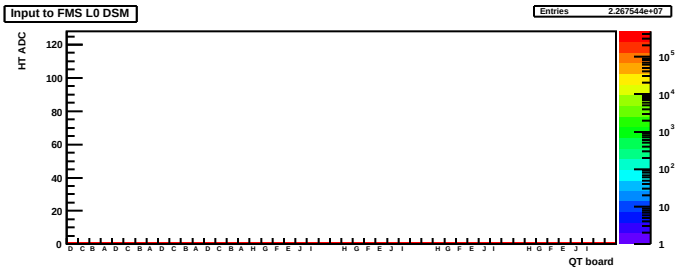

Input to FMS L0 DSM
Entries 3779240
QT8(1) sum - simulated
QT board

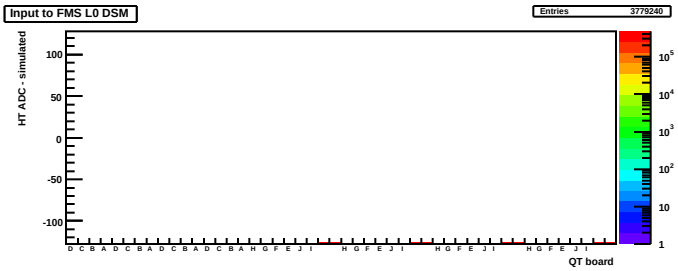

Input to FMS L0 DSM
Entries 2.267544e+07
QT8(2) sum
QT board

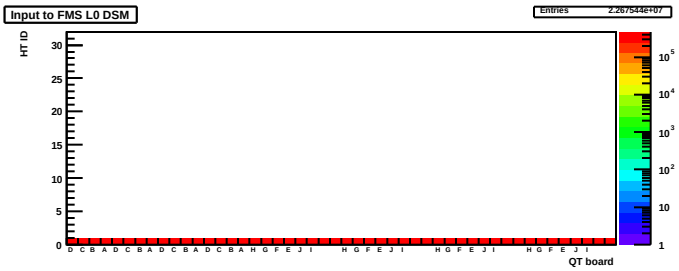

Input to FMS L0 DSM
Entries 3779240
QT8(2) sum - simulated
QT board

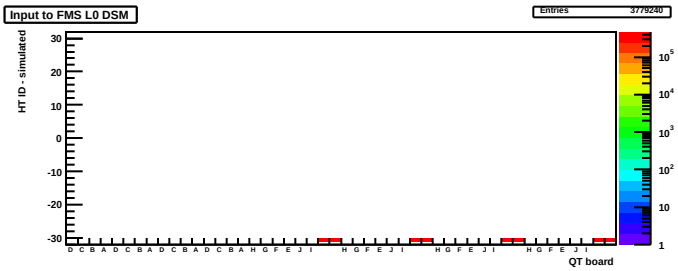

Input to FMS L0 DSM
Entries 2.267544e+07
QT8(3) sum
QT board

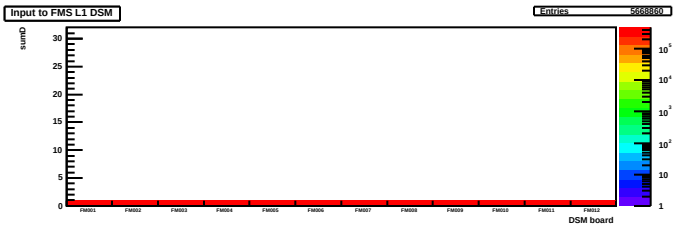

Input to FMS L0 DSM
Entries 3779240
QT8(3) sum - simulated
QT board

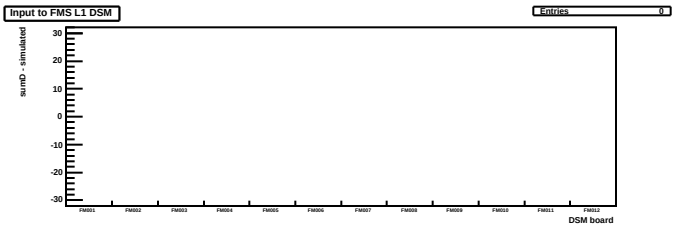

Input to FMS L0 DSM
Entries 2.267544e+07
HT ADC
QT board

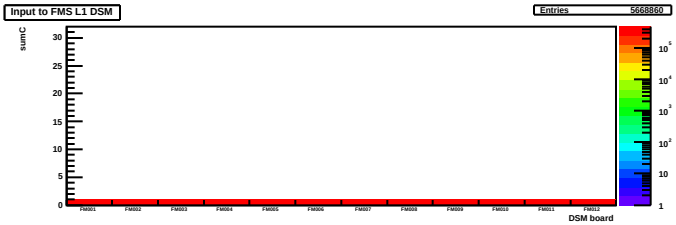

Input to FMS L0 DSM
Entries 3779240
HT ADC - simulated
QT board

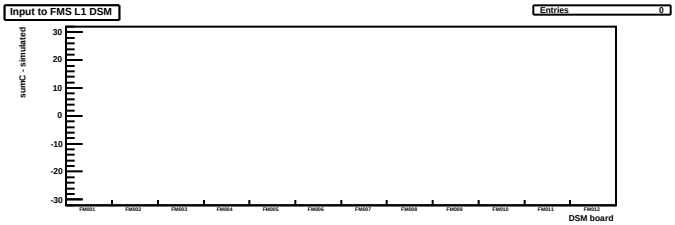

Input to FMS L0 DSM
Entries 2.267544e+07
HT ID
QT board

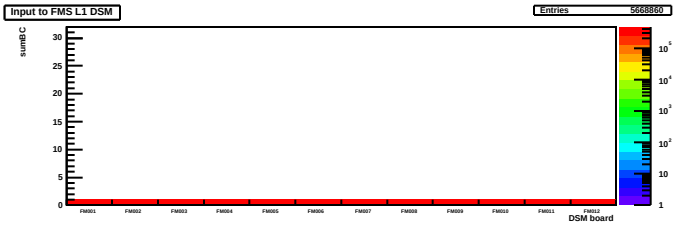

Input to FMS L0 DSM
Entries 3779240
HT ID - simulated
QT board

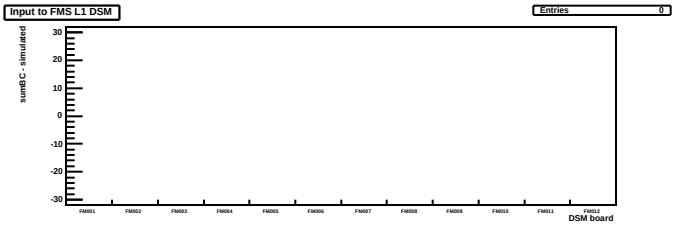

Input to FMS L1 DSM
Entries 5668860
sumD
DSM board

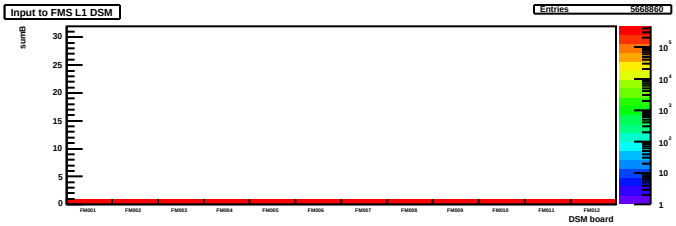

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

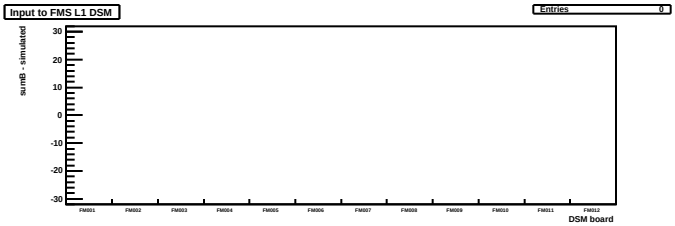

Input to FMS L1 DSM
Entries 5668860
sumC
DSM board

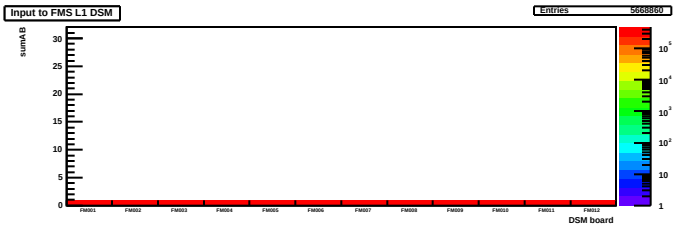

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

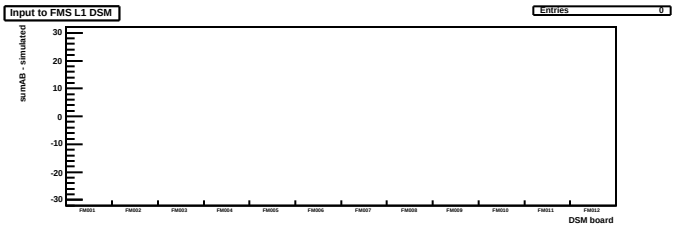

Input to FMS L1 DSM
Entries 5668860
sumBC
DSM board

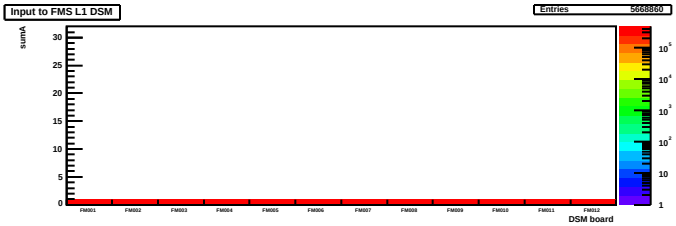

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

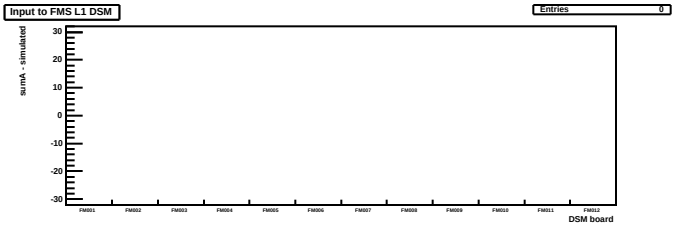

Input to FMS L1 DSM
Entries 5668860
sumB
DSM board

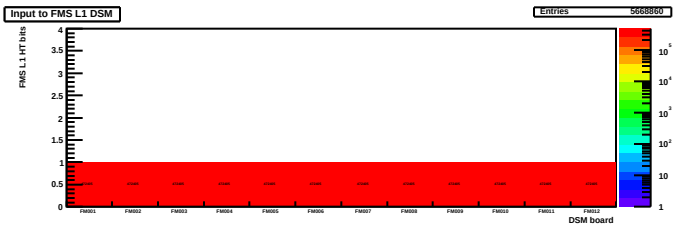

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

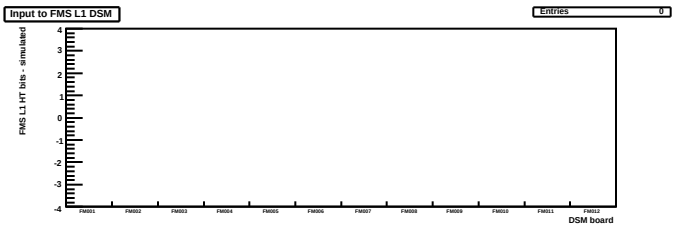

Input to FMS L1 DSM
Entries 5668860
sumAB
DSM board

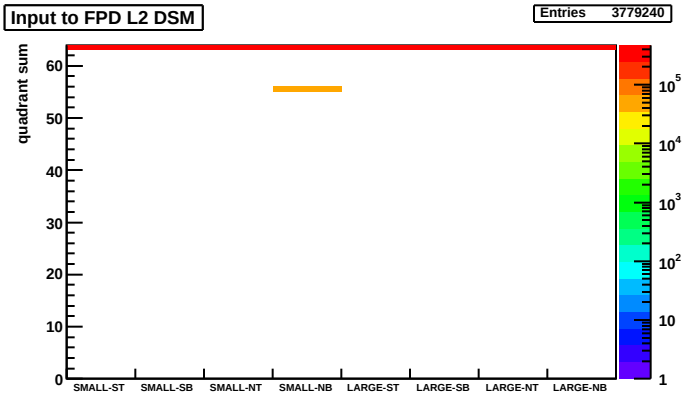

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

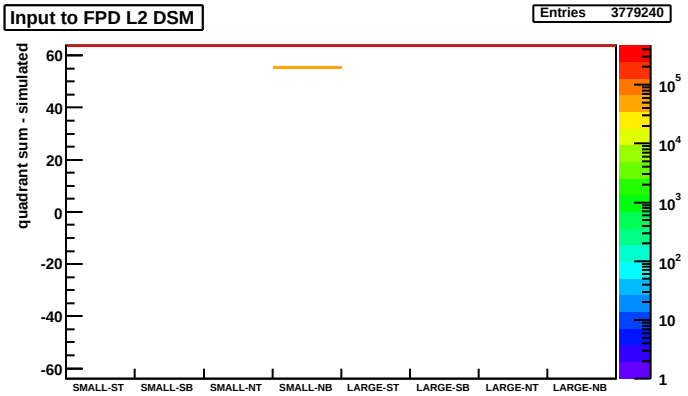

Input to FMS L1 DSM
Entries 5668860
sumA
DSM board

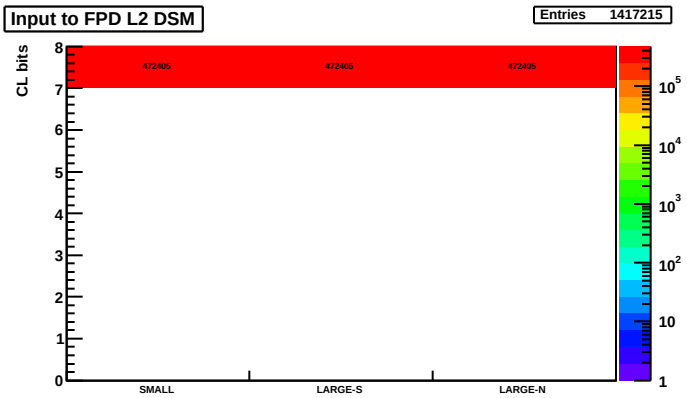

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

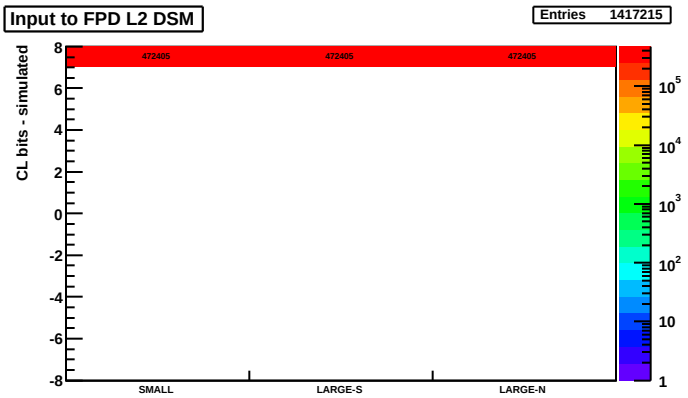

Input to FMS L1 DSM
Entries 5668860
FMS L1 HT bits
DSM board

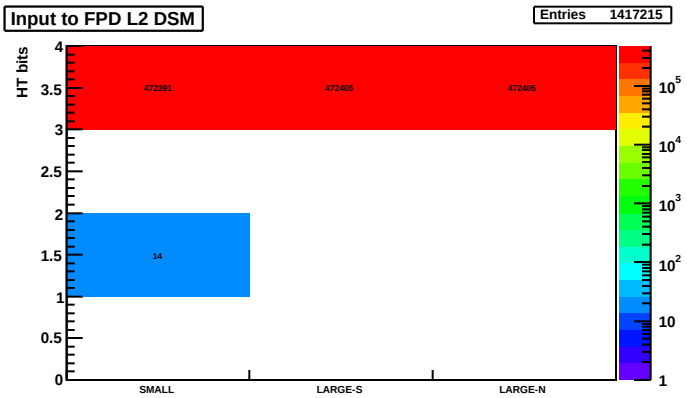

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

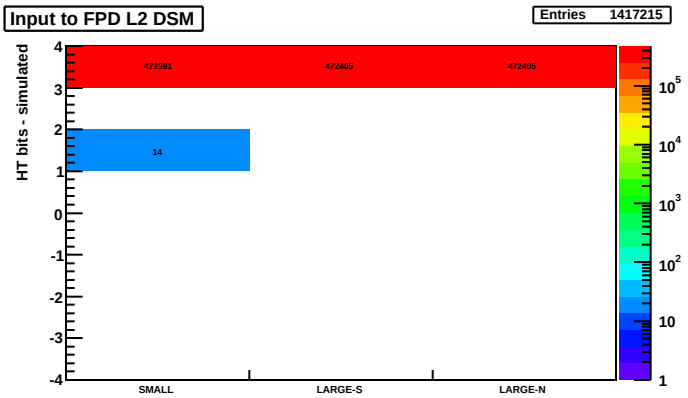

Input to FPD L2 DSM
Entries 3779240
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

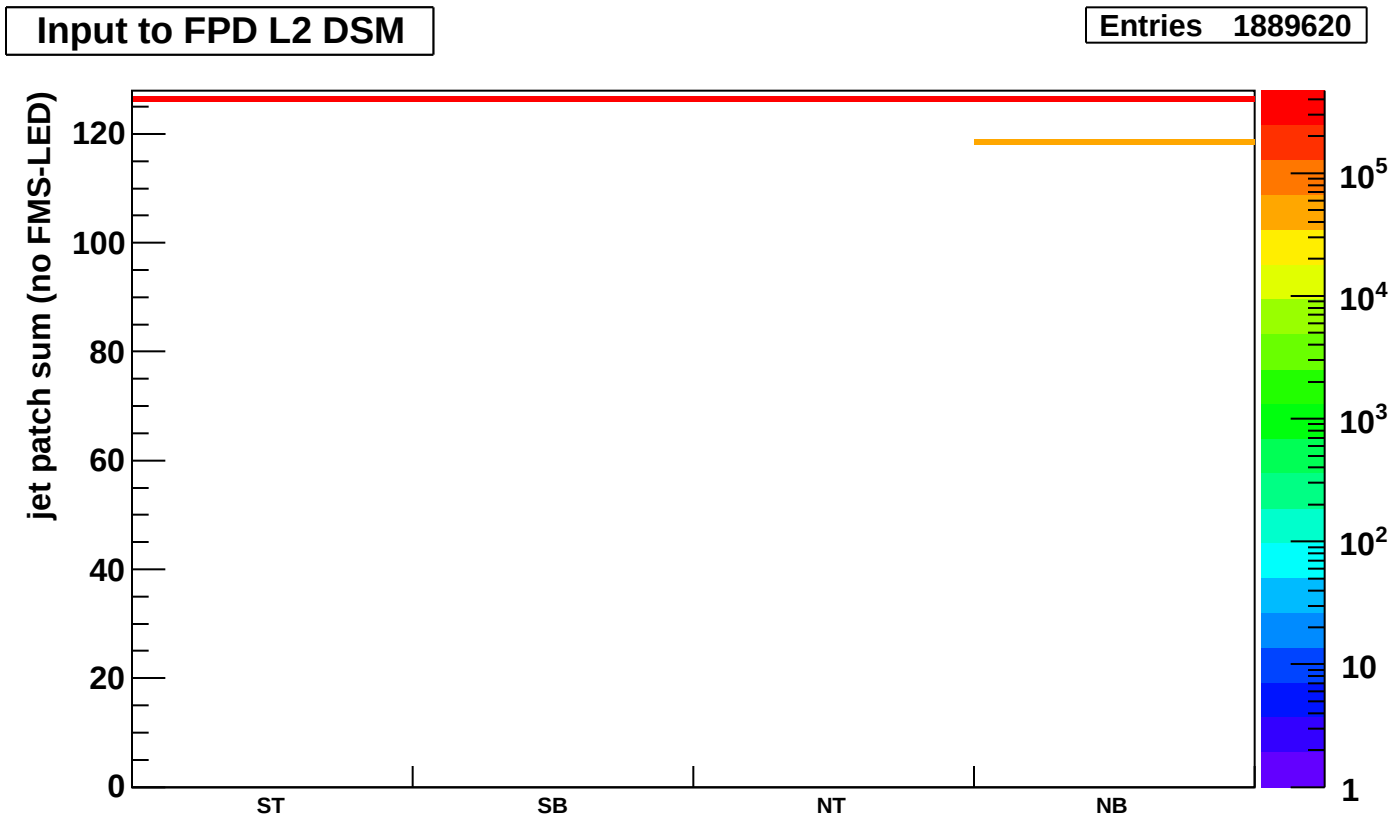

Input to FPD L2 DSM
Entries 3779240
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

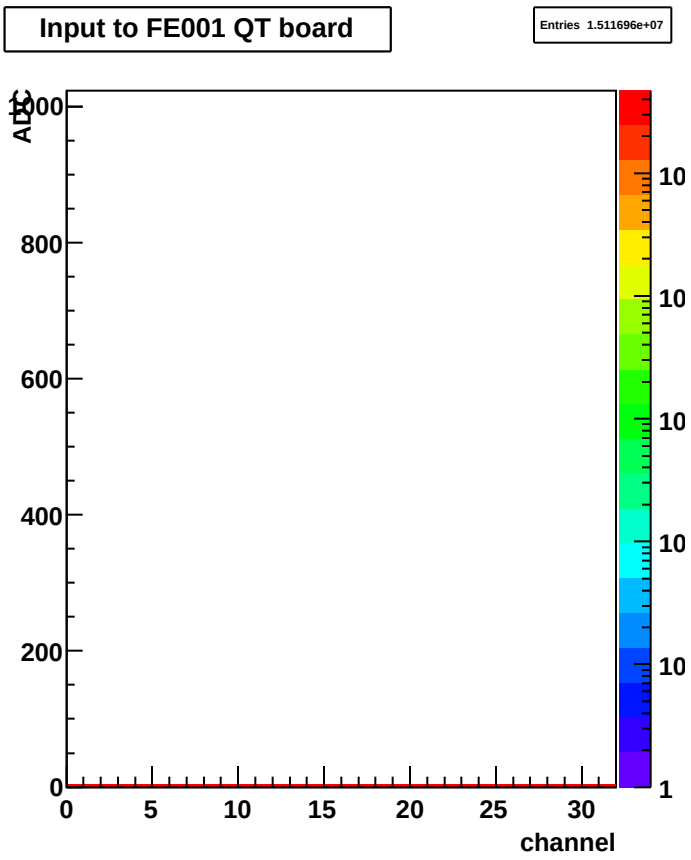

Input to FPD L2 DSM
Entries 1417215
CL bits
472405
472405
472405
SMALL
LARGE-S
LARGE-N

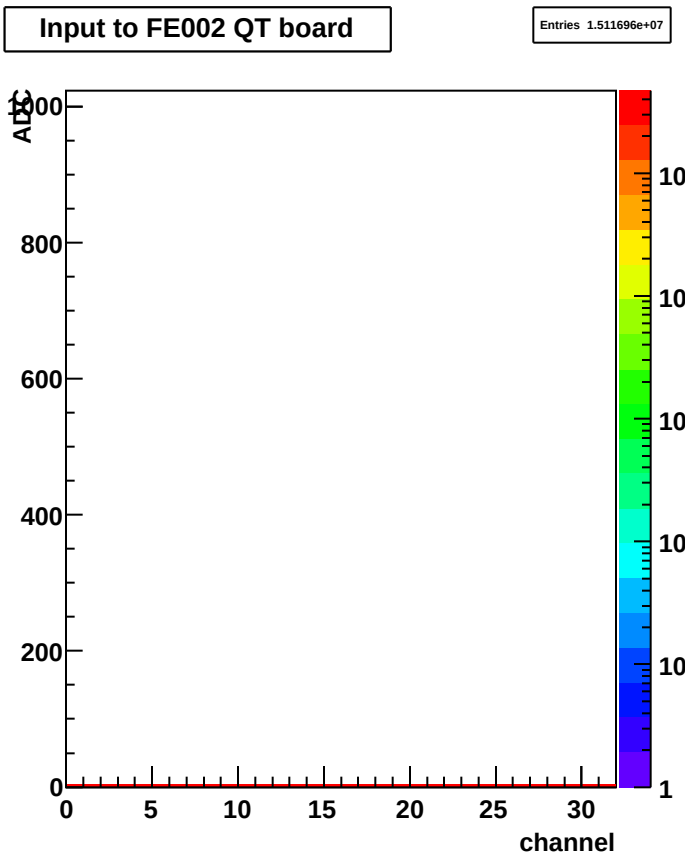

Input to FPD L2 DSM
Entries 1417215
CL bits - simulated
472405
472405
472405
SMALL
LARGE-S
LARGE-N

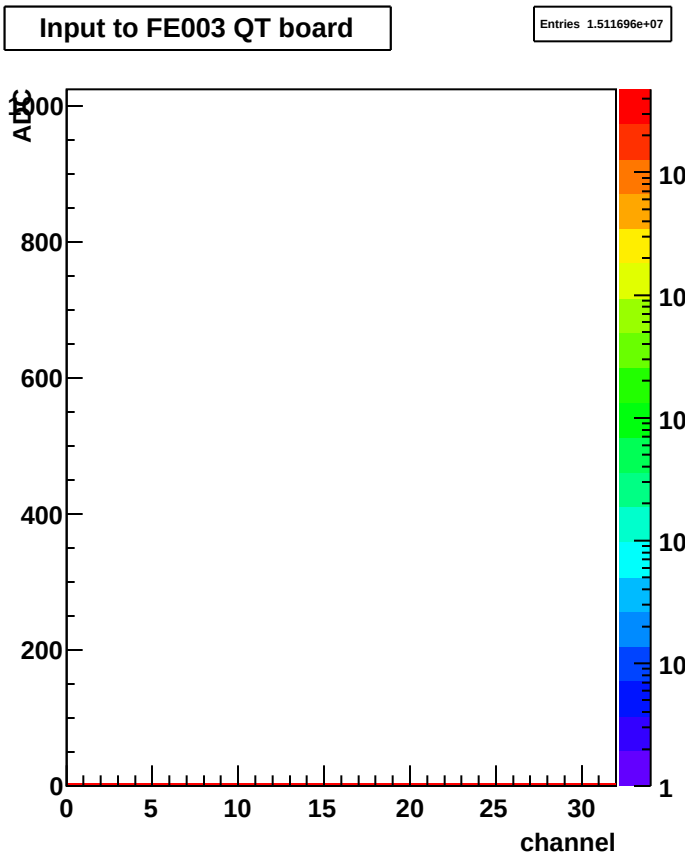

Input to FPD L2 DSM
Entries 1417215
HT bits
472391
472405
472405
14
SMALL
LARGE-S
LARGE-N

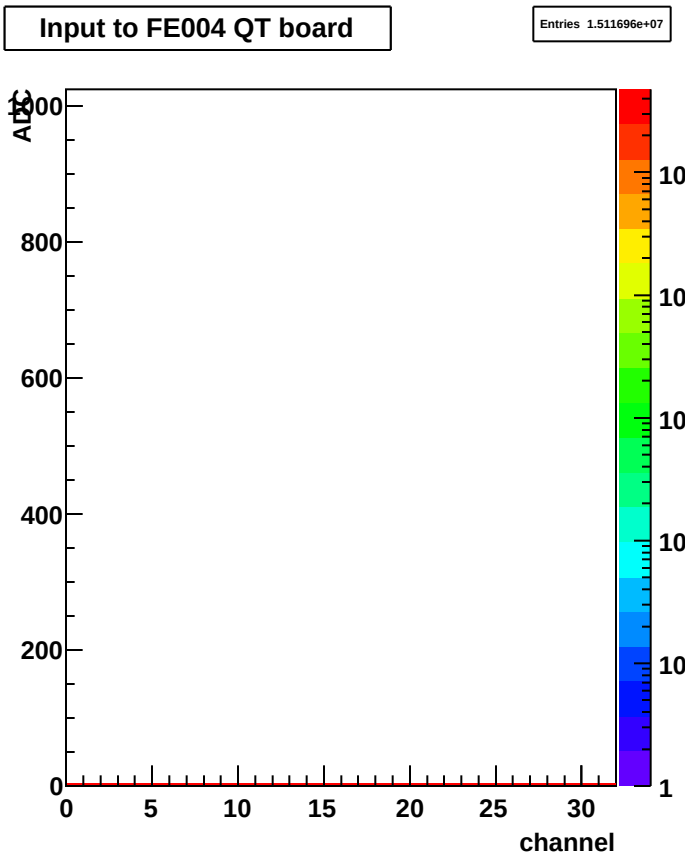

Input to FPD L2 DSM
Entries 1417215
HT bits - simulated
472391
472405
472405
14
SMALL
LARGE-S
LARGE-N

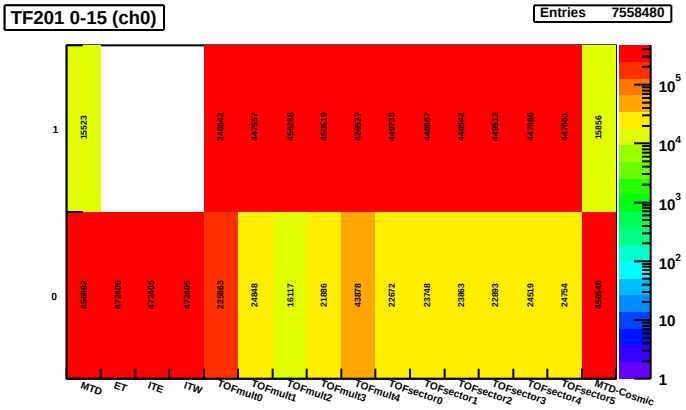

Input to FPD L2 DSM
Entries 1889620
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

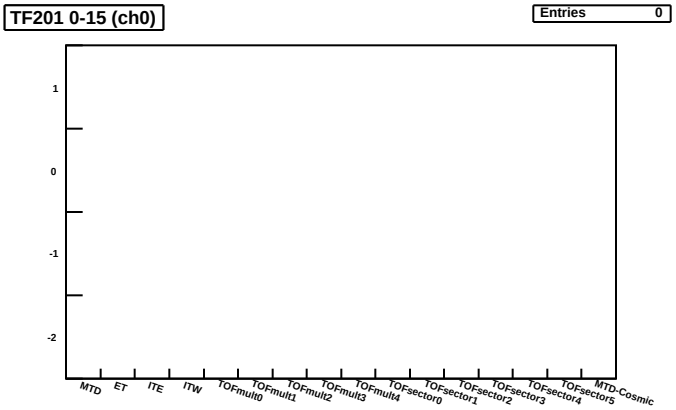

Input to FE001 QT board
Entries 1.511696e+07
ADC
channel

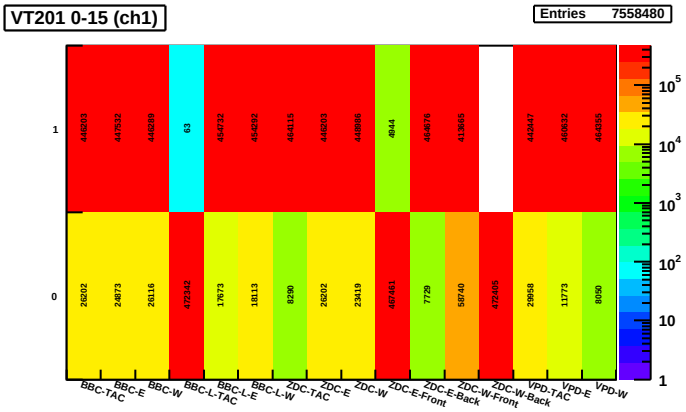

Input to FE002 QT board
Entries 1.511696e+07
ADC
channel

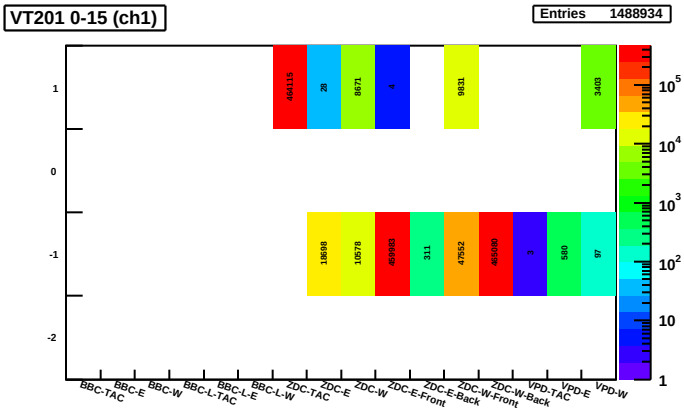

Input to FE003 QT board
Entries 1.511696e+07
ADC
channel

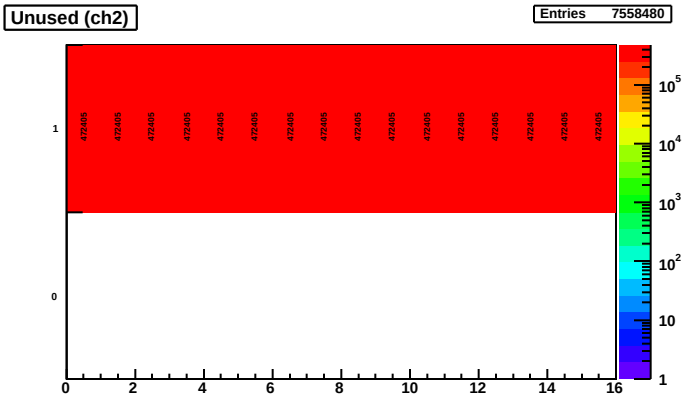

Input to FE004 QT board
Entries 1.511696e+07
ADC
channel

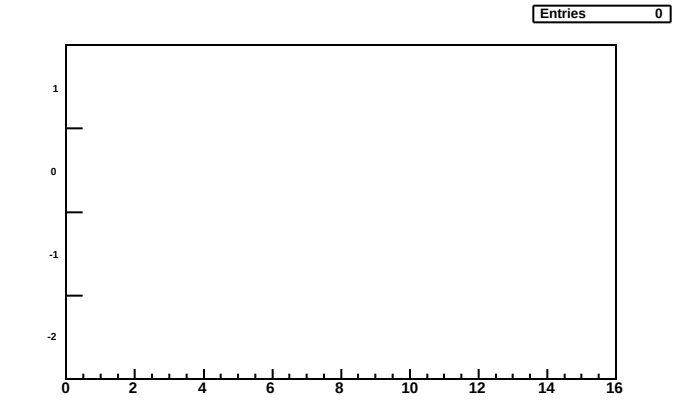

TF201 0-15 (ch0)
Entries 7558480
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

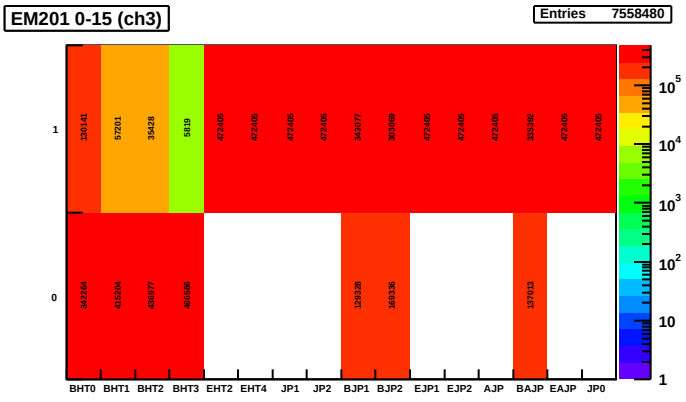

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

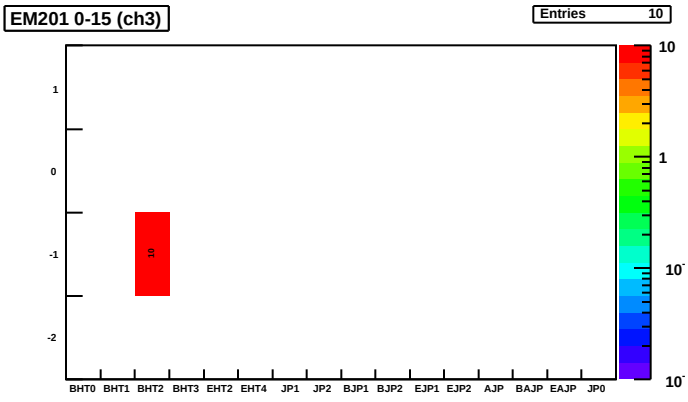

VT201 0-15 (ch1)
Entries 7558480
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 1488934
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

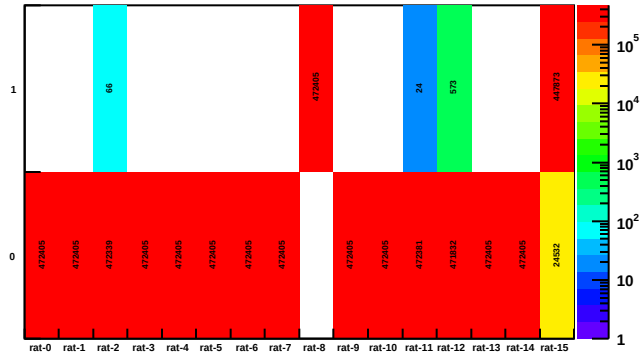

Unused (ch2)
Entries 7558480

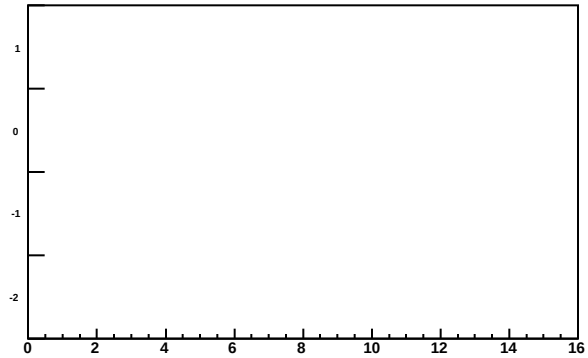

Entries 0


EM201 0-15 (ch3)
Entries 7558480
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

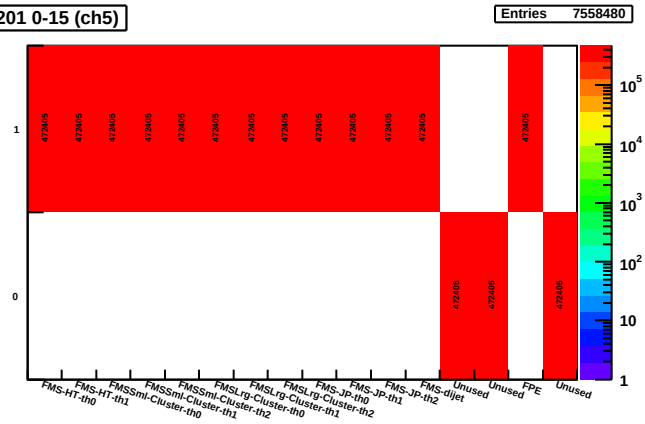

EM201 0-15 (ch3)
Entries 10
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

RAT board (ch4)

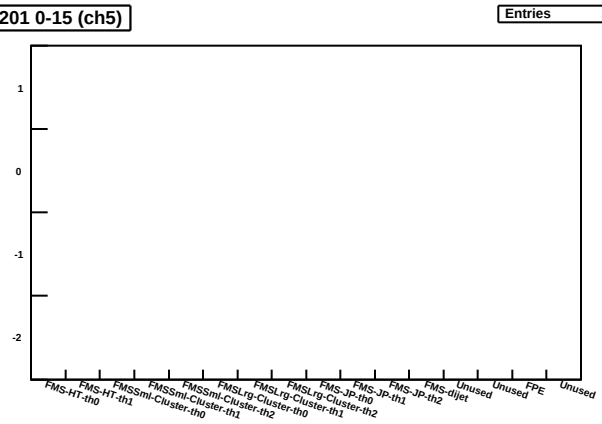




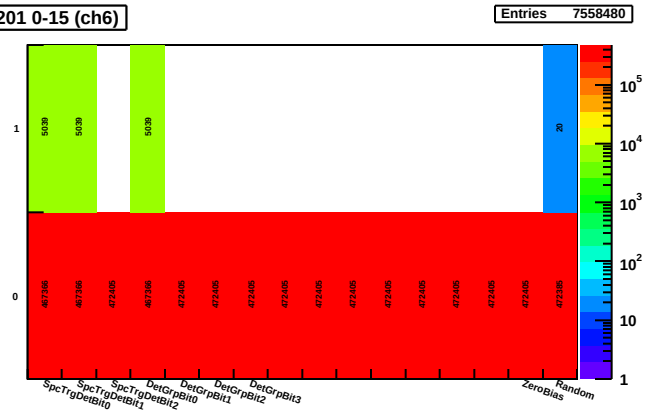
FP201 0-15 (ch5)

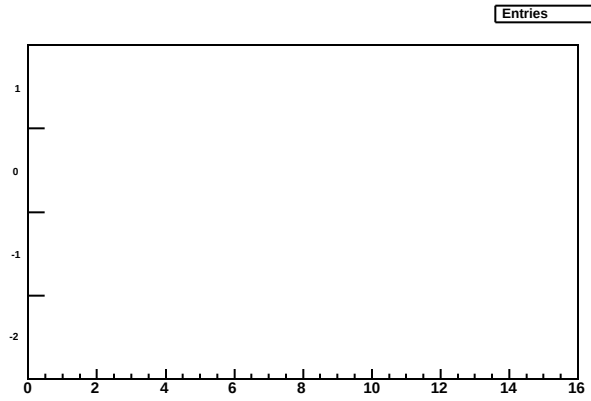
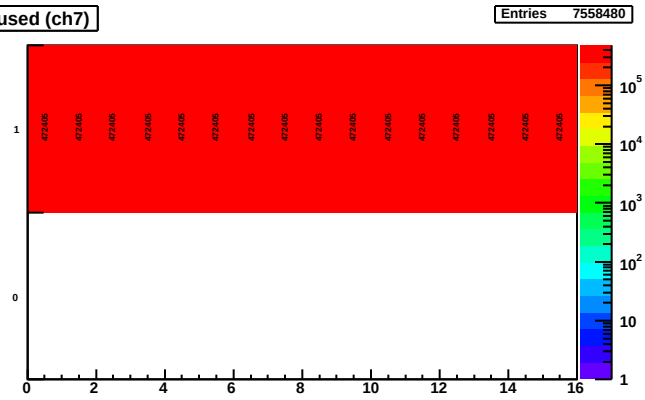
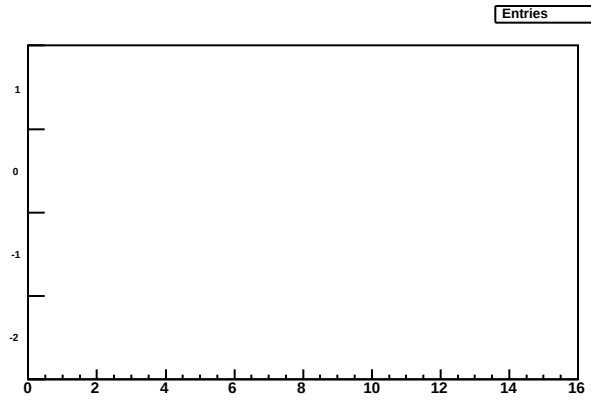


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)