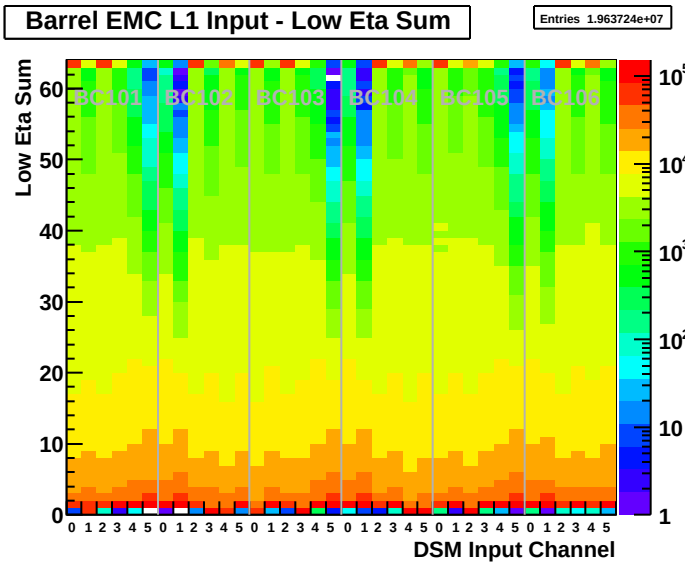
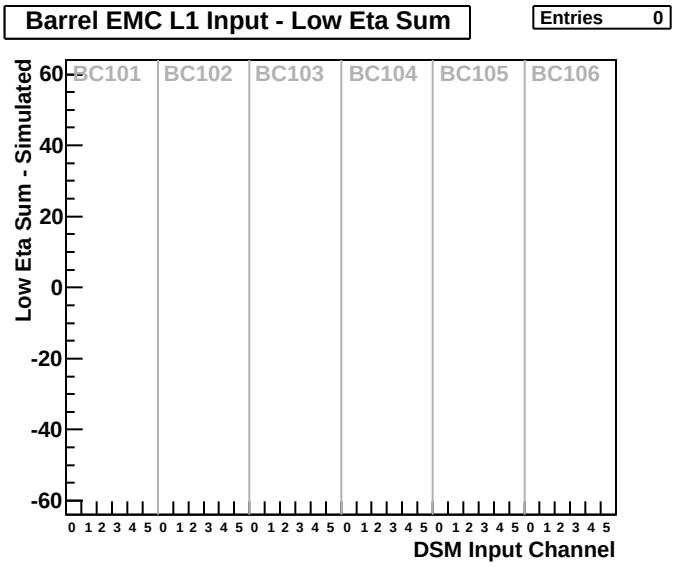
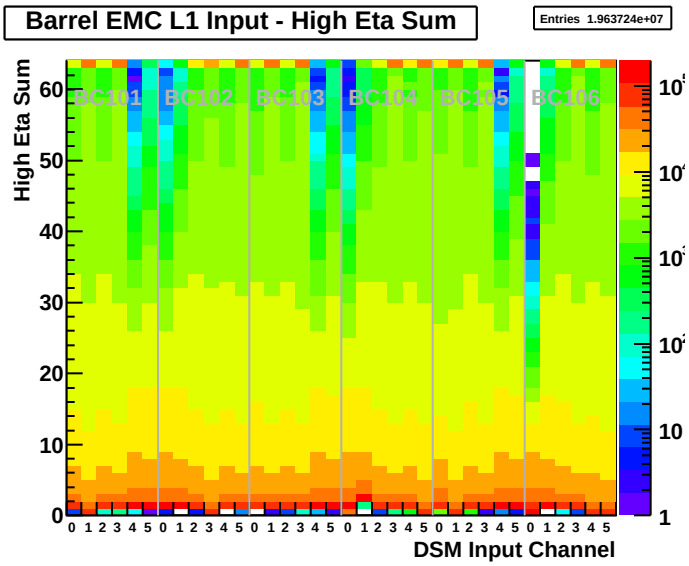
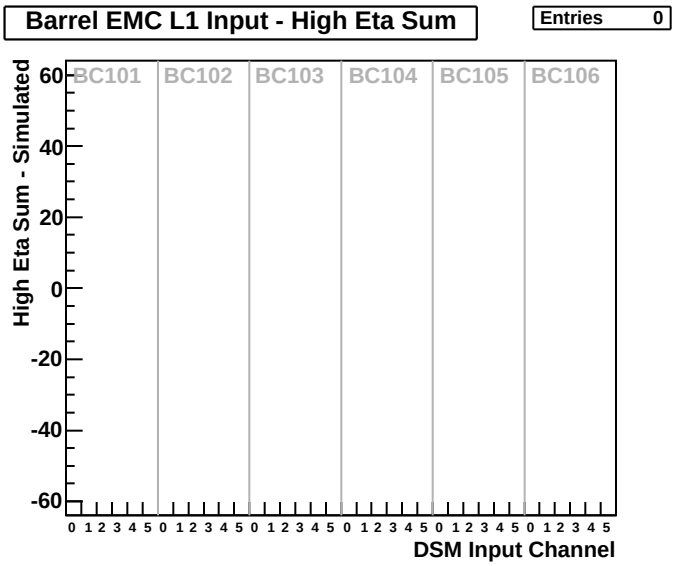
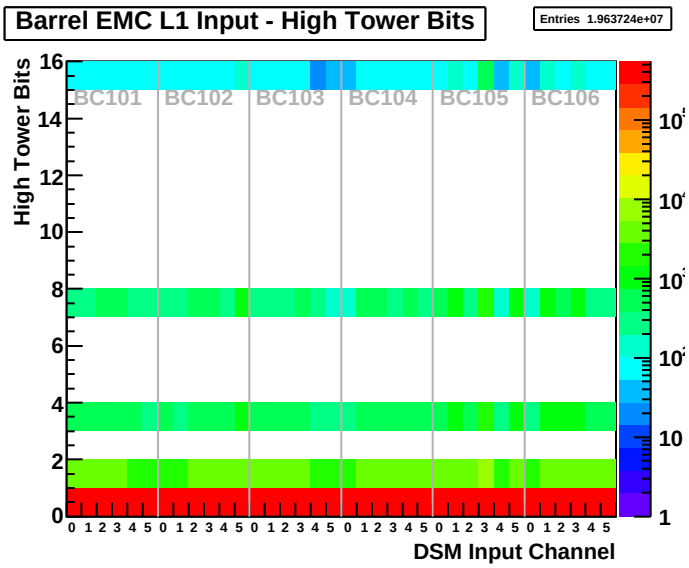
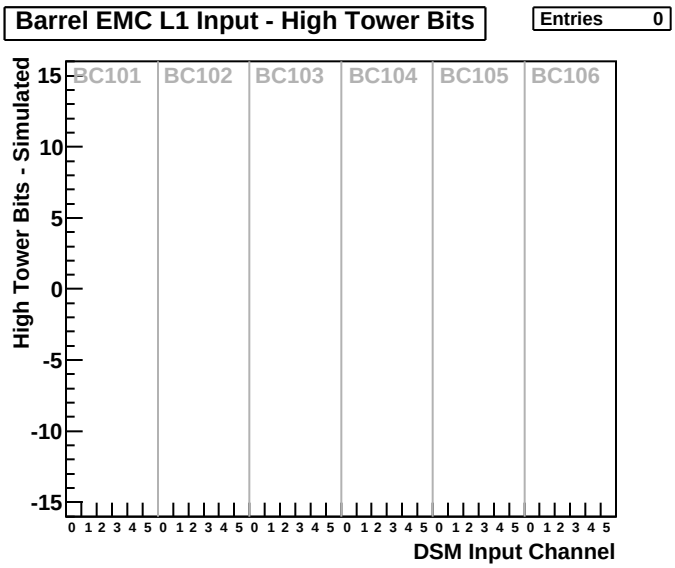

Barrel EMC L1 Input - Low Eta Sum
Entries 1.963724e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

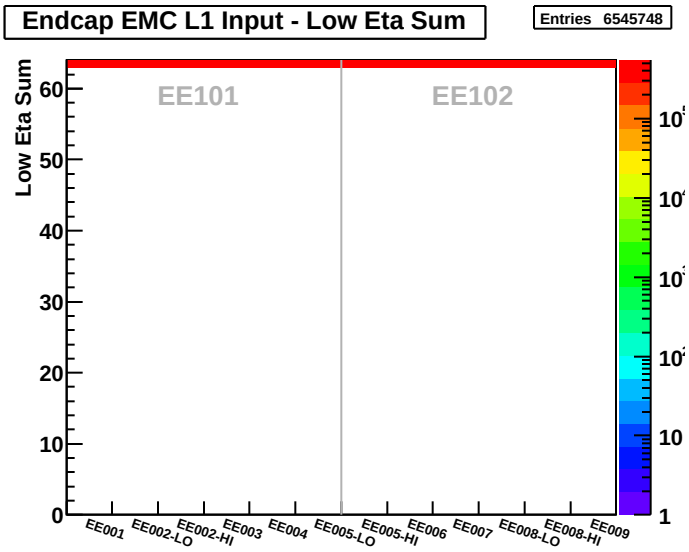

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

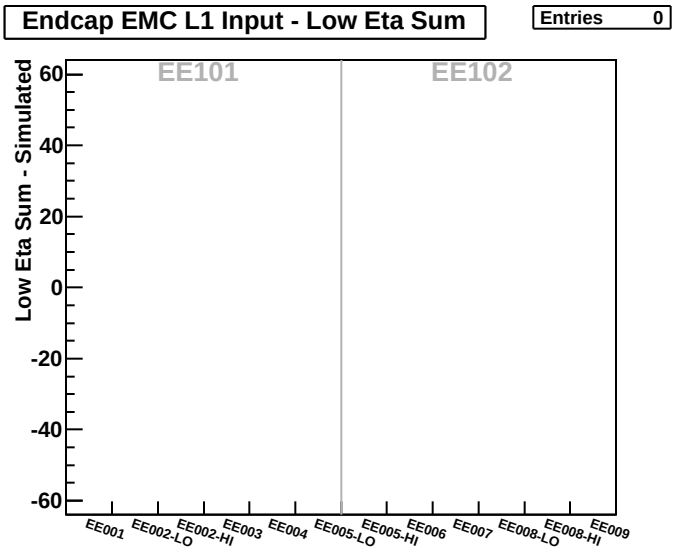

Barrel EMC L1 Input - High Eta Sum
Entries 1.963724e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

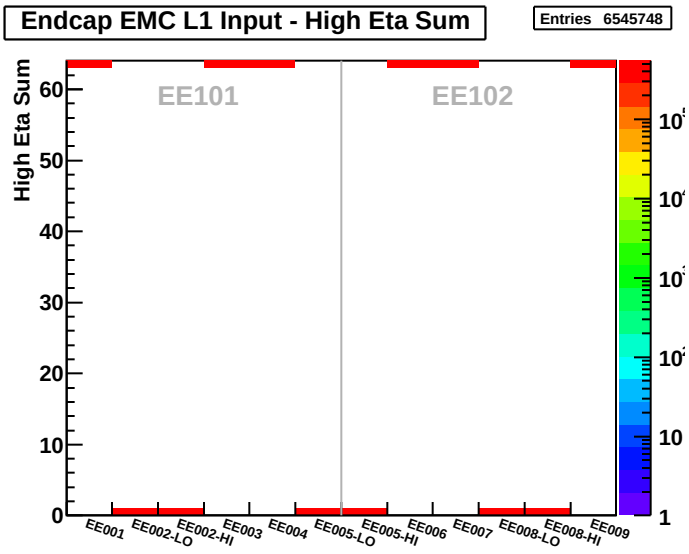

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

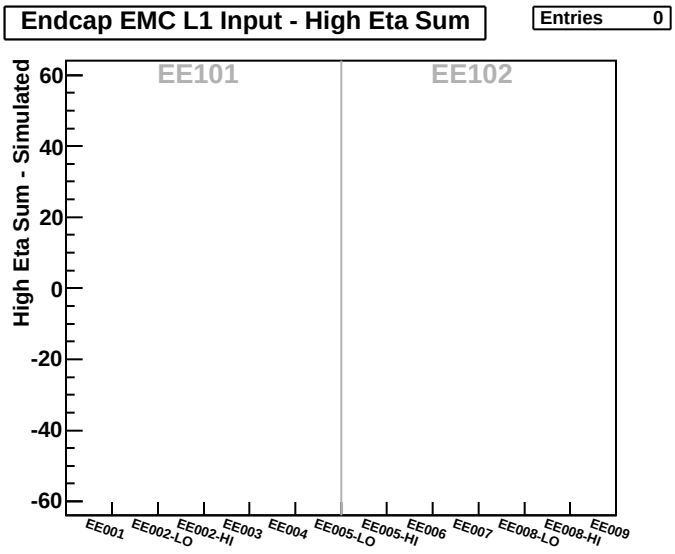

Barrel EMC L1 Input - High Tower Bits
Entries 1.963724e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

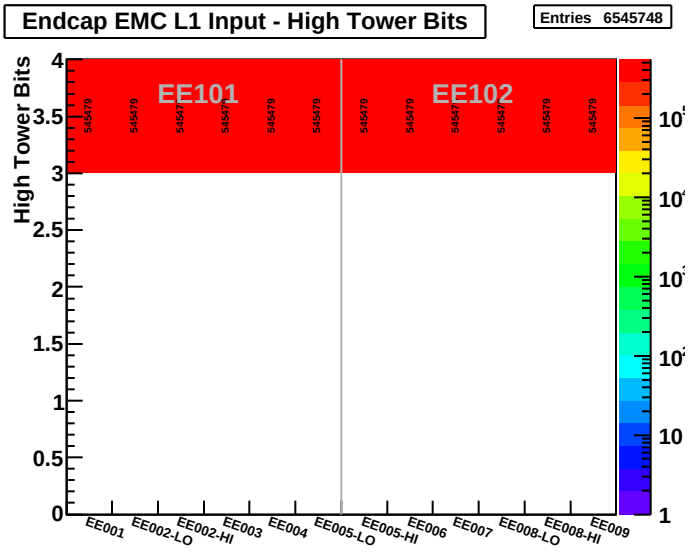

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

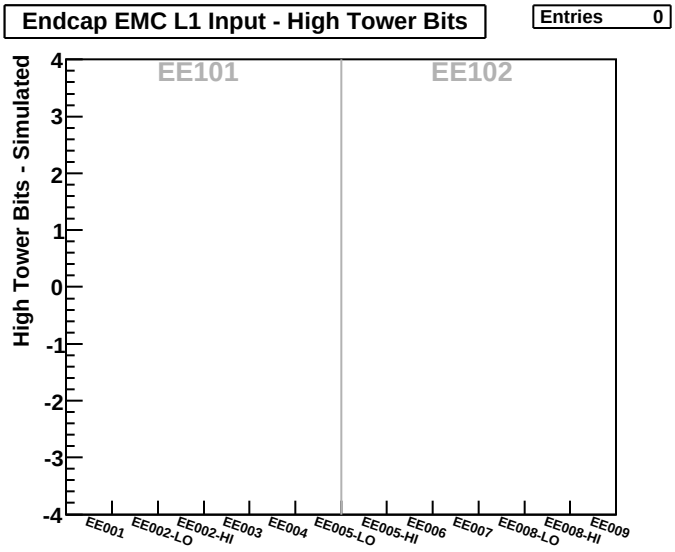

Endcap EMC L1 Input - Low Eta Sum
Entries 6545748
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

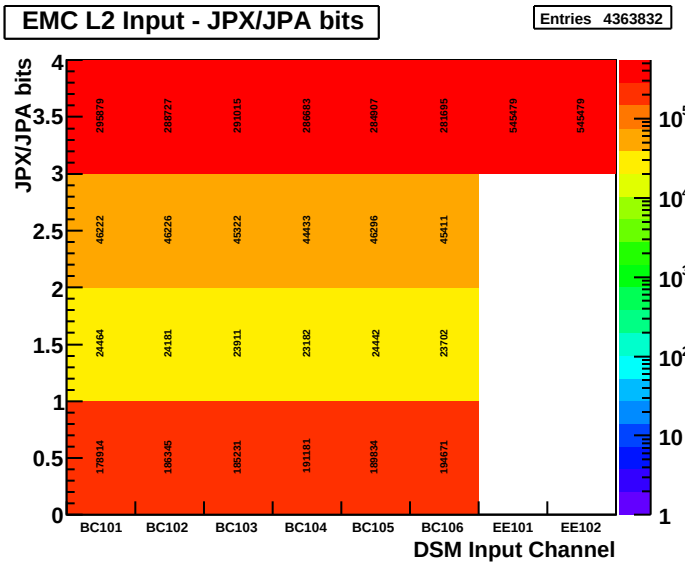

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

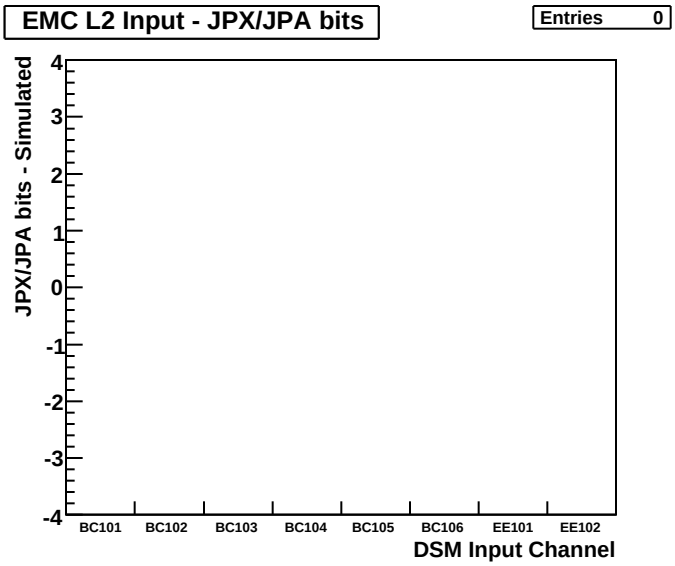

Endcap EMC L1 Input - High Eta Sum
Entries 6545748
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

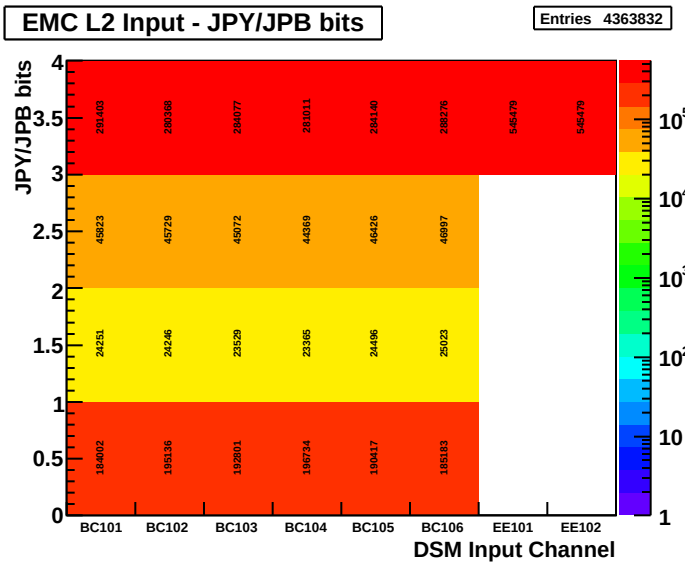

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

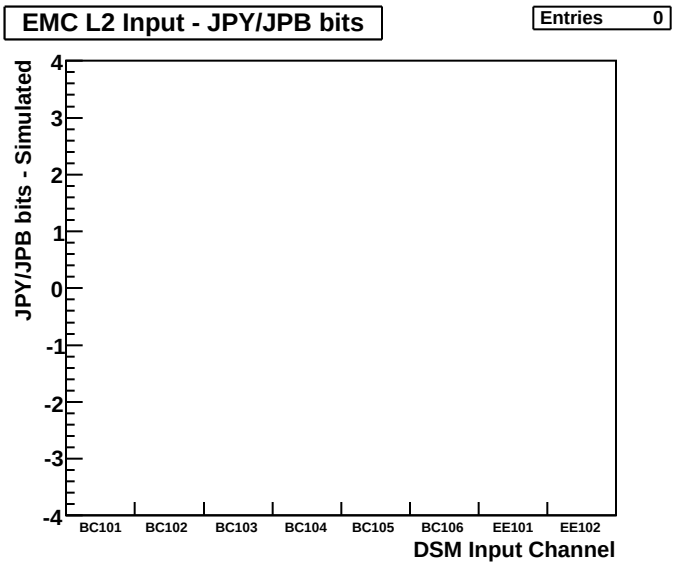

Endcap EMC L1 Input - High Tower Bits
Entries 6545748
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

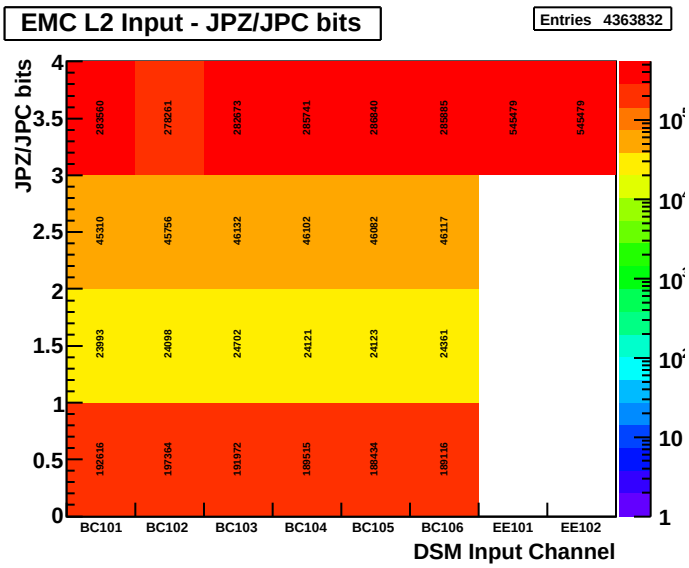

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

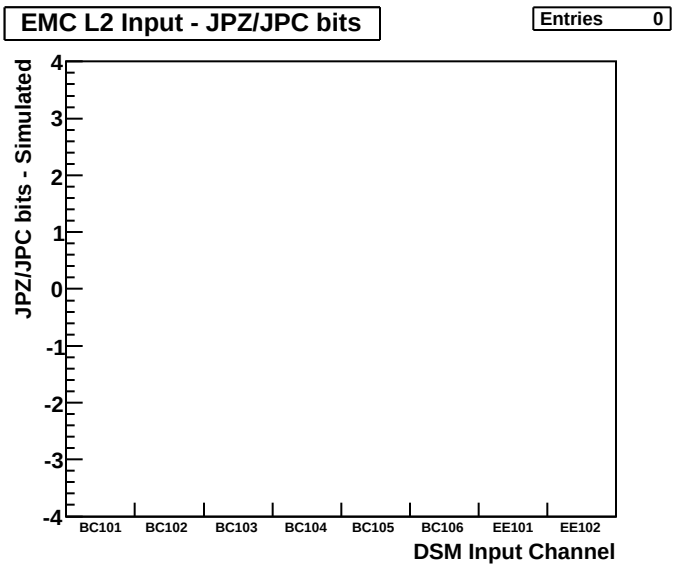

EMC L2 Input - JPX/JPA bits
Entries 4363832
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
295879 288727 291015 286683 294907 281695 545479 545479
46222 46226 45322 44433 46296 45411
24464 24181 23911 23182 24442 23702
178914 186345 185231 191181 189834 194671

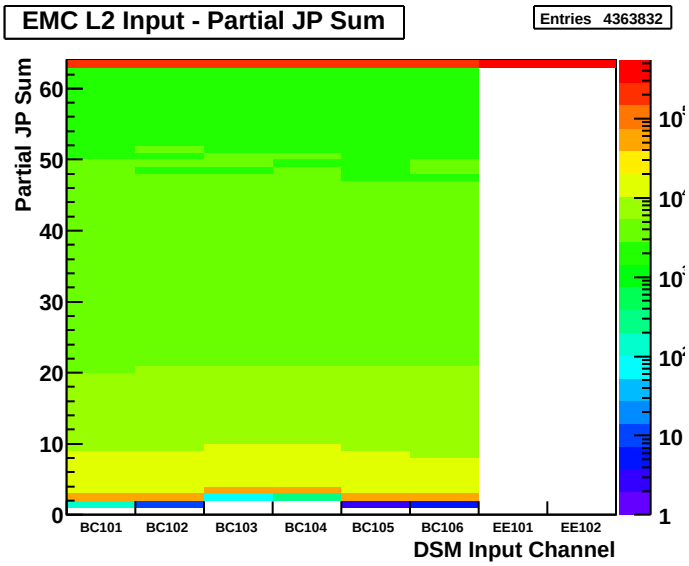

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

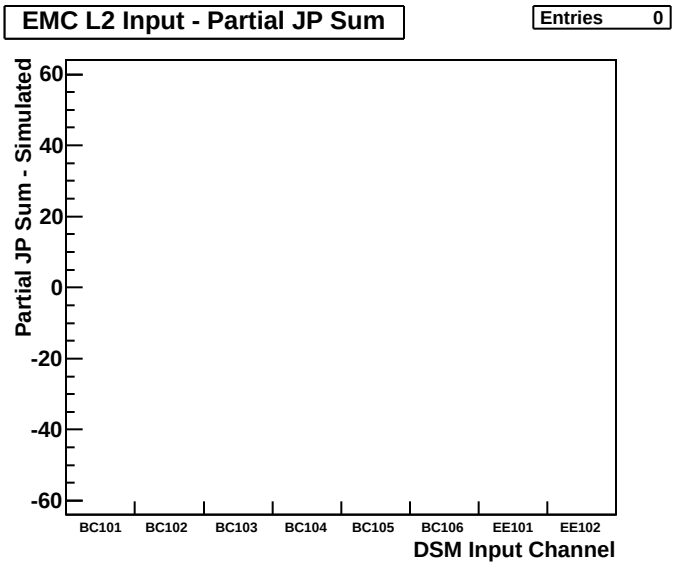

EMC L2 Input - JPY/JPB bits
Entries 4363832
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
291403 280388 284077 281011 284140 288276 545479 545479
45823 45729 45072 44369 46426 46997
24251 24246 23529 23365 24496 25023
184002 195136 192801 196734 190417 185183

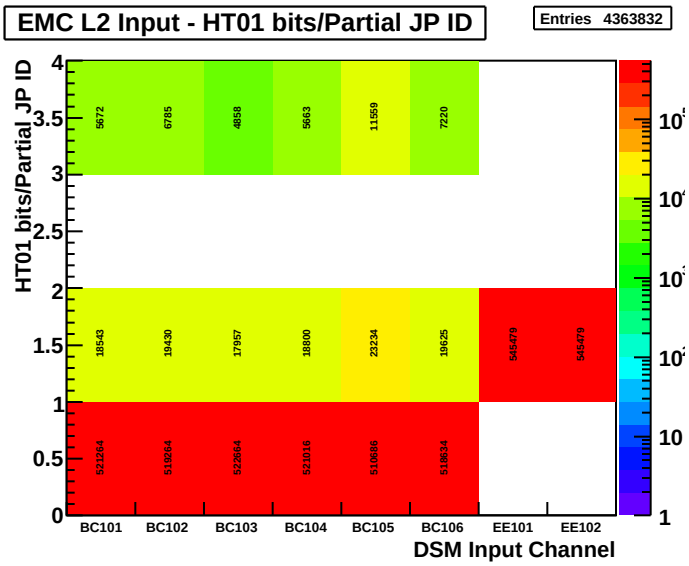

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

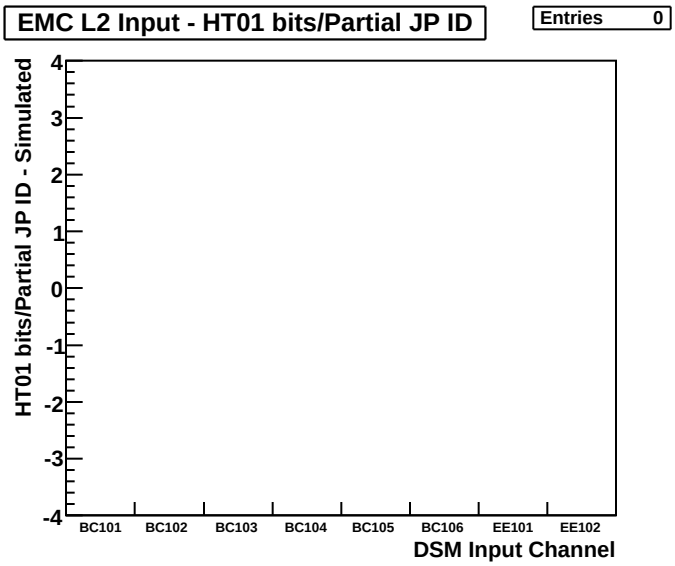

EMC L2 Input - JPZ/JPC bits
Entries 4363832
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
283560 278261 282673 285741 288840 283885 545479 545479
45310 45756 46132 46102 46082 46117
23993 24098 24702 24121 24123 24361
192516 197364 191972 189515 188434 189116

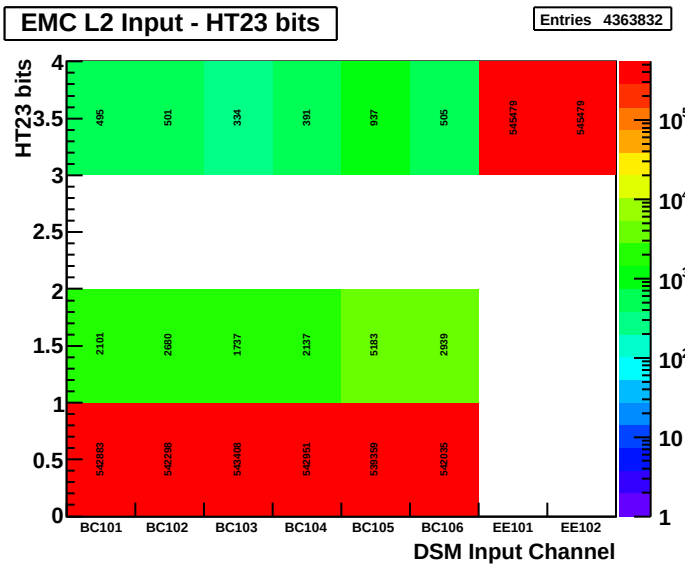

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

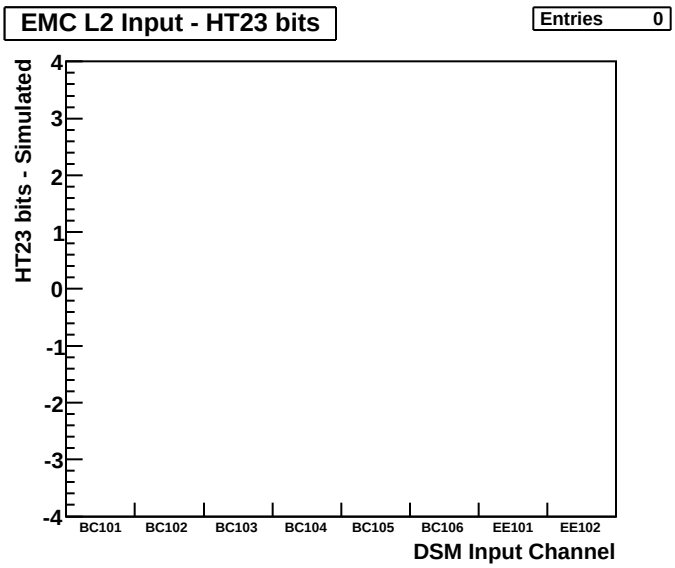

EMC L2 Input - Partial JP Sum
Entries 4363832
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

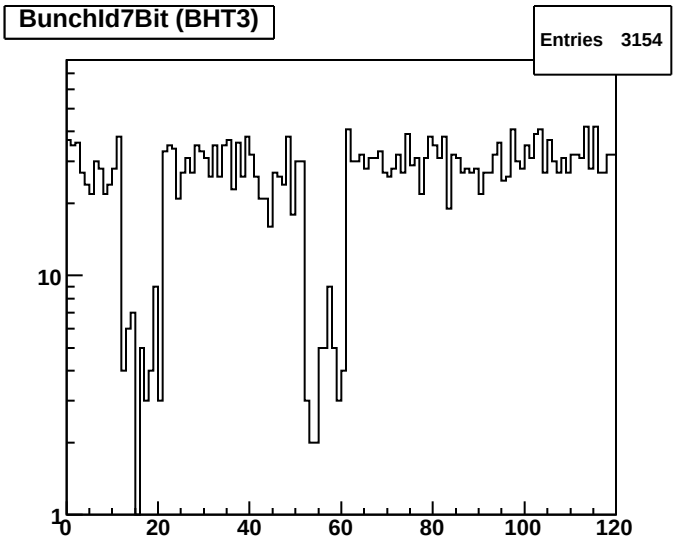

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

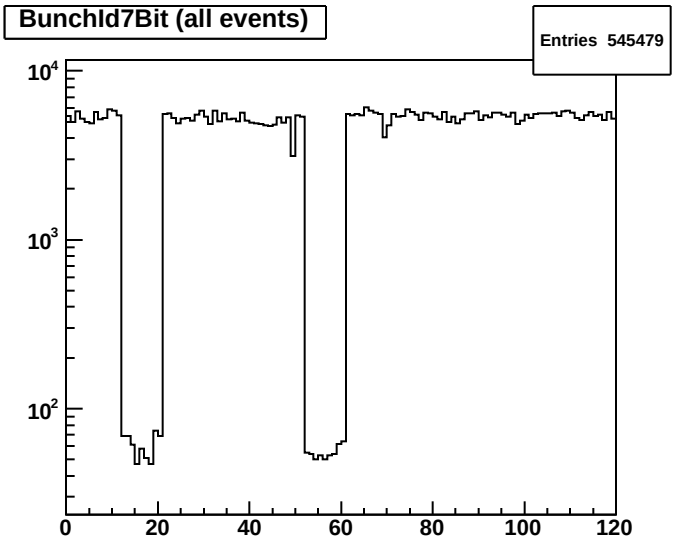

EMC L2 Input - HT01 bits/Partial JP ID
Entries 4363832
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

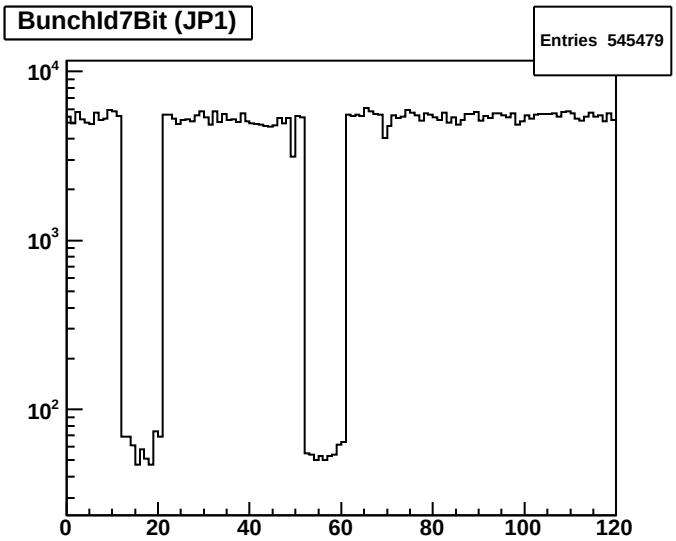

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

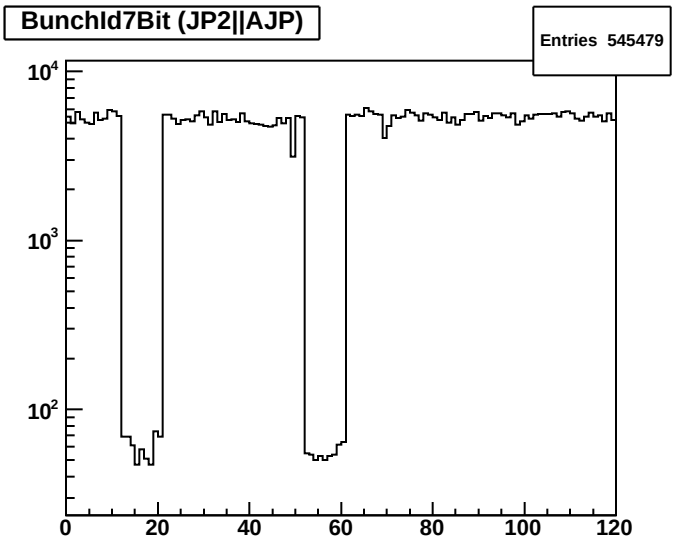

EMC L2 Input - HT23 bits
Entries 4363832
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

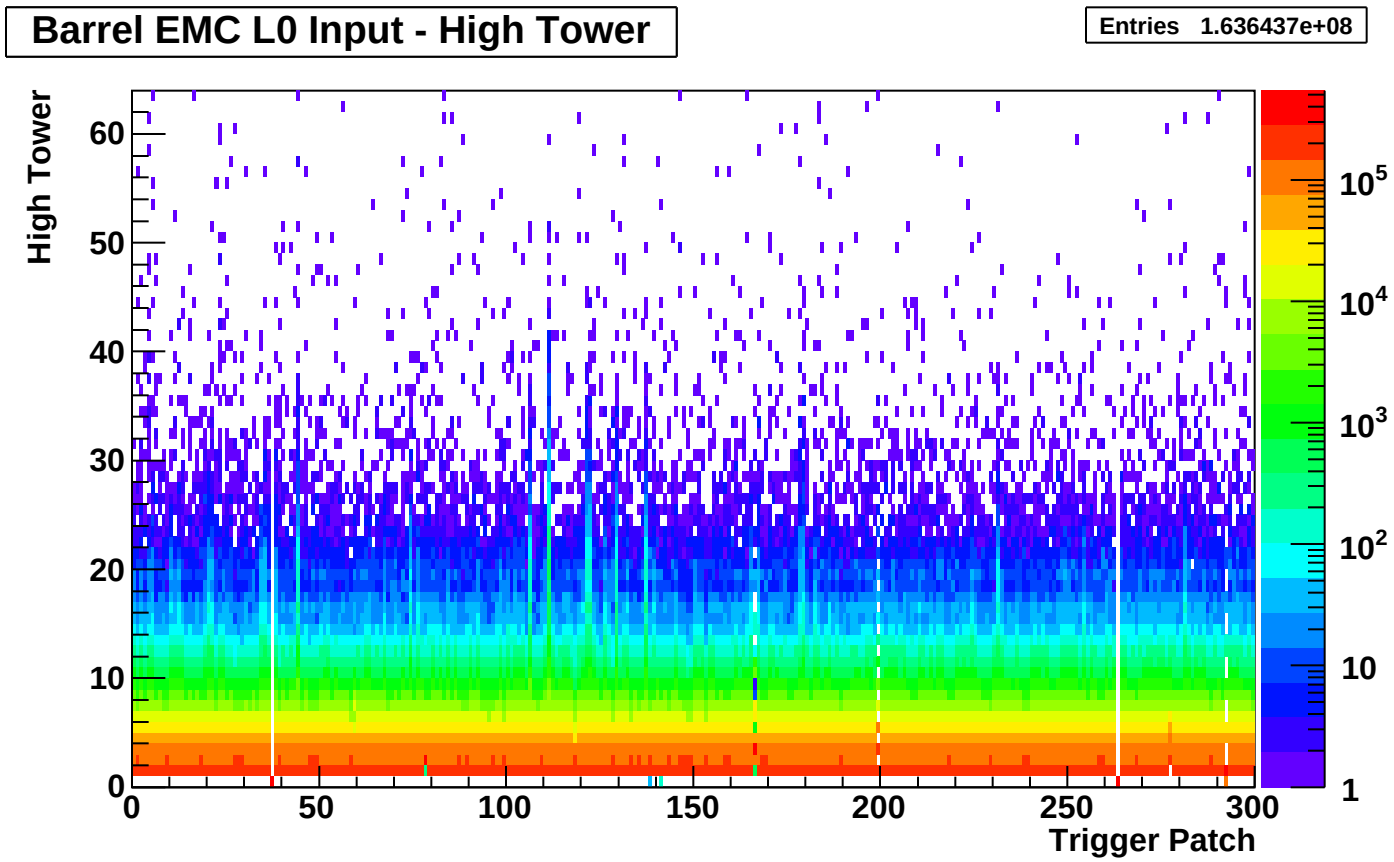

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

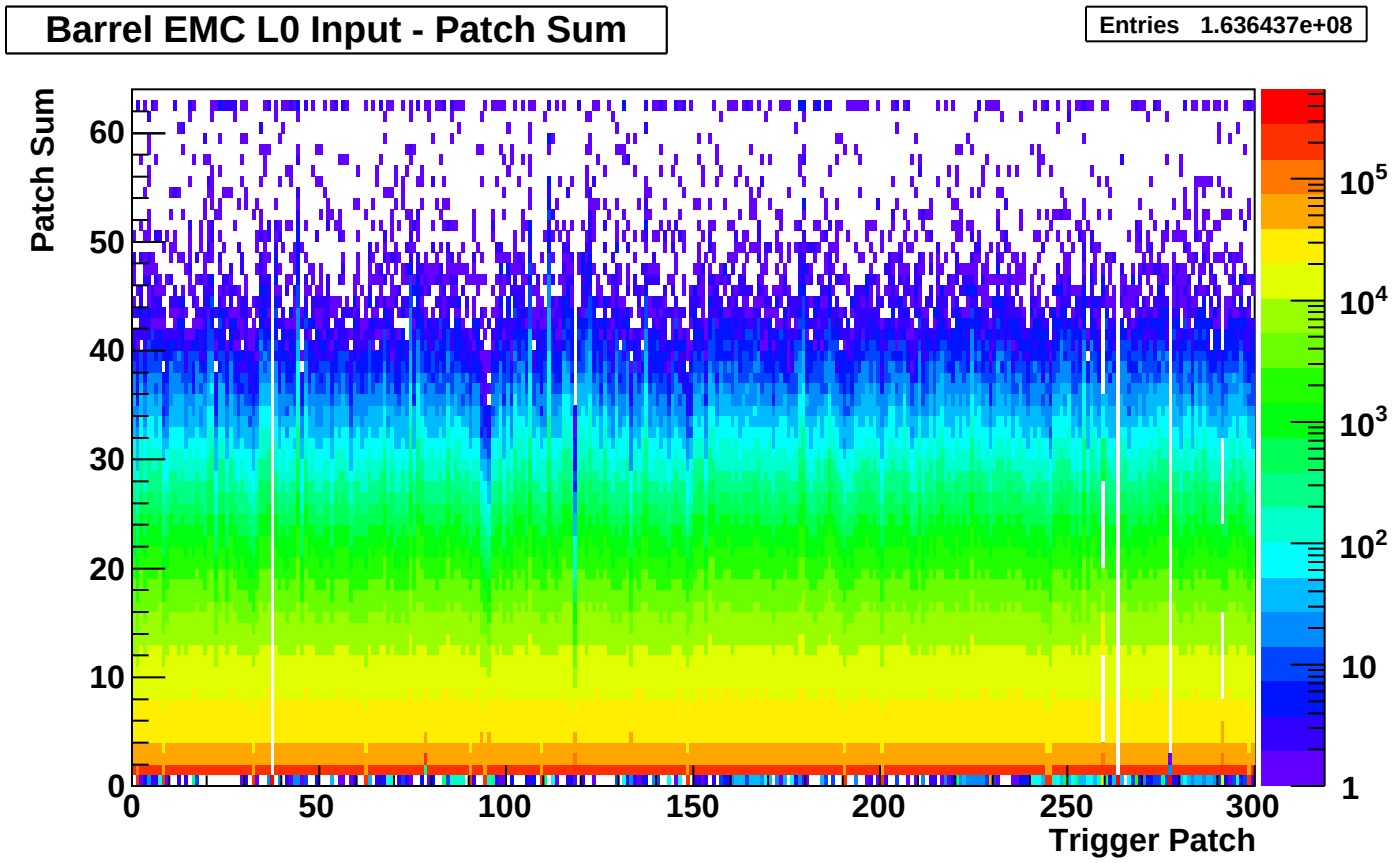

BunchId7Bit (BHT3)
Entries 3154
10
1
0
20
40
60
80
100
120

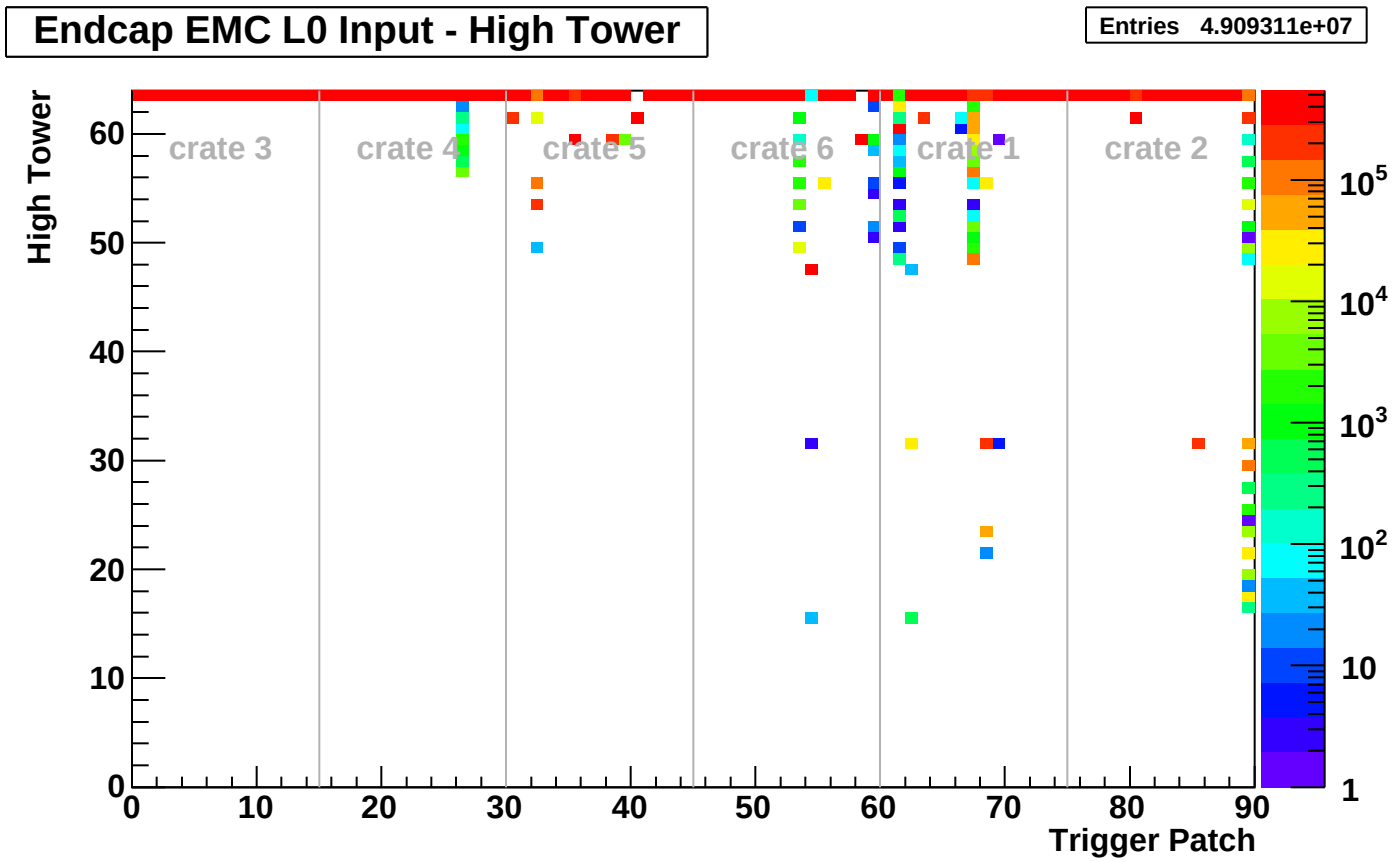

BunchId7Bit (all events)
Entries 545479
10^4
10^3
10^2
0
20
40
60
80
100
120

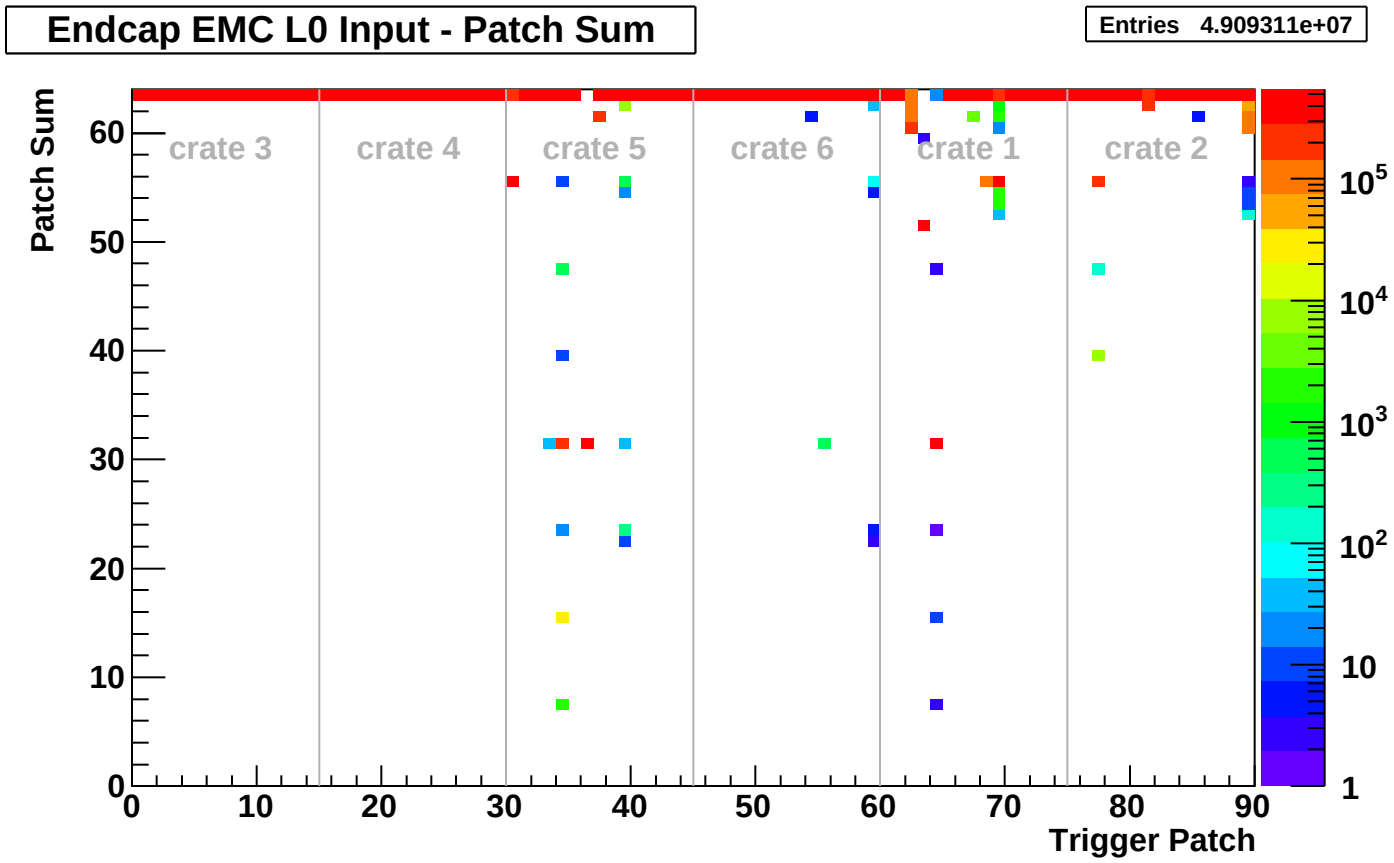

BunchId7Bit (JP1)
Entries 545479
10^4
10^3
10^2
0
20
40
60
80
100
120

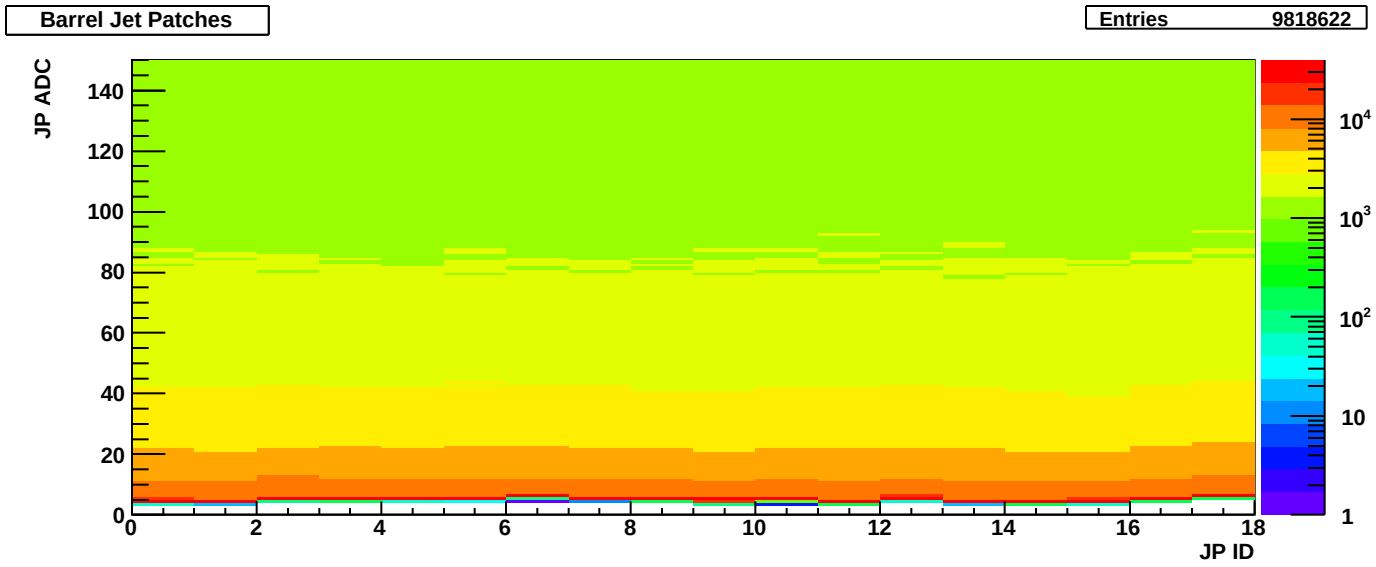

BunchId7Bit (JP2||AJP)
Entries 545479
10^4
10^3
10^2
0
20
40
60
80
100
120

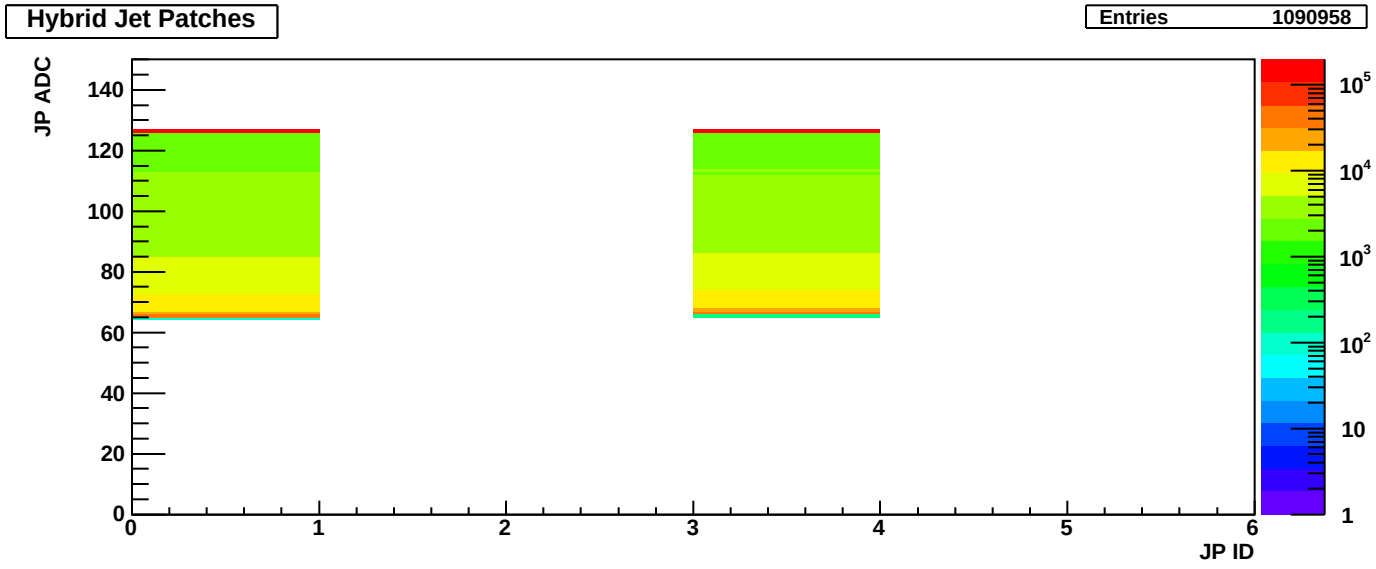

Barrel EMC L0 Input - High Tower
Entries 1.636437e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

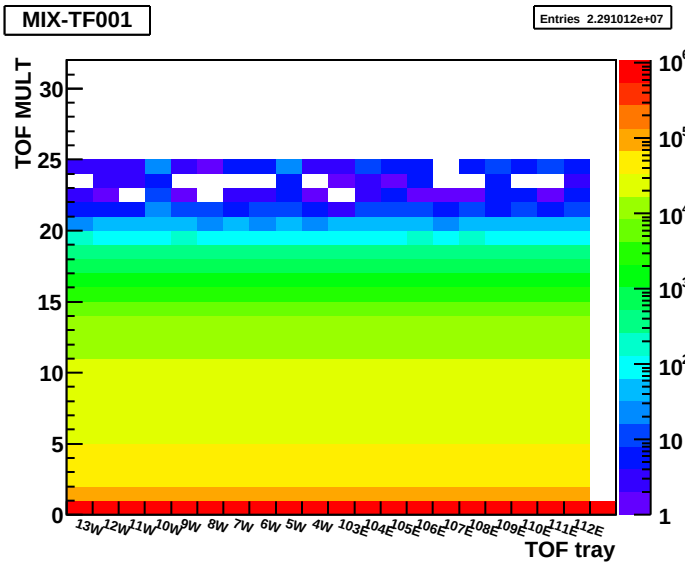

Barrel EMC L0 Input - Patch Sum
Entries 1.636437e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

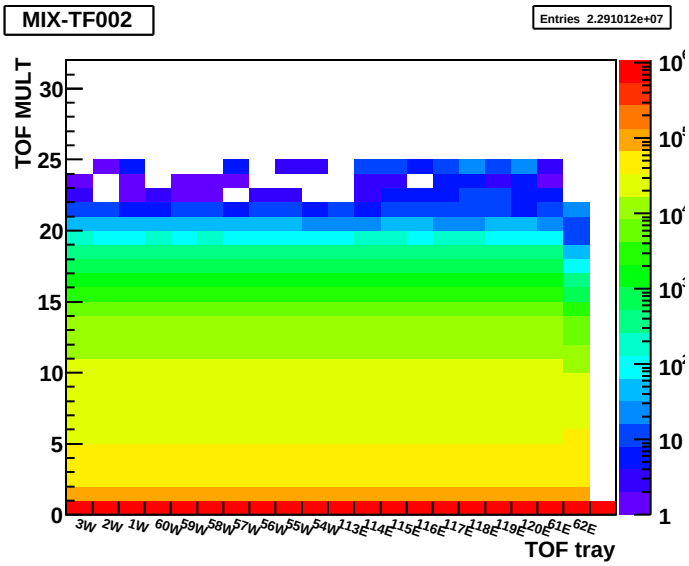

Endcap EMC L0 Input - High Tower
Entries 4.909311e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

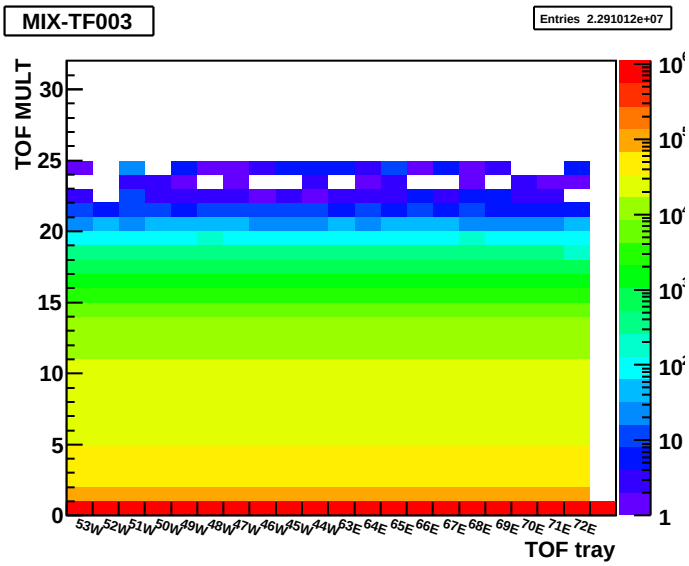

Endcap EMC L0 Input - Patch Sum
Entries 4.909311e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

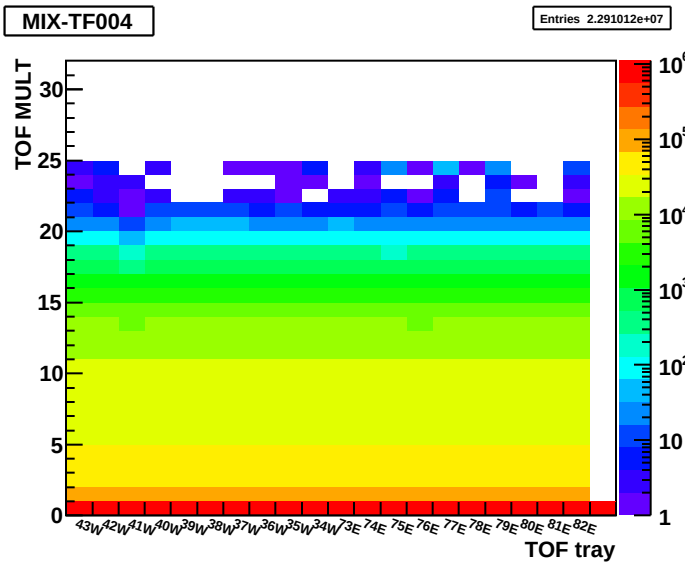

Barrel Jet Patches
Entries 9818622
JP ADC
JP ID

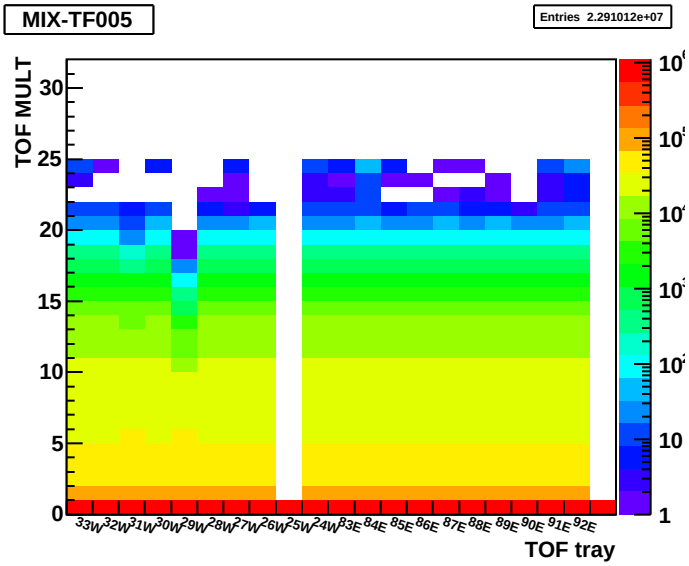

Hybrid Jet Patches
Entries 1090958
JP ADC
JP ID

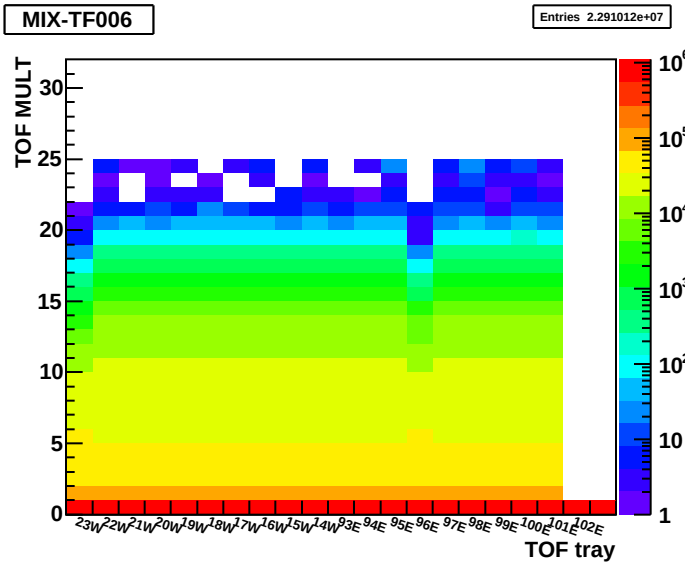

MIX-TF001
Entries 2.291012e+07
TOF MULT
TOF tray

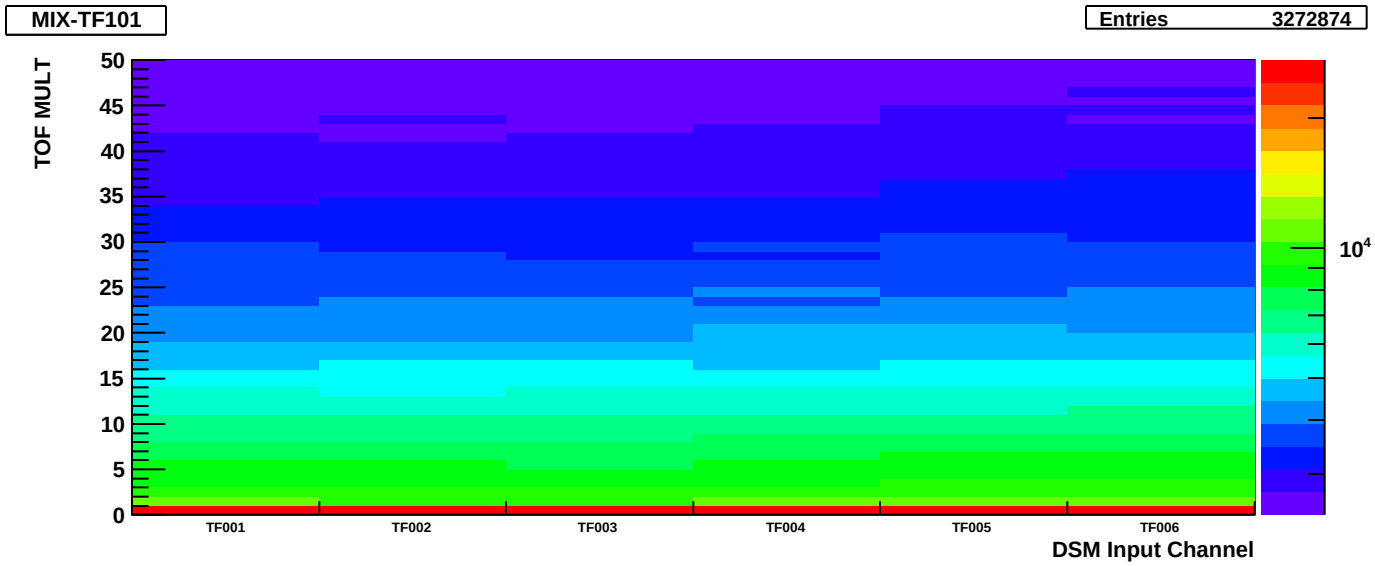

MIX-TF002
Entries 2.291012e+07
TOF MULT
TOF tray

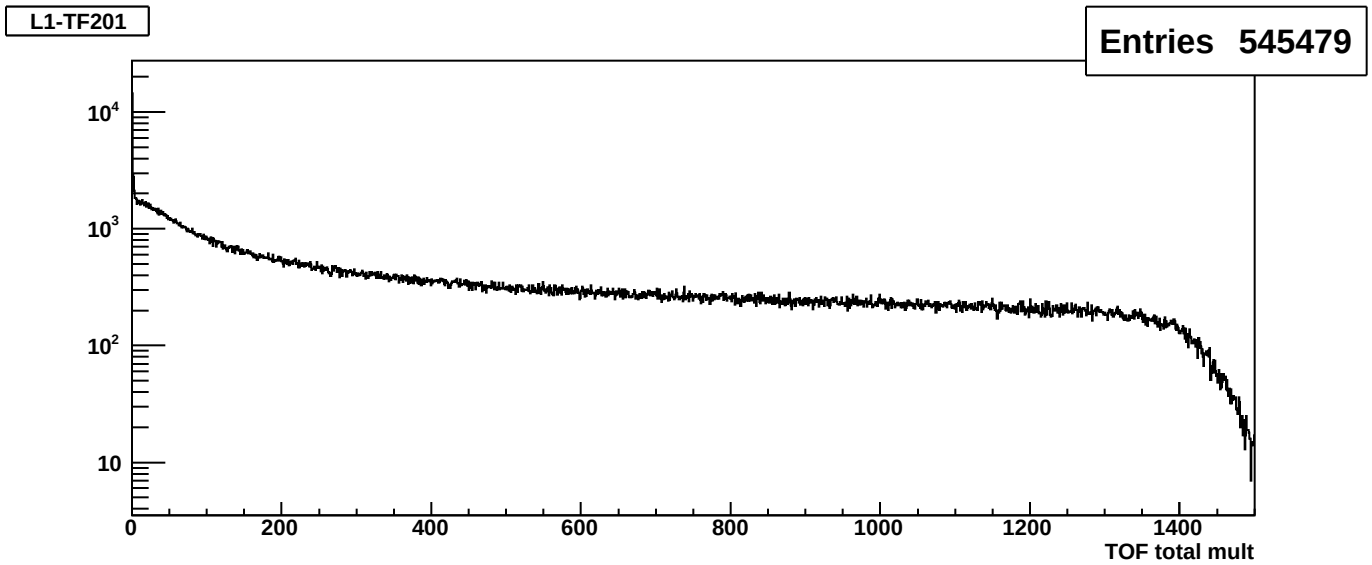

MIX-TF003
Entries 2.291012e+07
TOF MULT
TOF tray

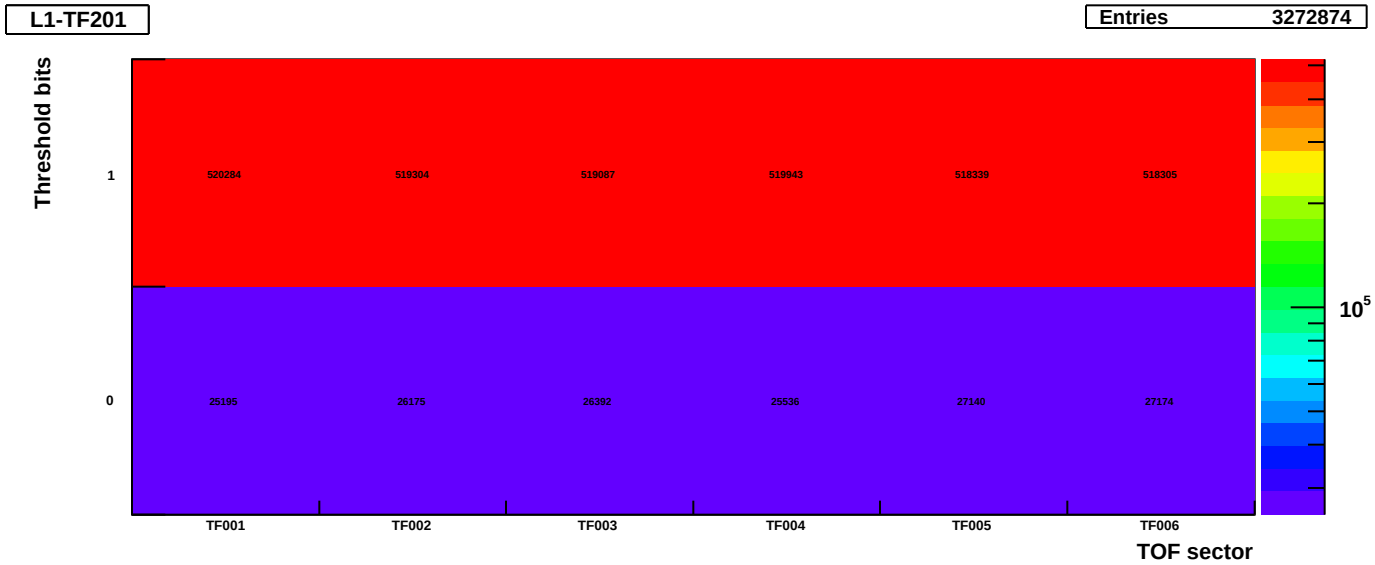

MIX-TF004
Entries 2.291012e+07
TOF MULT
TOF tray

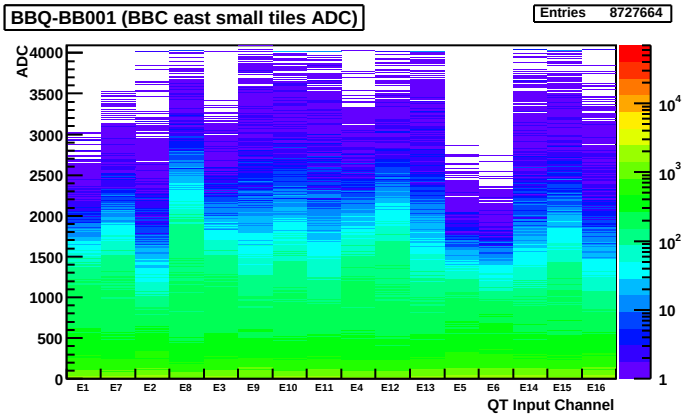

MIX-TF005
Entries 2.291012e+07
TOF MULT
TOF tray

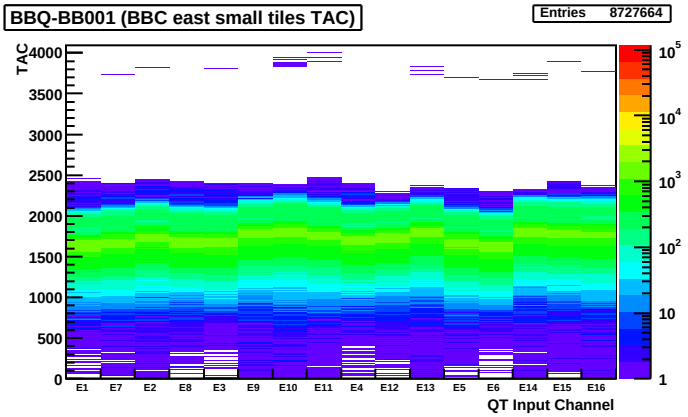

MIX-TF006
Entries 2.291012e+07
TOF MULT
TOF tray

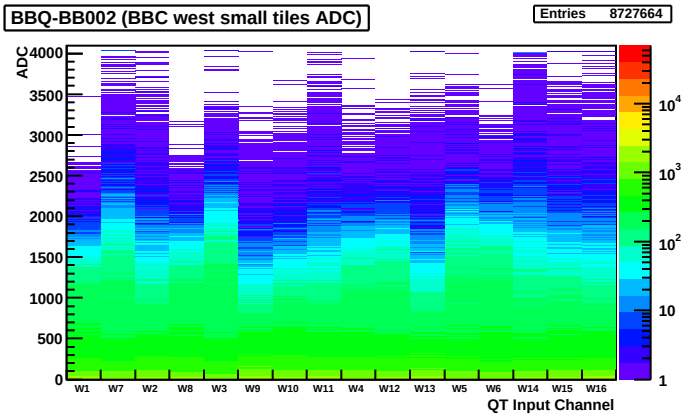

MIX-TF101
Entries 3272874
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

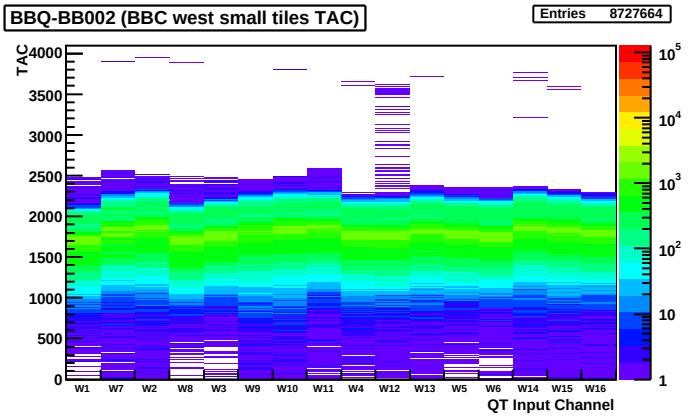

L1-TF201
Entries 545479
10^4
10^3
10^2
10
0
200
400
600
800
1000
1200
1400
TOF total mult

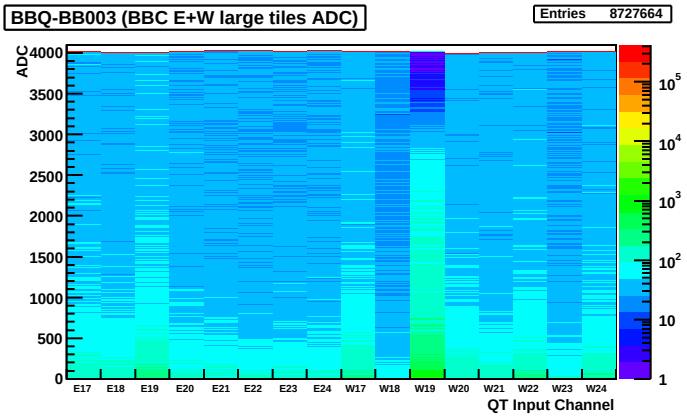

L1-TF201
Entries 3272874
Threshold bits
1
0
520284
519304
519087
519943
518339
518305
25195
26175
26392
25536
27140
27174
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

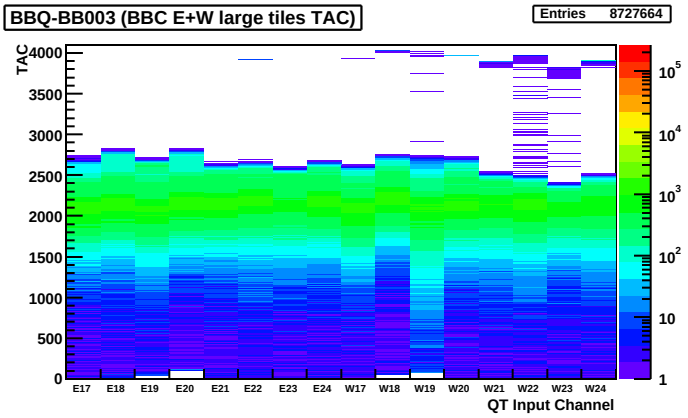

BBQ-BB001 (BBC east small tiles ADC)
Entries 8727664
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

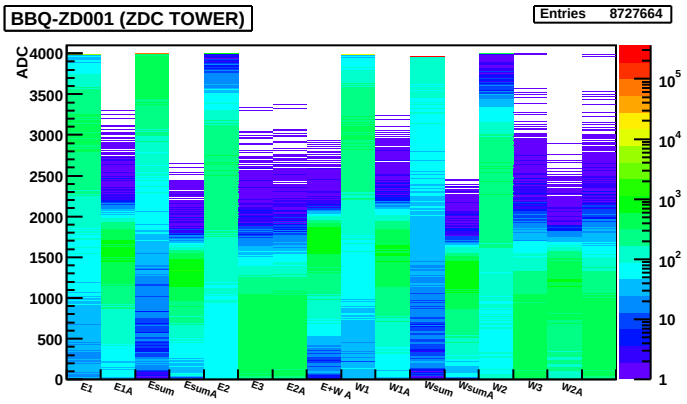

BBQ-BB001 (BBC east small tiles TAC)
Entries 8727664
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

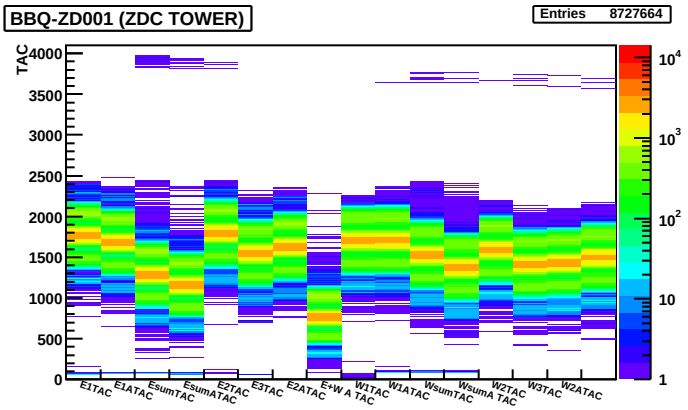

BBQ-BB002 (BBC west small tiles ADC)
Entries 8727664
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

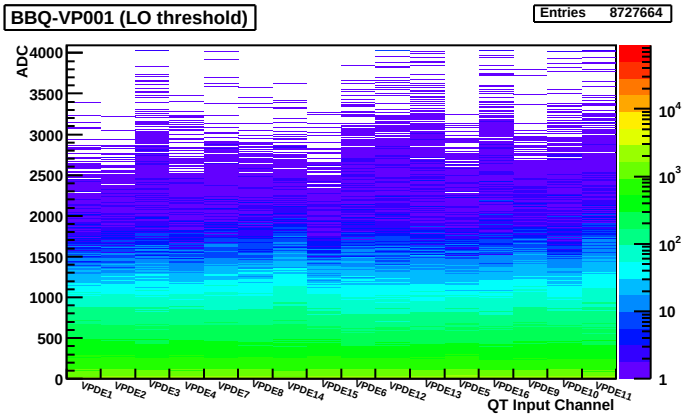

BBQ-BB002 (BBC west small tiles TAC)
Entries 8727664
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

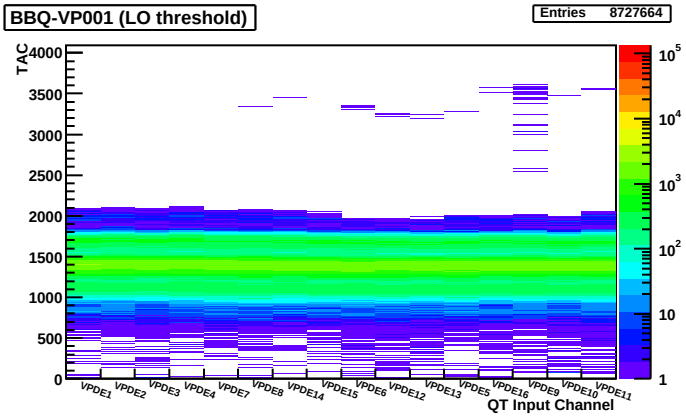

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 8727664
ADC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

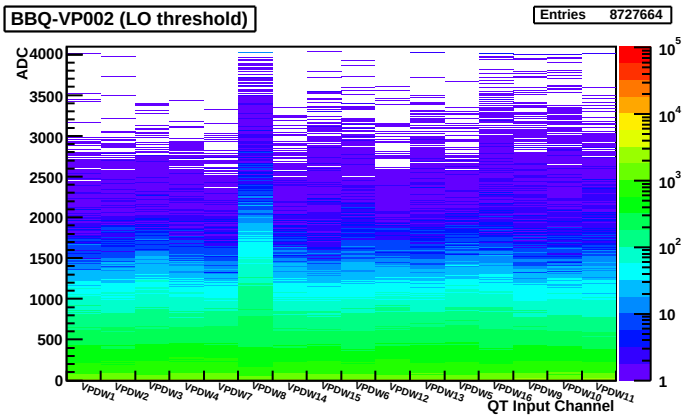

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 8727664
TAC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

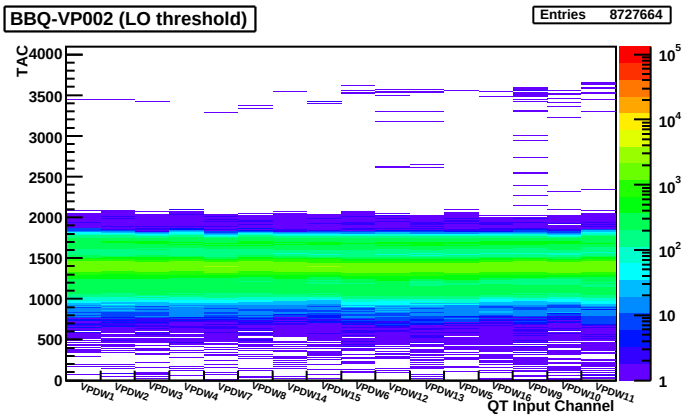

BBQ-ZD001 (ZDC TOWER)
Entries 8727664
ADC
E1 E1A Esum EsumA E2 E3 E2A E+W A W1 W1A Wsum WsumA W2 W3 W2A

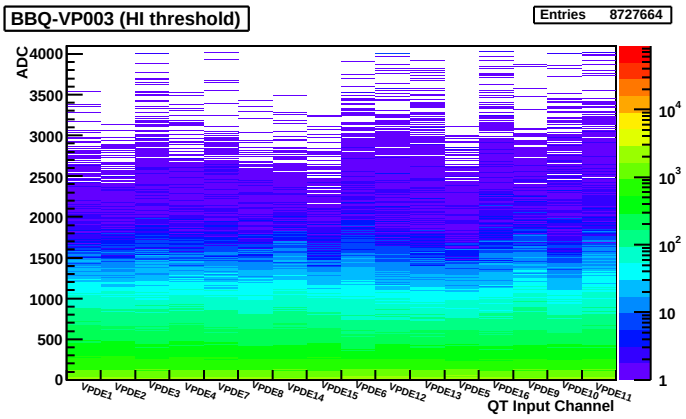

BBQ-ZD001 (ZDC TOWER)
Entries 8727664
TAC
E1TAC E1ATAC EsumTAC EsumATAC E2TAC E3TAC E2ATAC E+W A TAC W1TAC W1ATAC WsumTAC WsumA TAC W2TAC W3TAC W2ATAC

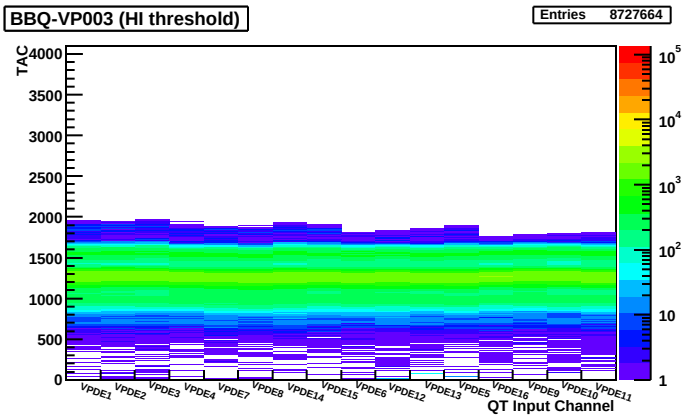

BBQ-VP001 (LO threshold)
Entries 8727664
ADC
QT Input Channel

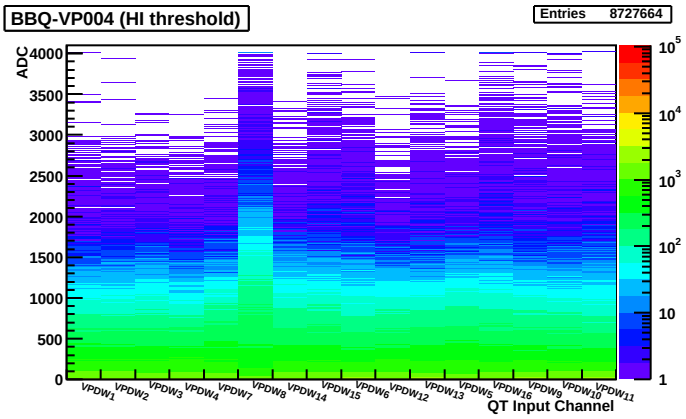

BBQ-VP001 (LO threshold)
Entries 8727664
TAC
QT Input Channel

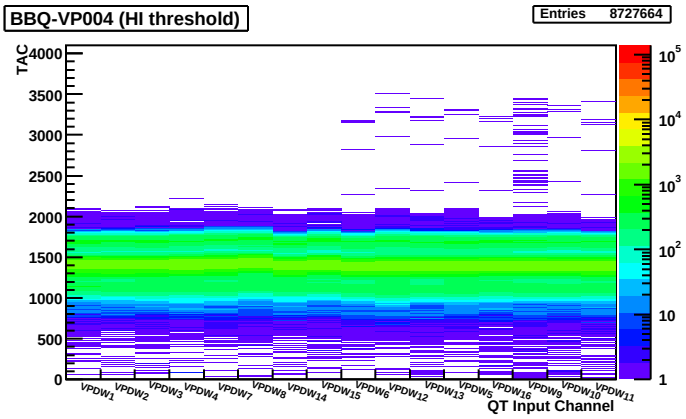

BBQ-VP002 (LO threshold)
Entries 8727664
ADC
QT Input Channel

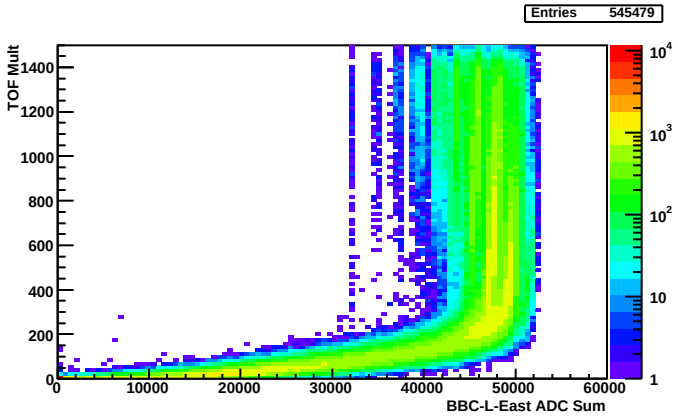

BBQ-VP002 (LO threshold)
Entries 8727664
TAC
QT Input Channel

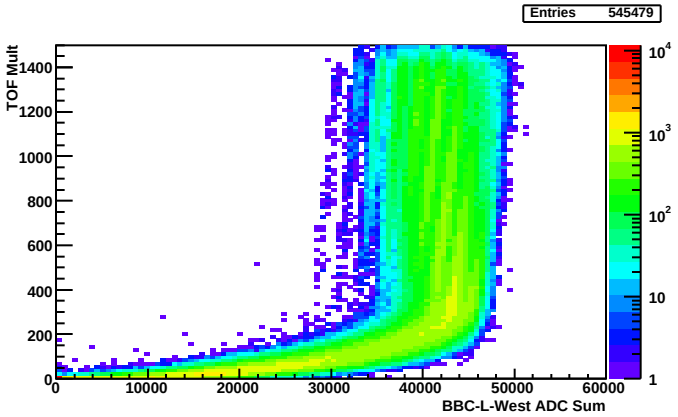

BBQ-VP003 (HI threshold)
Entries 8727664
ADC
QT Input Channel

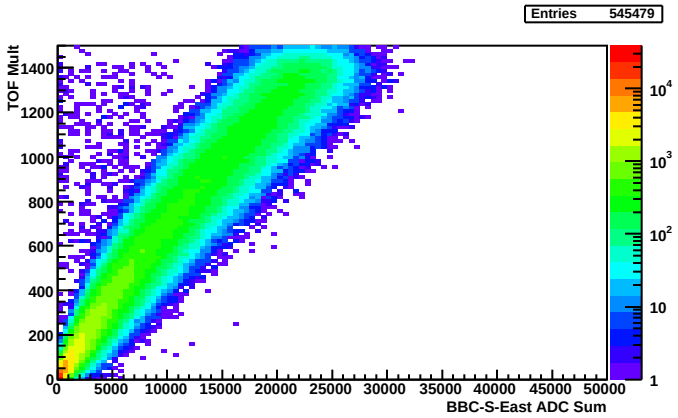

BBQ-VP003 (HI threshold)
Entries 8727664
TAC
QT Input Channel

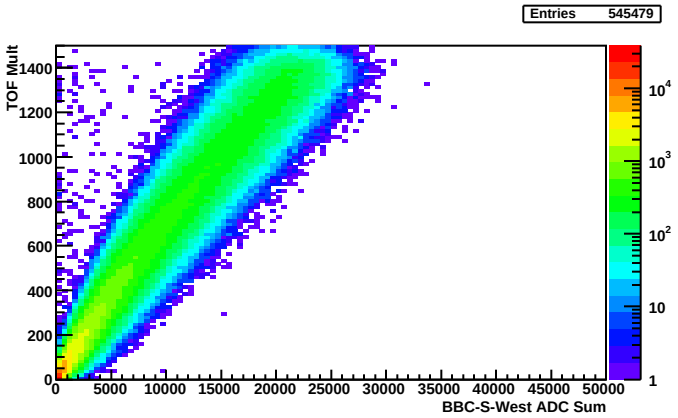

BBQ-VP004 (HI threshold)
Entries 8727664
ADC
QT Input Channel

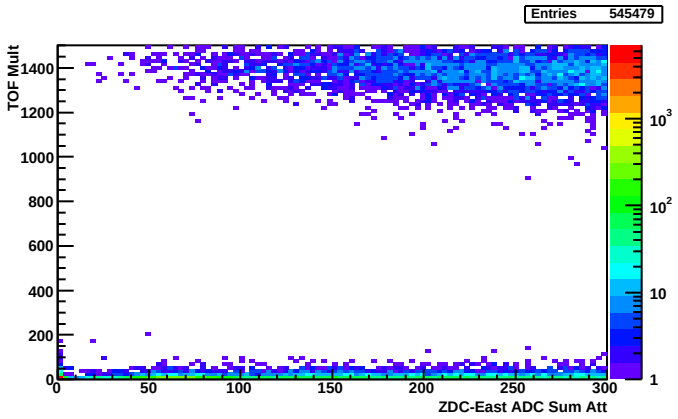

BBQ-VP004 (HI threshold)
Entries 8727664
TAC
QT Input Channel

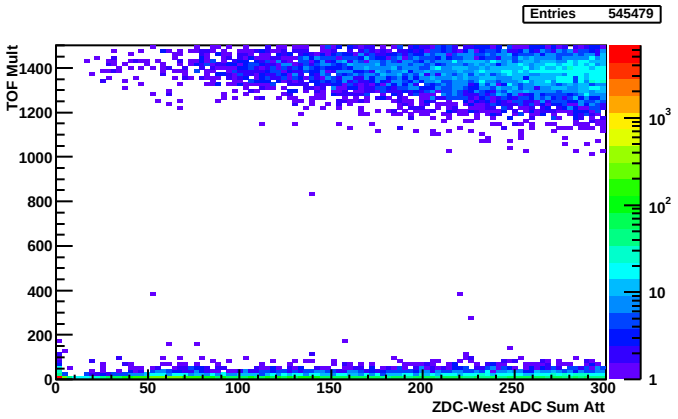

Entries 545479
TOF Mult
BBC-L-East ADC Sum

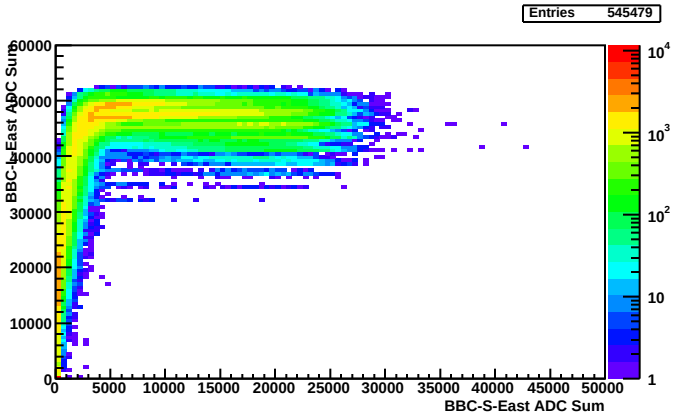

Entries 545479
TOF Mult
BBC-L-West ADC Sum

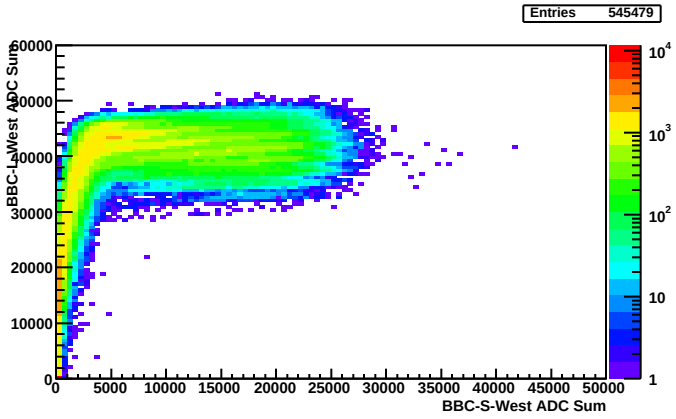

Entries 545479
TOF Mult
BBC-S-East ADC Sum


Entries 545479
TOF Mult
BBC-S-West ADC Sum


Entries 545479
TOF Mult
ZDC-East ADC Sum Att


Entries 545479
TOF Mult
ZDC-West ADC Sum Att


Entries 545479
BBC-L-East ADC Sum
BBC-S-East ADC Sum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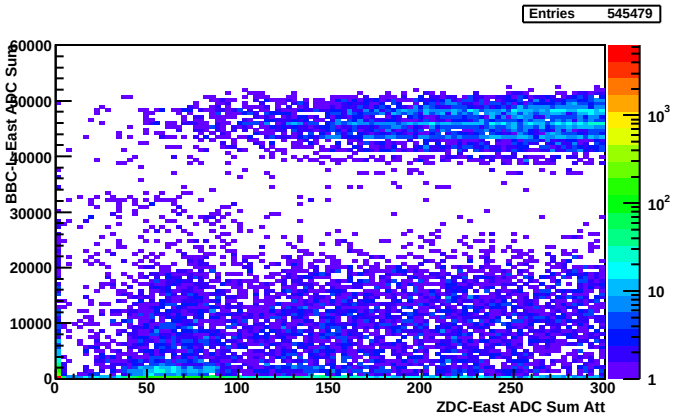
BBC-L-West ADC Sum
BBC-S-West ADC Sum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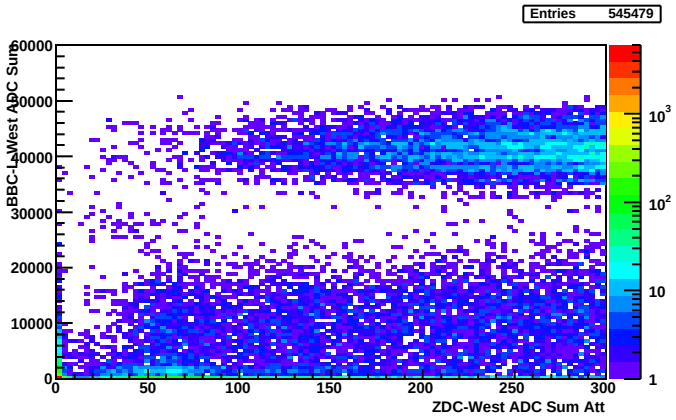
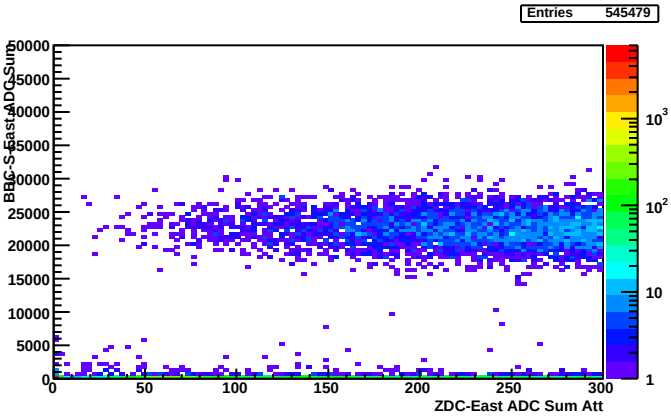
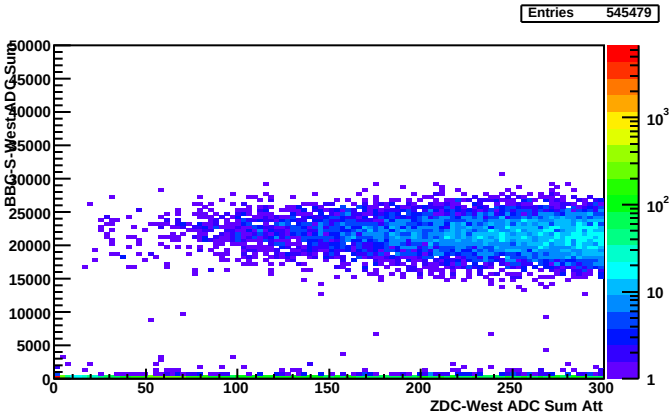
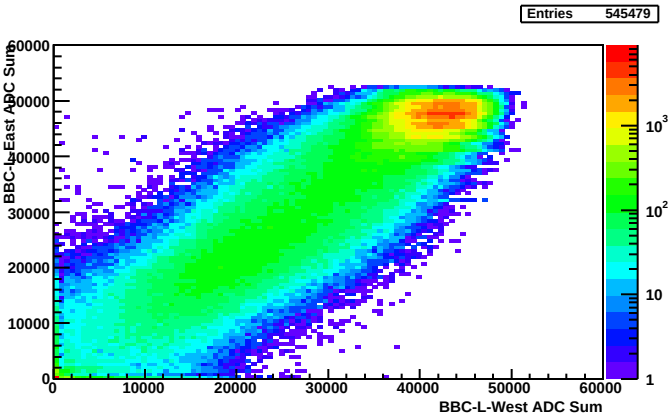
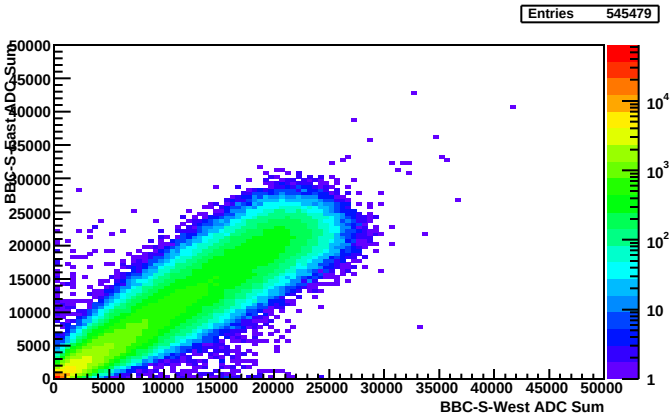
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

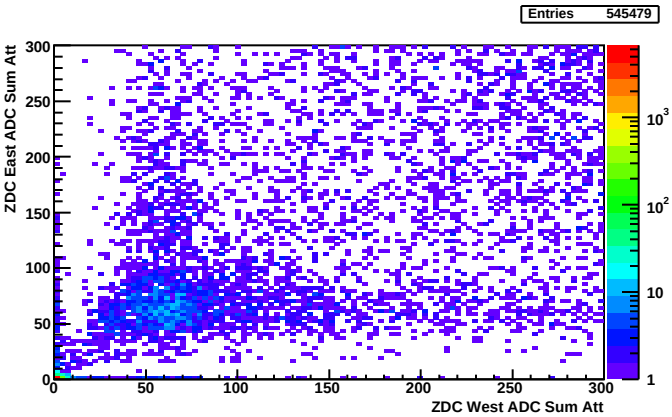

Entries 545479
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

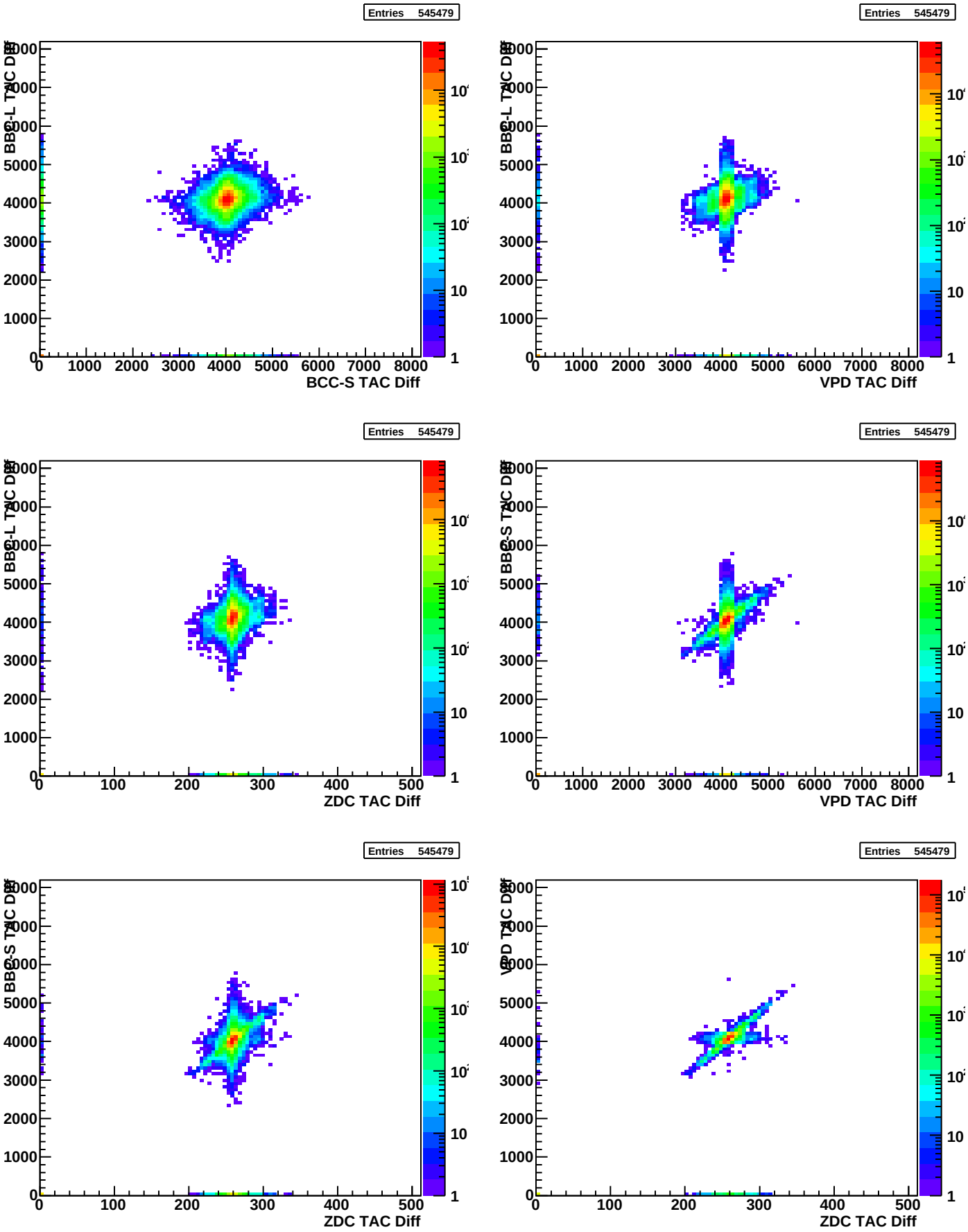

Entries 545479
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

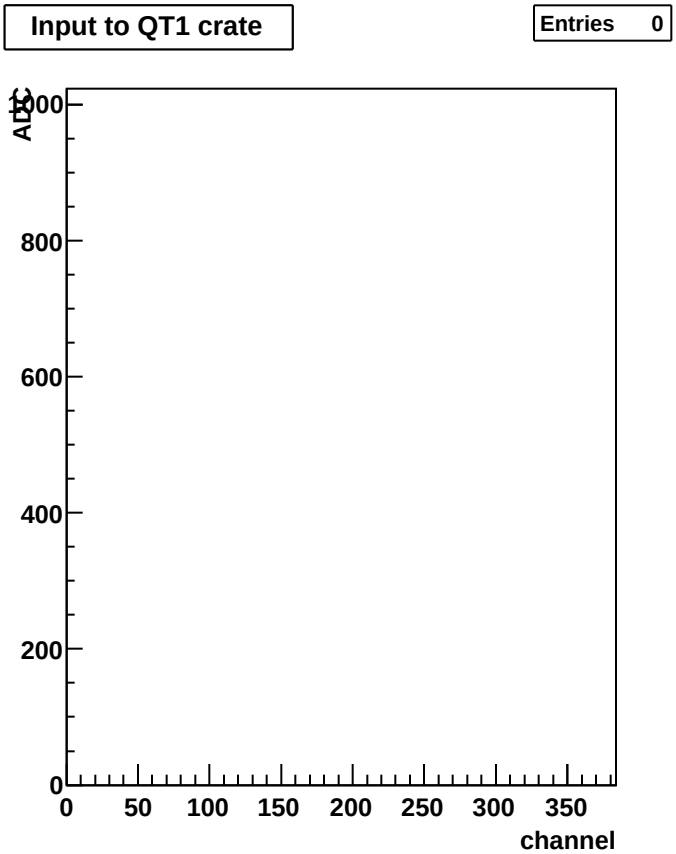

Entries 545479
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

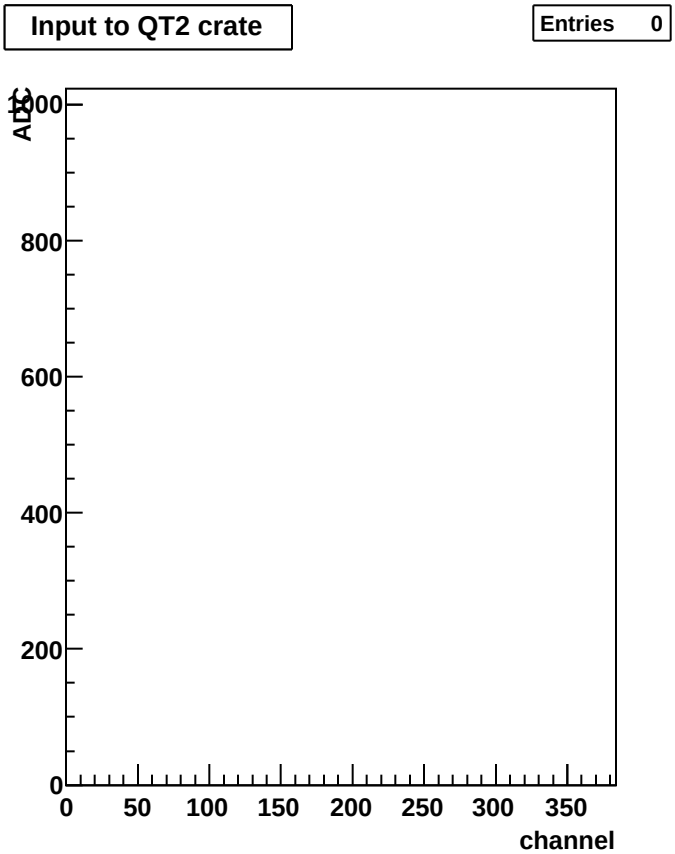

Entries 545479
BBC-L-East ADC Sum
BBC-L-West ADC Sum

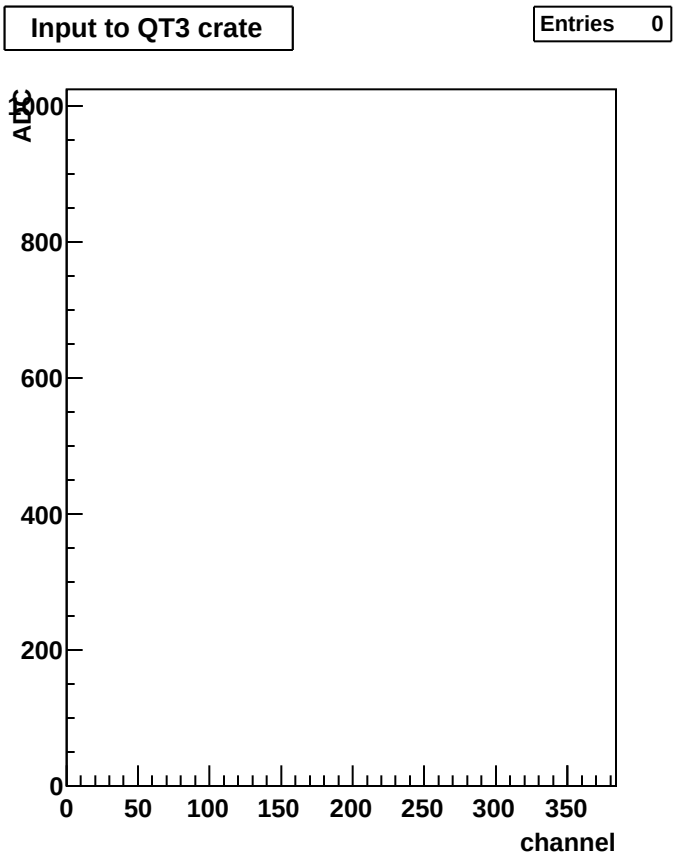

Entries 545479
BBC-S-East ADC Sum
BBC-S-West ADC Sum

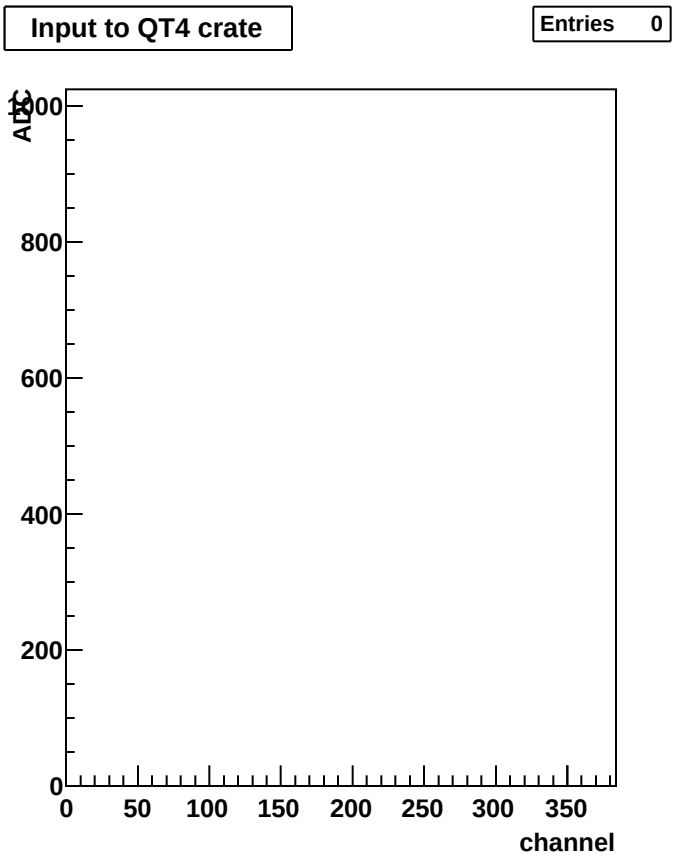

Entries 545479
ZDC East ADC Sum Att
ZDC West ADC Sum Att

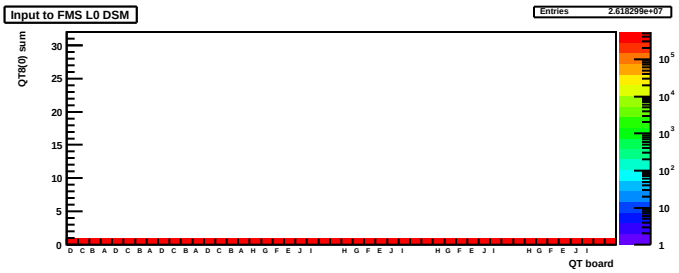

Entries 545479
BBC-L TAC Diff
BCC-S TAC Diff
Entries 545479
BBC-L TAC Diff
VPD TAC Diff
Entries 545479
BBC-L TAC Diff
ZDC TAC Diff
Entries 545479
BBC-S TAC Diff
VPD TAC Diff
Entries 545479
BBC-S TAC Diff
ZDC TAC Diff
Entries 545479
VPD TAC Diff
ZDC TAC Diff

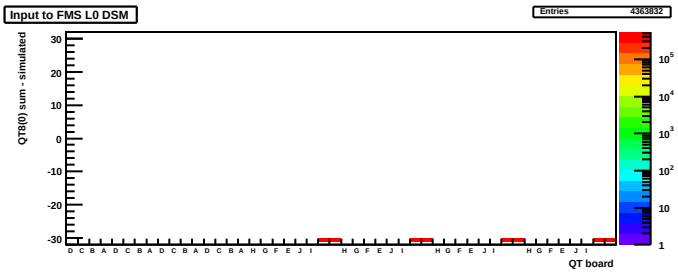

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

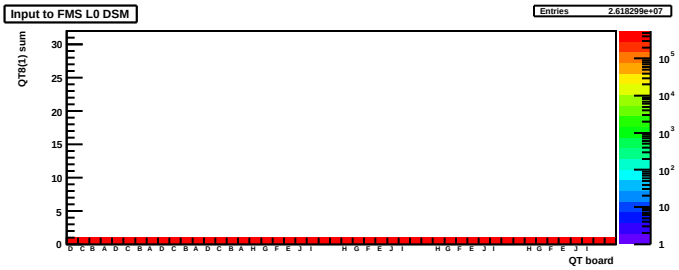

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

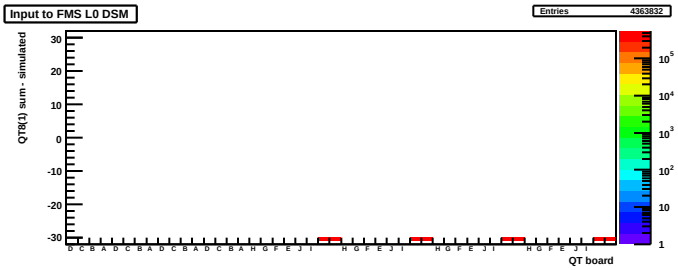

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

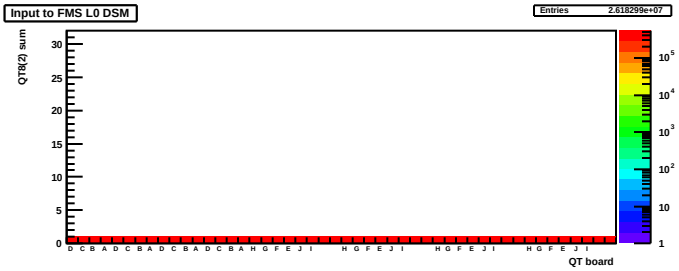

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

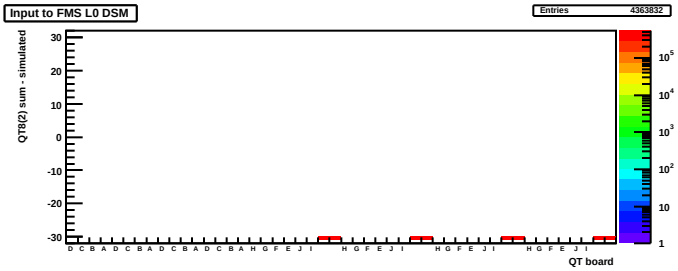

Input to FMS L0 DSM
Entries 2.618299e+07
QT8(0) sum
QT board

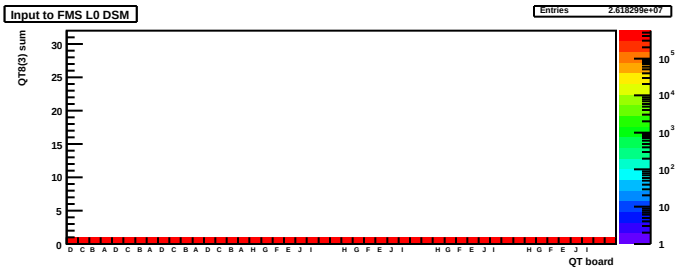

Input to FMS L0 DSM
Entries 4363832
QT8(0) sum - simulated
QT board

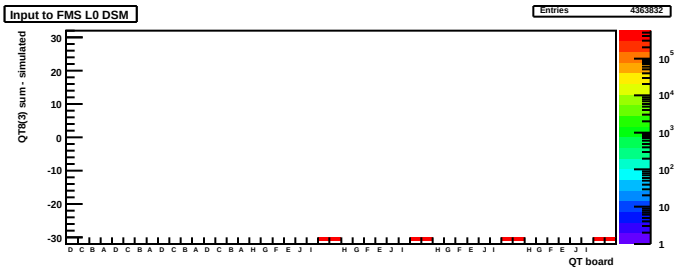

Input to FMS L0 DSM
Entries 2.618299e+07
QT8(1) sum
QT board

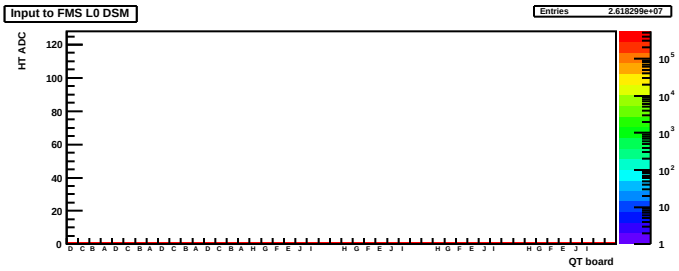

Input to FMS L0 DSM
Entries 4363832
QT8(1) sum - simulated
QT board

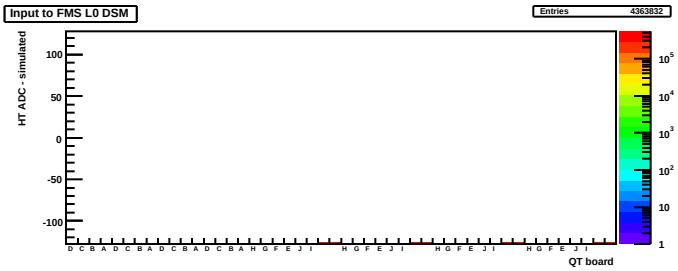

Input to FMS L0 DSM
Entries 2.618299e+07
QT8(2) sum
QT board

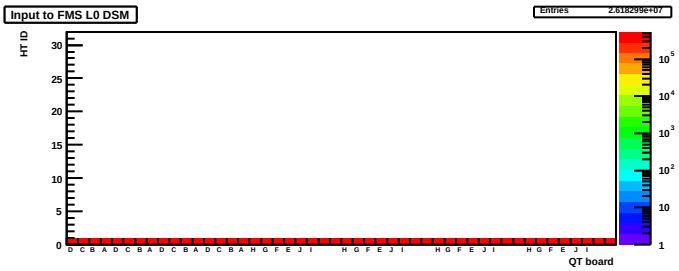

Input to FMS L0 DSM
Entries 4363832
QT8(2) sum - simulated
QT board

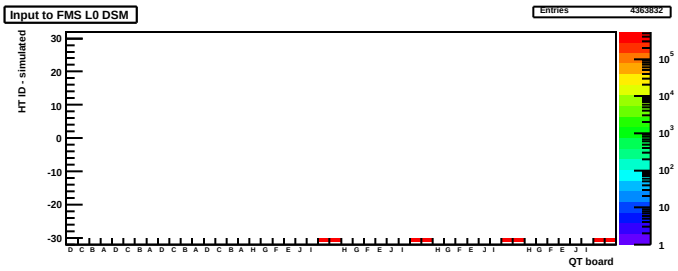

Input to FMS L0 DSM
Entries 2.618299e+07
QT8(3) sum
QT board

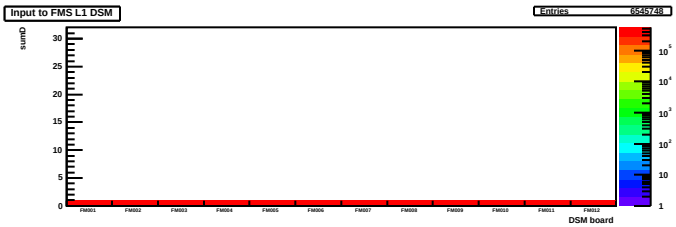

Input to FMS L0 DSM
Entries 4363832
QT8(3) sum - simulated
QT board

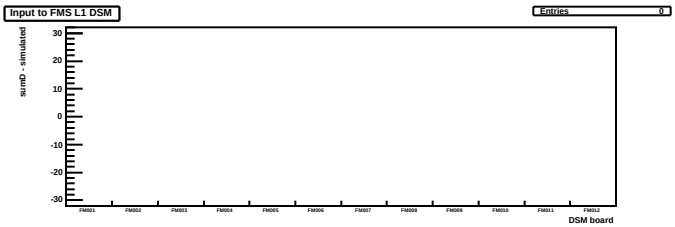

Input to FMS L0 DSM
Entries 2.618299e+07
HT ADC
QT board

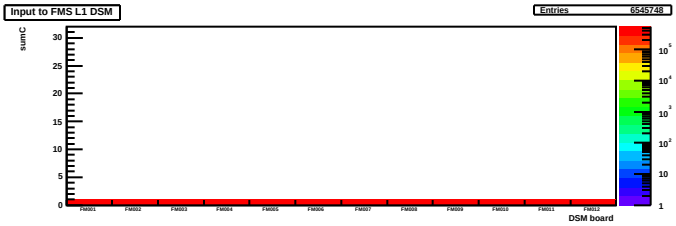

Input to FMS L0 DSM
Entries 4363832
HT ADC - simulated
QT board

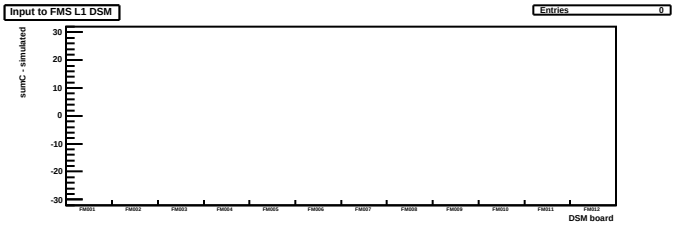

Input to FMS L0 DSM
Entries 2.618299e+07
HT ID
QT board

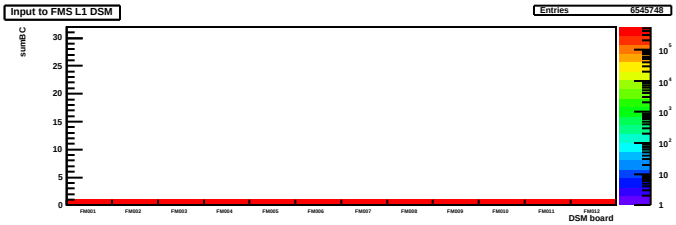

Input to FMS L0 DSM
Entries 4363832
HT ID - simulated
QT board

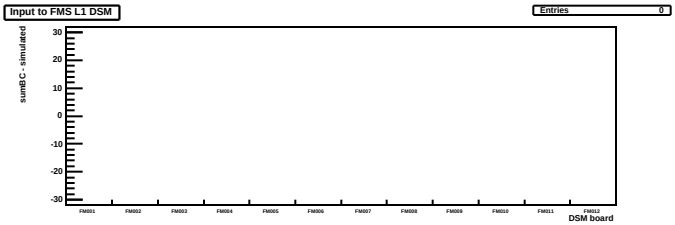

Input to FMS L1 DSM
Entries 6545748
sumD
DSM board

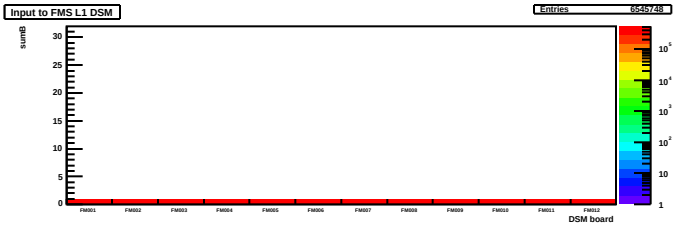

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

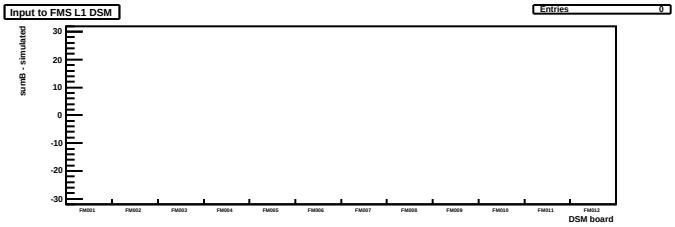

Input to FMS L1 DSM
Entries 6545748
sumC
DSM board

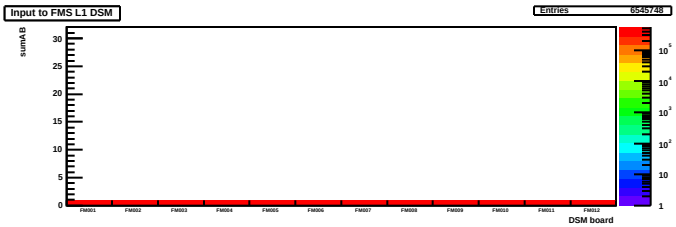

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

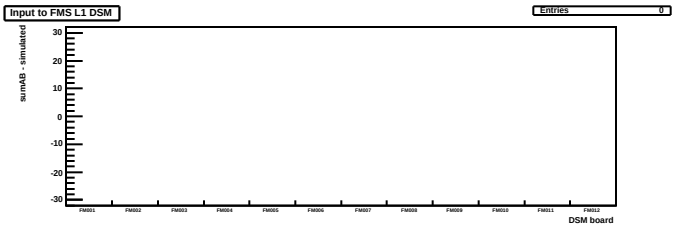

Input to FMS L1 DSM
Entries 6545748
sumBC
DSM board

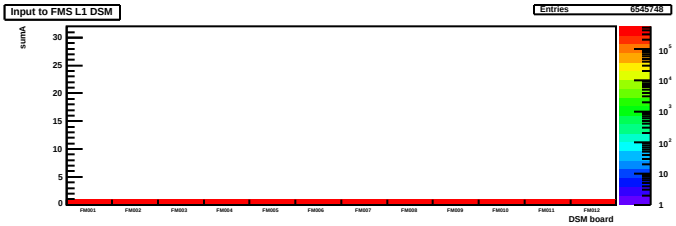

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

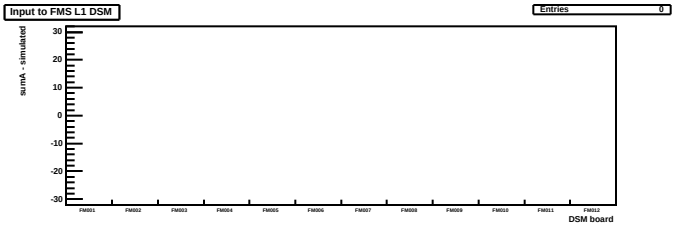

Input to FMS L1 DSM
Entries 6545748
sumB
DSM board

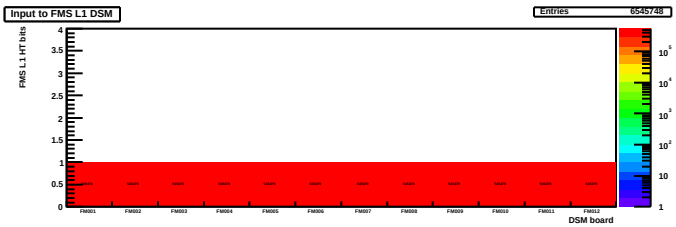

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

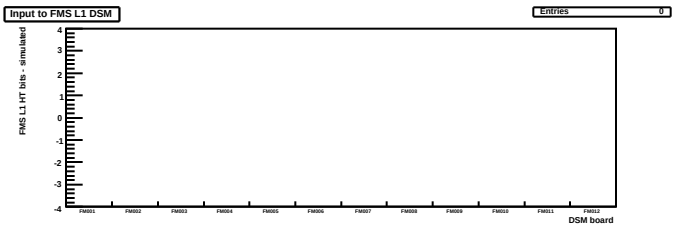

Input to FMS L1 DSM
Entries 6545748
sumAB
DSM board

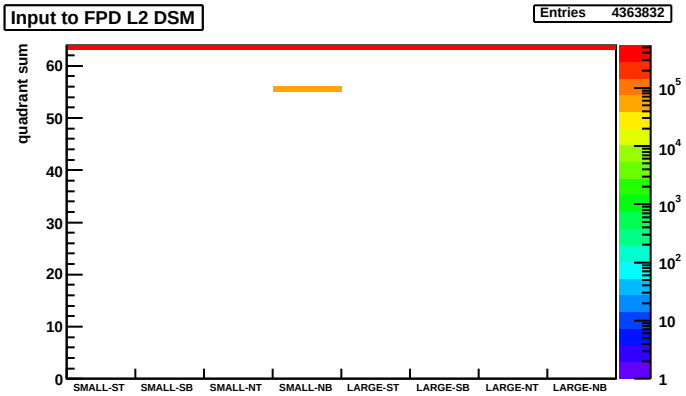

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

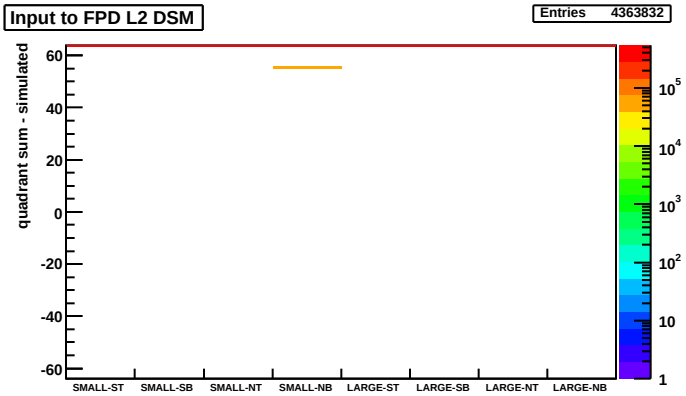

Input to FMS L1 DSM
Entries 6545748
sumA
DSM board

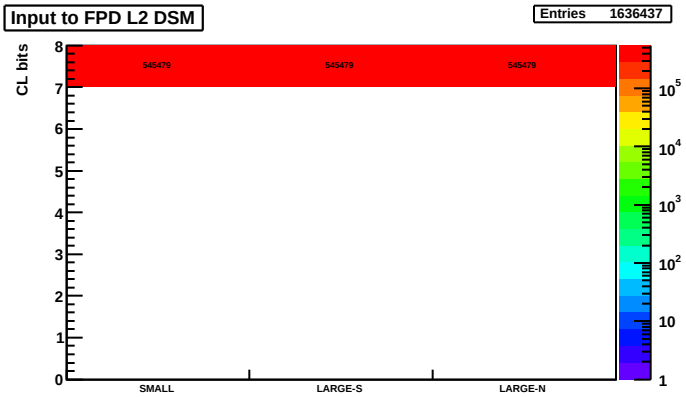

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

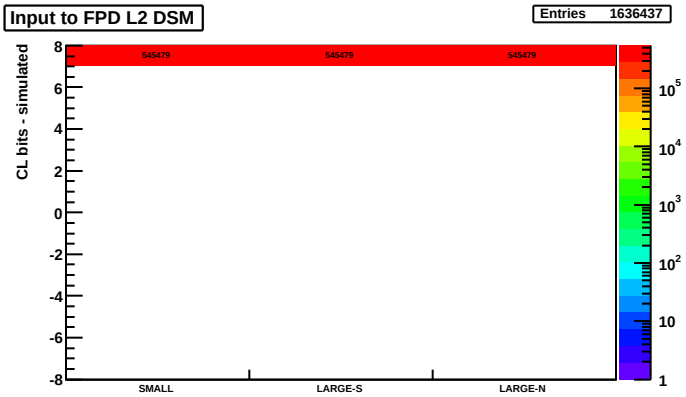

Input to FMS L1 DSM
Entries 6545748
FMS L1 HT bits
DSM board

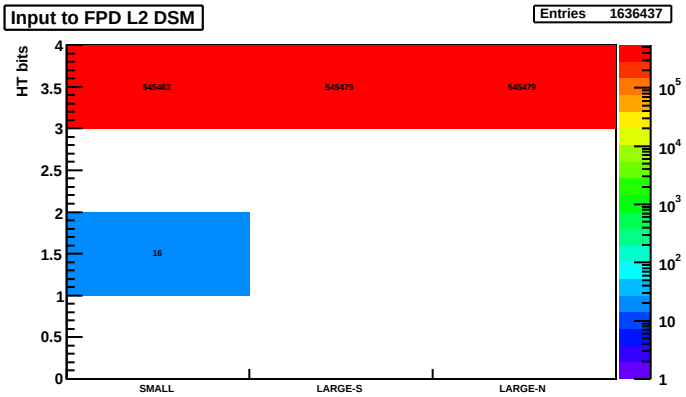

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

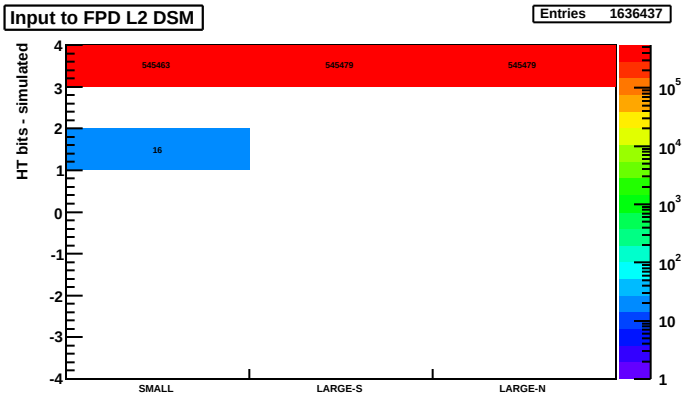

Input to FPD L2 DSM
Entries 4363832
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

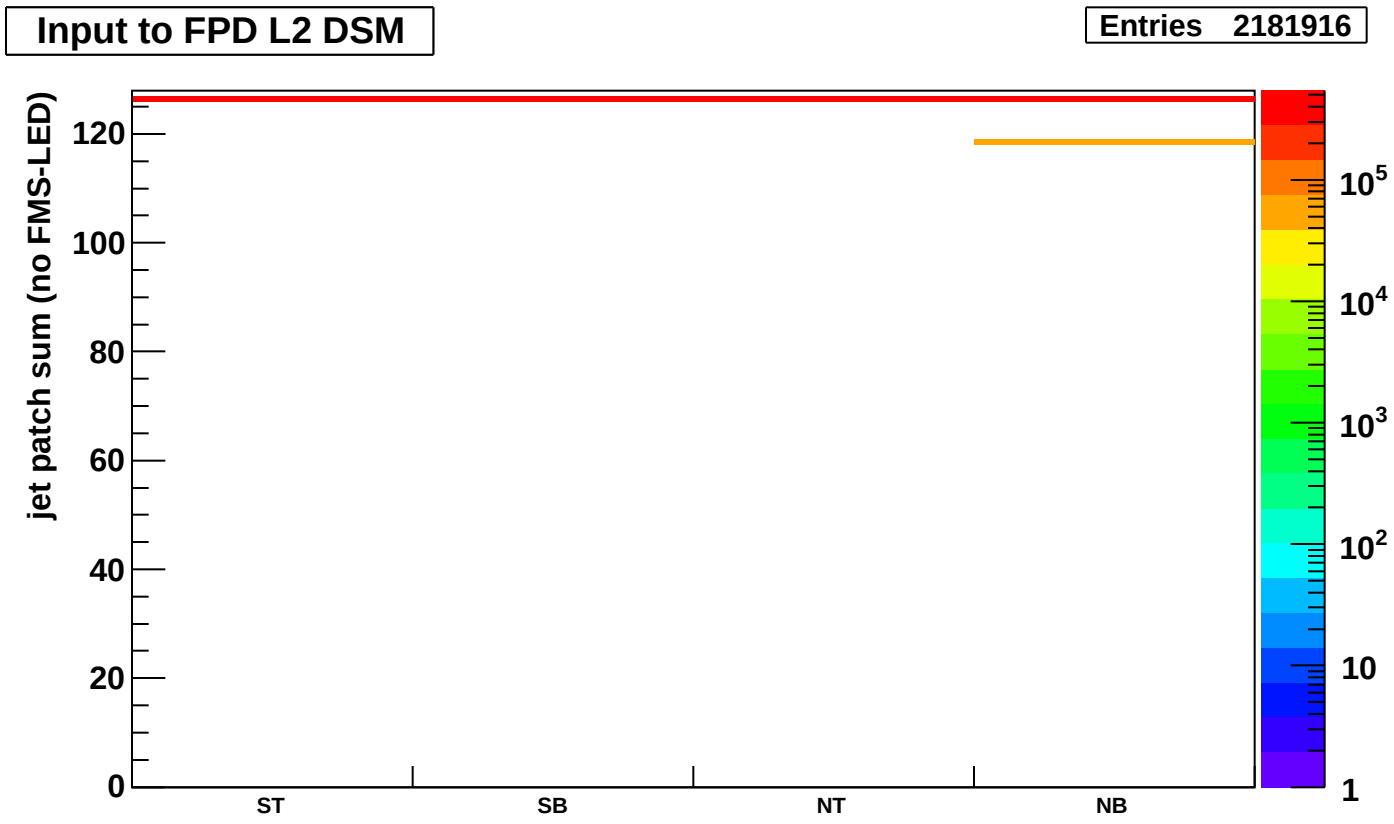

Input to FPD L2 DSM
Entries 4363832
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

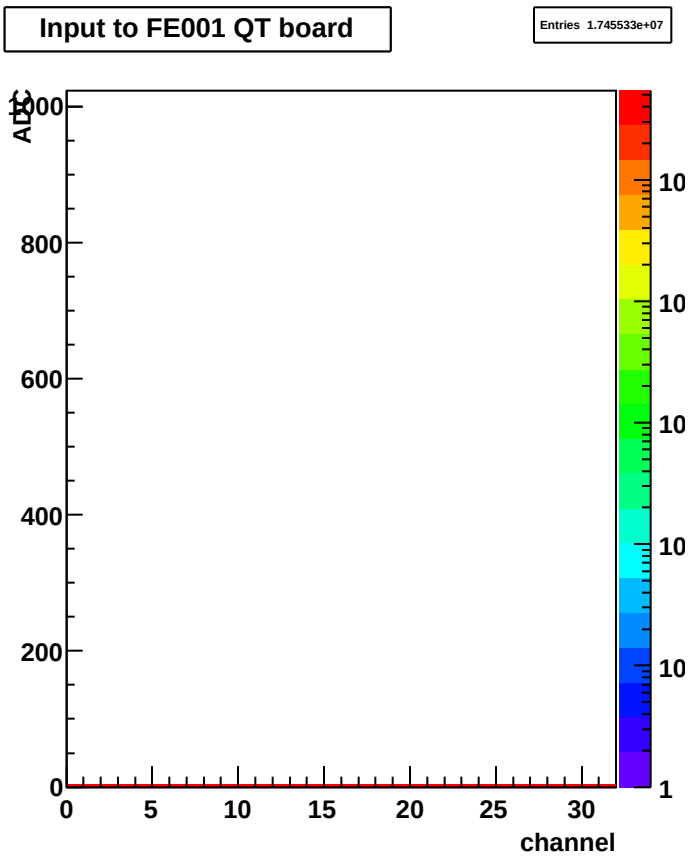

Input to FPD L2 DSM
Entries 1636437
CL bits
545479
545479
545479
SMALL
LARGE-S
LARGE-N

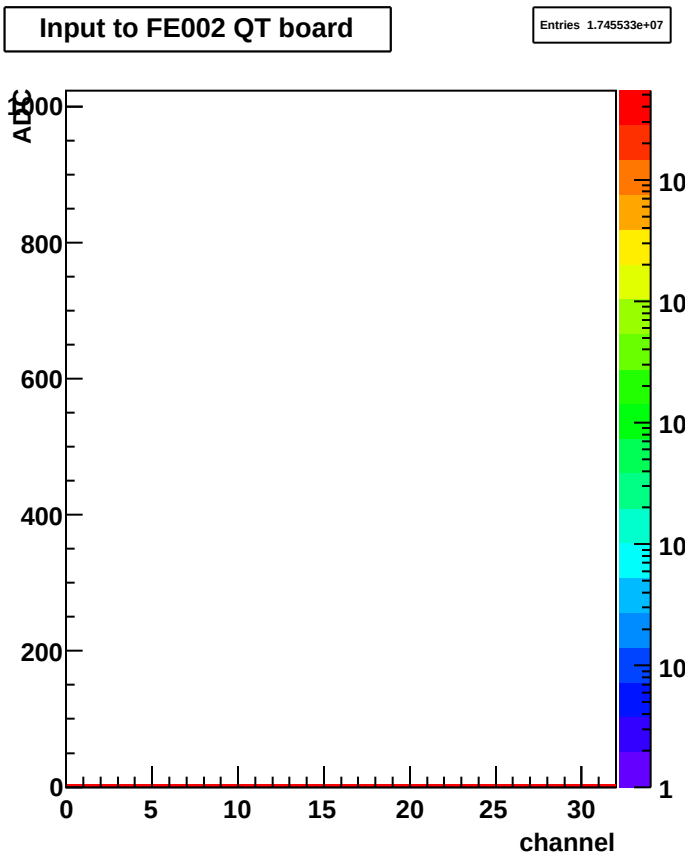

Input to FPD L2 DSM
Entries 1636437
CL bits - simulated
545479
545479
545479
SMALL
LARGE-S
LARGE-N

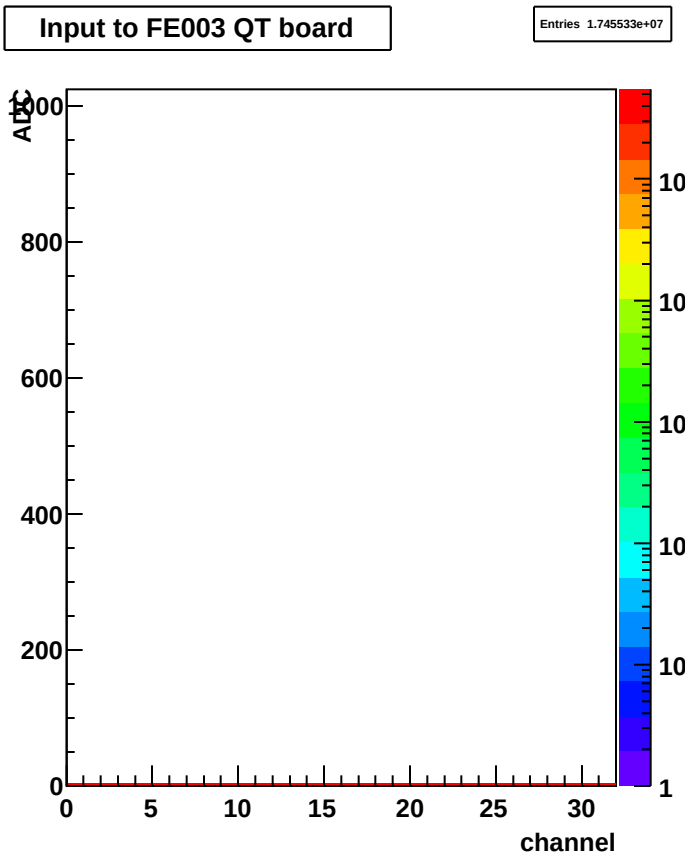

Input to FPD L2 DSM
Entries 1636437
HT bits
545463
545479
545479
16
SMALL
LARGE-S
LARGE-N

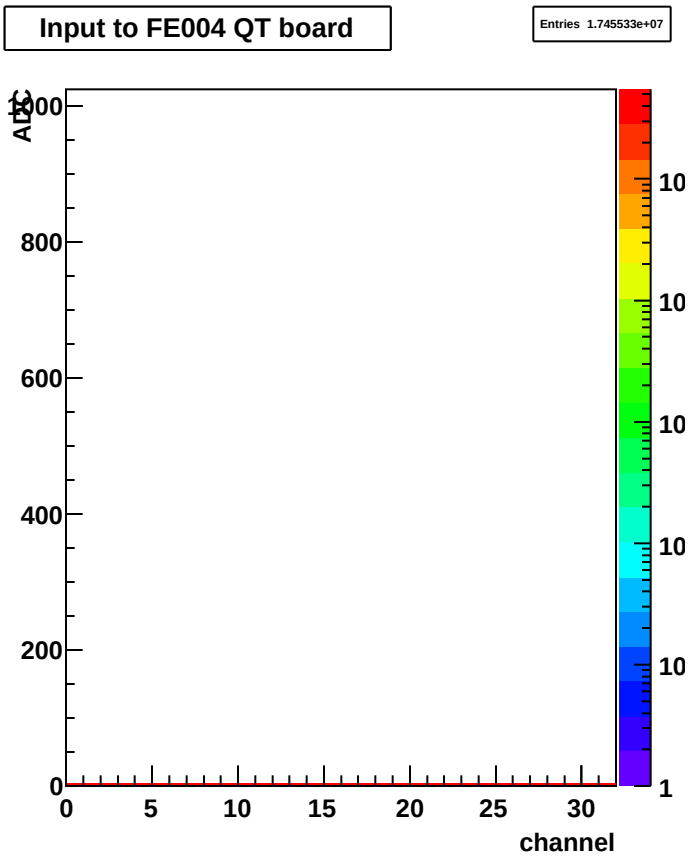

Input to FPD L2 DSM
Entries 1636437
HT bits - simulated
545463
545479
545479
16
SMALL
LARGE-S
LARGE-N


Input to FPD L2 DSM
Entries 2181916
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

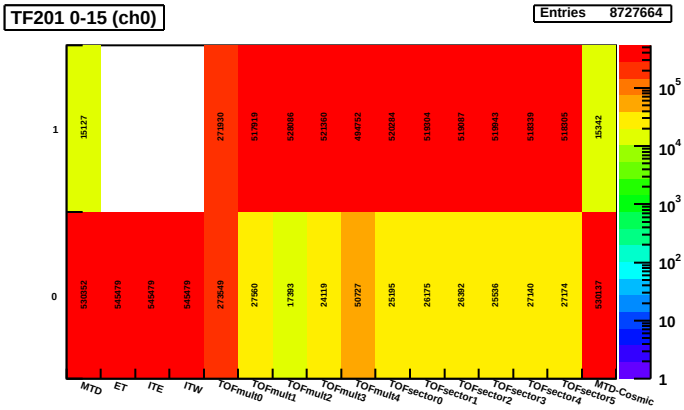

Input to FE001 QT board
Entries 1.745533e+07
ADC
channel


Input to FE002 QT board
Entries 1.745533e+07
ADC
channel

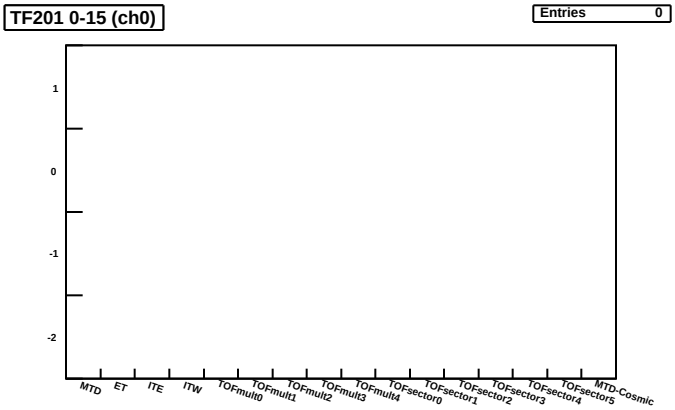

Input to FE003 QT board
Entries 1.745533e+07
ADC
channel


Input to FE004 QT board
Entries 1.745533e+07
ADC
channel

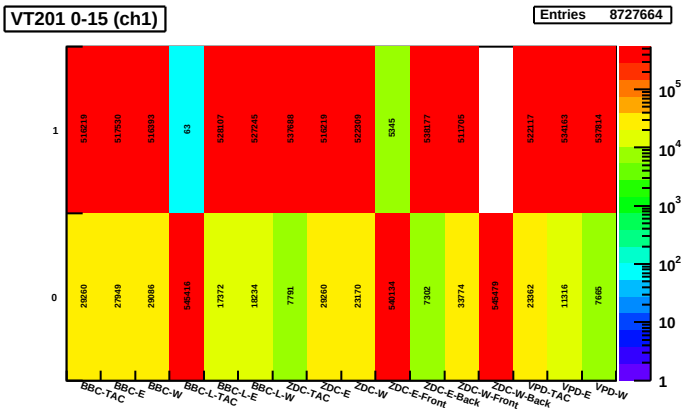
TF201 0-15 (ch0)



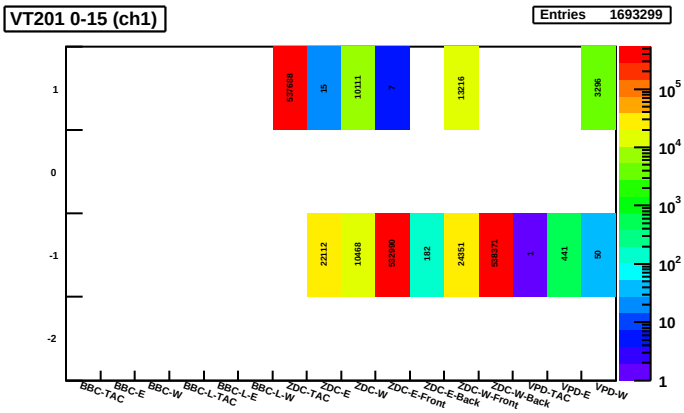
TF201 0-15 (ch0)



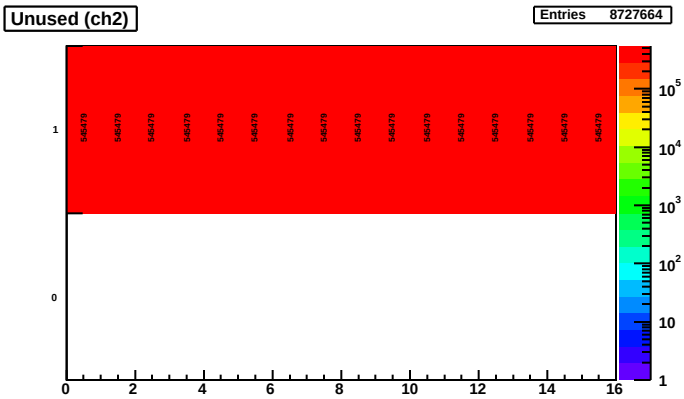
VT201 0-15 (ch1)

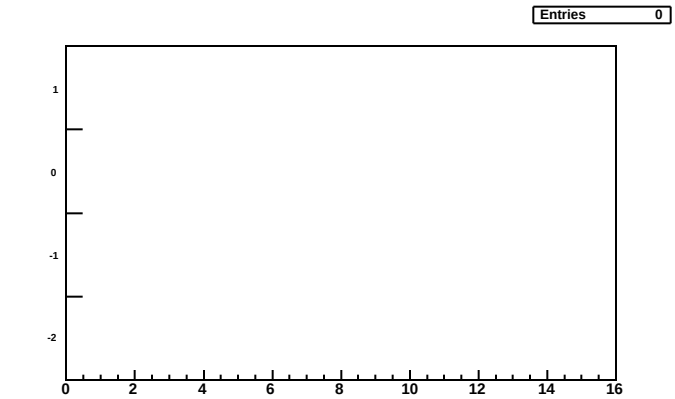


VT201 0-15 (ch1)

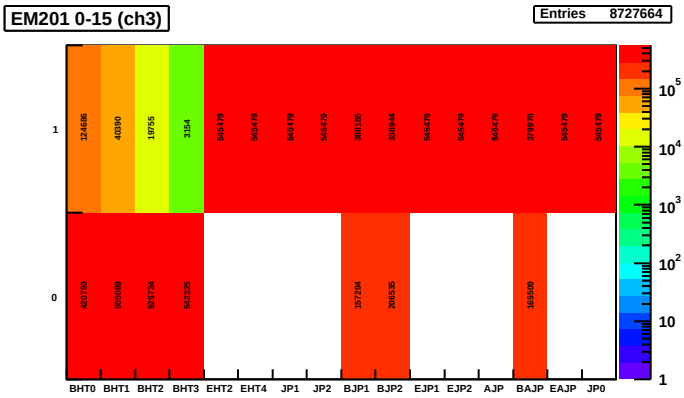


Unused (ch2)

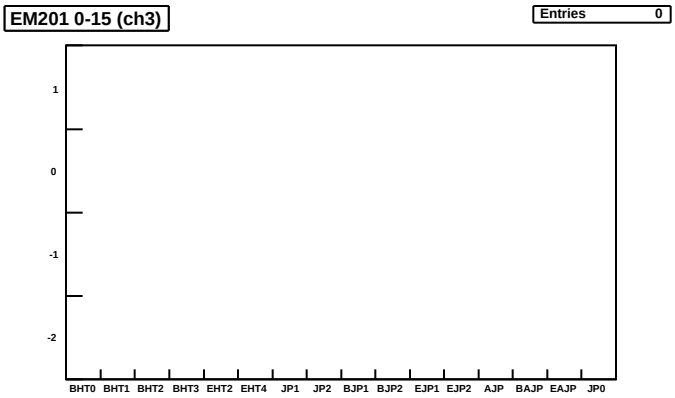




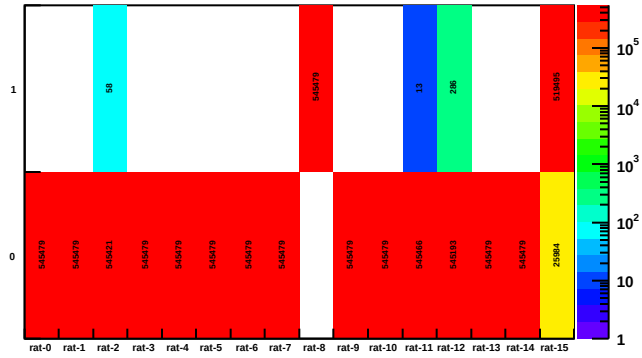
EM201 0-15 (ch3)

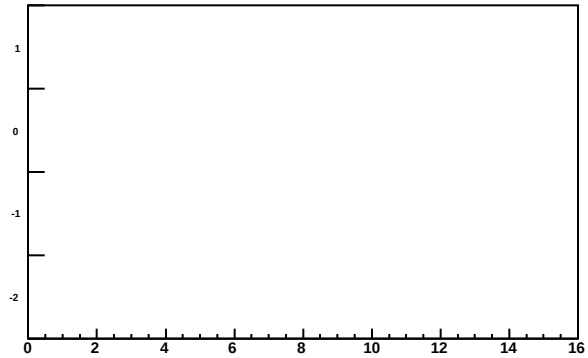


EM201 0-15 (ch3)

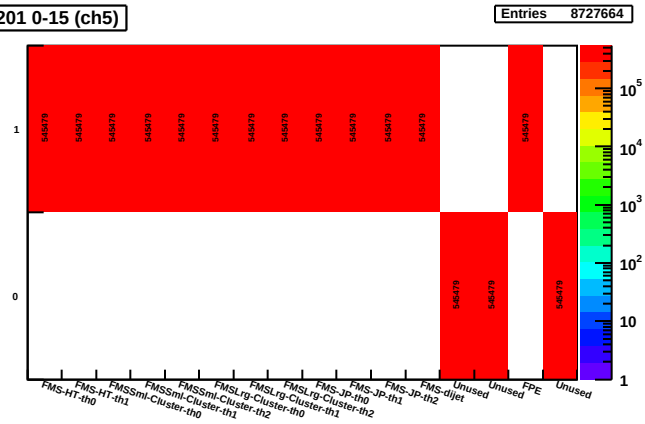


RAT board (ch4)

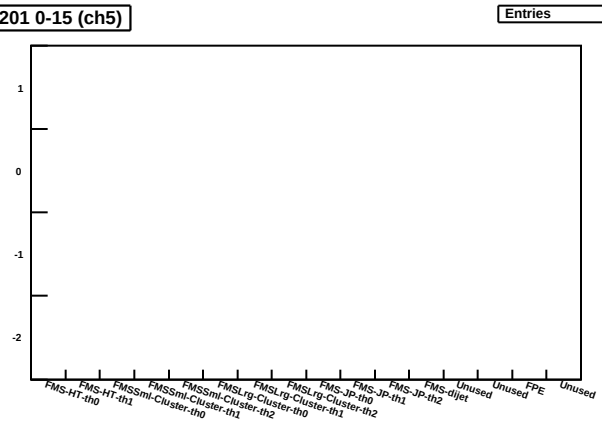




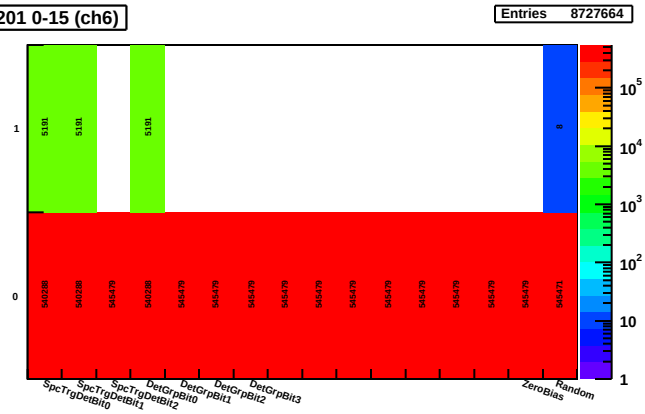
FP201 0-15 (ch5)

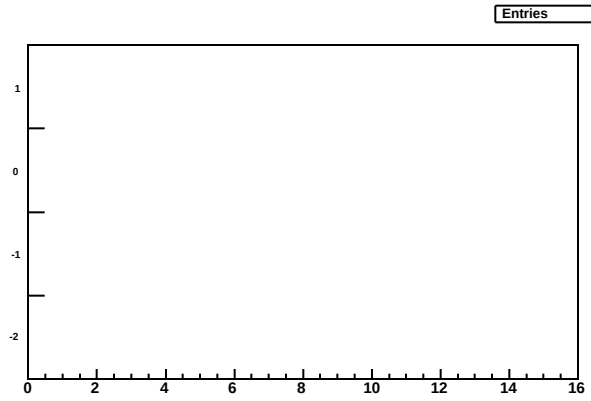
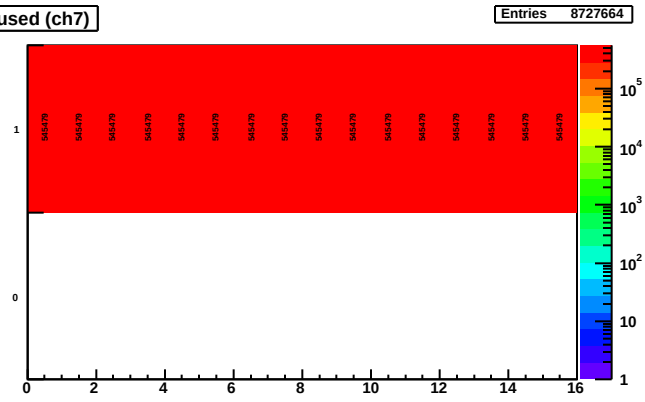
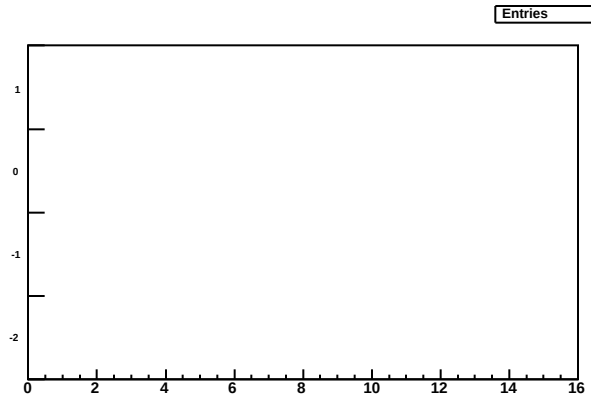


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)