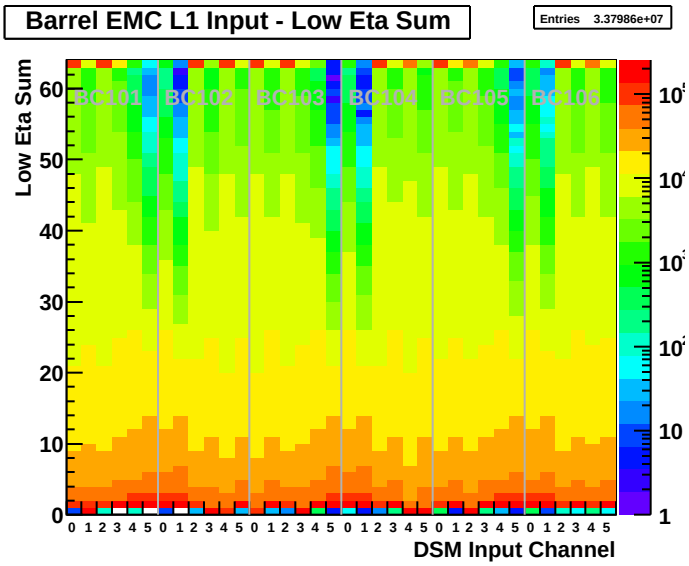
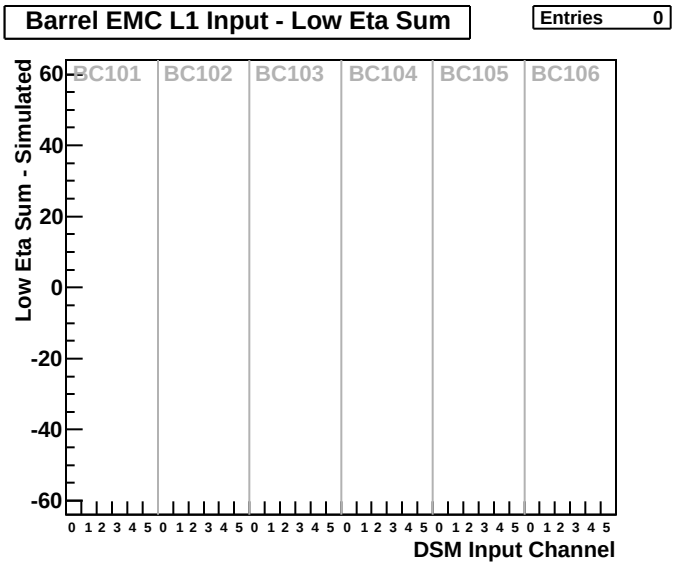
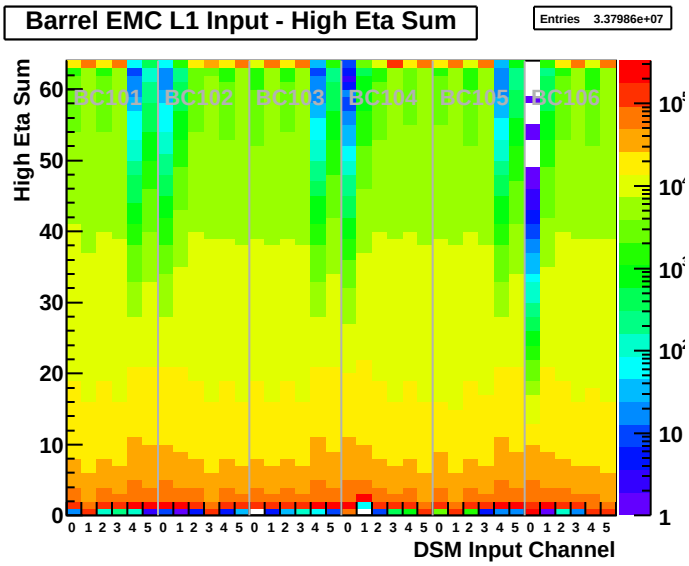
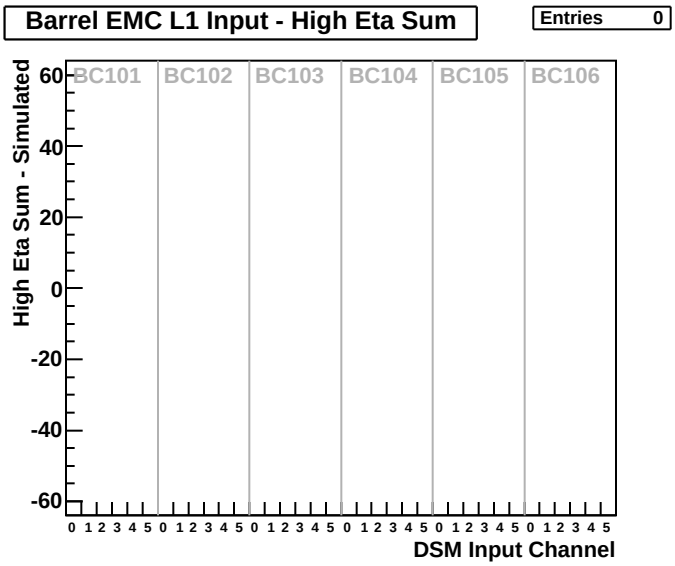
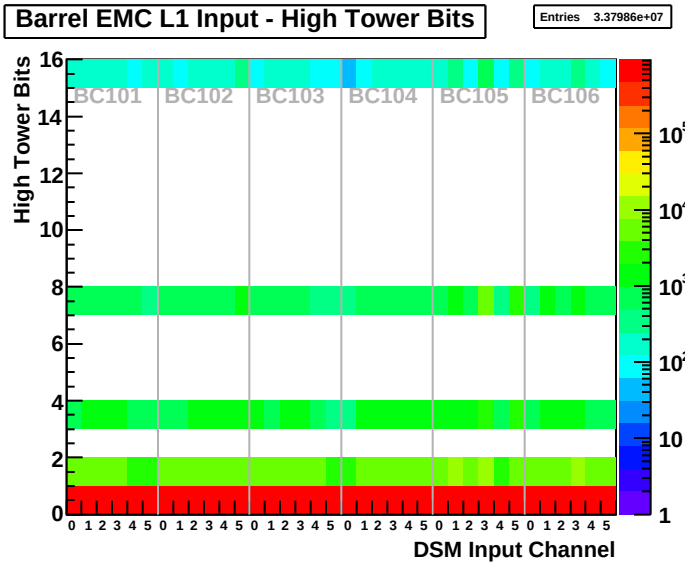
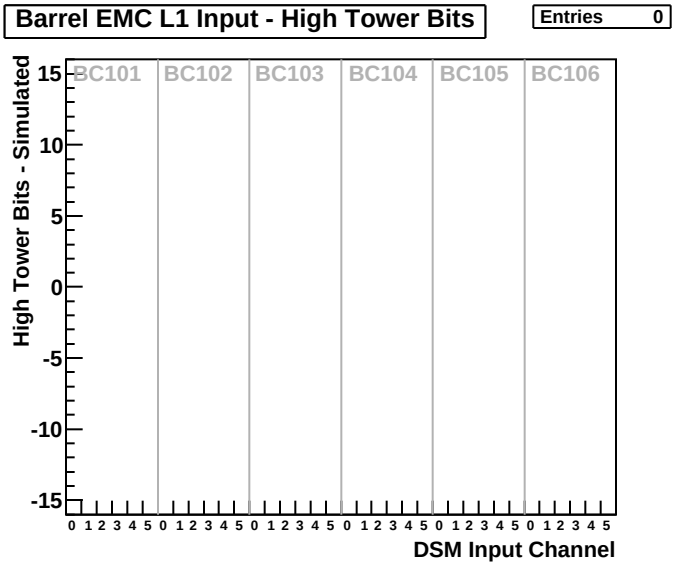

Barrel EMC L1 Input - Low Eta Sum
Entries 3.37986e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

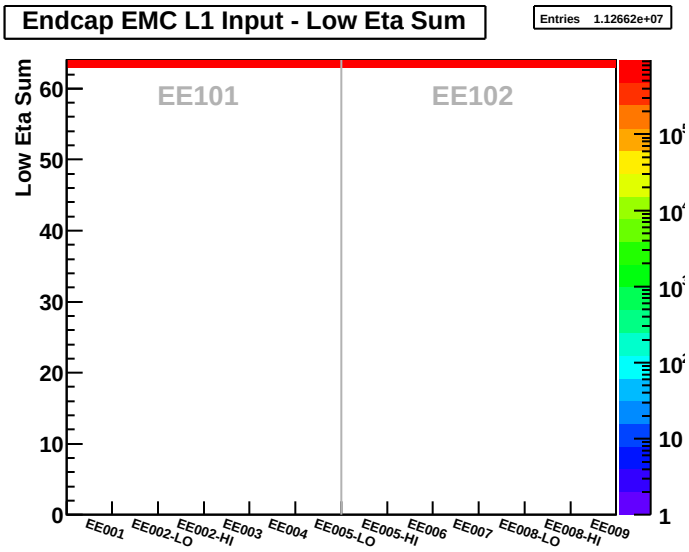

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

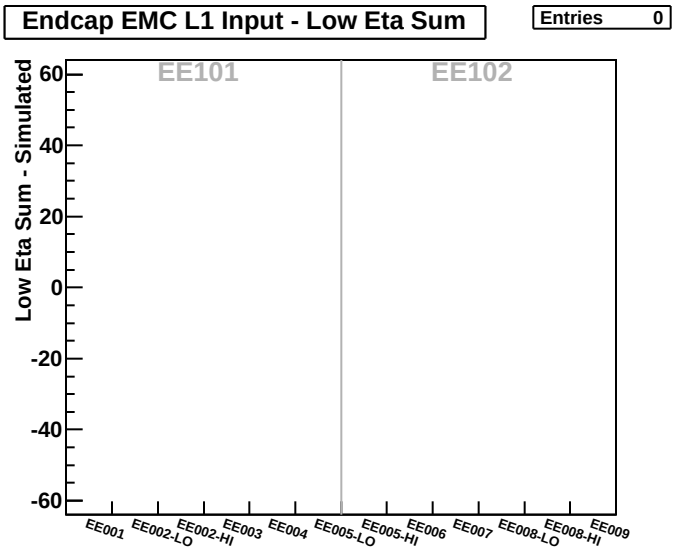

Barrel EMC L1 Input - High Eta Sum
Entries 3.37986e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

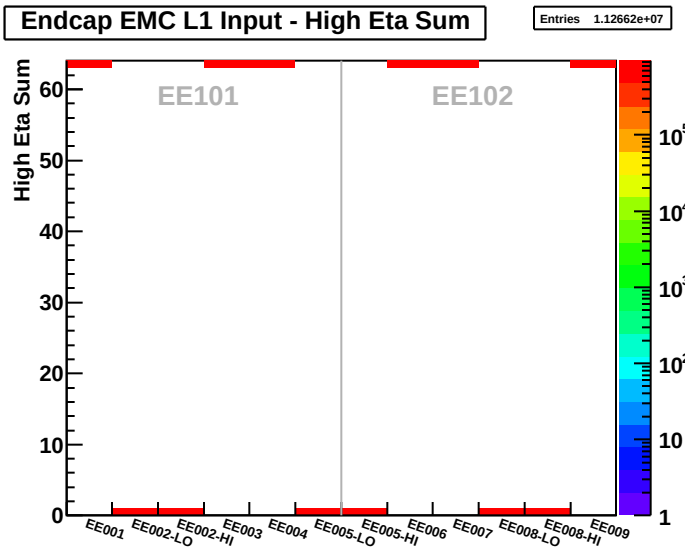

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

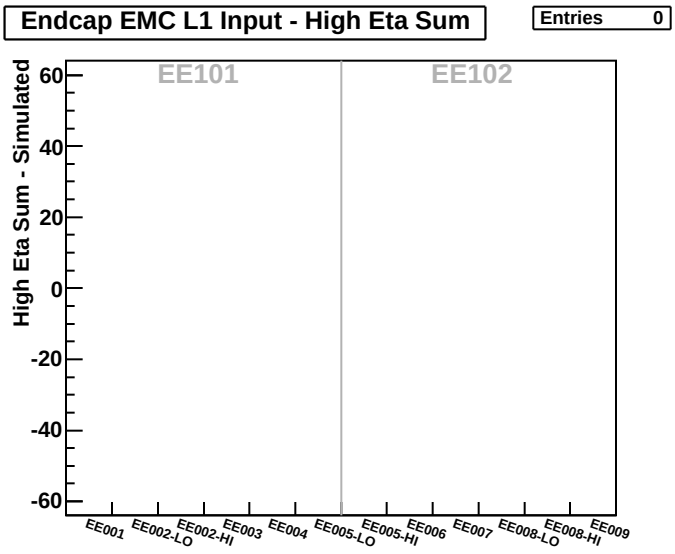

Barrel EMC L1 Input - High Tower Bits
Entries 3.37986e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

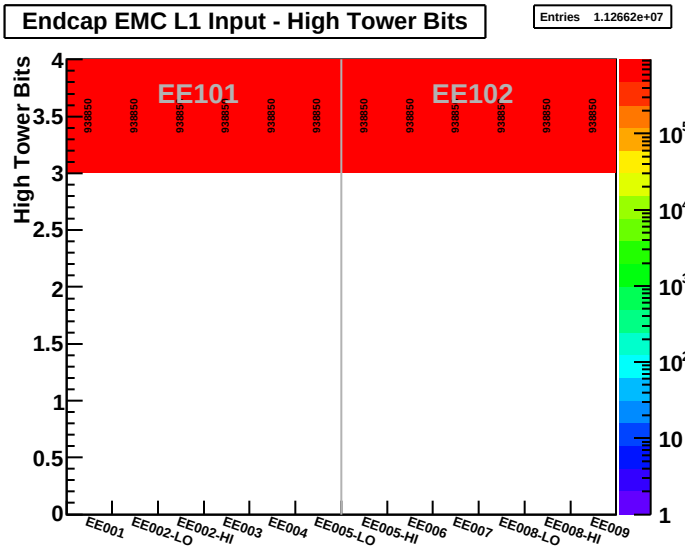

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

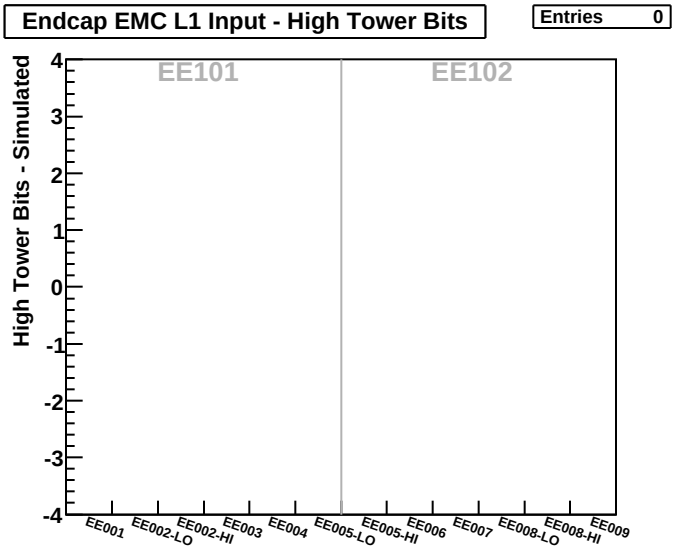

Endcap EMC L1 Input - Low Eta Sum
Entries 1.12662e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

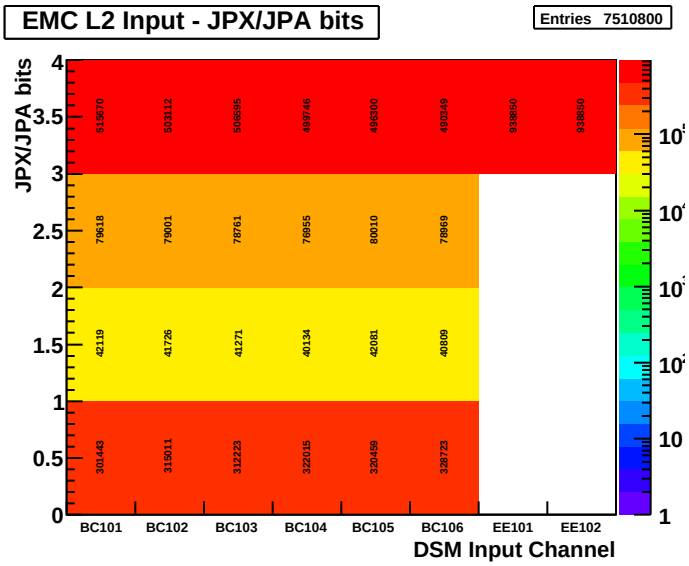

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

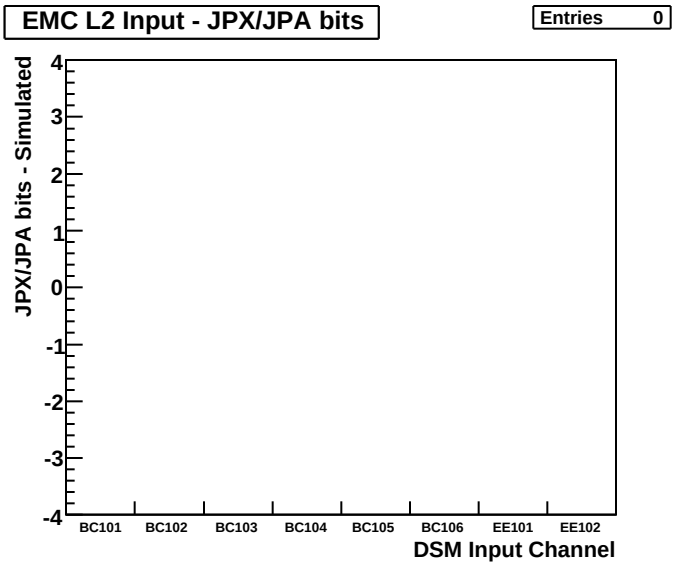

Endcap EMC L1 Input - High Eta Sum
Entries 1.12662e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

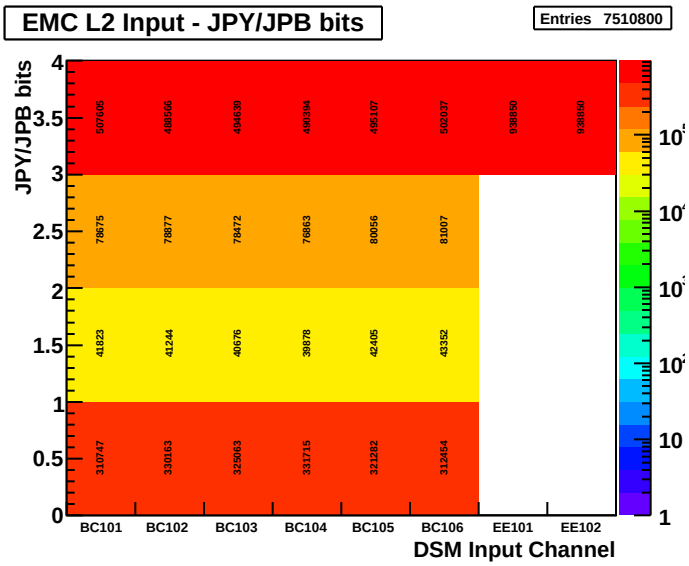

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

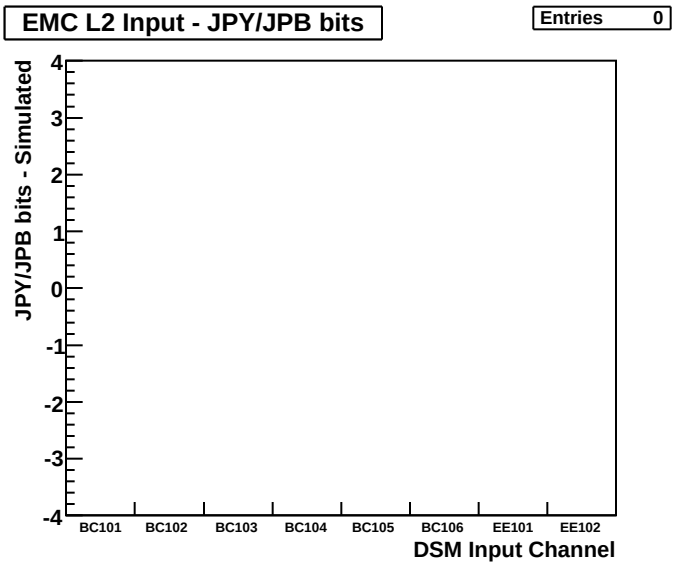

Endcap EMC L1 Input - High Tower Bits
Entries 1.12662e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

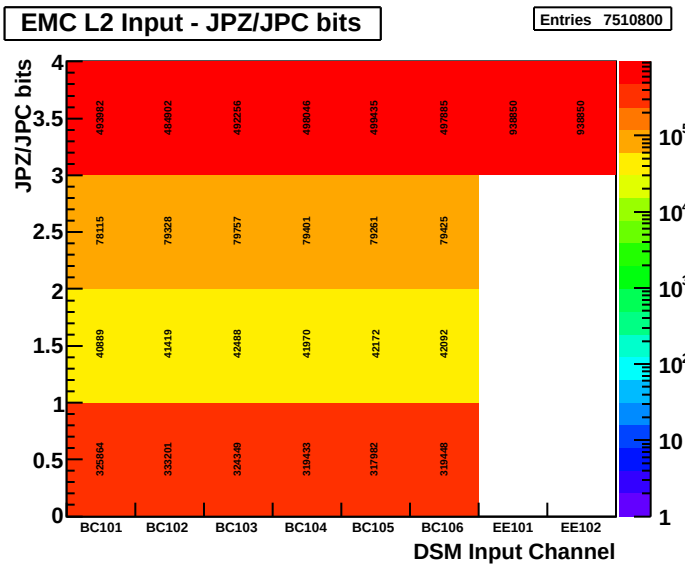

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

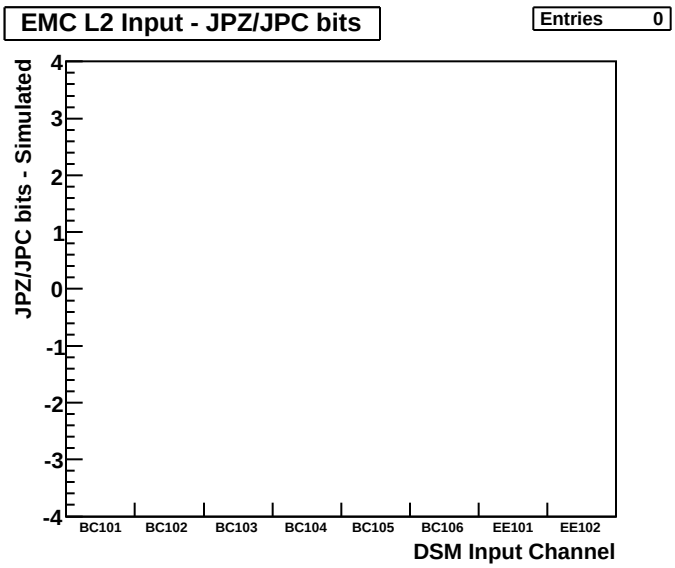

EMC L2 Input - JPX/JPA bits
Entries 7510800
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
515670 503112 506595 499746 496300 490349 938850 938850
79618 79001 78761 76955 80010 78969
42119 41726 41271 40134 42081 40809
301443 315011 312223 322015 320459 328723

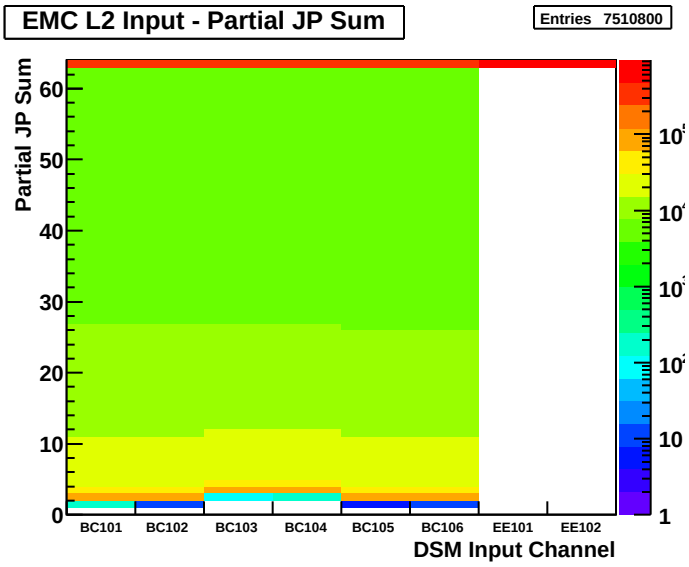

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

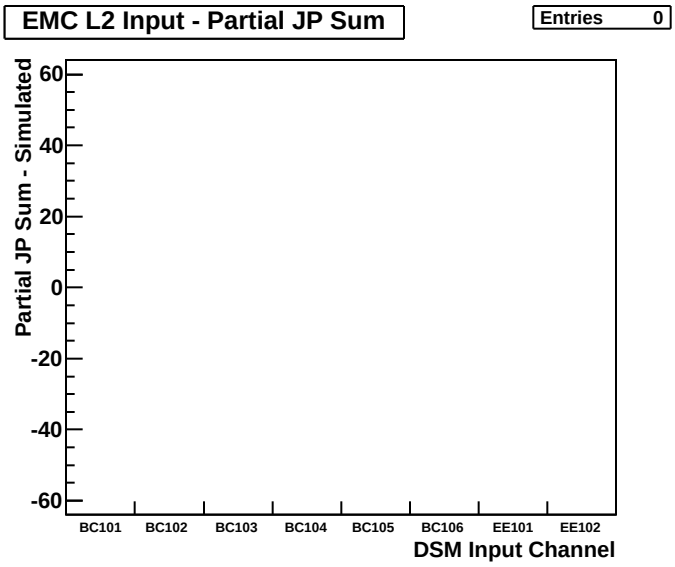

EMC L2 Input - JPY/JPB bits
Entries 7510800
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
507605 498566 494639 490394 495107 502037 938850 938850
78675 78877 78472 76863 80056 81007
41823 41244 40676 39878 42405 43352
310747 330163 325063 331715 321282 312454

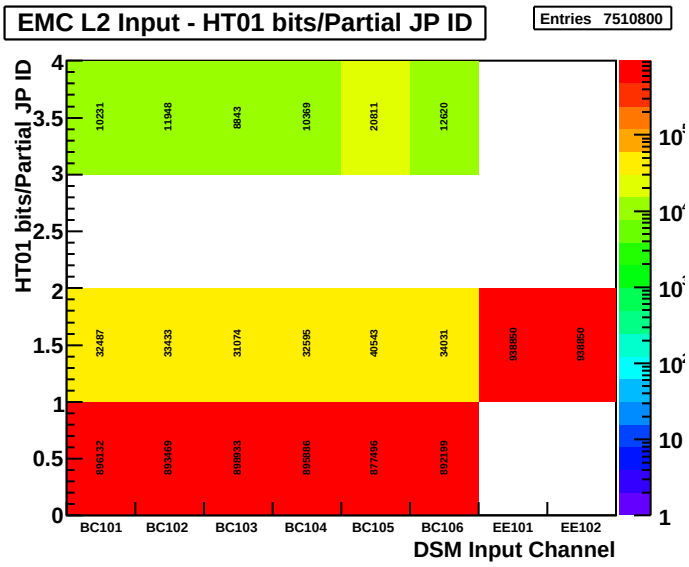

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

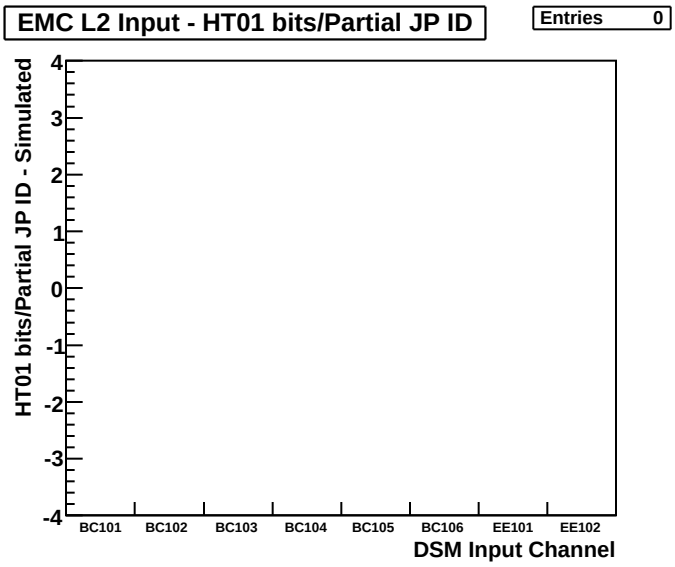

EMC L2 Input - JPZ/JPC bits
Entries 7510800
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
493962 484902 492256 490046 499435 497885 938850 938850
78115 79328 79757 79401 79261 79425
40889 41419 42488 41970 42172 42092
325864 333201 324340 319433 317982 319448

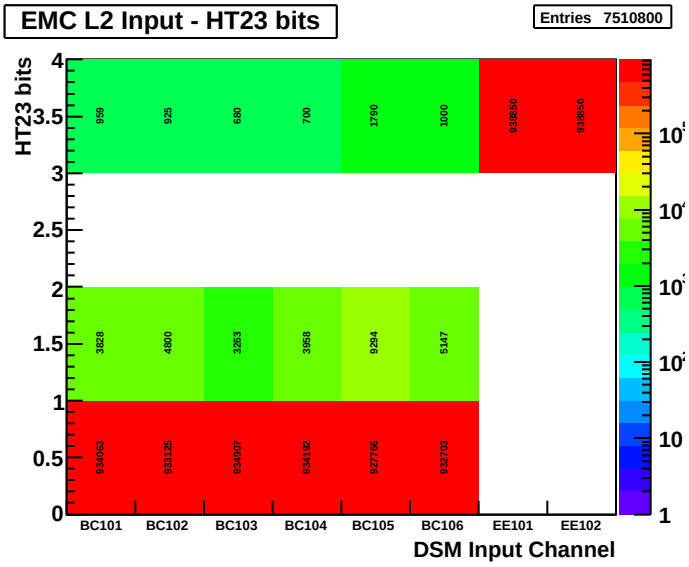

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

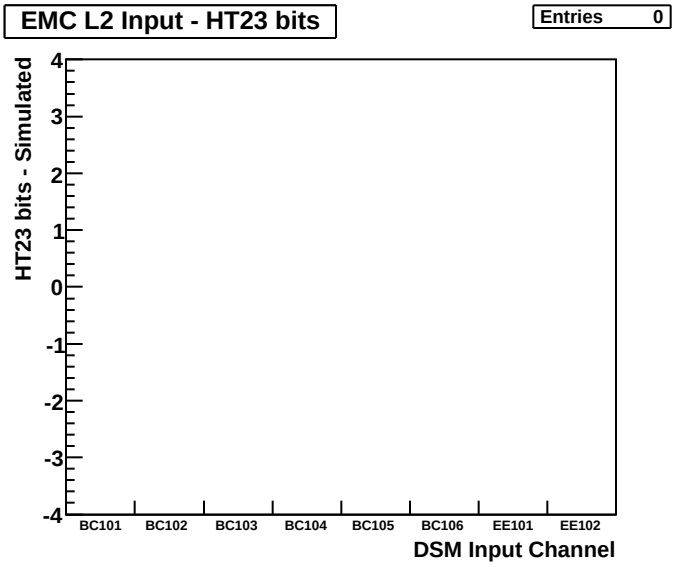

EMC L2 Input - Partial JP Sum
Entries 7510800
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

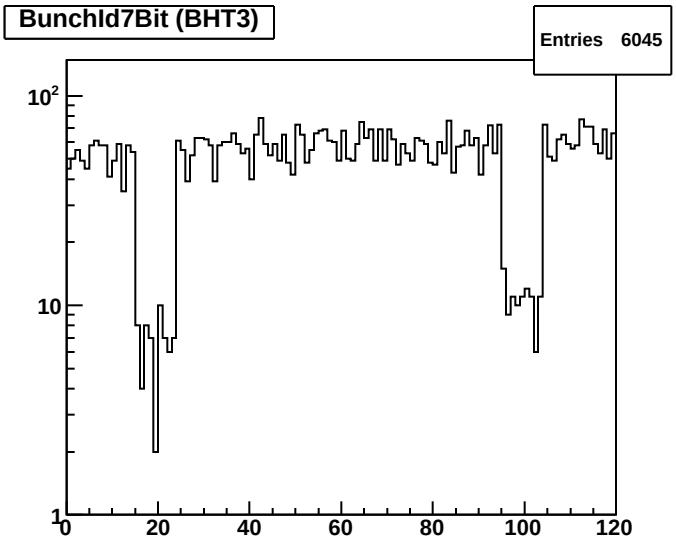

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

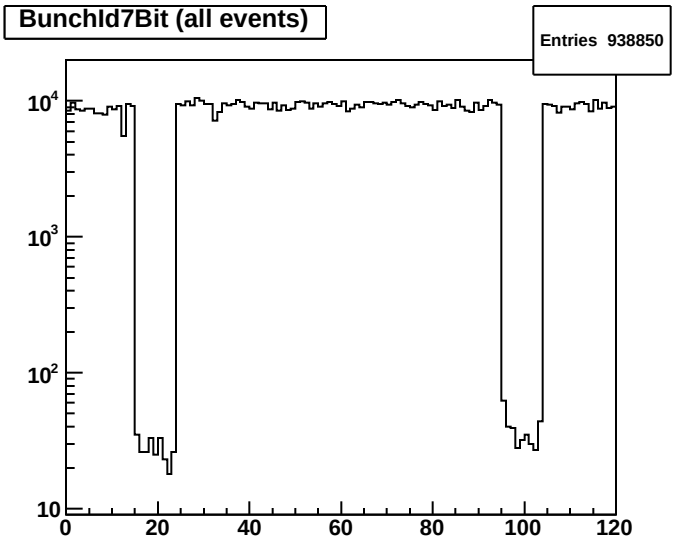

EMC L2 Input - HT01 bits/Partial JP ID
Entries 7510800
HT01 bits/Partial JP ID
10231 11948 8843 10369 20811 12620
32487 33433 31074 32595 40543 34031 938850 938850
896132 893469 898933 895886 877496 892199
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

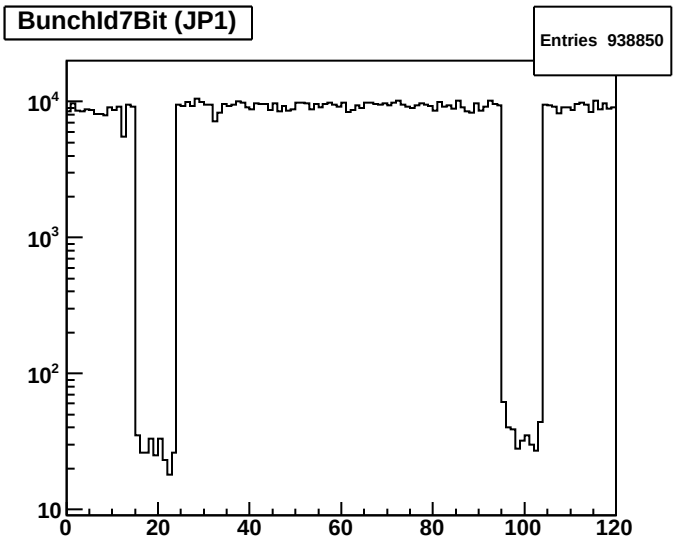

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

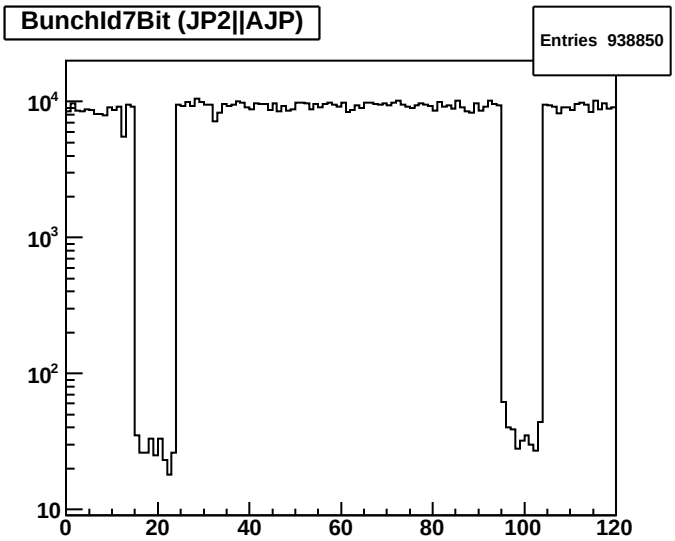

EMC L2 Input - HT23 bits
Entries 7510800
HT23 bits
959 925 680 700 1790 1000 938850 938850
3828 4800 3263 3958 9294 5147
934063 933125 934907 934192 927766 932703
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

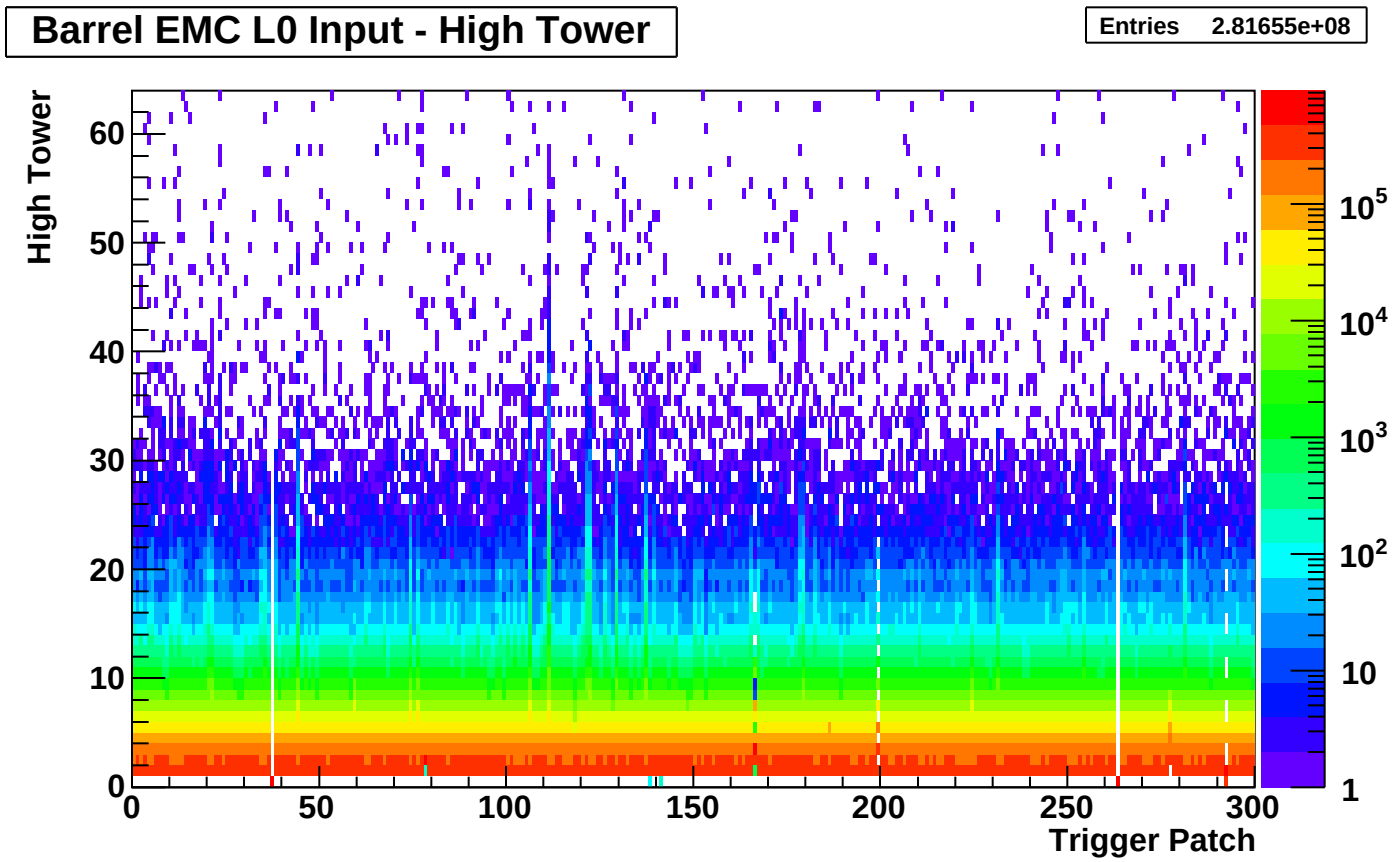

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

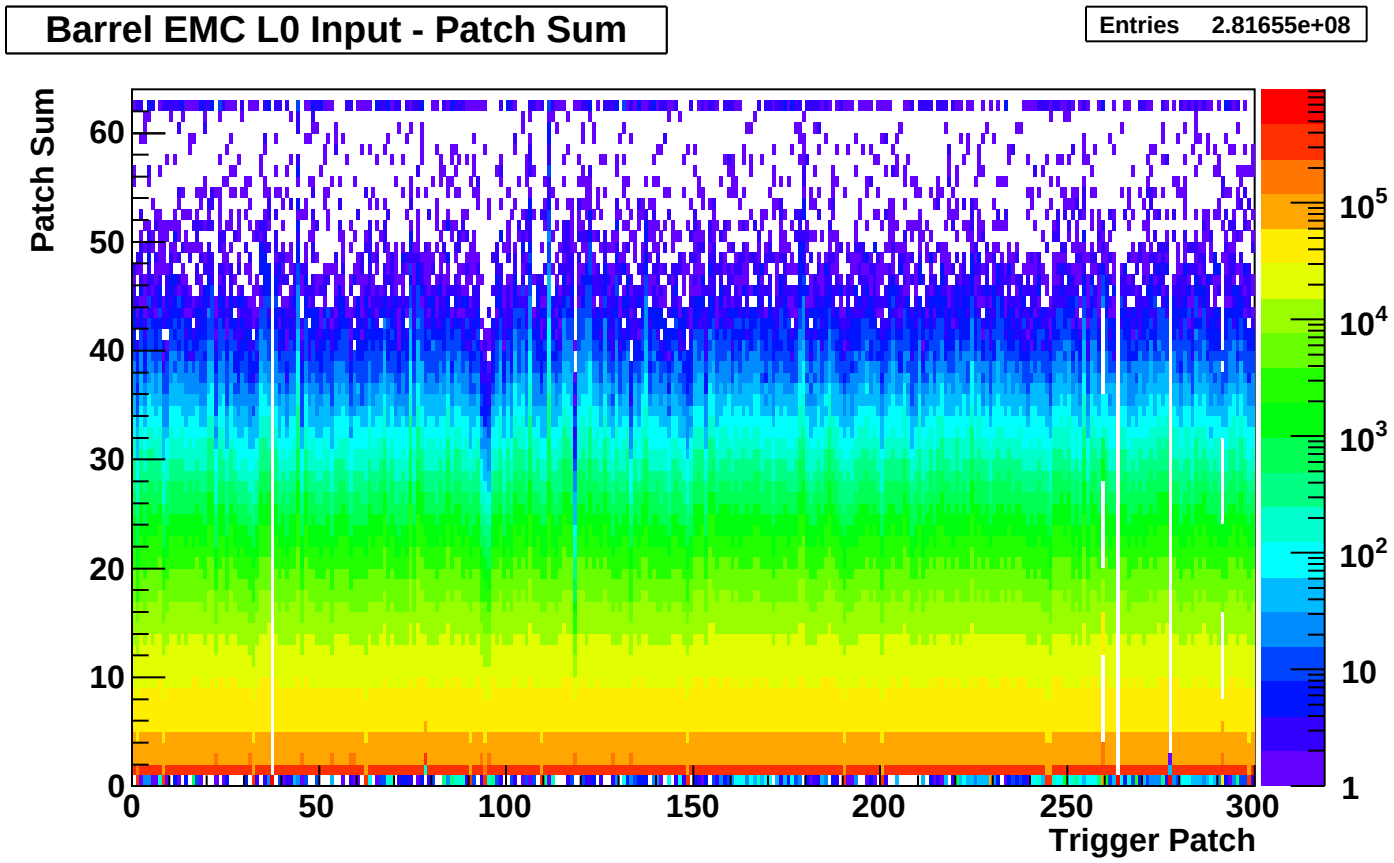

BunchId7Bit (BHT3)
Entries 6045
10^2
10
1
0
20
40
60
80
100
120

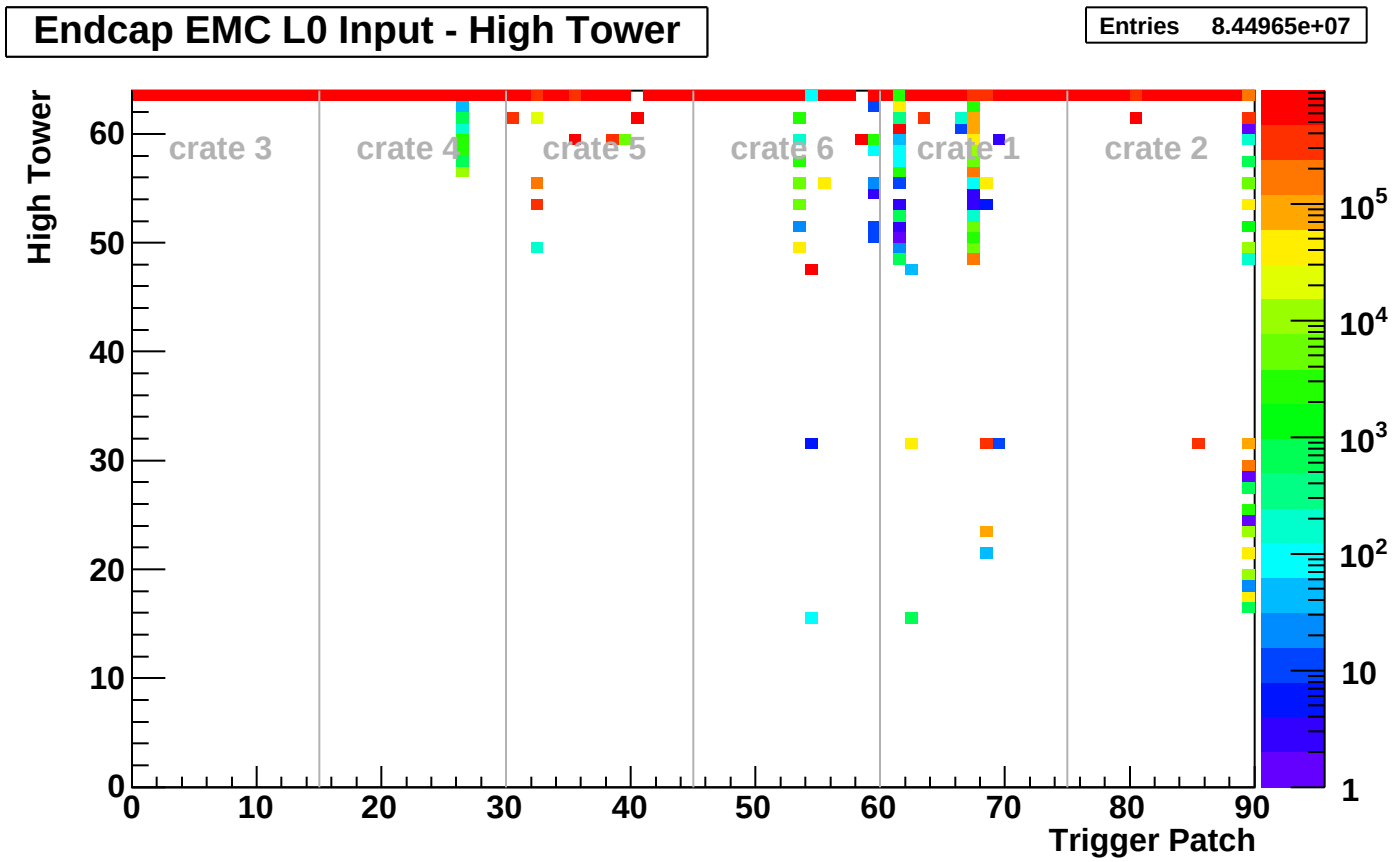

BunchId7Bit (all events)
Entries 938850
10^4
10^3
10^2
10
0
20
40
60
80
100
120

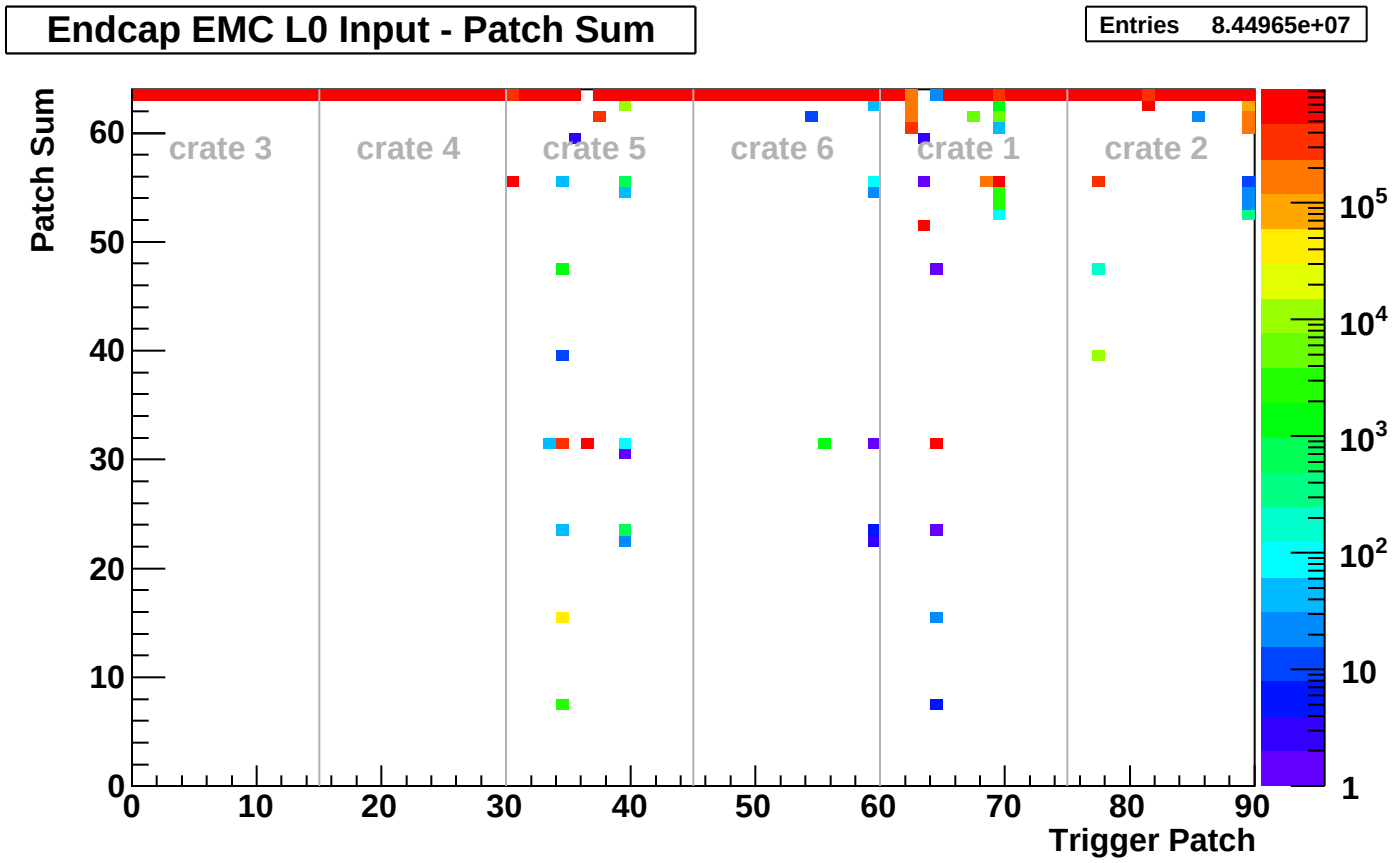

BunchId7Bit (JP1)
Entries 938850
10^4
10^3
10^2
10
0
20
40
60
80
100
120

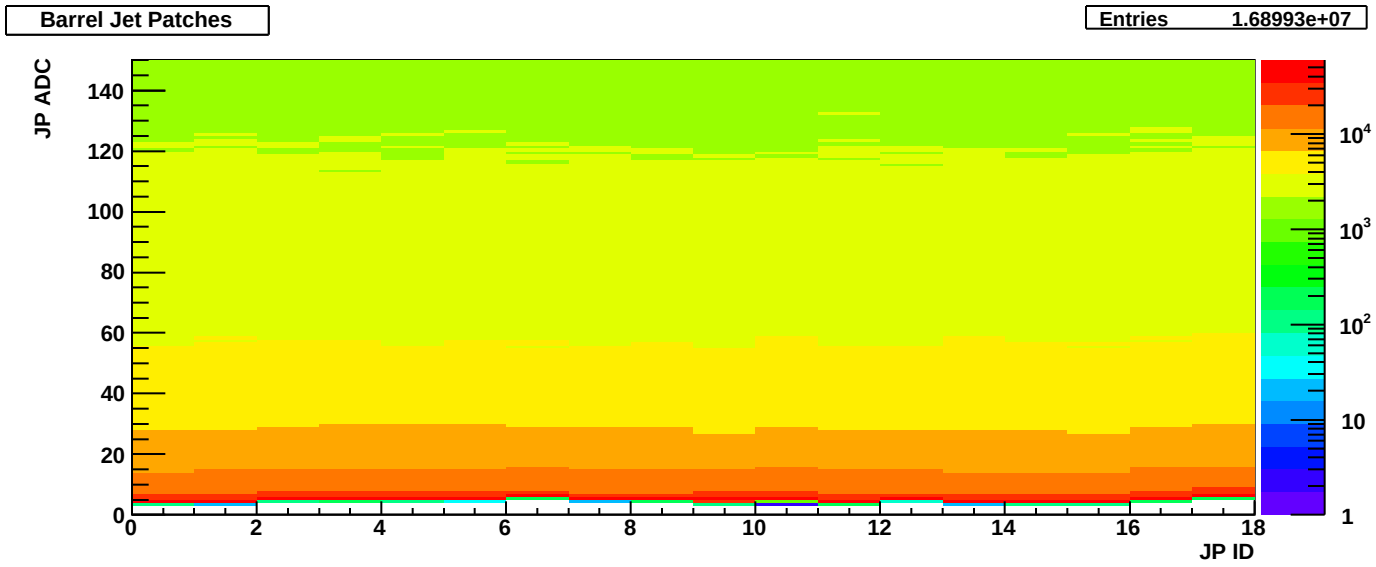

BunchId7Bit (JP2||AJP)
Entries 938850
10^4
10^3
10^2
10
0
20
40
60
80
100
120

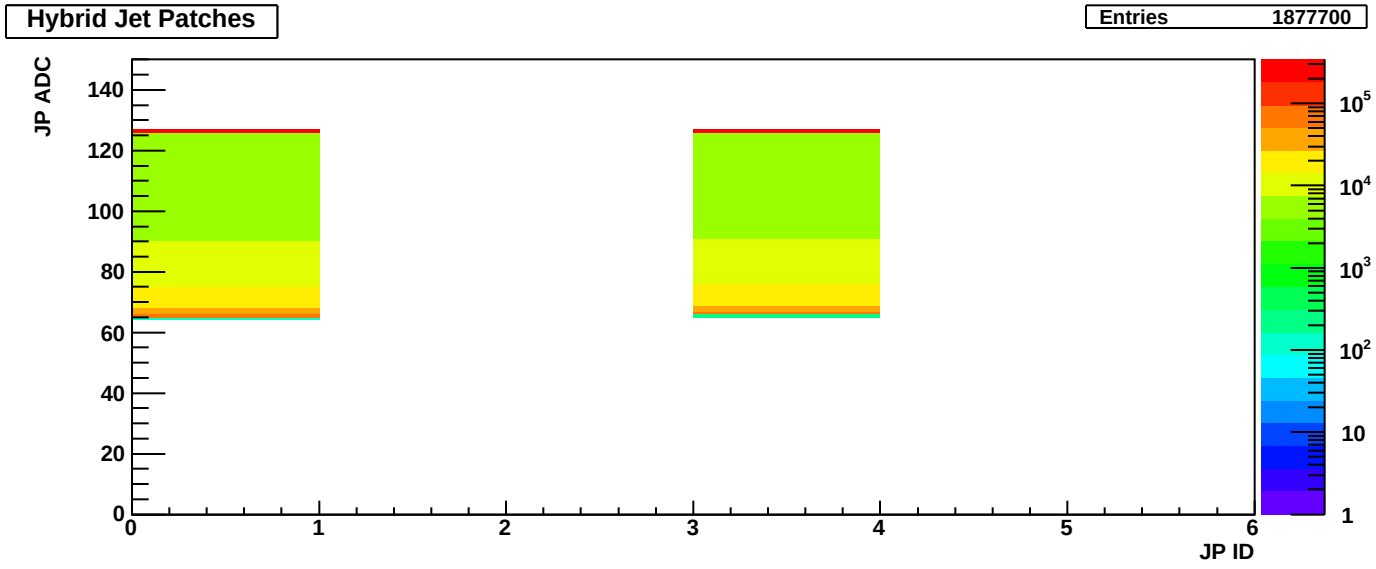

Barrel EMC L0 Input - High Tower
Entries 2.81655e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

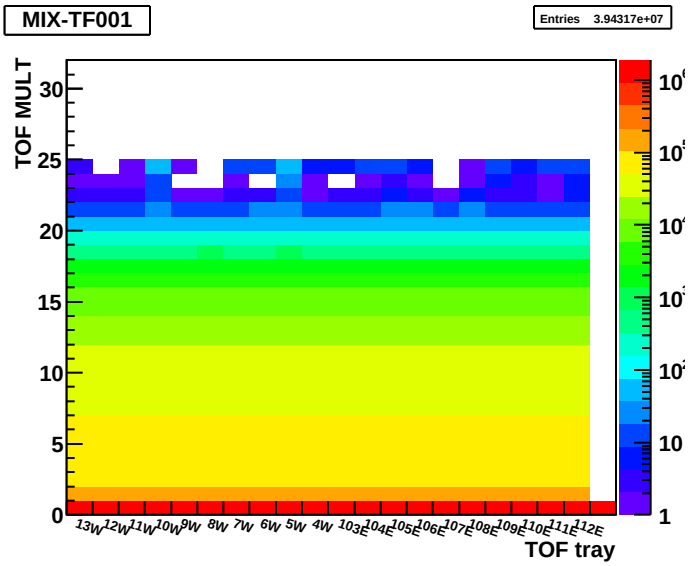

Barrel EMC L0 Input - Patch Sum
Entries 2.81655e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

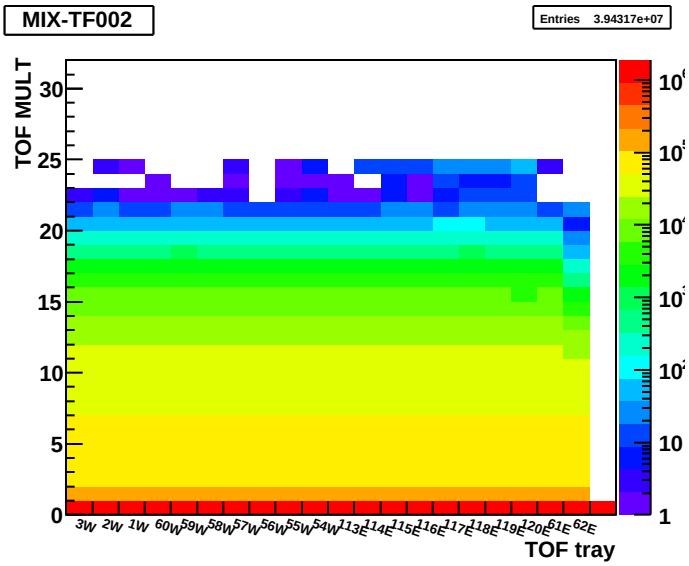

Endcap EMC L0 Input - High Tower
Entries 8.44965e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

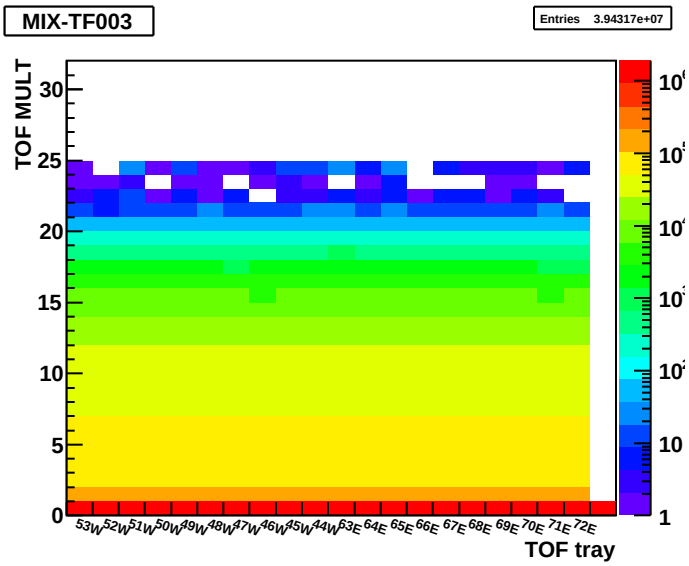

Endcap EMC L0 Input - Patch Sum
Entries 8.44965e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

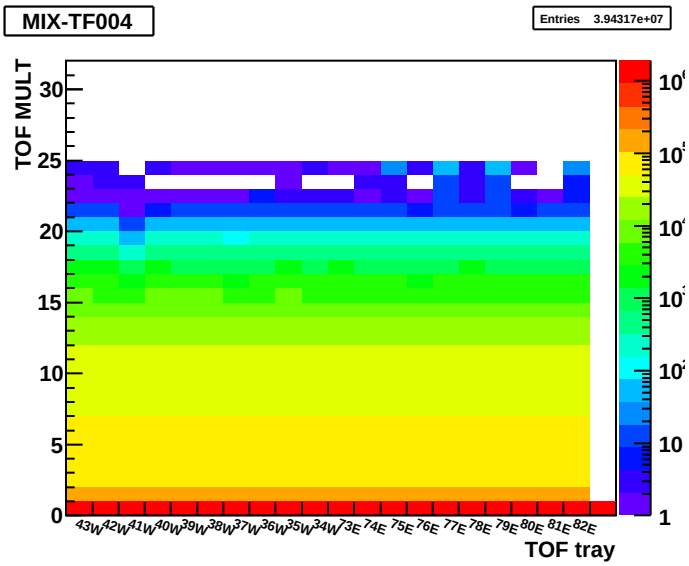

Barrel Jet Patches
Entries 1.68993e+07
JP ADC
JP ID

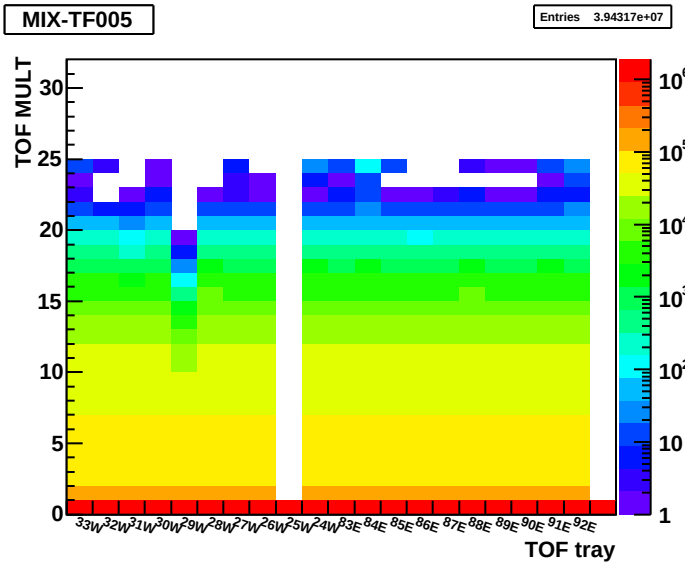

Hybrid Jet Patches
Entries 1877700
JP ADC
JP ID

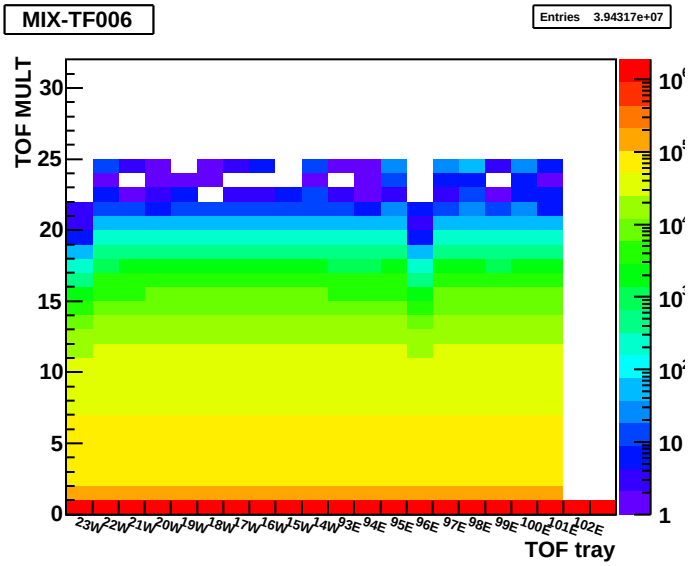

MIX-TF001
Entries 3.94317e+07
TOF MULT
TOF tray

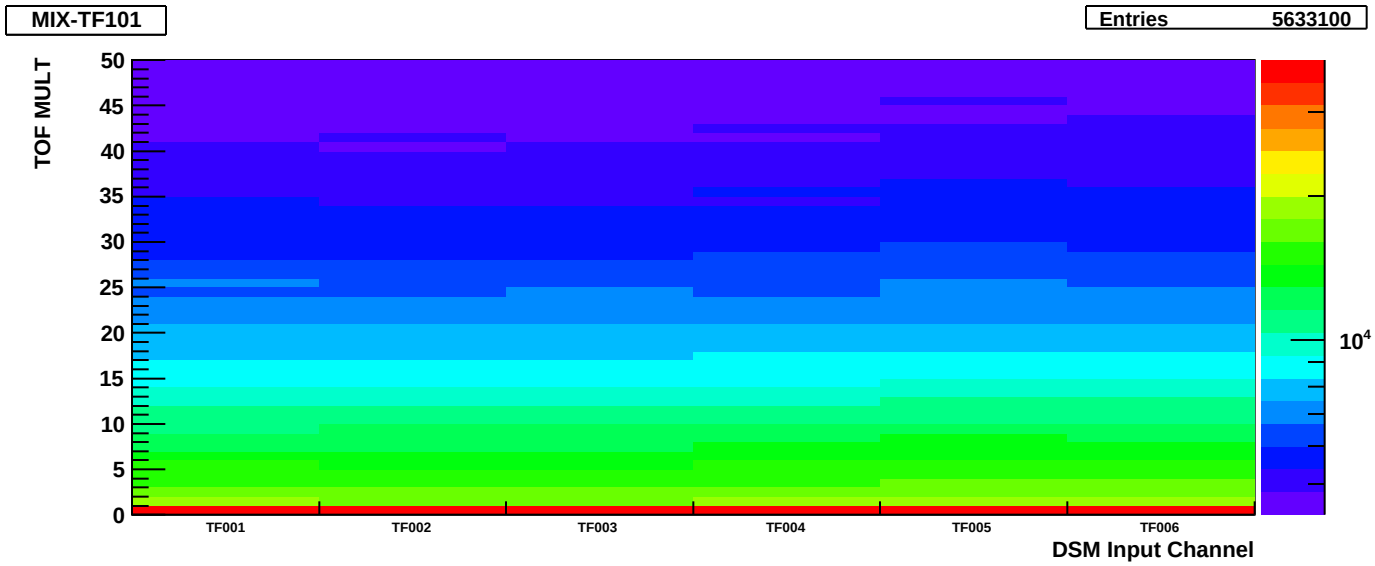

MIX-TF002
Entries 3.94317e+07
TOF MULT
TOF tray

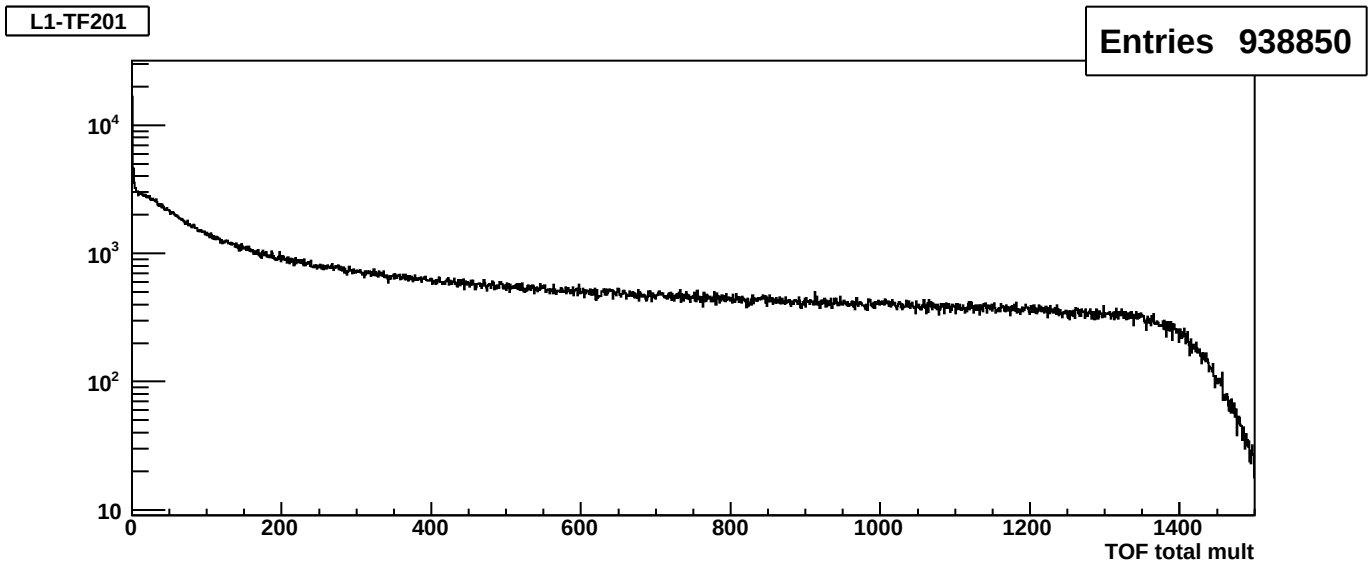

MIX-TF003
Entries 3.94317e+07
TOF MULT
TOF tray

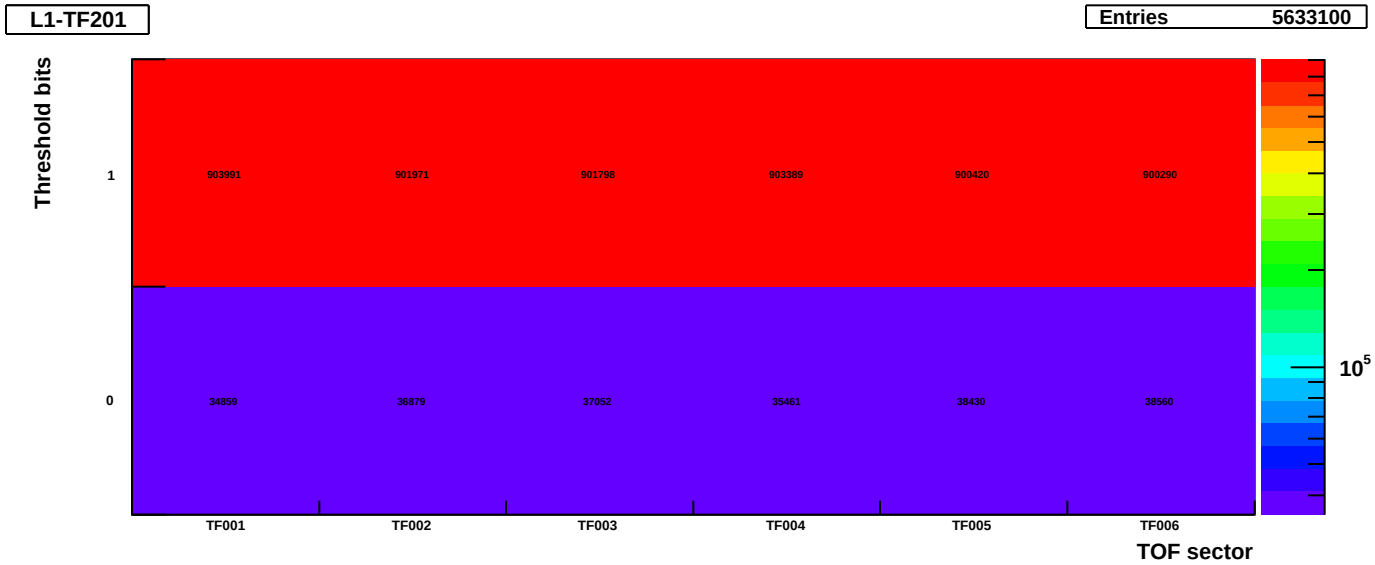

MIX-TF004
Entries 3.94317e+07
TOF MULT
TOF tray

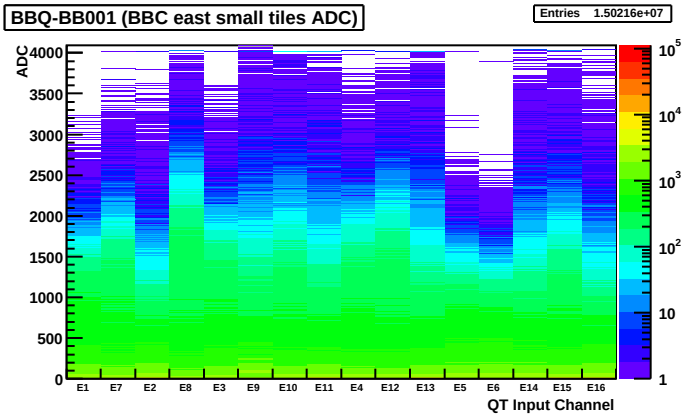

MIX-TF005
Entries 3.94317e+07
TOF MULT
TOF tray

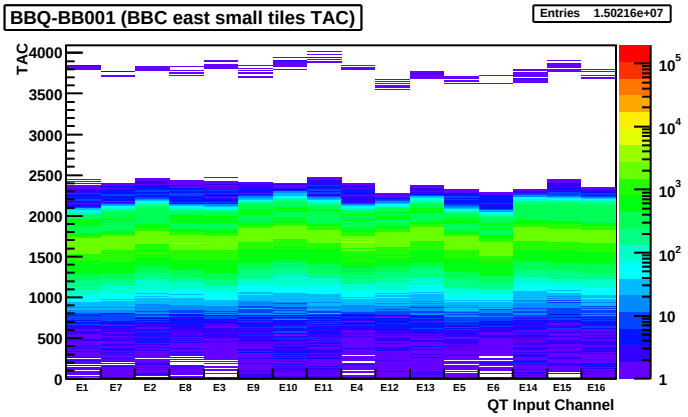

MIX-TF006
Entries 3.94317e+07
TOF MULT
TOF tray

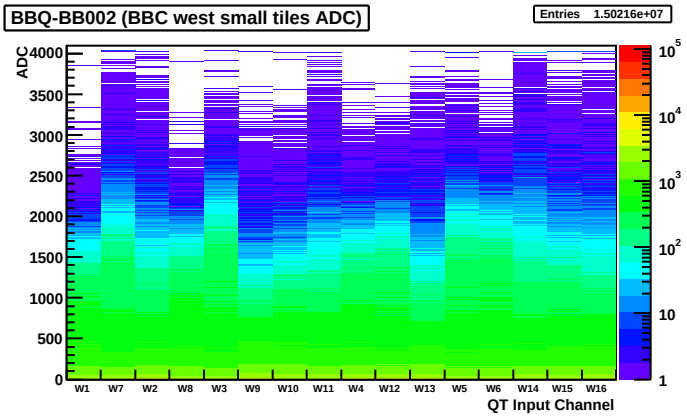

MIX-TF101
Entries 5633100
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

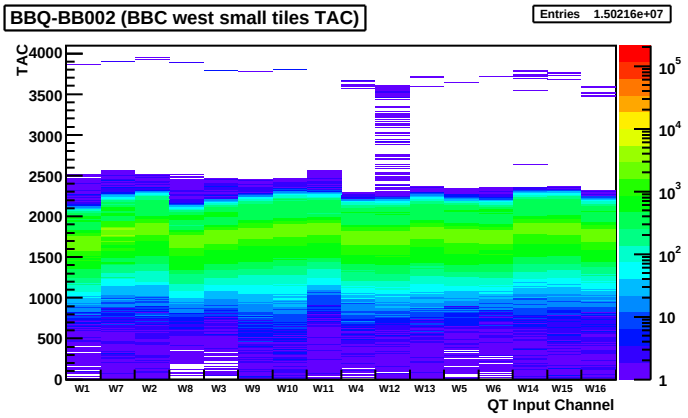

L1-TF201
Entries 938850
10^4
10^3
10^2
10
0
200
400
600
800
1000
1200
1400
TOF total mult

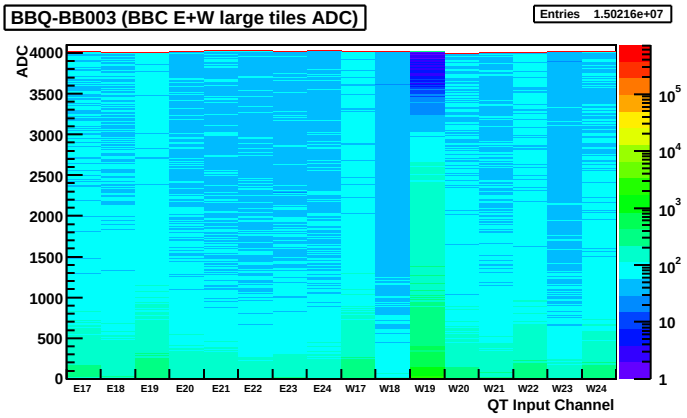

L1-TF201
Entries 5633100
Threshold bits
1
0
903991
901971
901798
903389
900420
900290
34859
36879
37052
35461
38430
38560
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

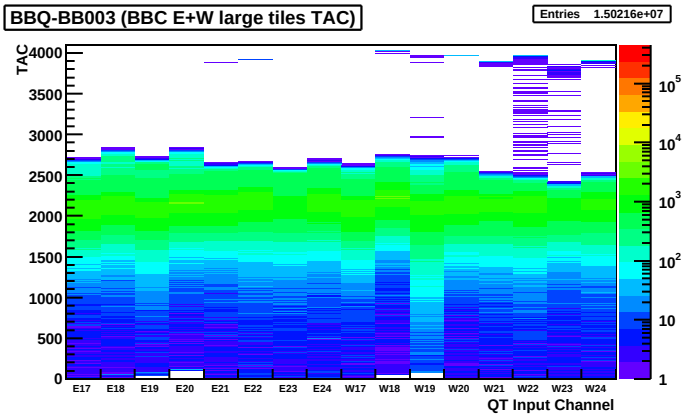

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.50216e+07
ADC
QT Input Channel

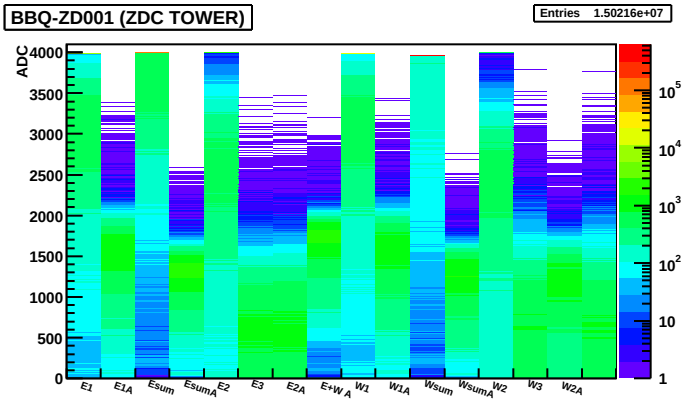

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.50216e+07
TAC
QT Input Channel

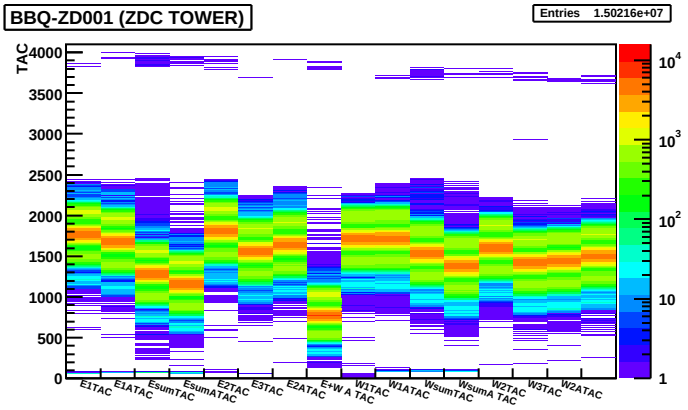

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.50216e+07
ADC
QT Input Channel

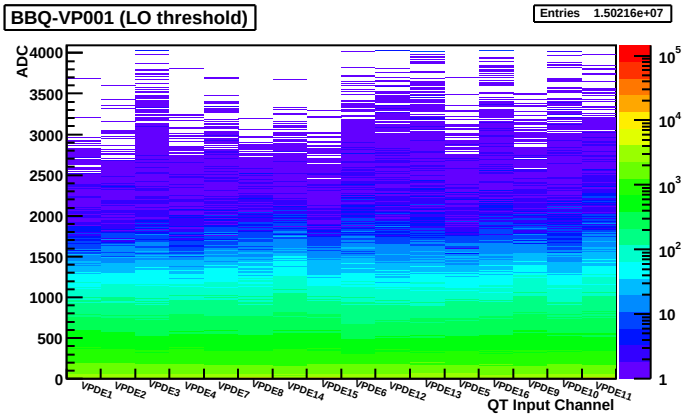

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.50216e+07
TAC
QT Input Channel

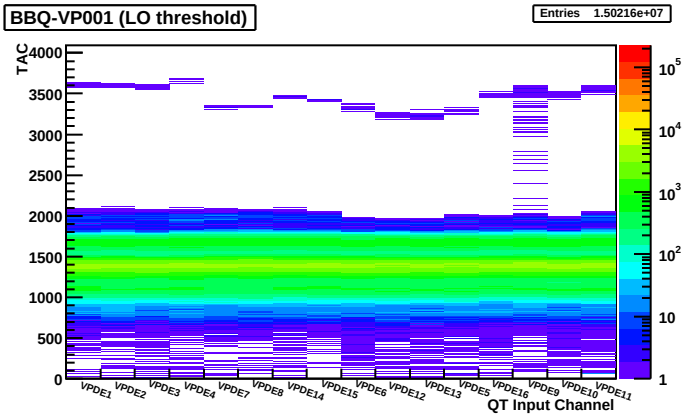

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.50216e+07
ADC
QT Input Channel

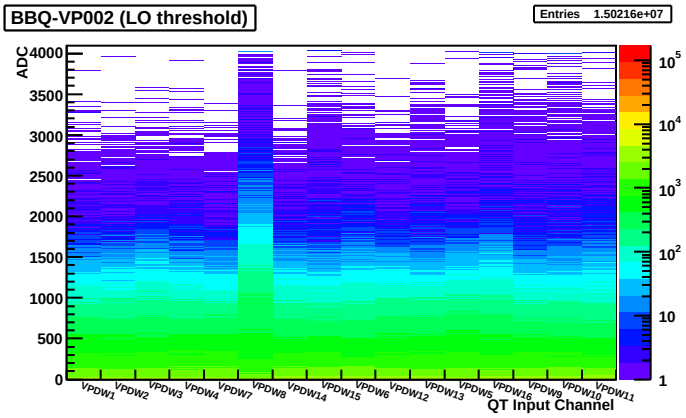

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.50216e+07
TAC
QT Input Channel

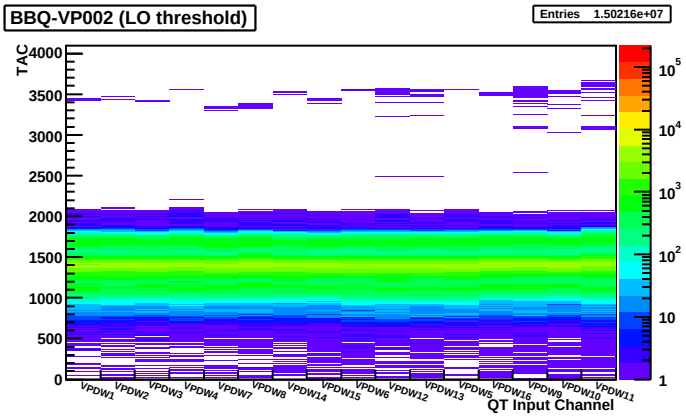

BBQ-ZD001 (ZDC TOWER)
Entries 1.50216e+07
ADC

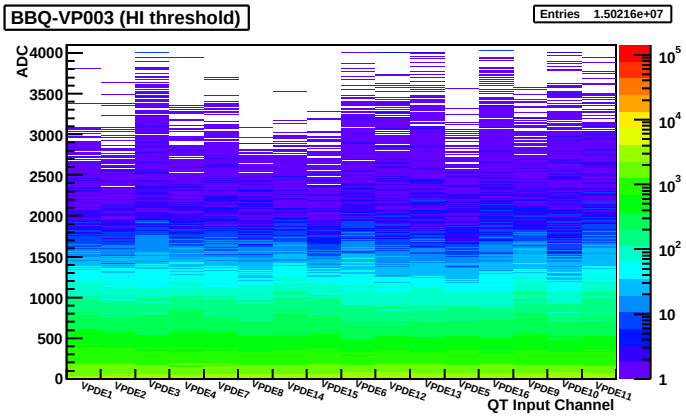

BBQ-ZD001 (ZDC TOWER)
Entries 1.50216e+07
TAC

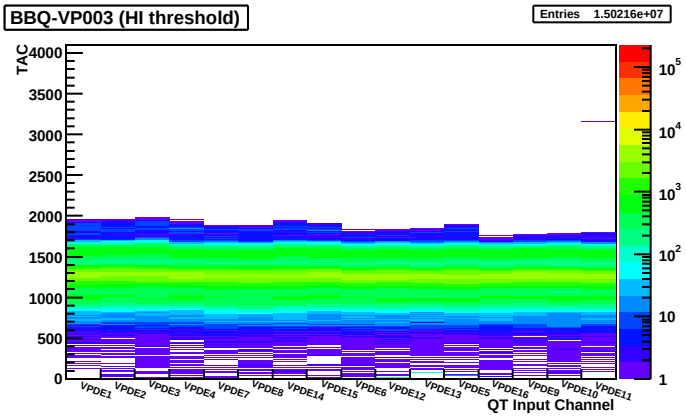

BBQ-VP001 (LO threshold)
Entries 1.50216e+07
ADC
QT Input Channel

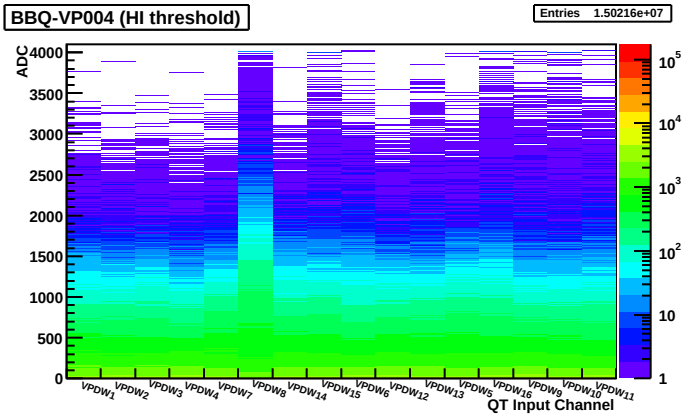

BBQ-VP001 (LO threshold)
Entries 1.50216e+07
TAC
QT Input Channel

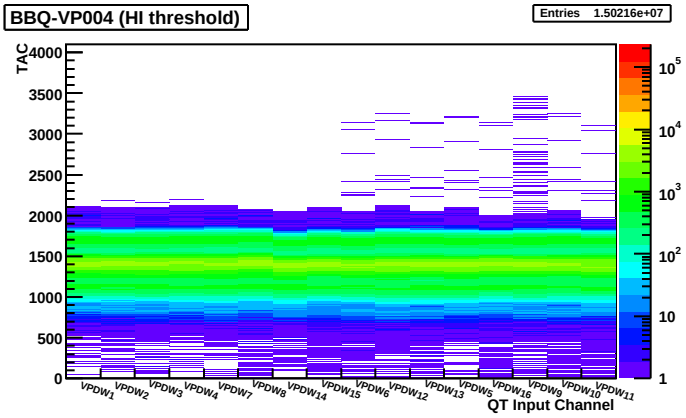

BBQ-VP002 (LO threshold)
Entries 1.50216e+07
ADC
QT Input Channel

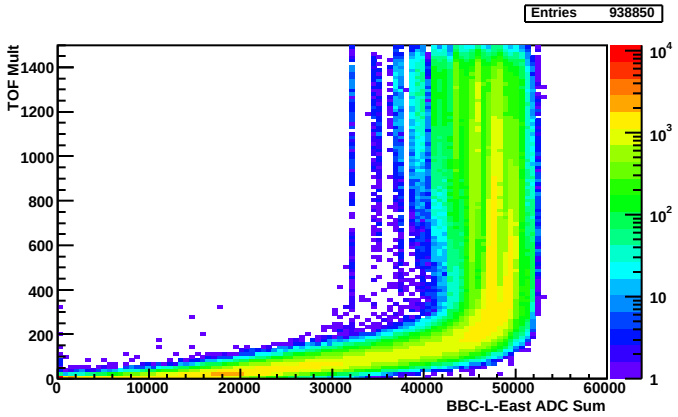

BBQ-VP002 (LO threshold)
Entries 1.50216e+07
TAC
QT Input Channel

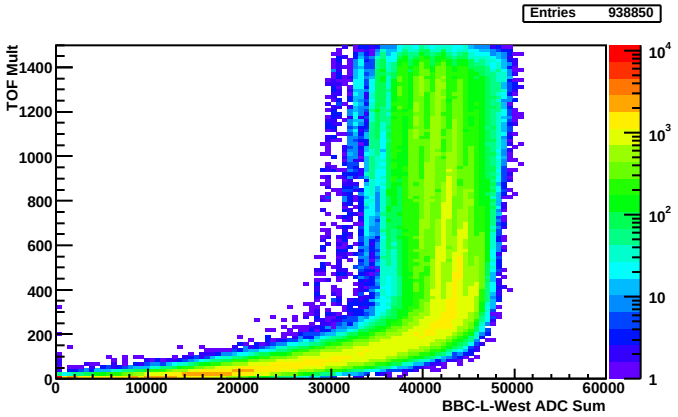

BBQ-VP003 (HI threshold)
Entries 1.50216e+07
ADC
QT Input Channel

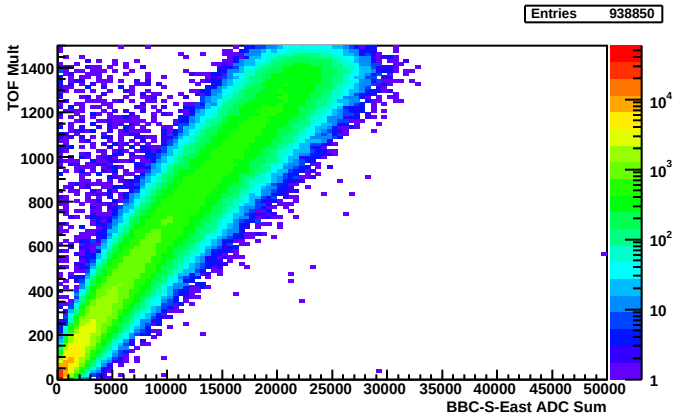

BBQ-VP003 (HI threshold)
Entries 1.50216e+07
TAC
QT Input Channel

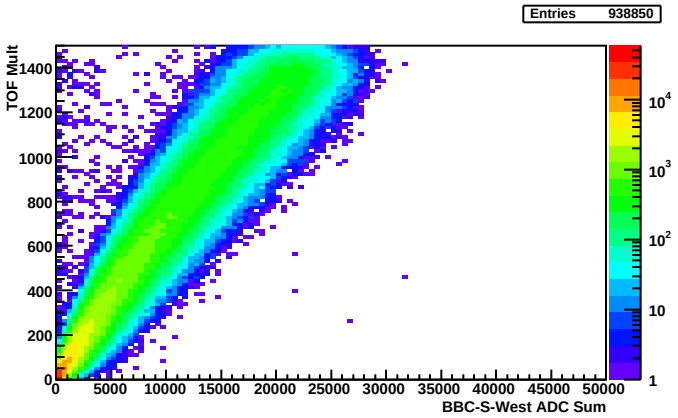

BBQ-VP004 (HI threshold)
Entries 1.50216e+07
ADC
QT Input Channel

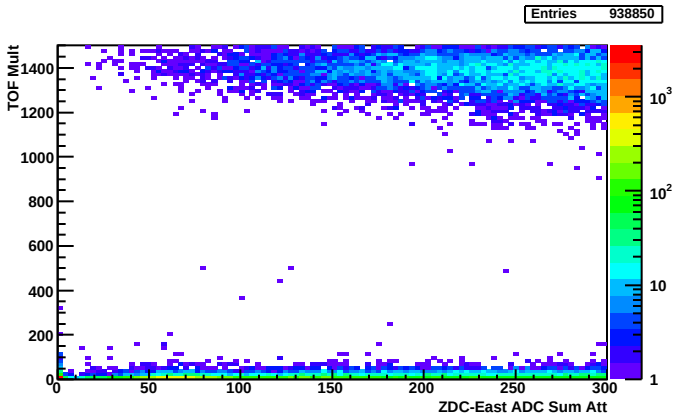

BBQ-VP004 (HI threshold)
Entries 1.50216e+07
TAC
QT Input Channel

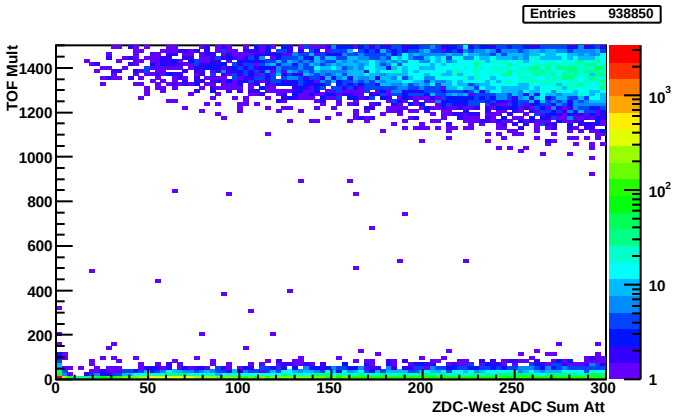

Entries 938850
TOF Mult
BBC-L-East ADC Sum

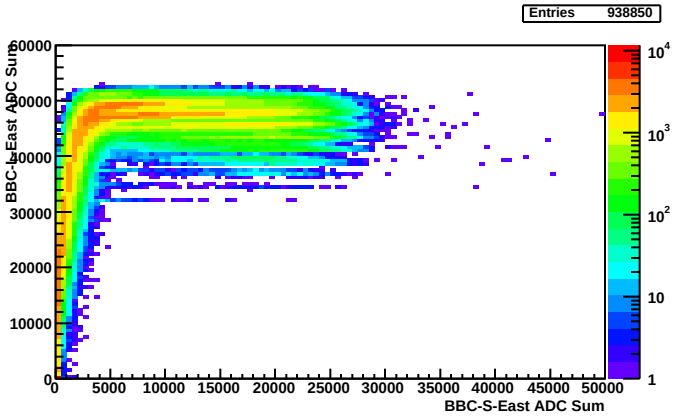

Entries 938850
TOF Mult
BBC-L-West ADC Sum

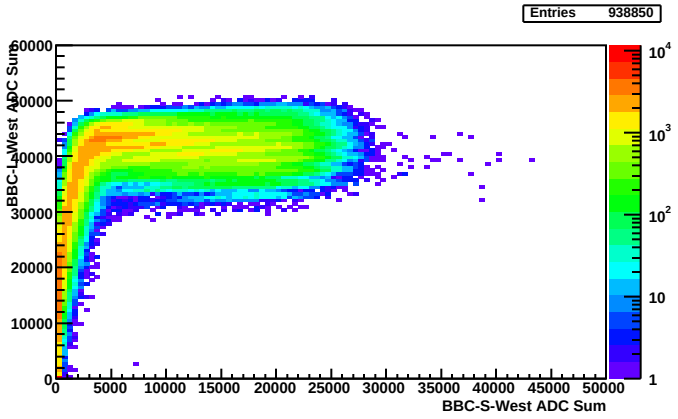

Entries 938850
TOF Mult
BBC-S-East ADC Sum

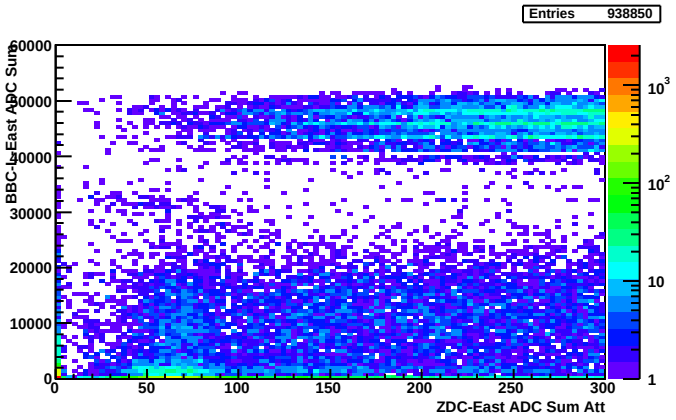

Entries 938850
TOF Mult
BBC-S-West ADC Sum

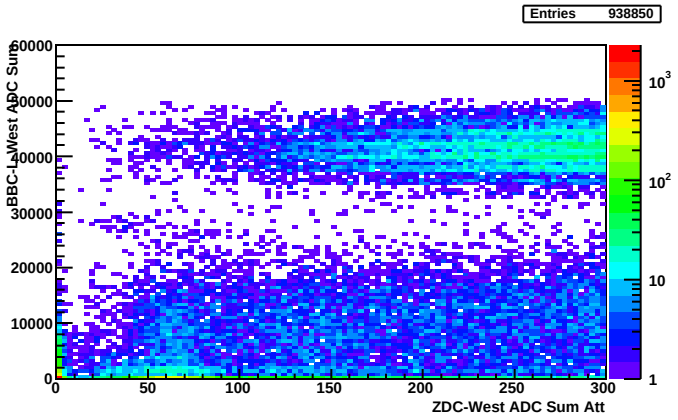

Entries 938850
TOF Mult
ZDC-East ADC Sum Att

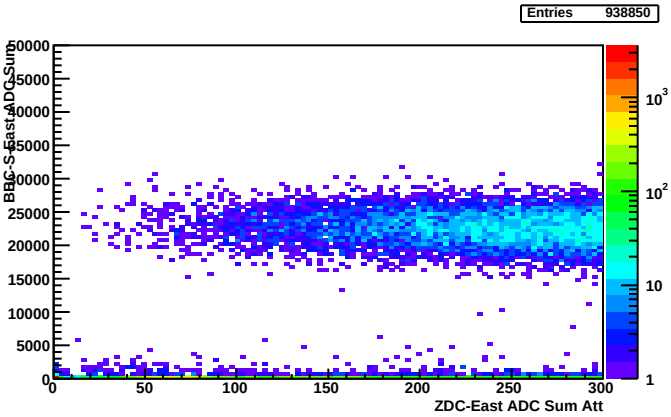

Entries 938850
TOF Mult
ZDC-West ADC Sum Att

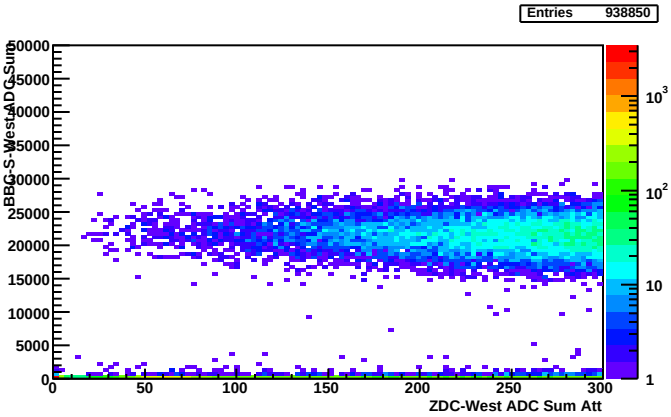

Entries 938850
BBC-L-East ADC Sum
BBC-S-East ADC Sum

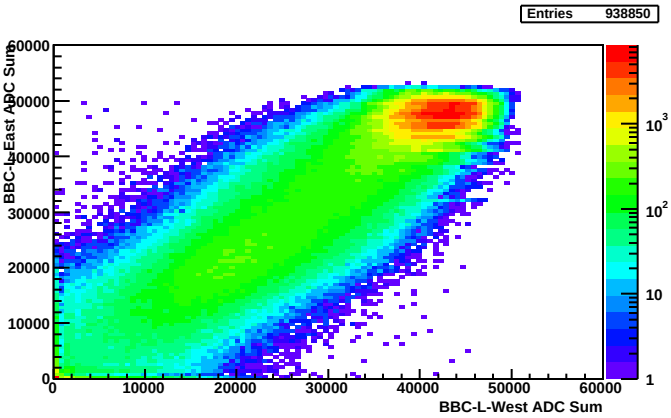

Entries 938850
BBC-L-West ADC Sum
BBC-S-West ADC Sum

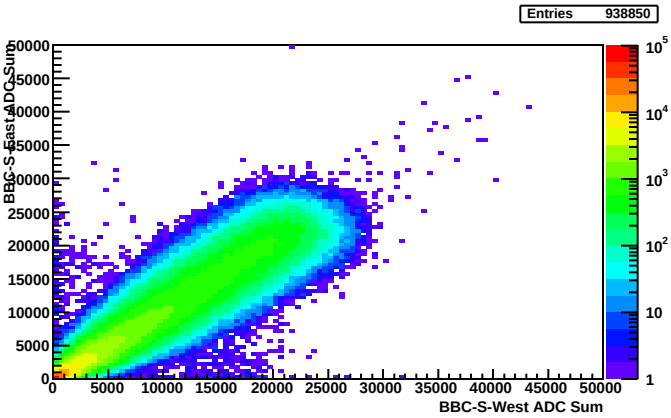

Entries 938850
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

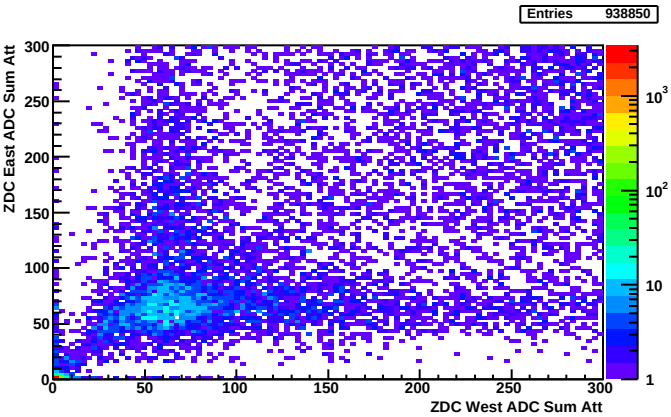

Entries 938850
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

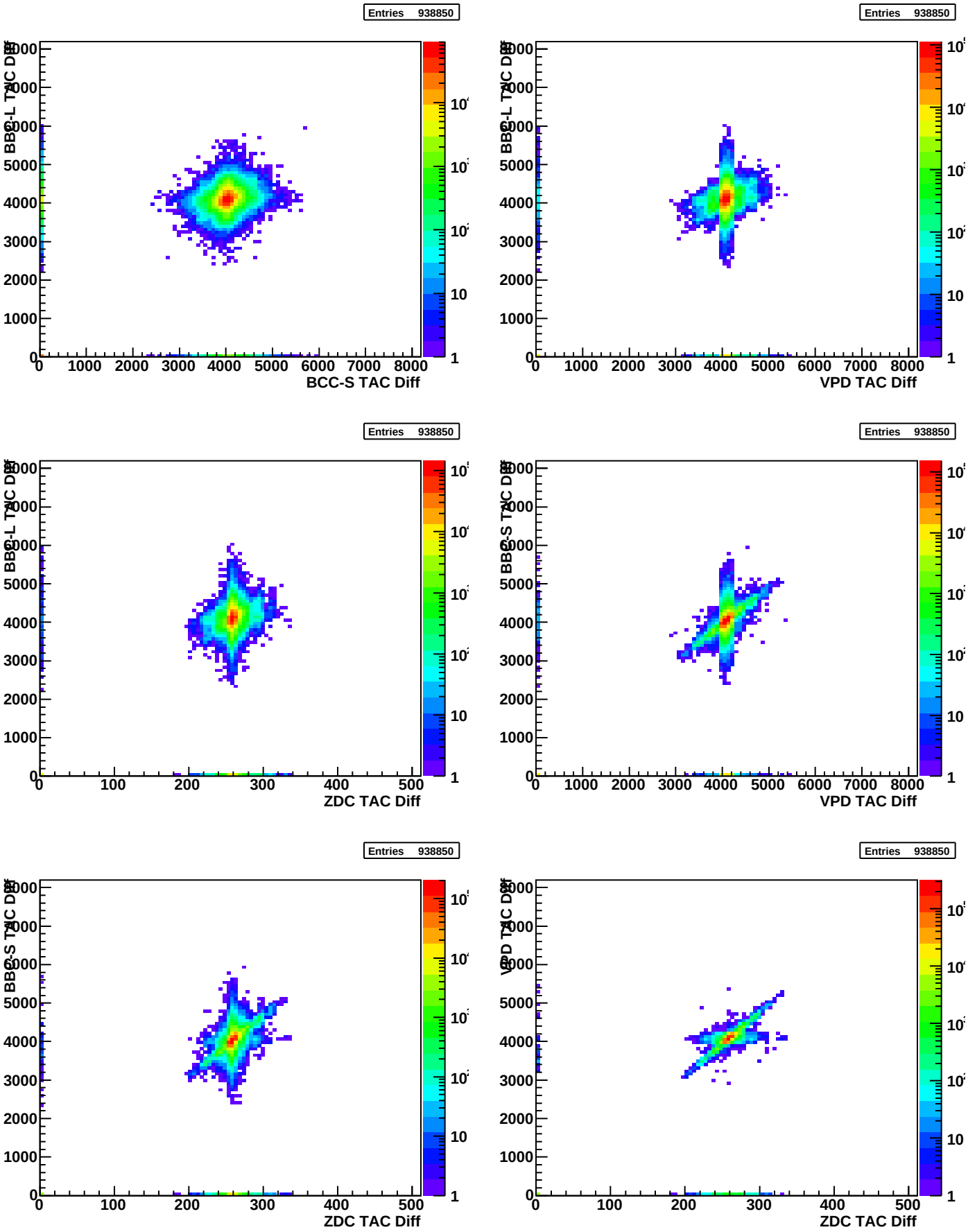

Entries 938850
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

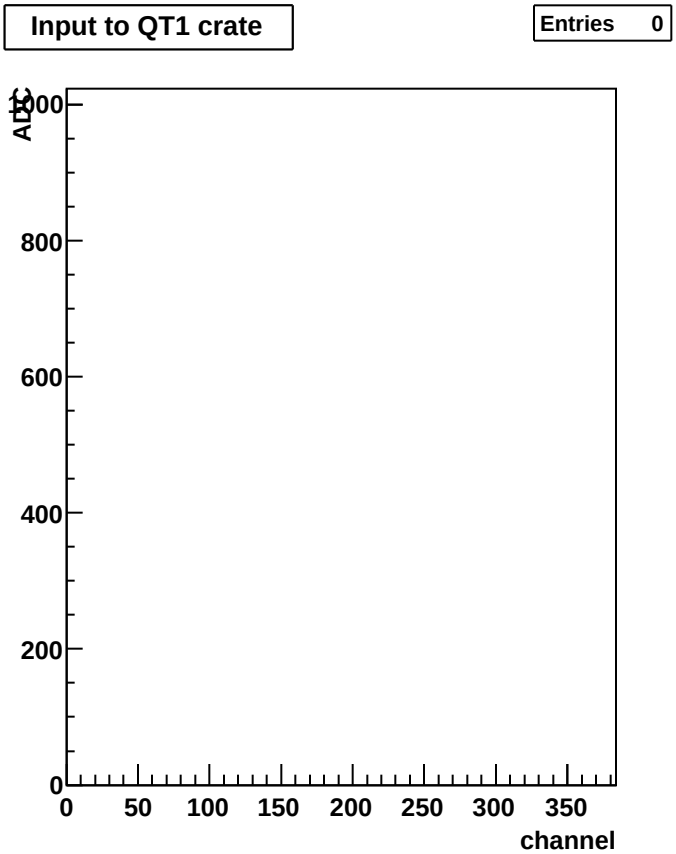

Entries 938850
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

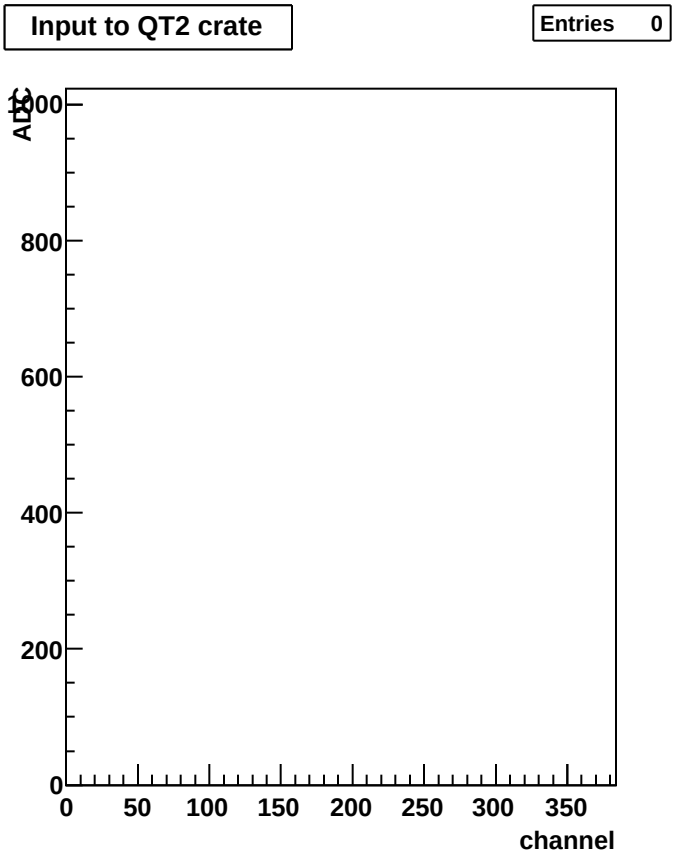

Entries 938850
BBC-L-East ADC Sum
BBC-L-West ADC Sum

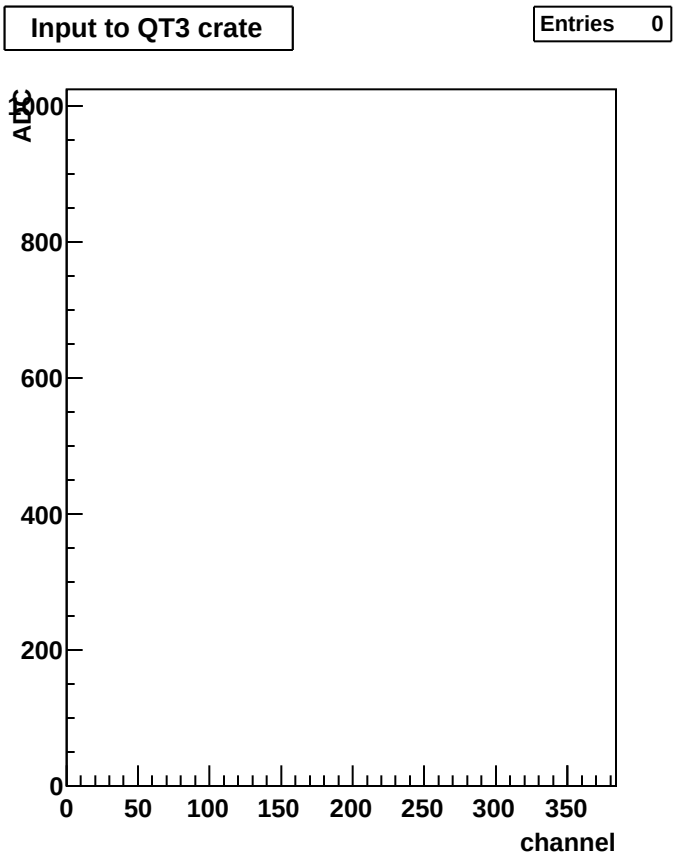

Entries 938850
BBC-S-East ADC Sum
BBC-S-West ADC Sum

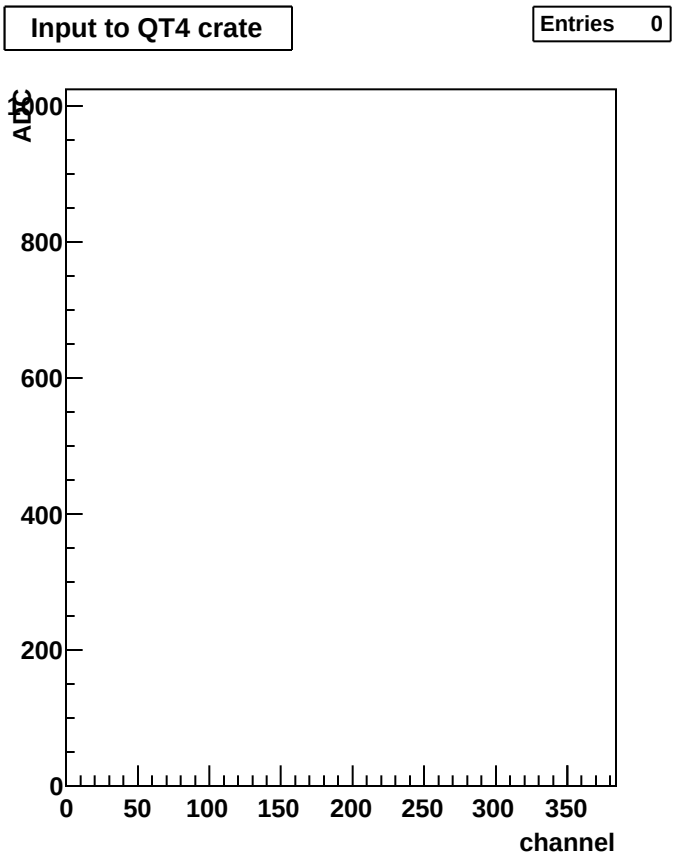

Entries 938850
ZDC East ADC Sum Att
ZDC West ADC Sum Att

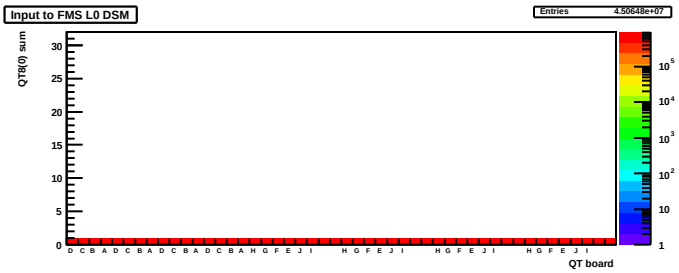

Entries 938850
BBC-L TAC Diff
BCC-S TAC Diff
Entries 938850
BBC-L TAC Diff
VPD TAC Diff
Entries 938850
BBC-L TAC Diff
ZDC TAC Diff
Entries 938850
BBC-S TAC Diff
VPD TAC Diff
Entries 938850
BBC-S TAC Diff
ZDC TAC Diff
Entries 938850
VPD TAC Diff
ZDC TAC Diff

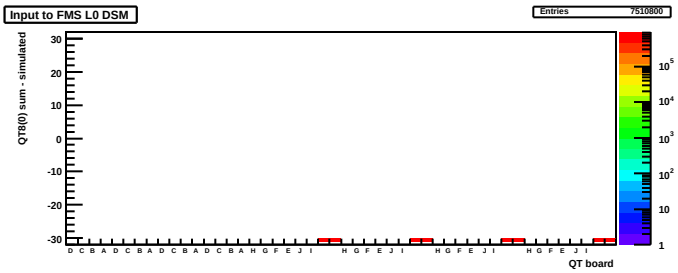

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

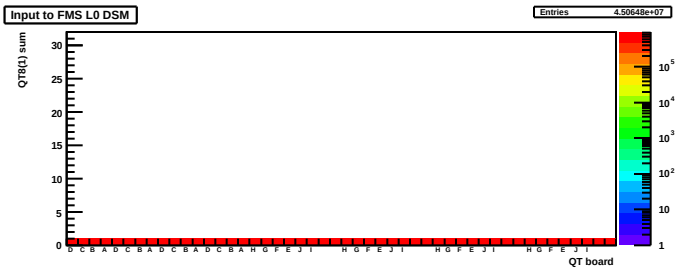

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

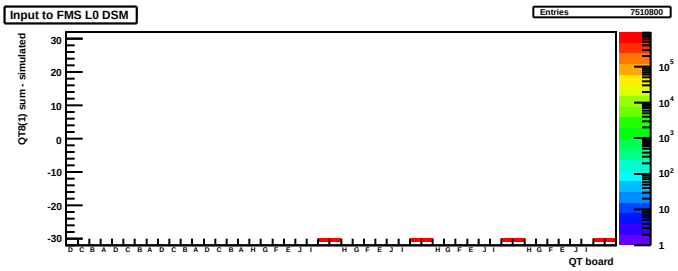

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

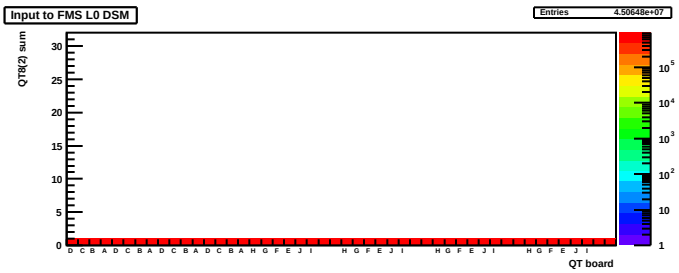

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

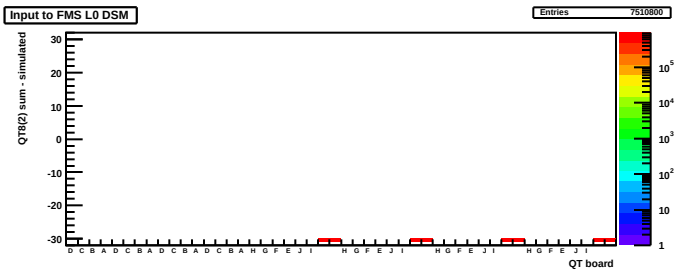

Input to FMS L0 DSM
Entries 4.50648e+07
QT8(0) sum
QT board

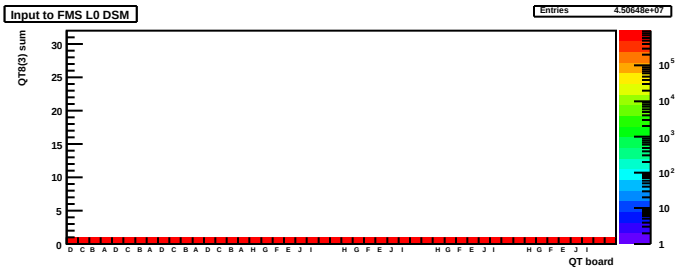

Input to FMS L0 DSM
Entries 7510800
QT8(0) sum - simulated
QT board

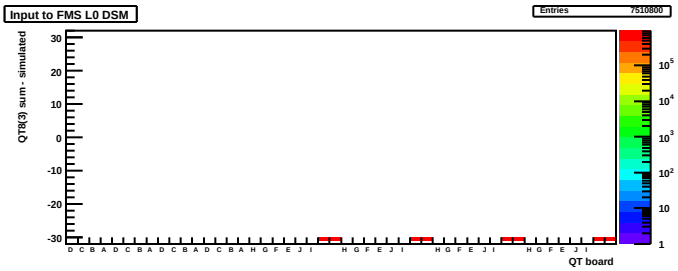

Input to FMS L0 DSM
Entries 4.50648e+07
QT8(1) sum
QT board

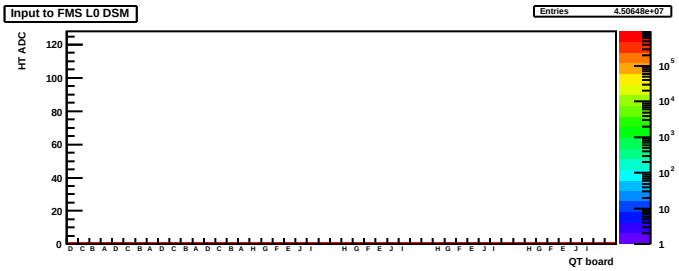

Input to FMS L0 DSM
Entries 7510800
QT8(1) sum - simulated
QT board

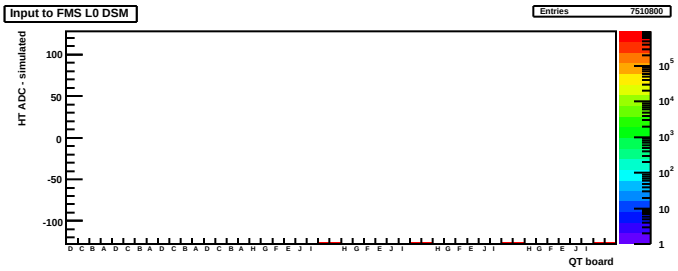

Input to FMS L0 DSM
Entries 4.50648e+07
QT8(2) sum
QT board

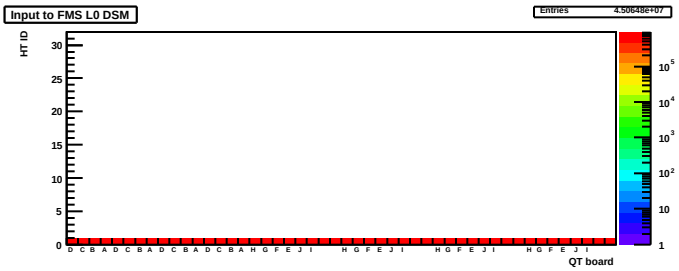

Input to FMS L0 DSM
Entries 7510800
QT8(2) sum - simulated
QT board

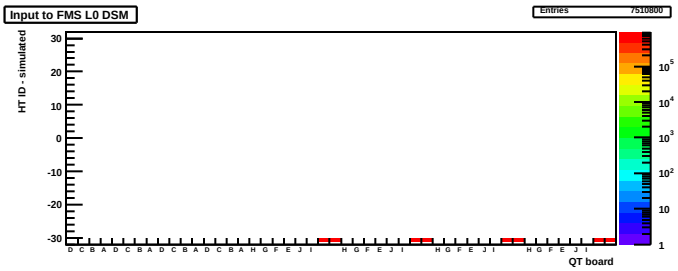

Input to FMS L0 DSM
Entries 4.50648e+07
QT8(3) sum
QT board

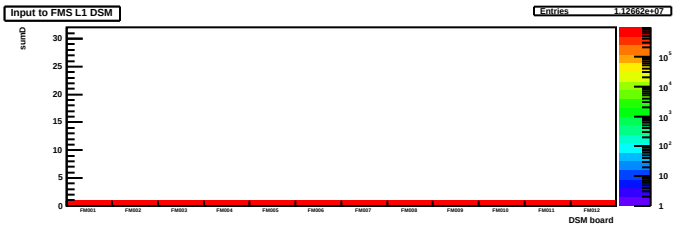

Input to FMS L0 DSM
Entries 7510800
QT8(3) sum - simulated
QT board

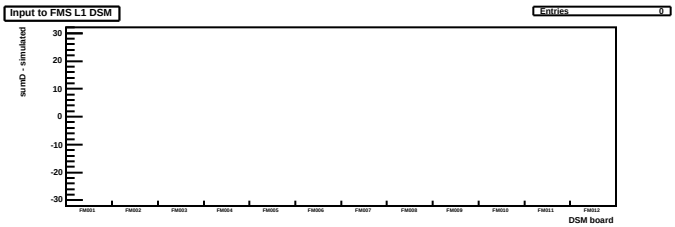

Input to FMS L0 DSM
Entries 4.50648e+07
HT ADC
QT board

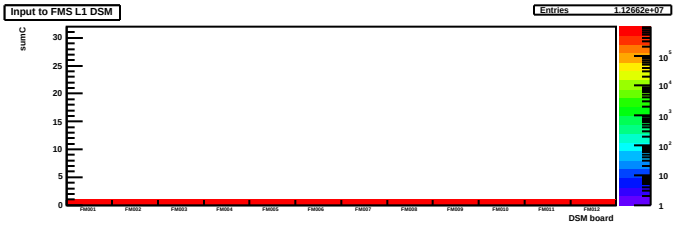

Input to FMS L0 DSM
Entries 7510800
HT ADC - simulated
QT board

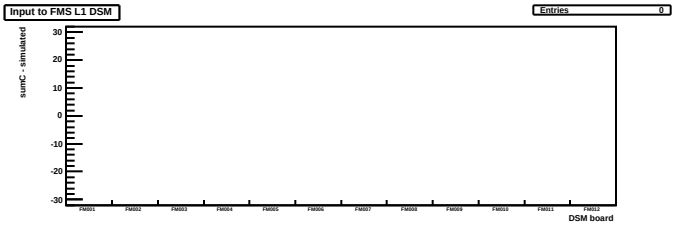

Input to FMS L0 DSM
Entries 4.50648e+07
HT ID
QT board

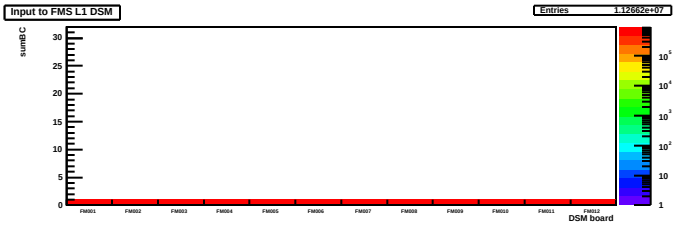

Input to FMS L0 DSM
Entries 7510800
HT ID - simulated
QT board

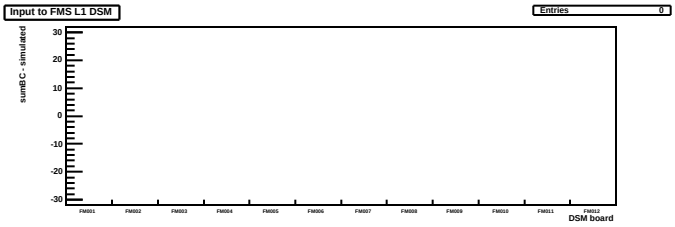

Input to FMS L1 DSM
Entries 1.12662e+07
sumD
DSM board

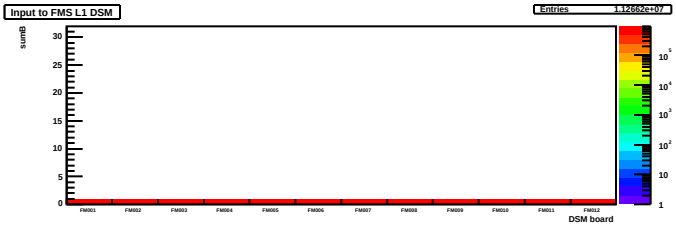

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

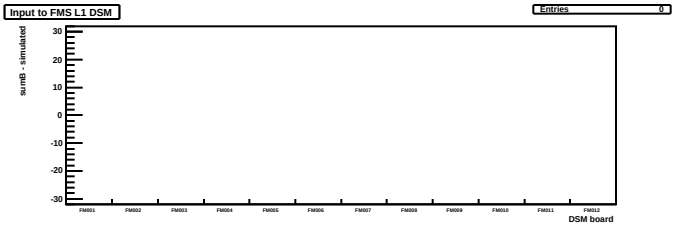

Input to FMS L1 DSM
Entries 1.12662e+07
sumC
DSM board

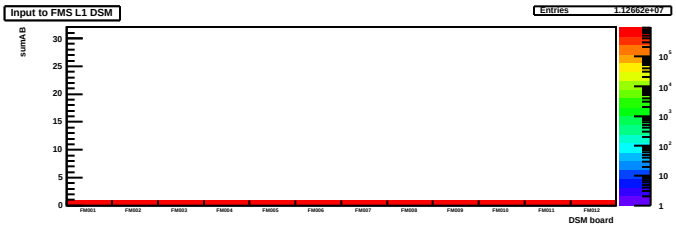

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

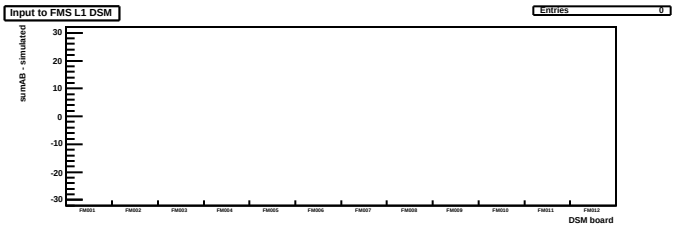

Input to FMS L1 DSM
Entries 1.12662e+07
sumBC
DSM board

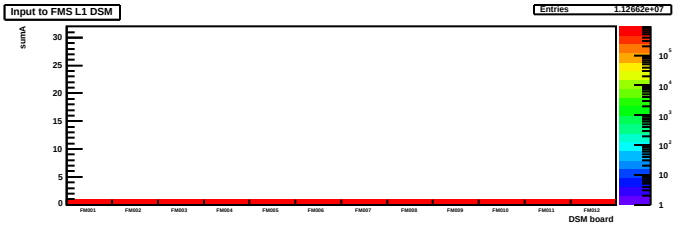

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

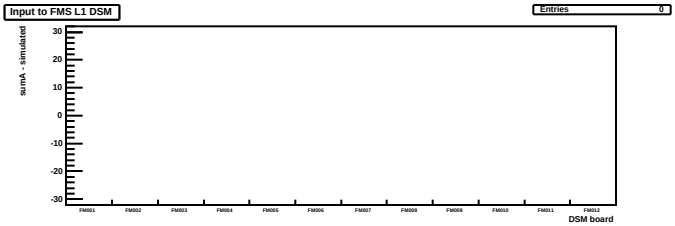

Input to FMS L1 DSM
Entries 1.12662e+07
sumB
DSM board

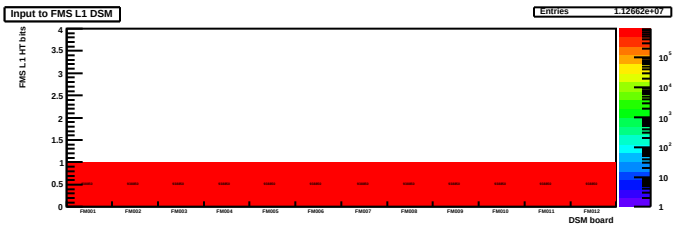

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

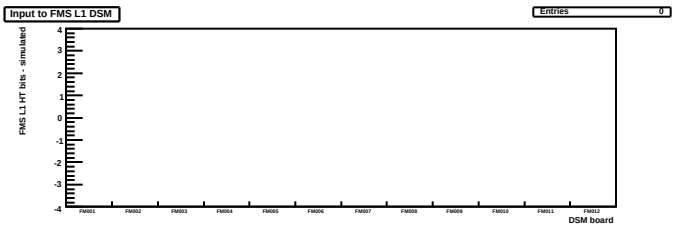

Input to FMS L1 DSM
Entries 1.12662e+07
sumAB
DSM board

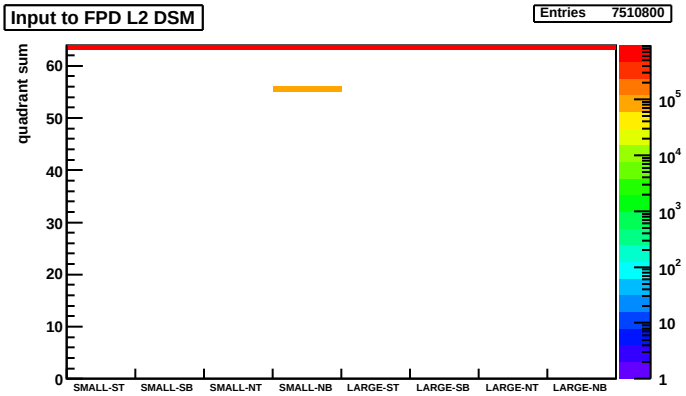

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

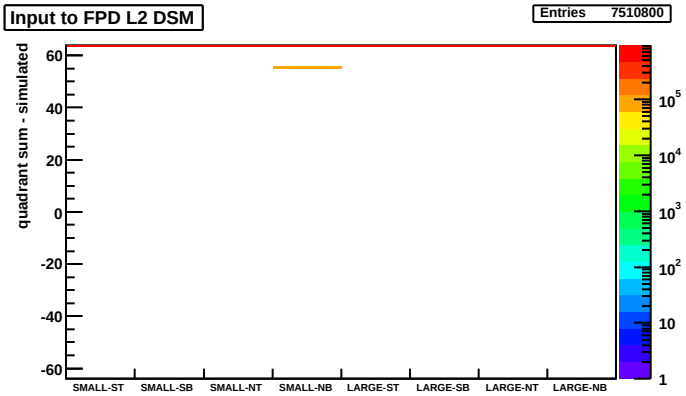

Input to FMS L1 DSM
Entries 1.12662e+07
sumA
DSM board

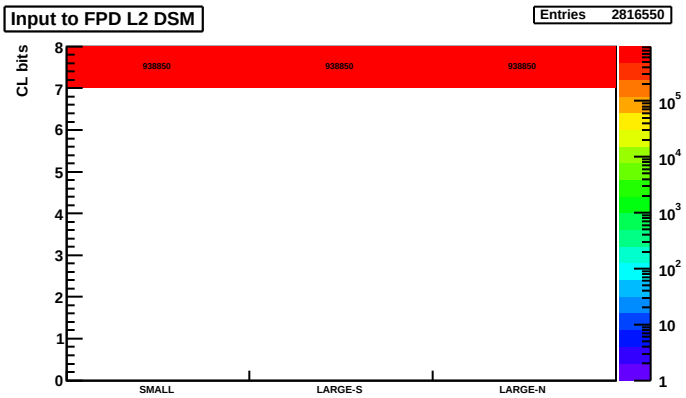

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

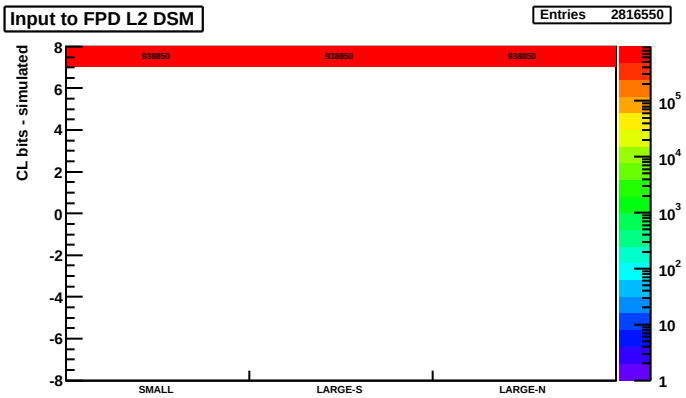

Input to FMS L1 DSM
Entries 1.12662e+07
FMS L1 HT bits
DSM board

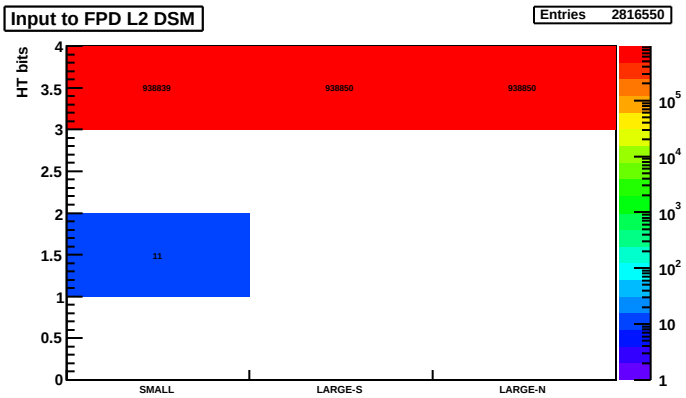

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

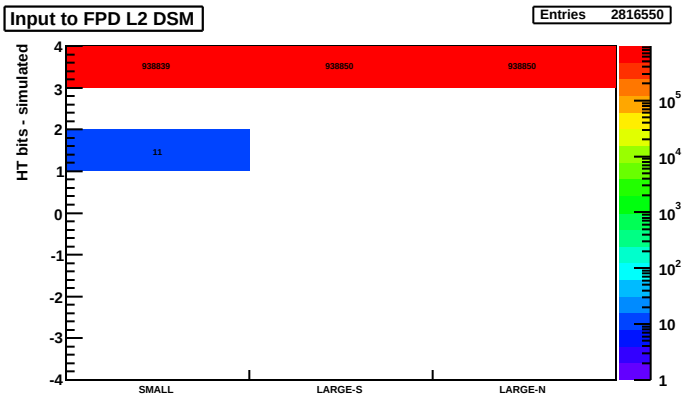

Input to FPD L2 DSM
Entries 7510800
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

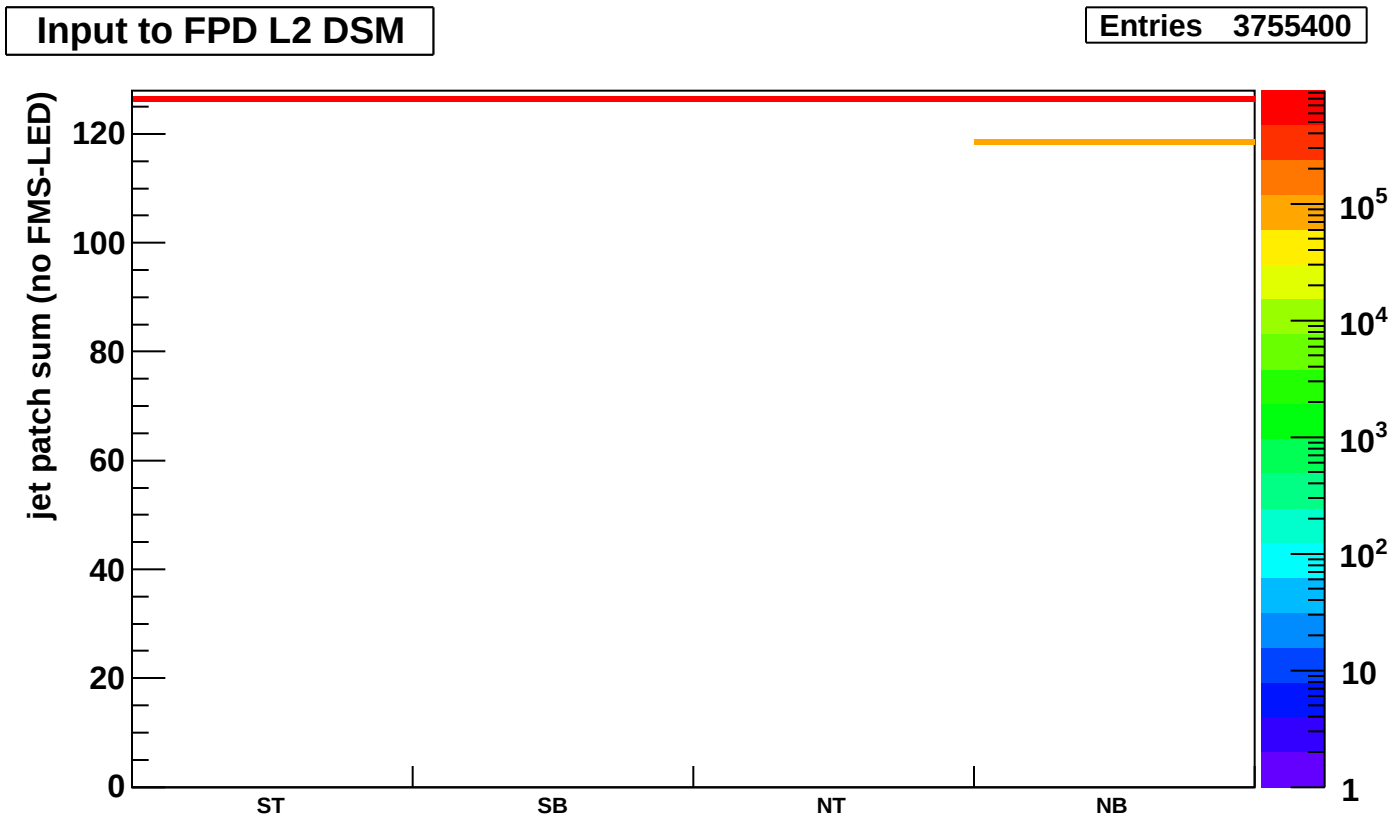

Input to FPD L2 DSM
Entries 7510800
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

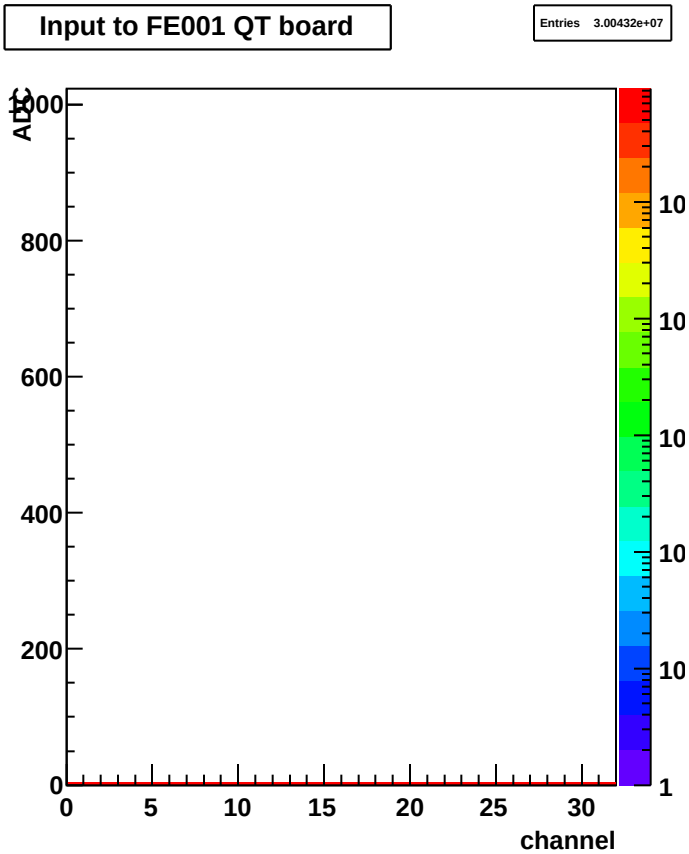

Input to FPD L2 DSM
Entries 2816550
CL bits
938850
938850
938850
SMALL
LARGE-S
LARGE-N

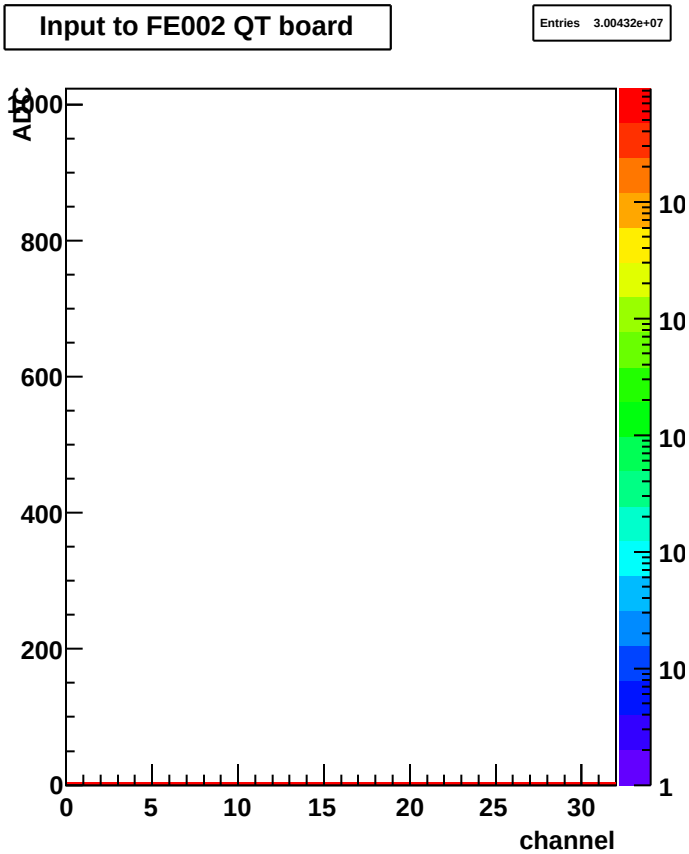

Input to FPD L2 DSM
Entries 2816550
CL bits - simulated
938850
938850
938850
SMALL
LARGE-S
LARGE-N

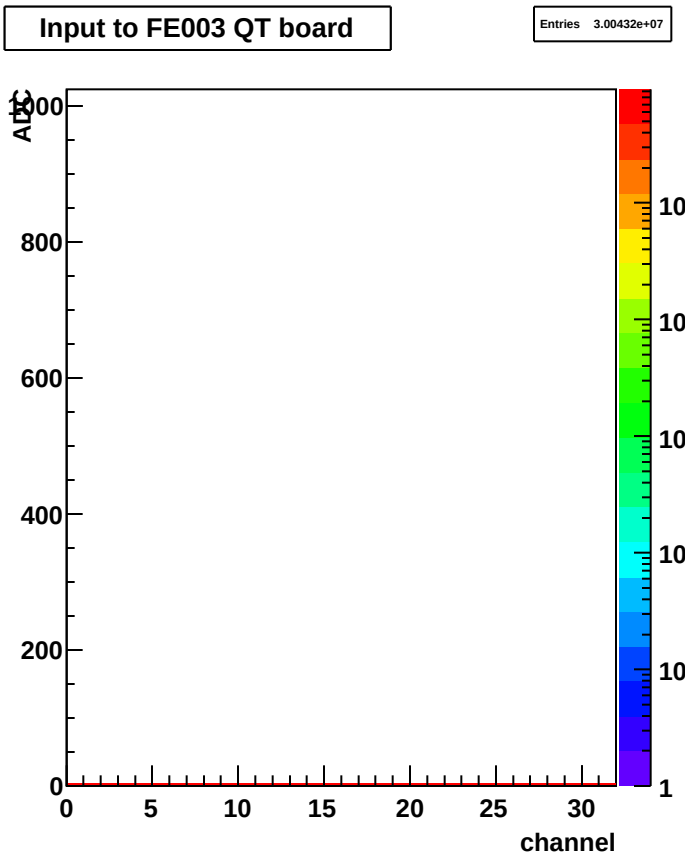

Input to FPD L2 DSM
Entries 2816550
HT bits
938839
938850
938850
11
SMALL
LARGE-S
LARGE-N

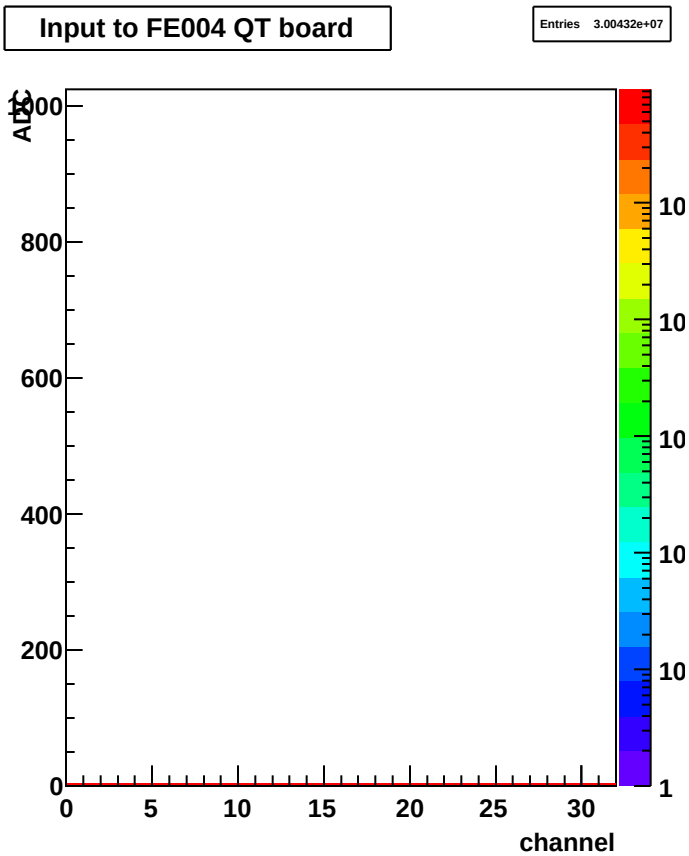

Input to FPD L2 DSM
Entries 2816550
HT bits - simulated
938839
938850
938850
11
SMALL
LARGE-S
LARGE-N


Input to FPD L2 DSM
Entries 3755400
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

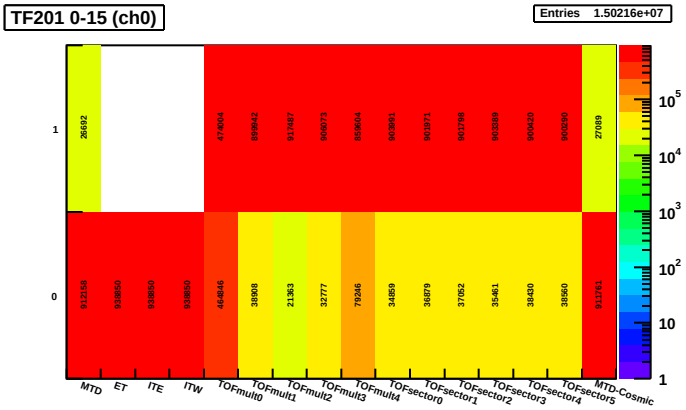

Input to FE001 QT board
Entries 3.00432e+07
ADC
channel


Input to FE002 QT board
Entries 3.00432e+07
ADC
channel

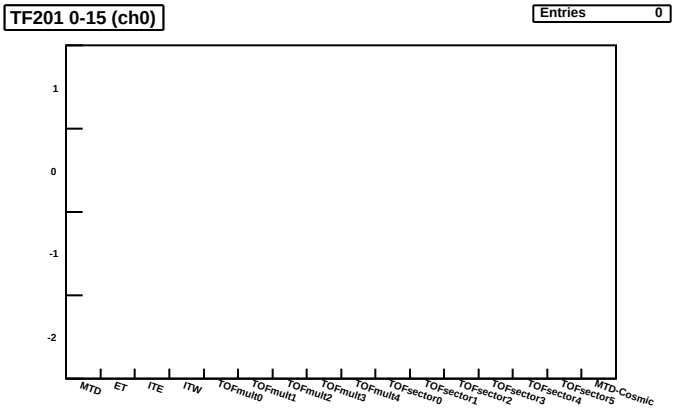

Input to FE003 QT board
Entries 3.00432e+07
ADC
channel


Input to FE004 QT board
Entries 3.00432e+07
ADC
channel

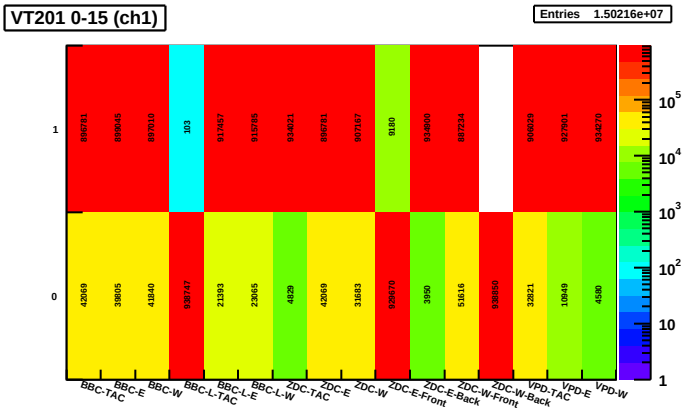
TF201 0-15 (ch0)



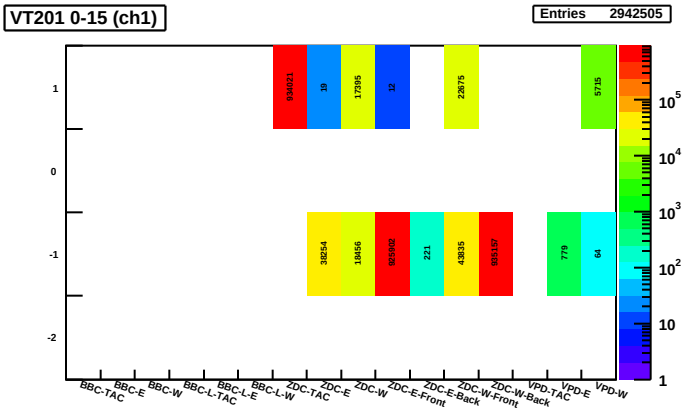
TF201 0-15 (ch0)



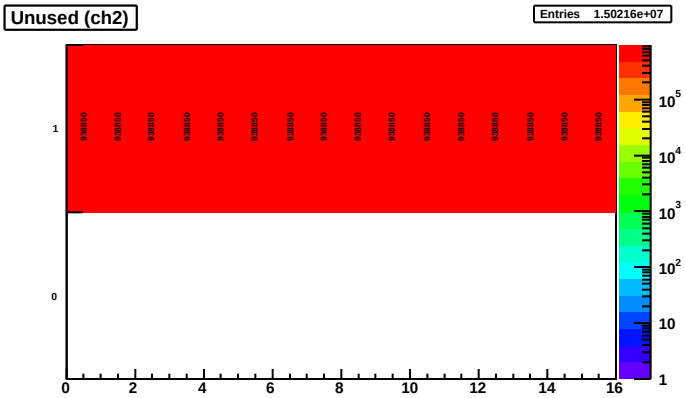
VT201 0-15 (ch1)

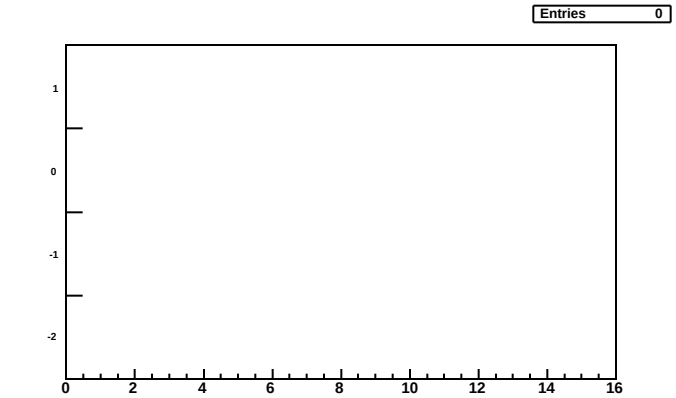


VT201 0-15 (ch1)

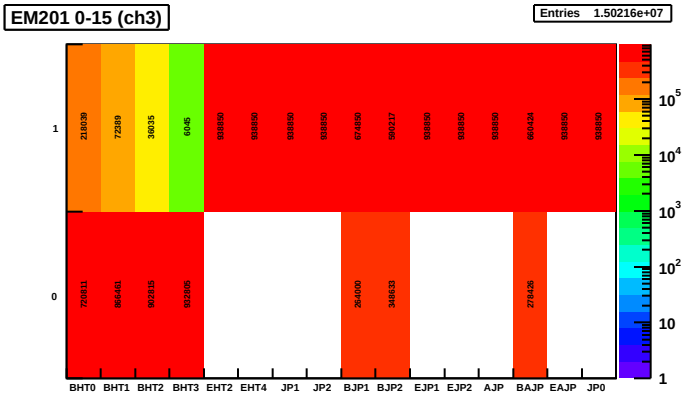


Unused (ch2)

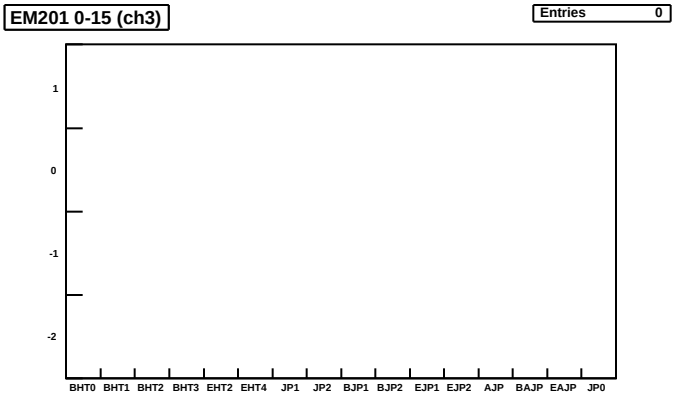




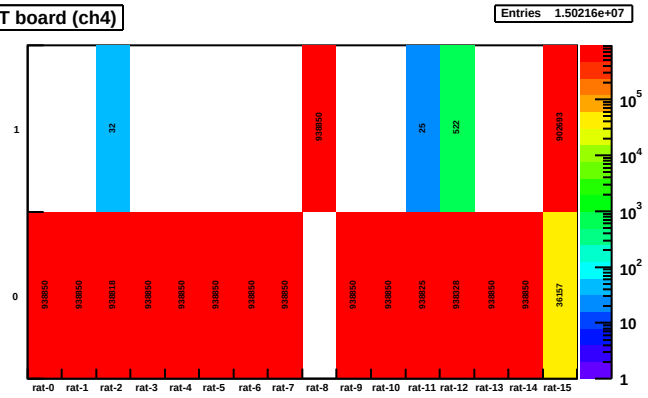
EM201 0-15 (ch3)

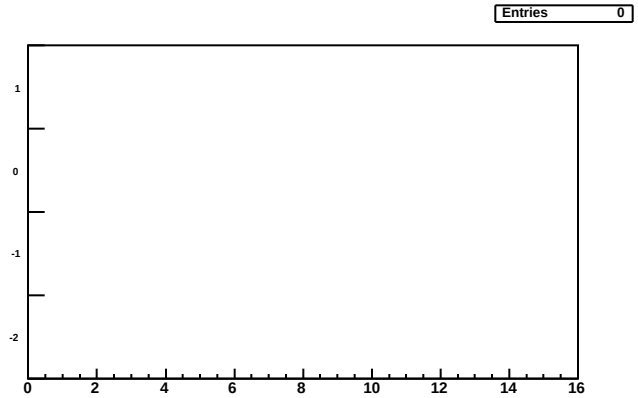


EM201 0-15 (ch3)

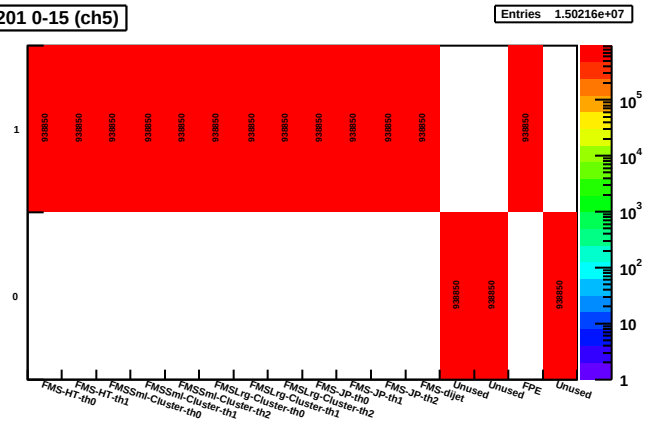


RAT board (ch4)

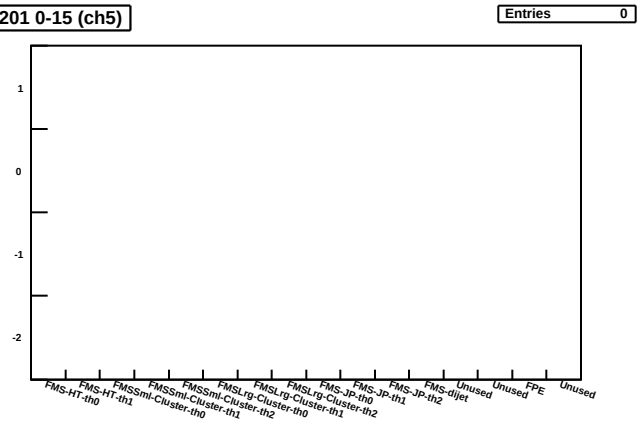




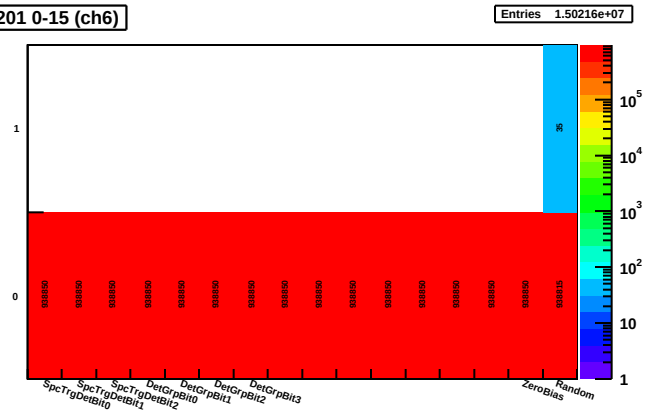
FP201 0-15 (ch5)

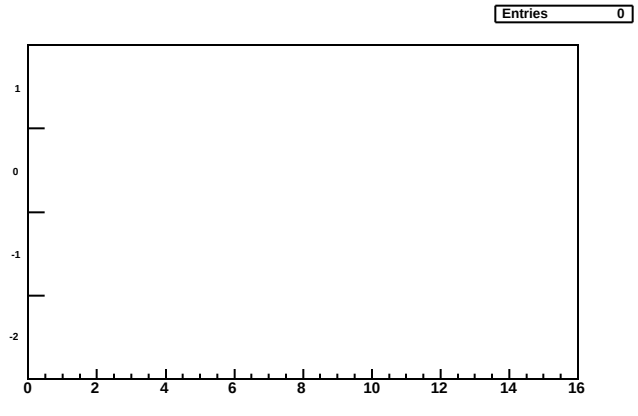
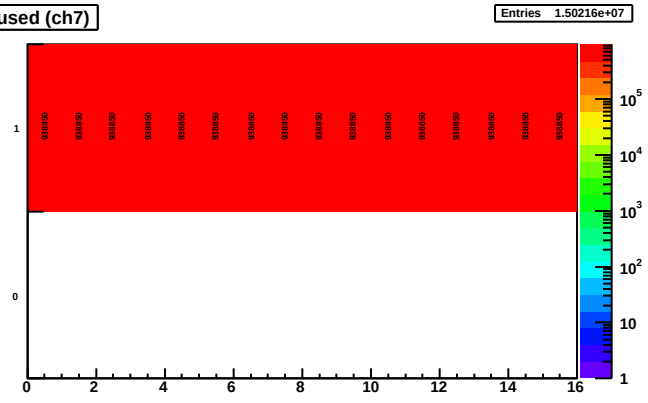
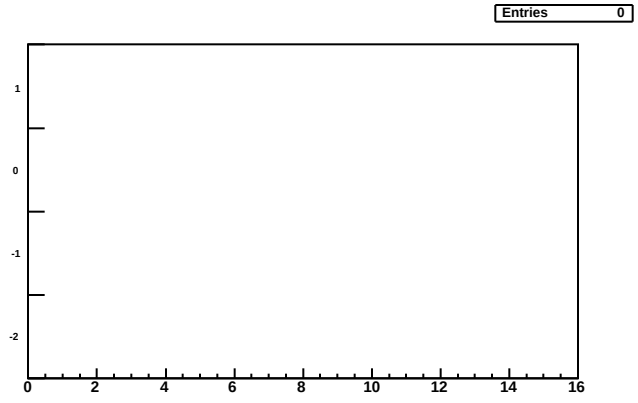


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)