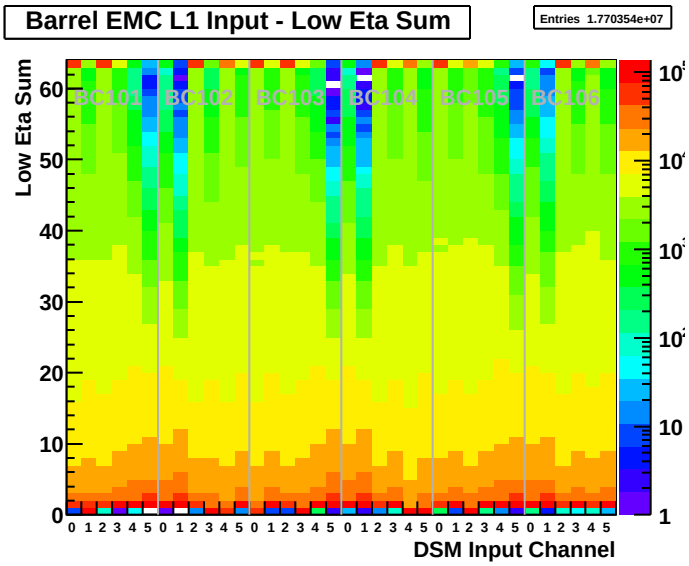
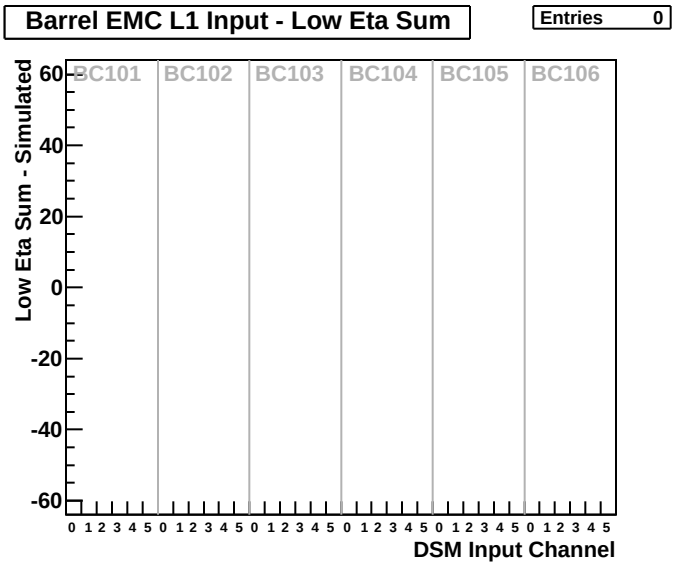
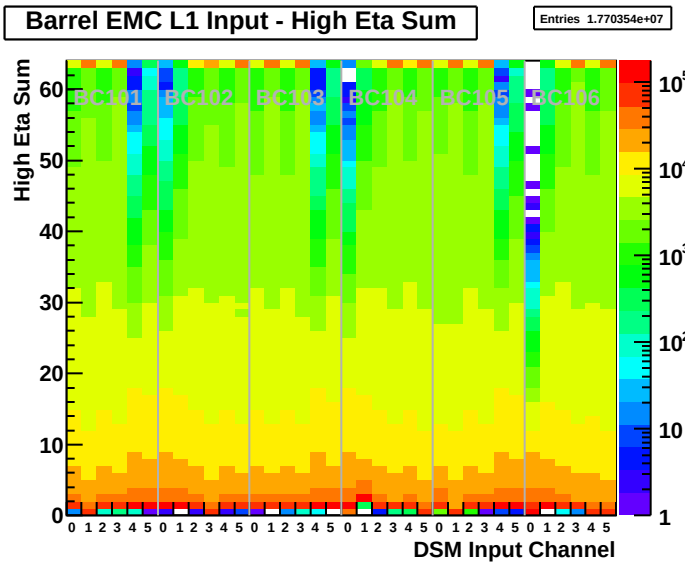
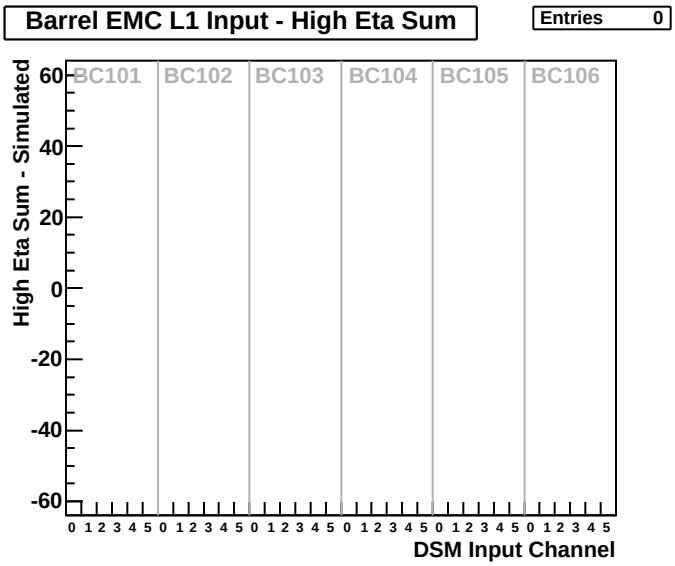
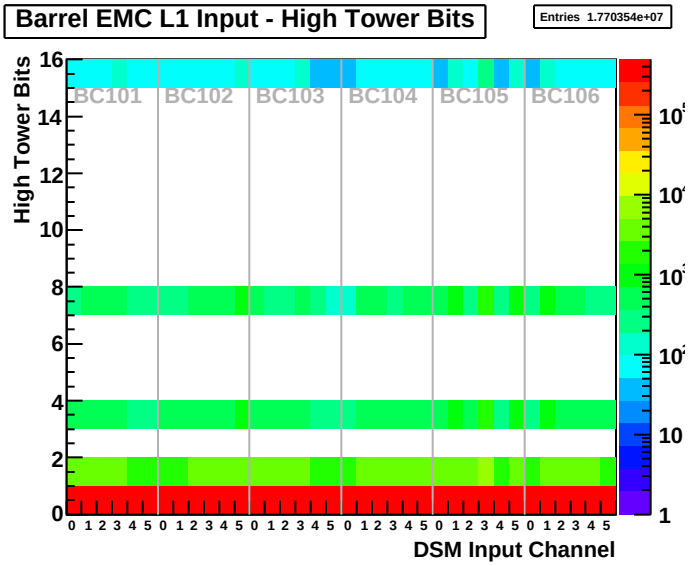
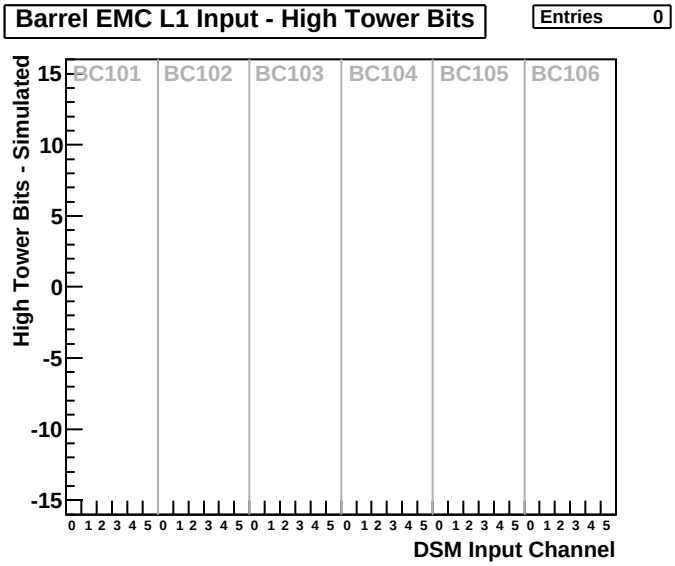

Barrel EMC L1 Input - Low Eta Sum
Entries 1.770354e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

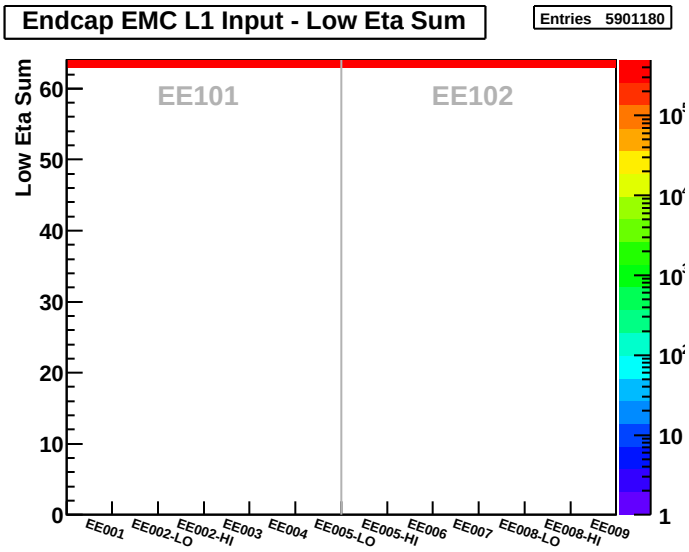

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

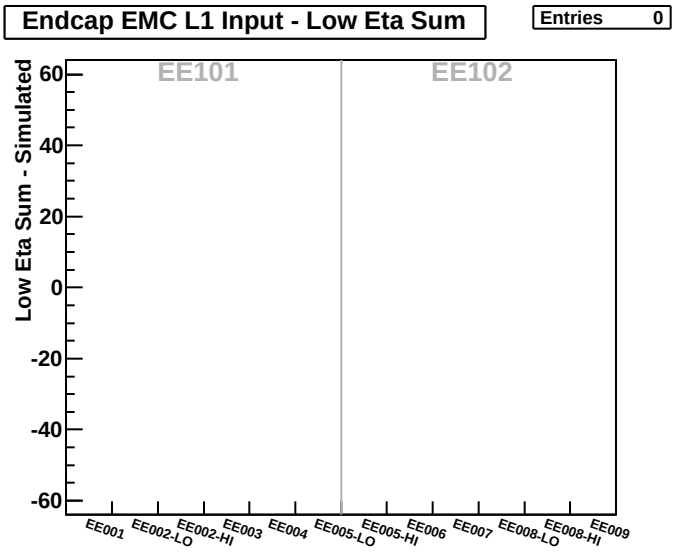

Barrel EMC L1 Input - High Eta Sum
Entries 1.770354e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

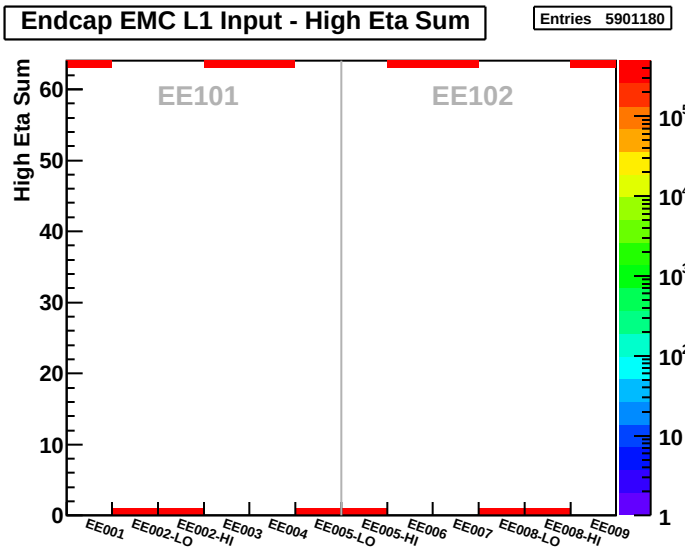

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

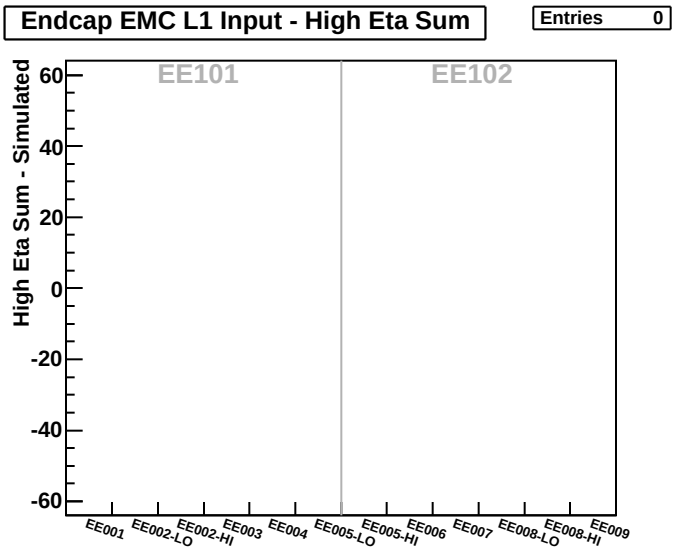

Barrel EMC L1 Input - High Tower Bits
Entries 1.770354e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

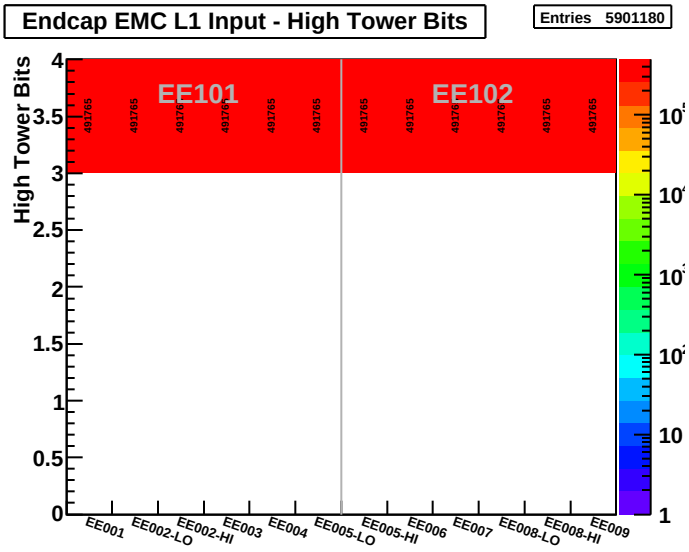

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

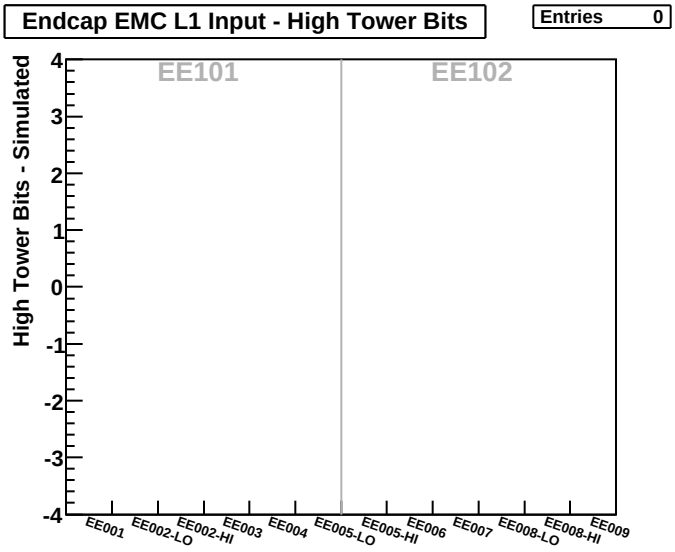

Endcap EMC L1 Input - Low Eta Sum
Entries 5901180
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

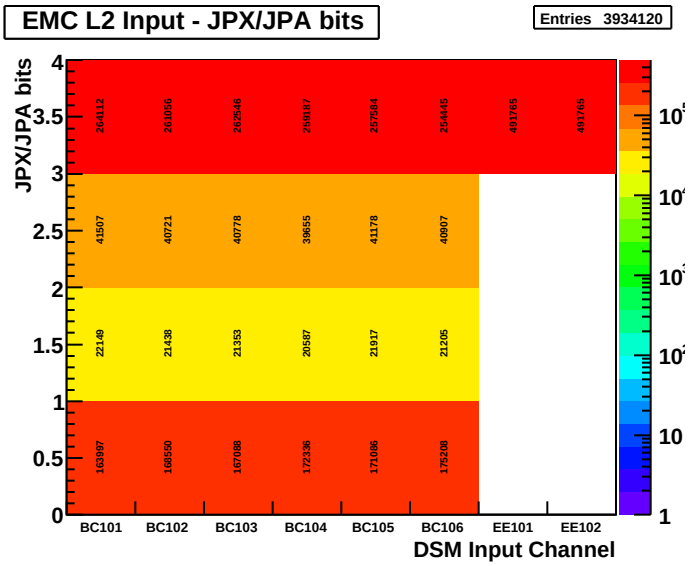

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

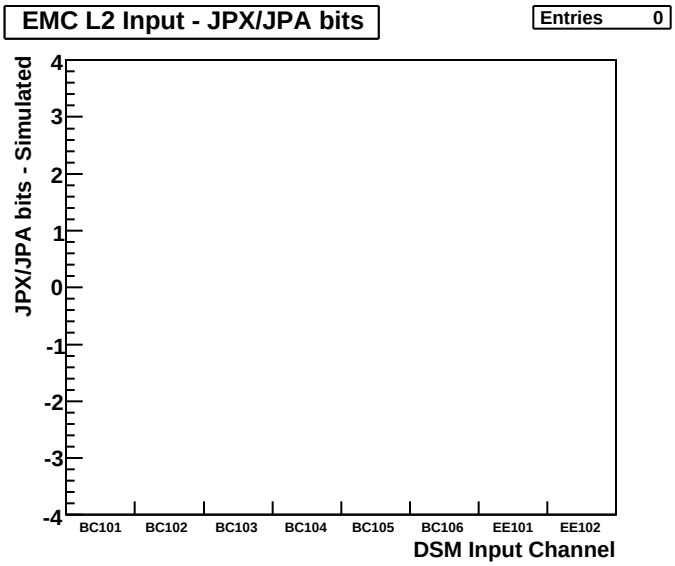

Endcap EMC L1 Input - High Eta Sum
Entries 5901180
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

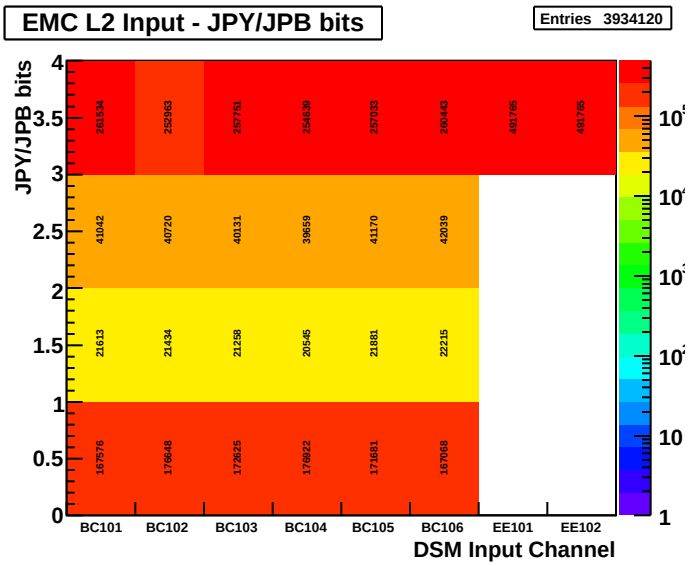

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

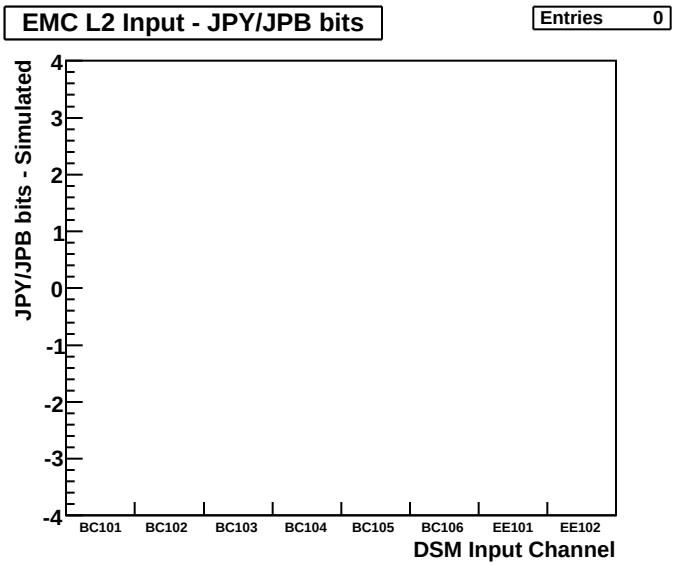

Endcap EMC L1 Input - High Tower Bits
Entries 5901180
High Tower Bits
EE101
EE102
491765
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

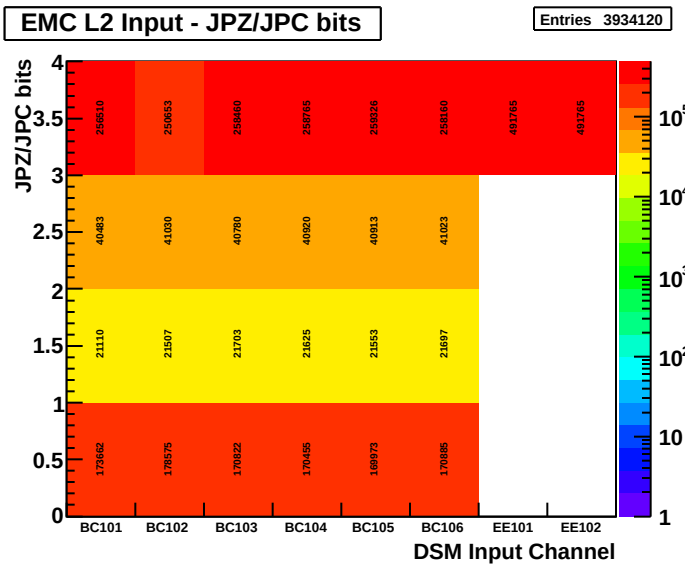

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

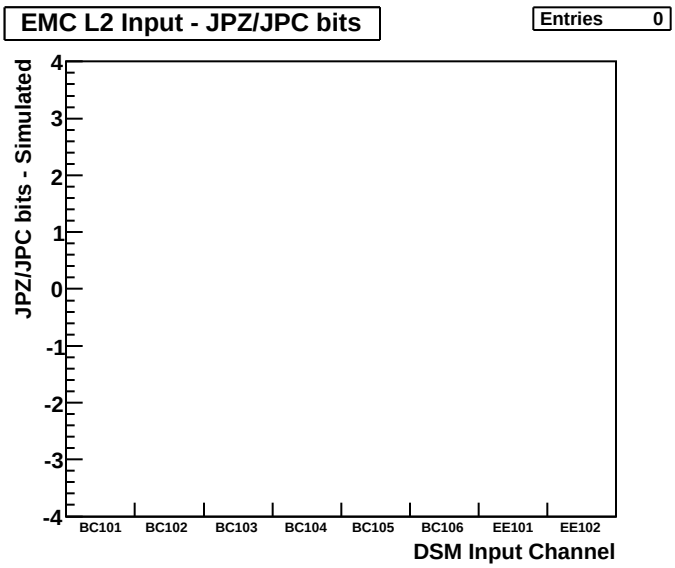

EMC L2 Input - JPX/JPA bits
Entries 3934120
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
254412 251056 262546 259187 257584 254445 491765 491765
41507 40721 40778 39655 41178 40907
22149 21438 21353 20567 21917 21205
163997 168550 167088 172336 171086 175208

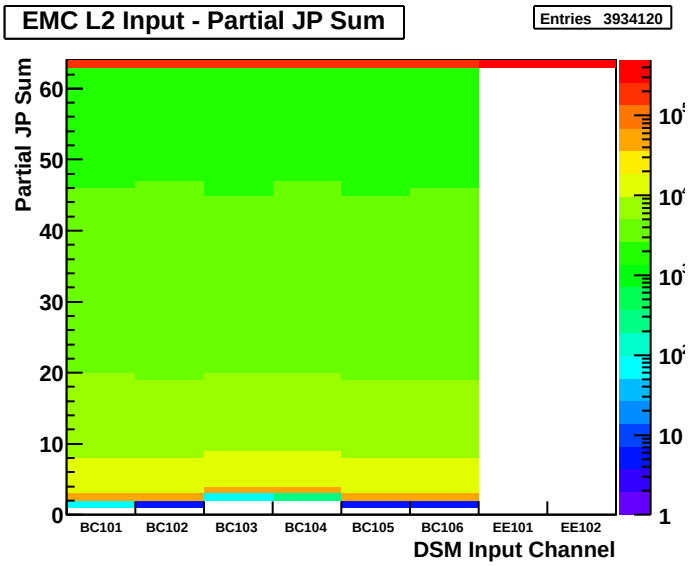

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

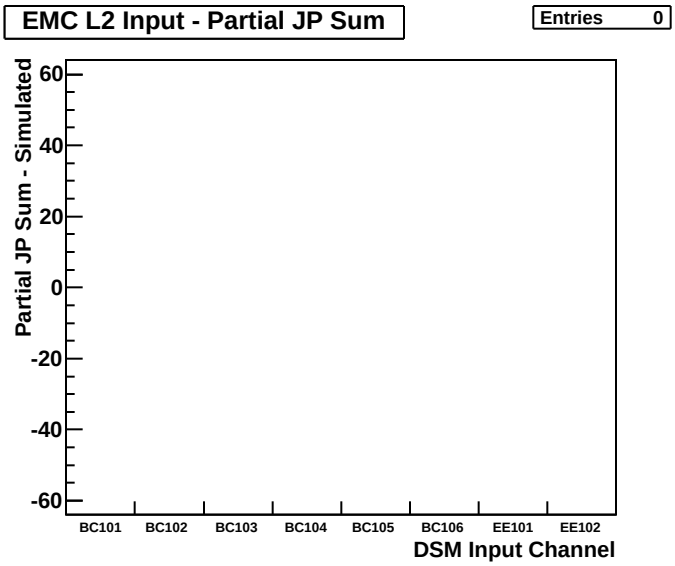

EMC L2 Input - JPY/JPB bits
Entries 3934120
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
261534 252963 257751 254639 257033 260443 491765 491765
41042 40720 40131 39659 41170 42039
21613 21434 21258 20545 21881 22215
167576 176648 172625 176922 171681 167068

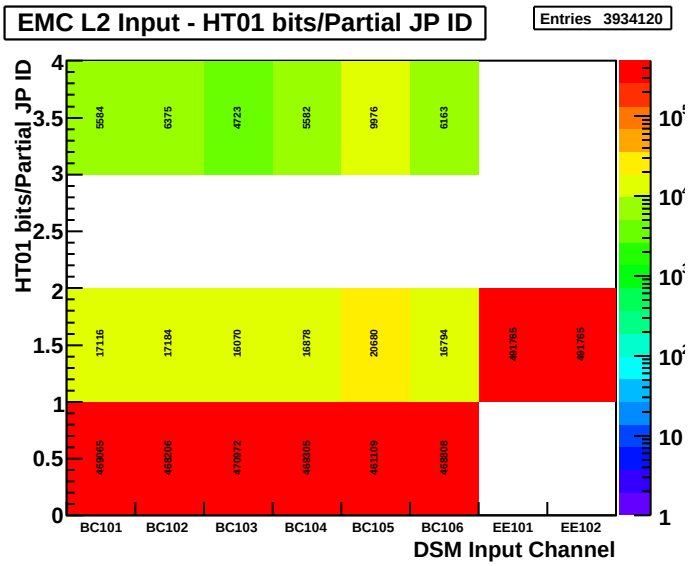

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

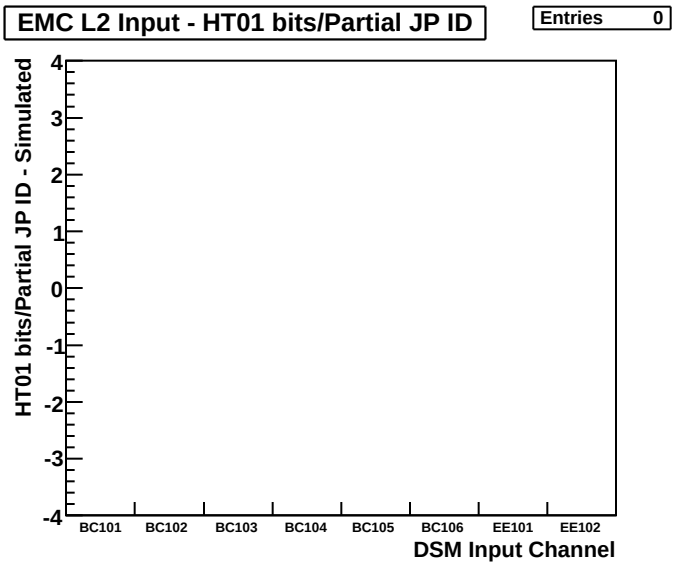

EMC L2 Input - JPZ/JPC bits
Entries 3934120
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
255510 250653 252460 253765 253326 253160 491765 491765
40483 41030 40780 40920 40913 41023
21110 21507 21703 21625 21553 21697
173662 178575 170822 170455 169973 170885

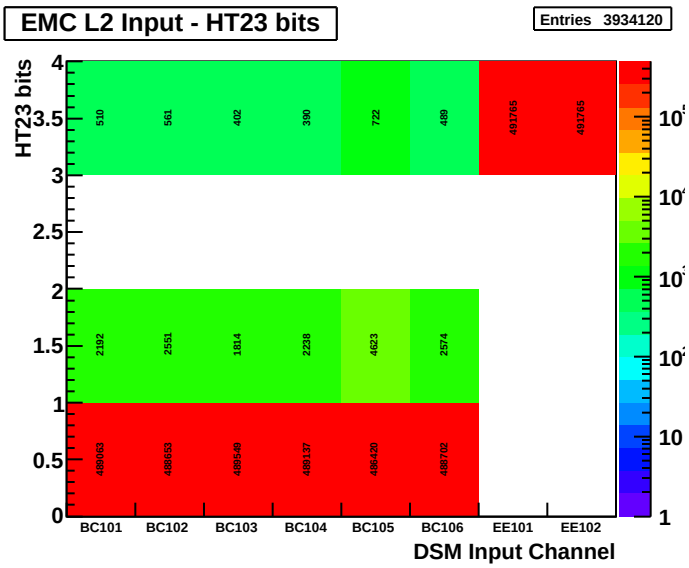

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

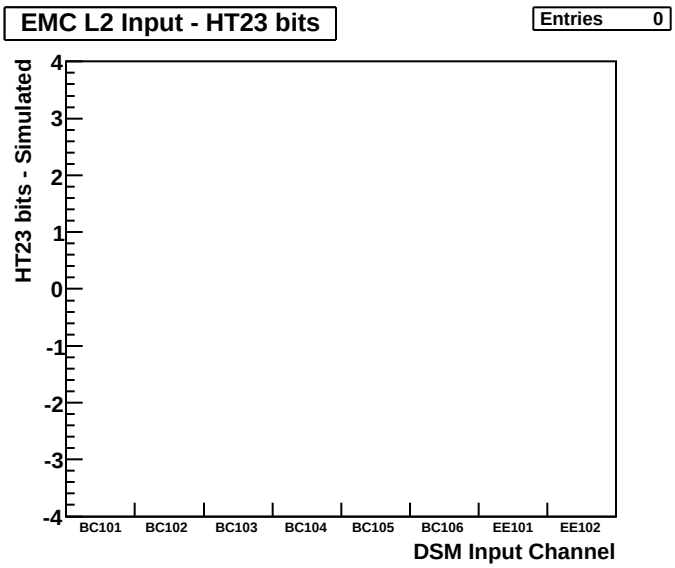

EMC L2 Input - Partial JP Sum
Entries 3934120
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

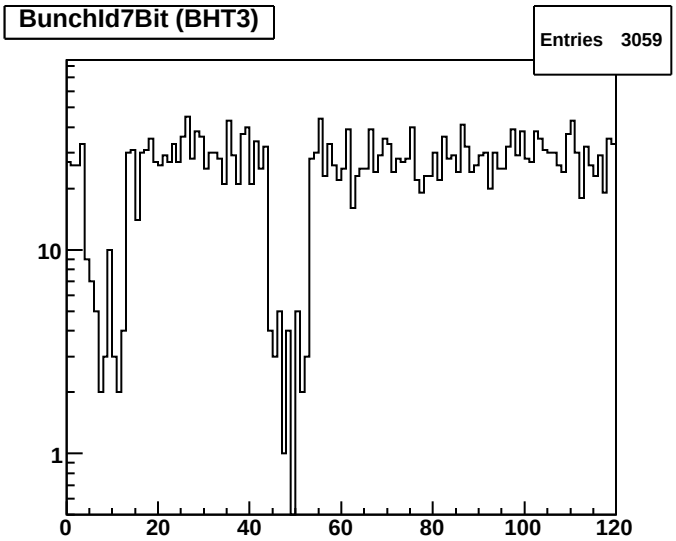

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

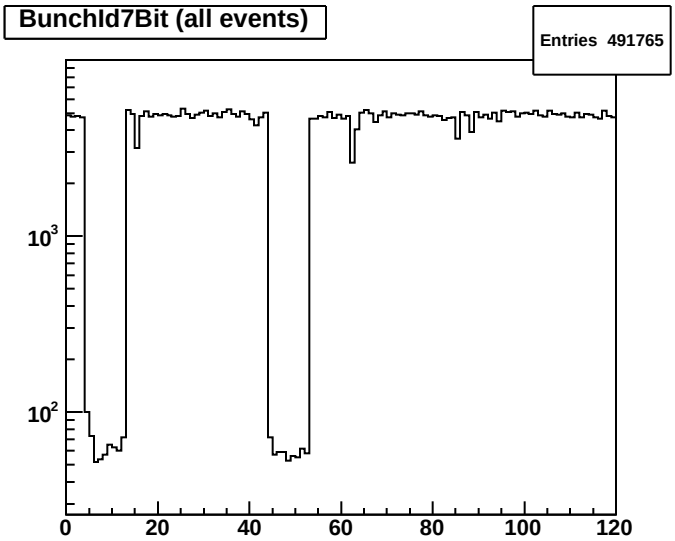

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3934120
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

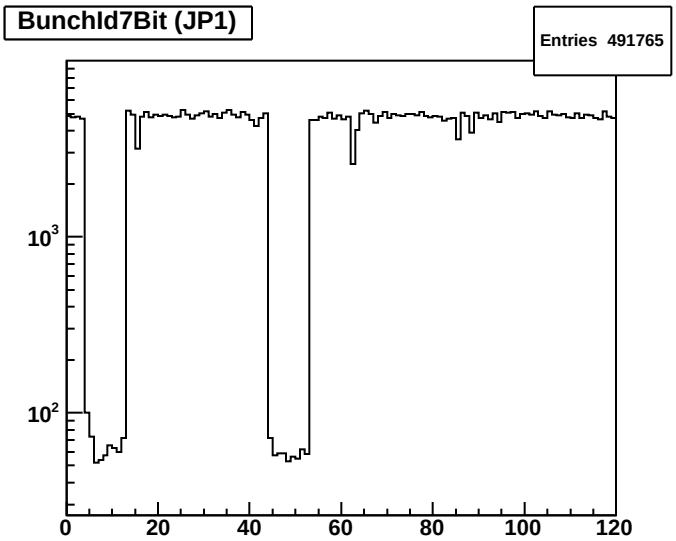

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

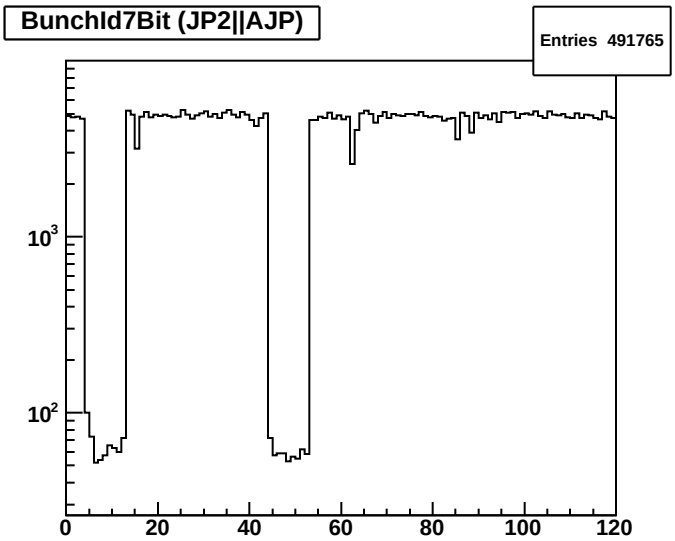

EMC L2 Input - HT23 bits
Entries 3934120
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

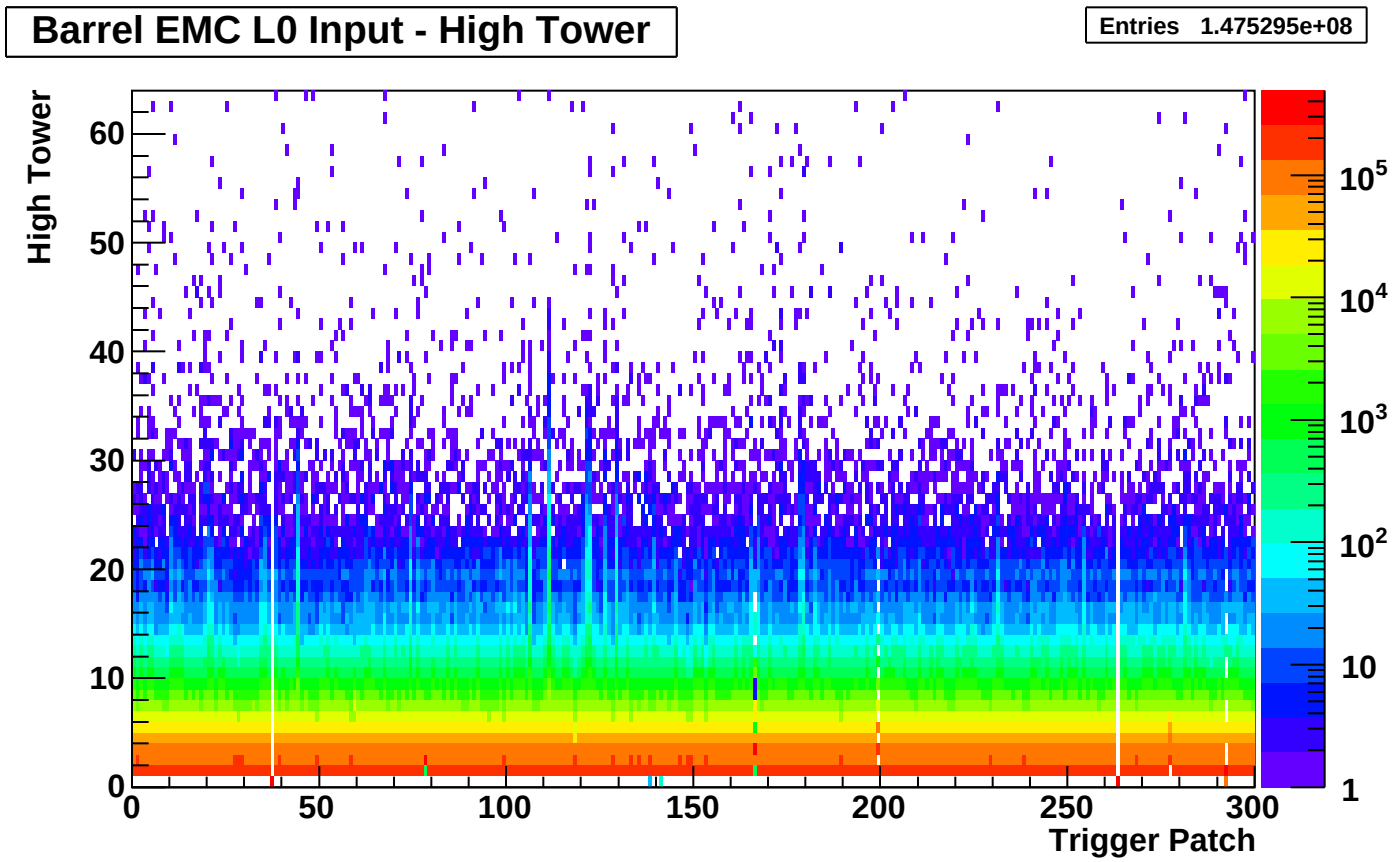

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

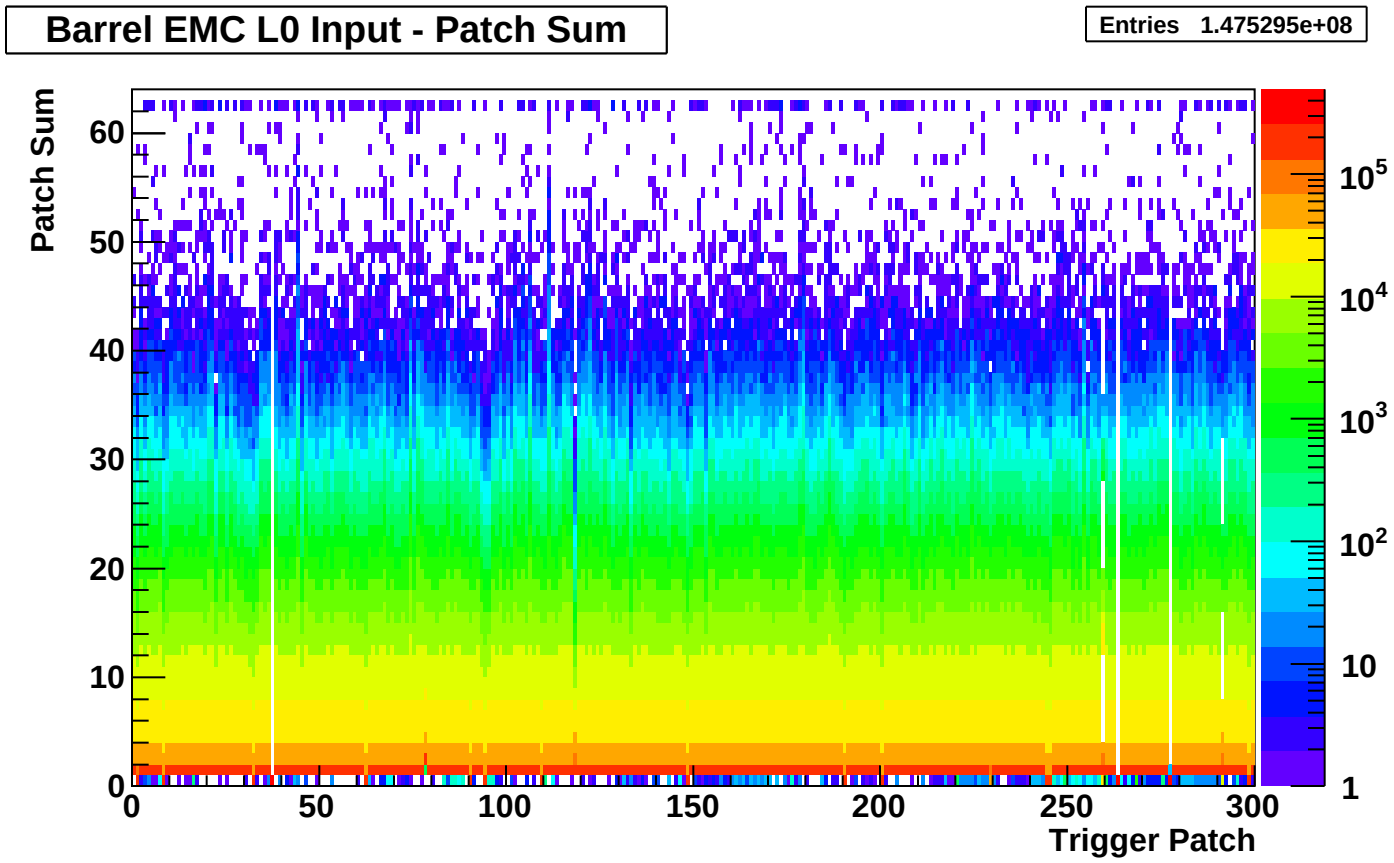

BunchId7Bit (BHT3)
Entries 3059
10
1
0
20
40
60
80
100
120

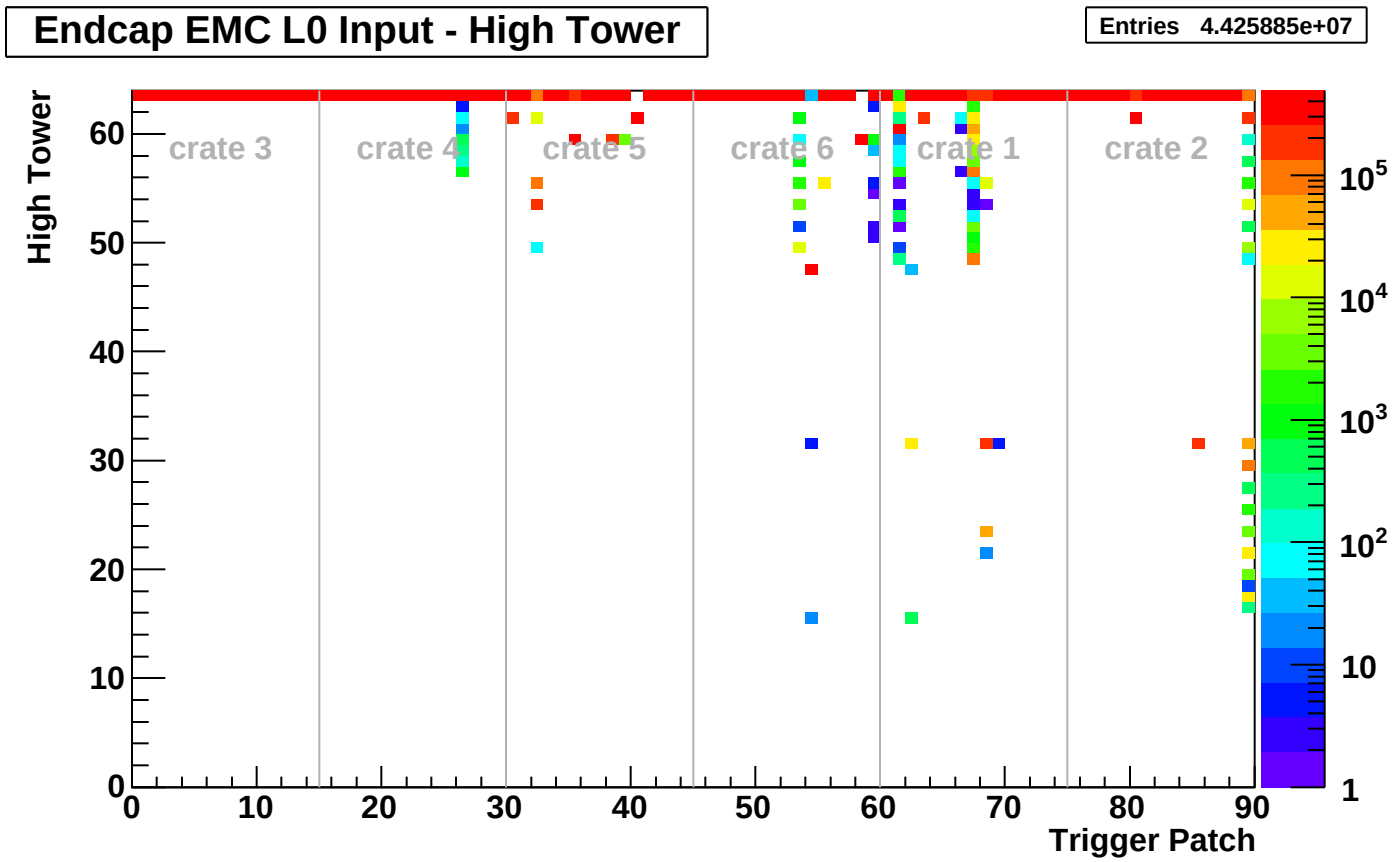

BunchId7Bit (all events)
Entries 491765
10^3
10^2
0
20
40
60
80
100
120

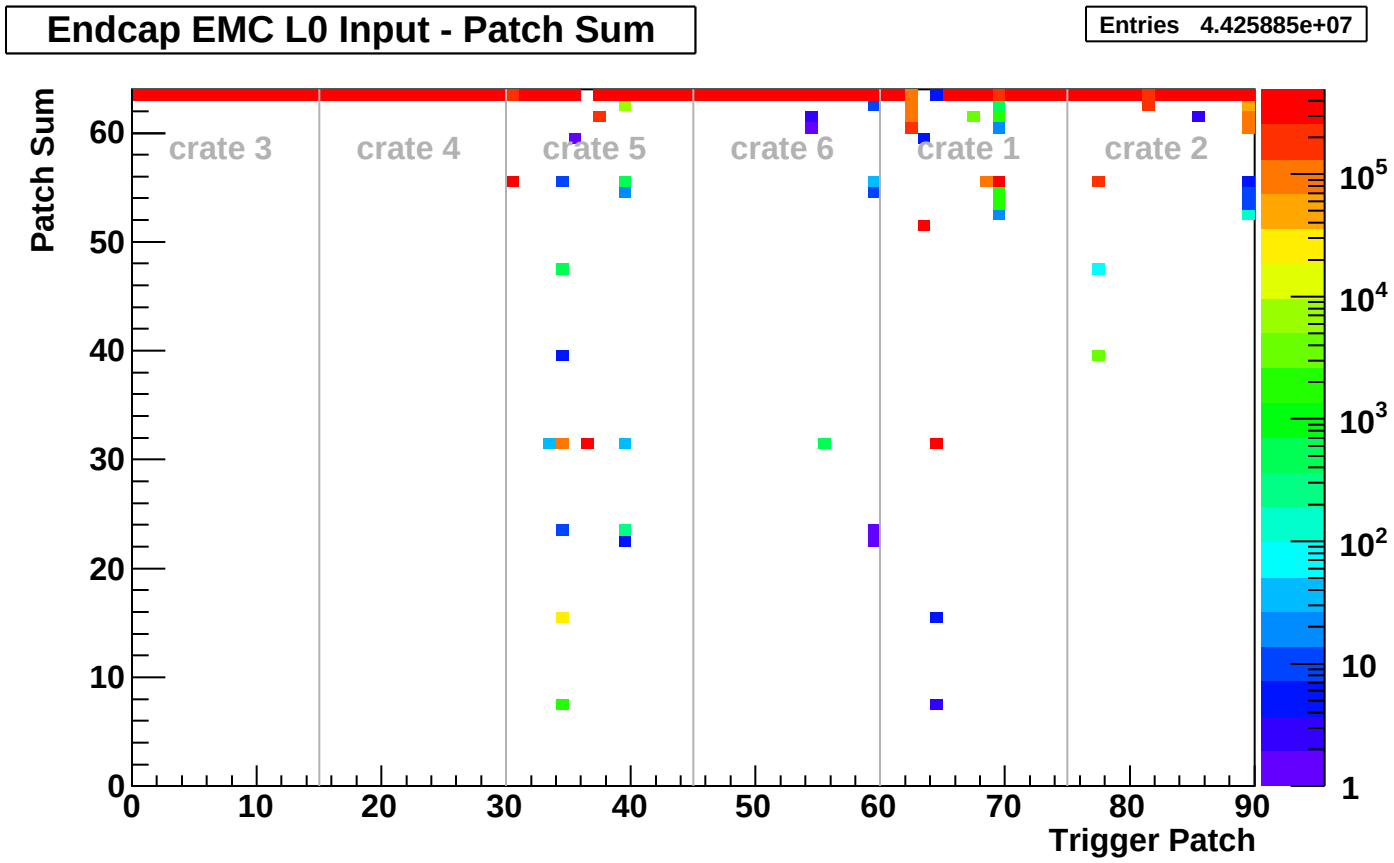

BunchId7Bit (JP1)
Entries 491765
10^3
10^2
0
20
40
60
80
100
120

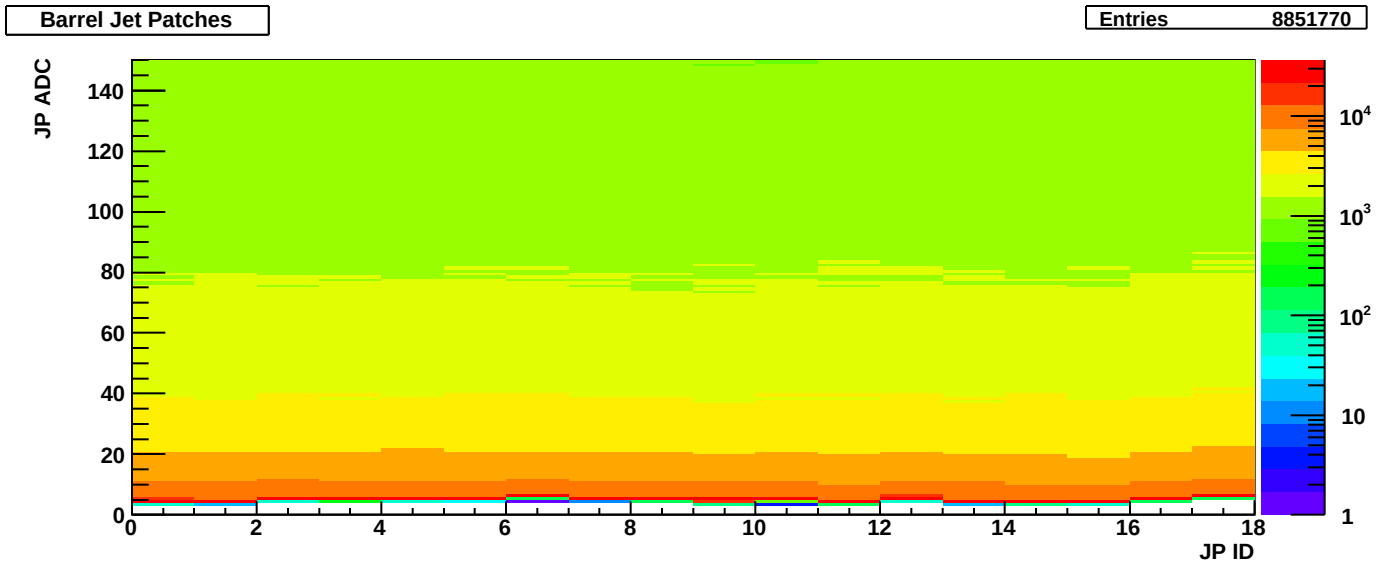

BunchId7Bit (JP2||AJP)
Entries 491765
10^3
10^2
0
20
40
60
80
100
120

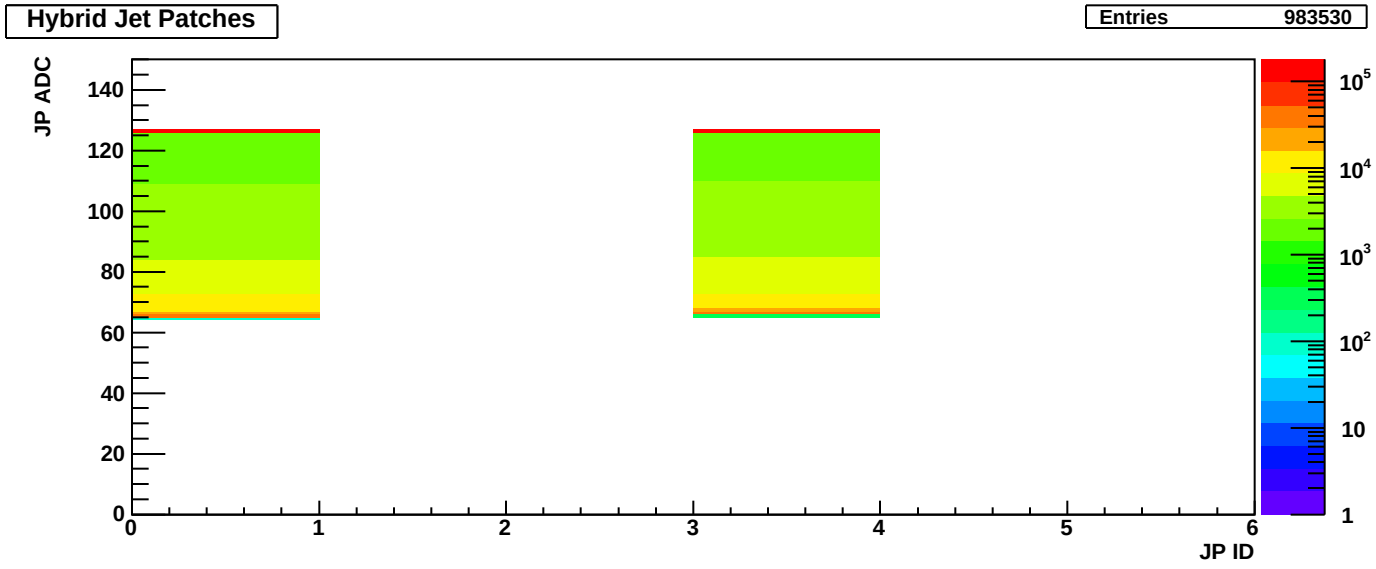

Barrel EMC L0 Input - High Tower
Entries 1.475295e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

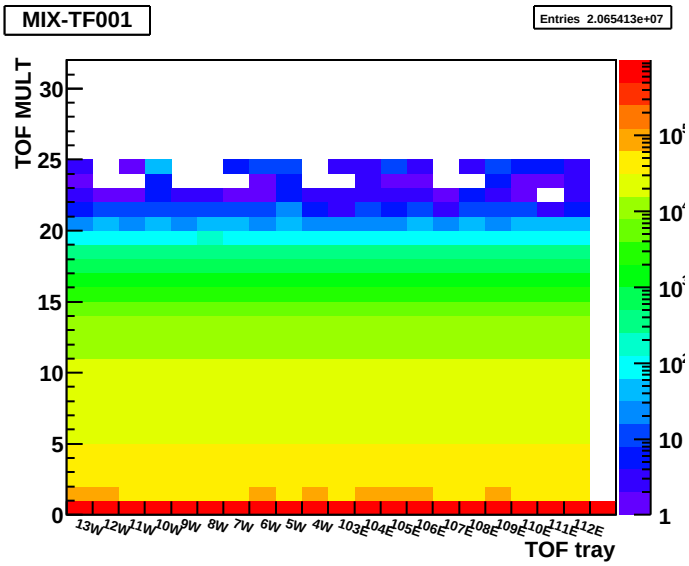

Barrel EMC L0 Input - Patch Sum
Entries 1.475295e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

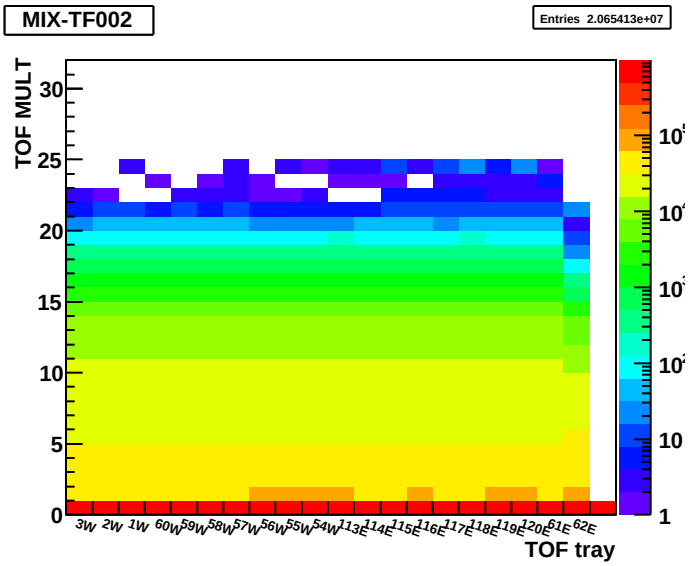

Endcap EMC L0 Input - High Tower
Entries 4.425885e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

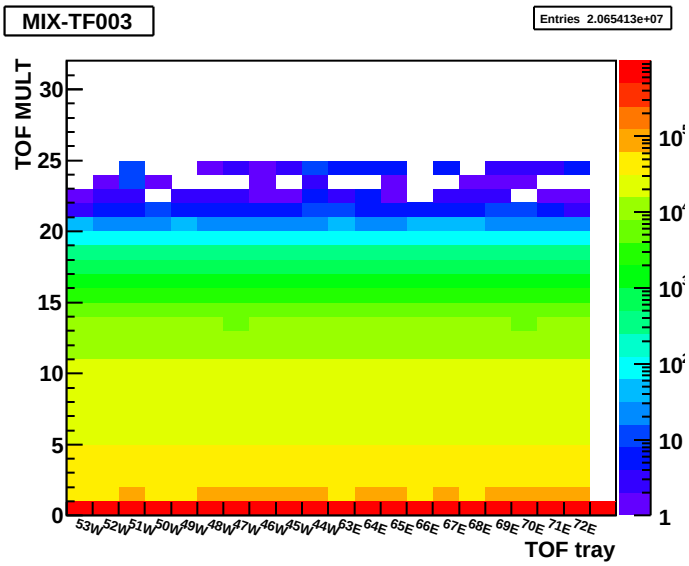

Endcap EMC L0 Input - Patch Sum
Entries 4.425885e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

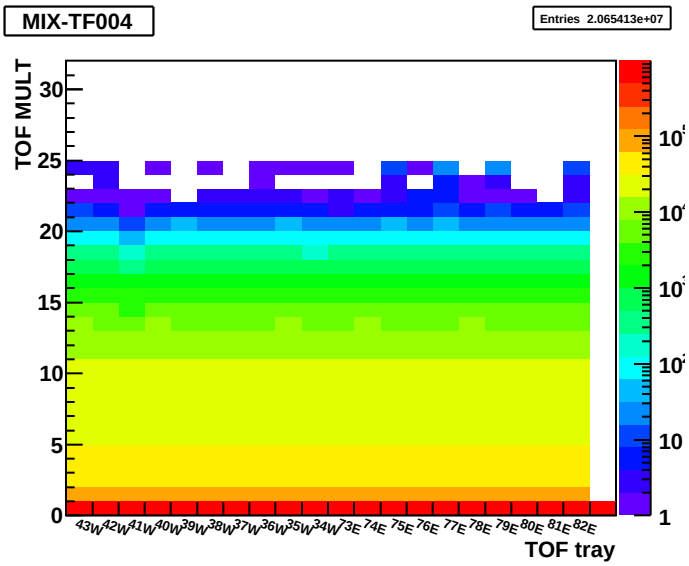

Barrel Jet Patches
Entries 8851770
JP ADC
JP ID

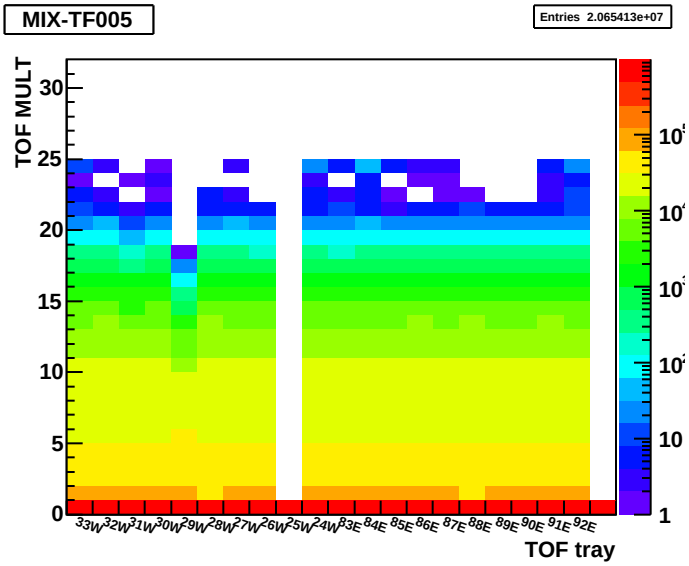

Hybrid Jet Patches
Entries 983530
JP ADC
JP ID

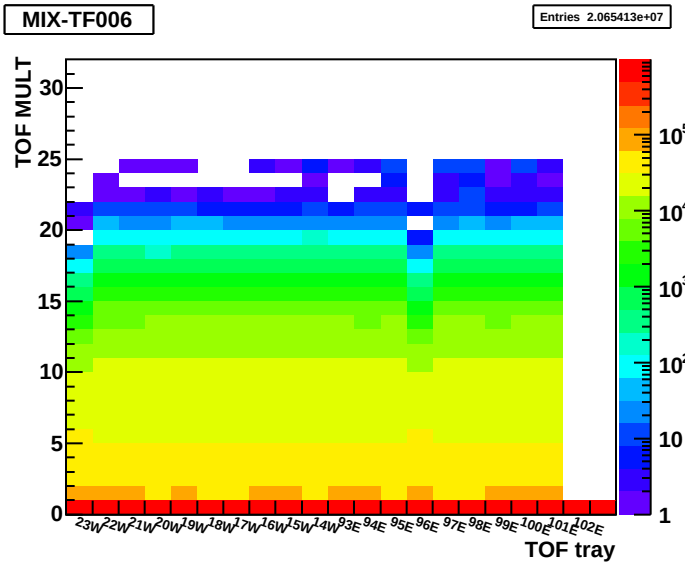

MIX-TF001
Entries 2.065413e+07
TOF MULT
TOF tray

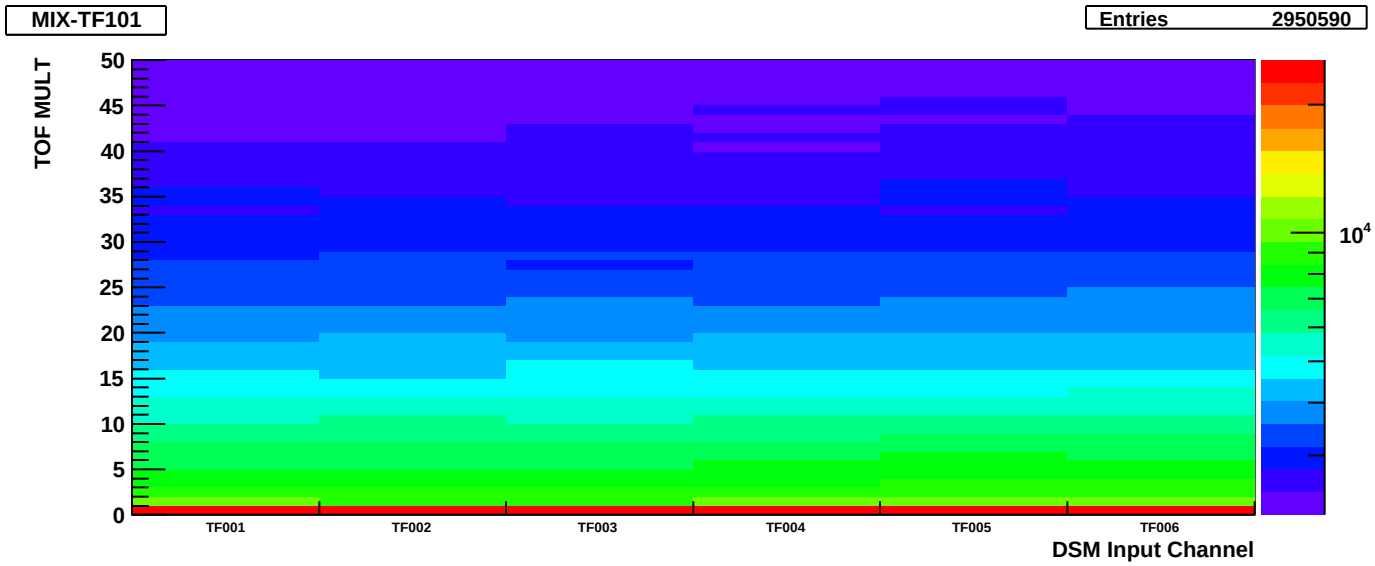

MIX-TF002
Entries 2.065413e+07
TOF MULT
TOF tray

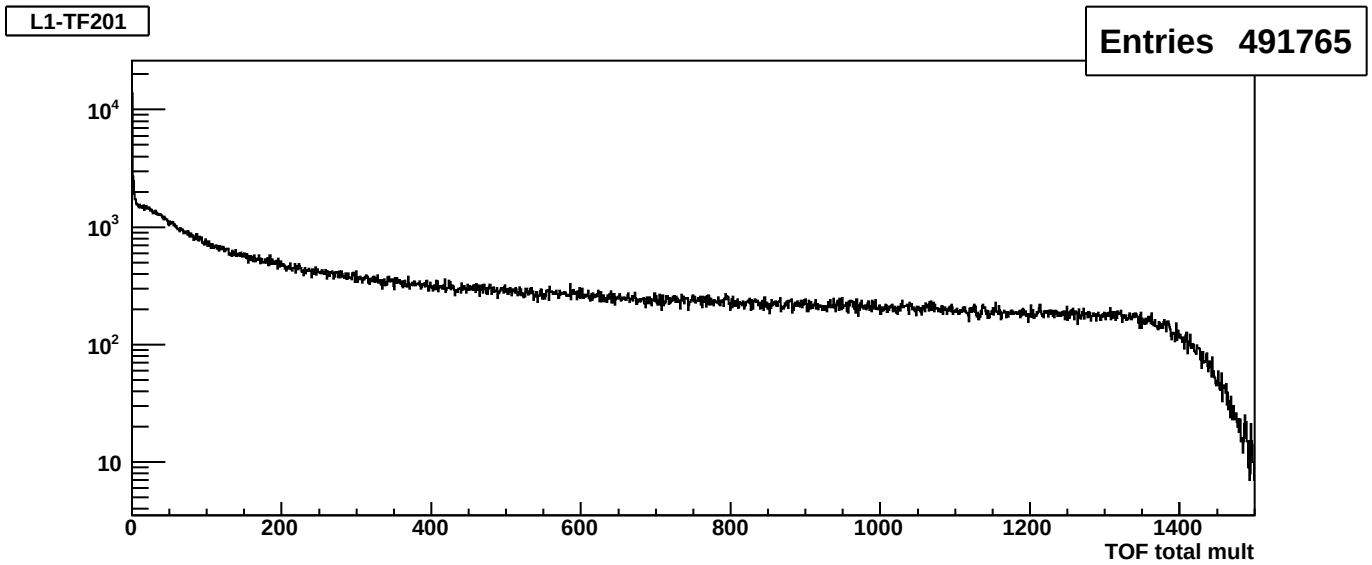

MIX-TF003
Entries 2.065413e+07
TOF MULT
TOF tray

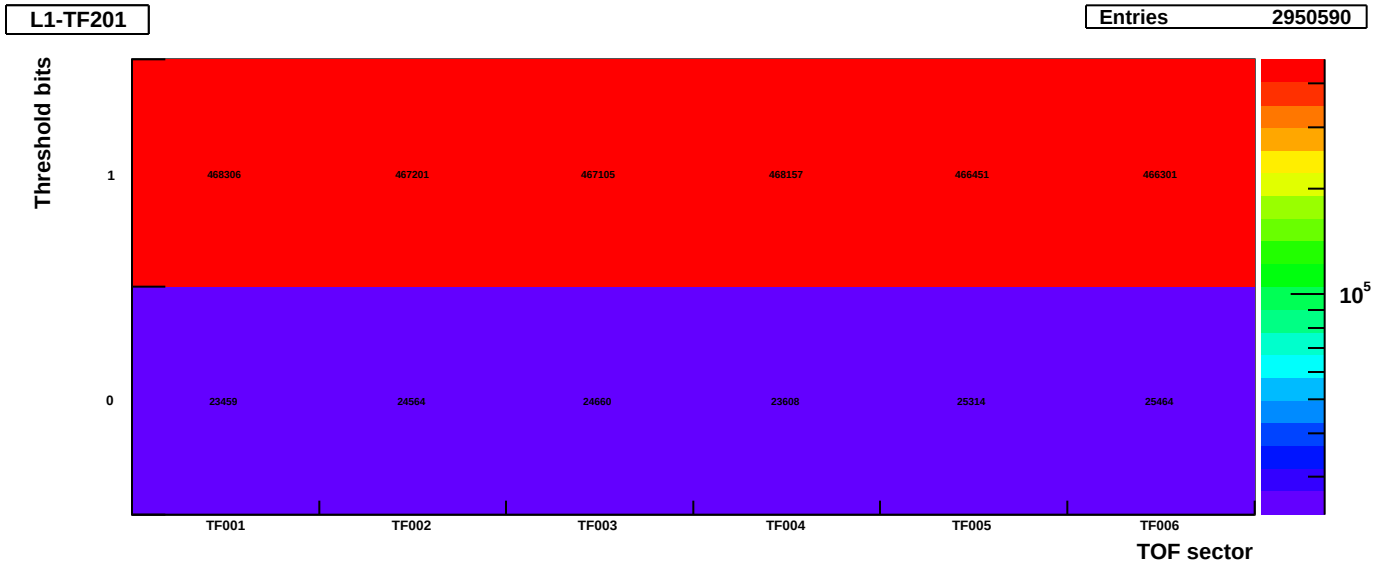

MIX-TF004
Entries 2.065413e+07
TOF MULT
TOF tray

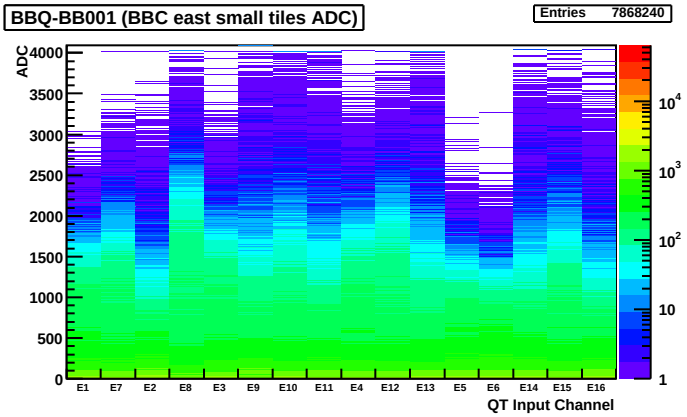

MIX-TF005
Entries 2.065413e+07
TOF MULT
TOF tray

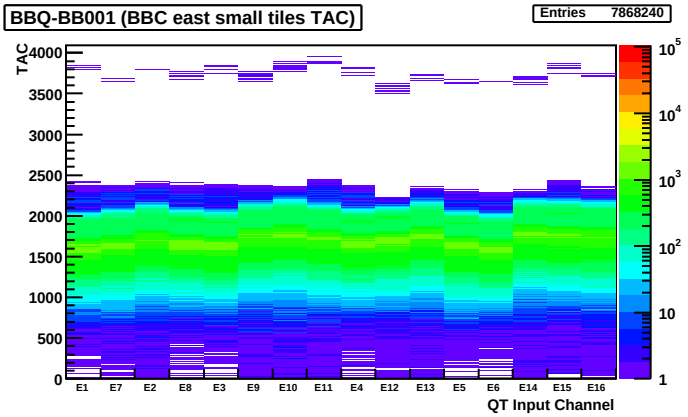

MIX-TF006
Entries 2.065413e+07
TOF MULT
TOF tray

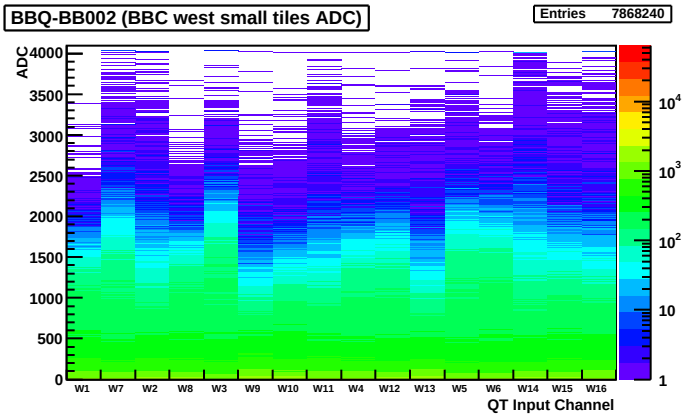

MIX-TF101
Entries 2950590
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

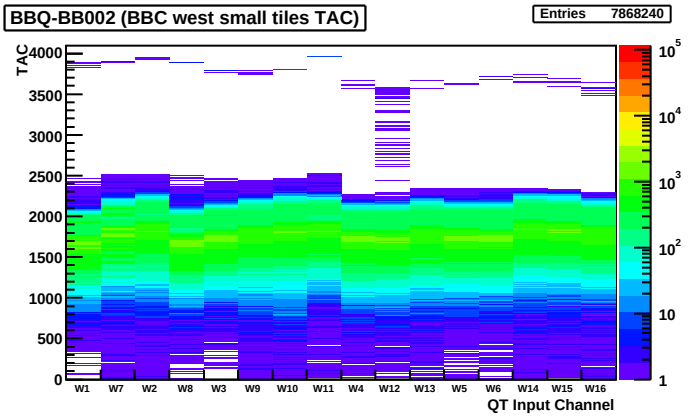

L1-TF201
Entries 491765
10^4
10^3
10^2
10
0
200
400
600
800
1000
1200
1400
TOF total mult

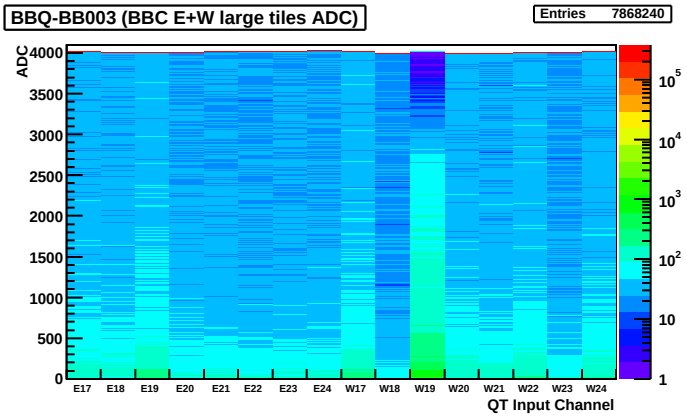

L1-TF201
Entries 2950590
Threshold bits
1
0
468306
467201
467105
468157
466451
466301
23459
24564
24660
23608
25314
25464
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

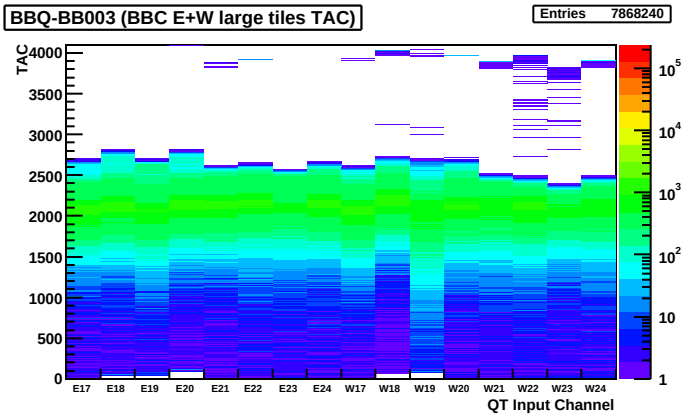

BBQ-BB001 (BBC east small tiles ADC)
Entries 7868240
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

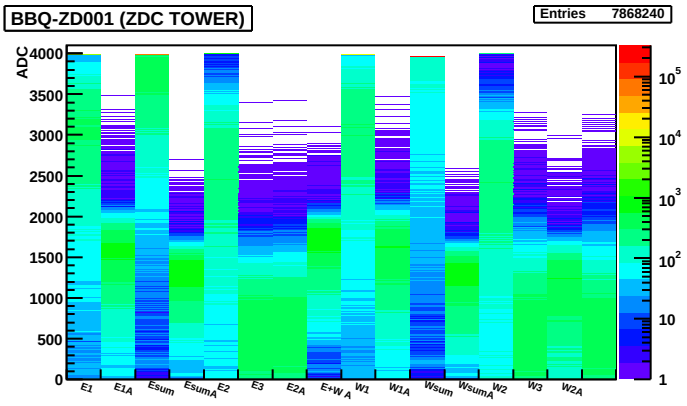

BBQ-BB001 (BBC east small tiles TAC)
Entries 7868240
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

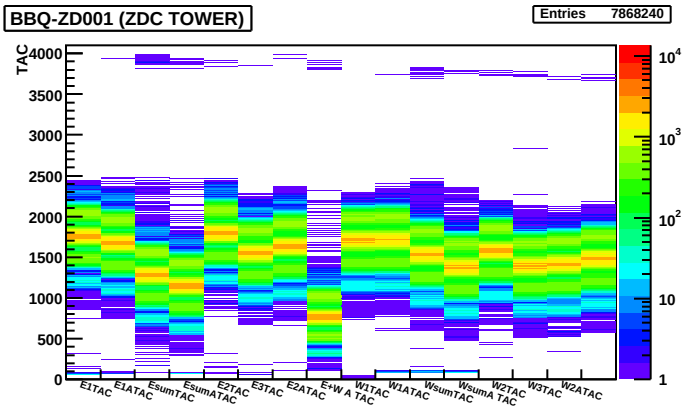

BBQ-BB002 (BBC west small tiles ADC)
Entries 7868240
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

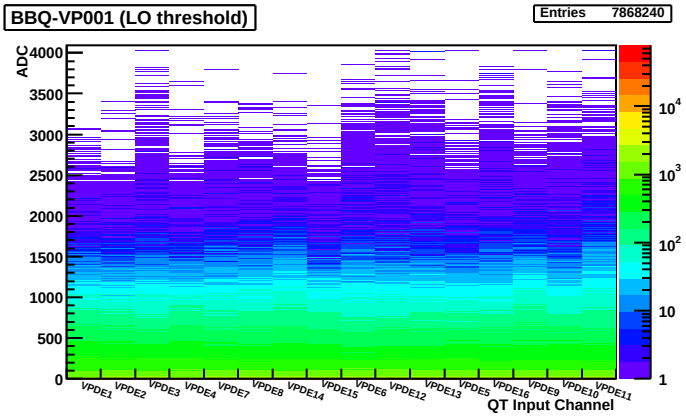

BBQ-BB002 (BBC west small tiles TAC)
Entries 7868240
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

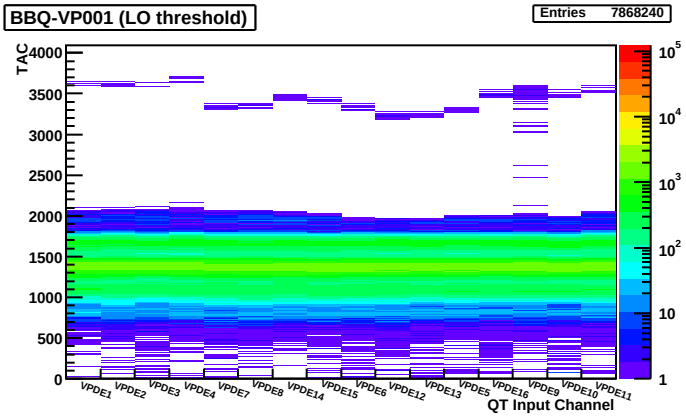

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 7868240
ADC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

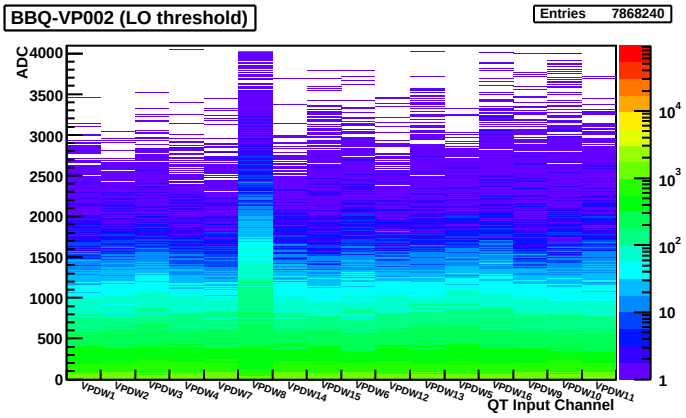

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 7868240
TAC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

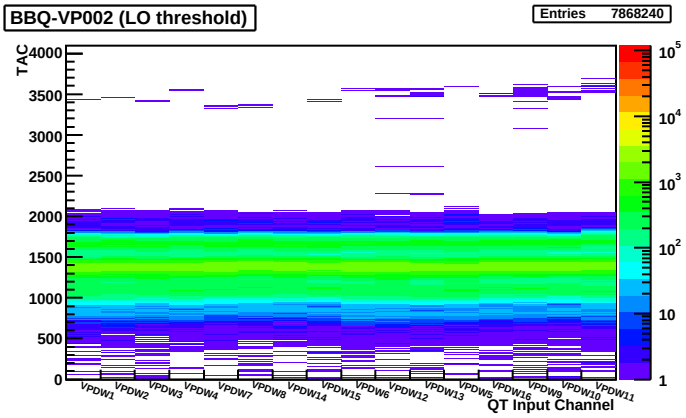

BBQ-ZD001 (ZDC TOWER)
Entries 7868240
ADC
E1 E1A Esum EsumA E2 E3 E2A E+W A W1 W1A Wsum WsumA W2 W3 W2A

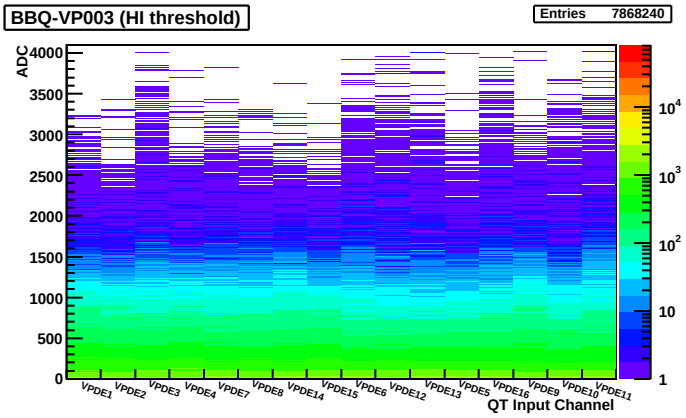

BBQ-ZD001 (ZDC TOWER)
Entries 7868240
TAC
E1TAC E1ATAC EsumTAC EsumATAC E2TAC E3TAC E2ATAC E+W A TAC W1TAC W1ATAC WsumTAC WsumA TAC W2TAC W3TAC W2ATAC

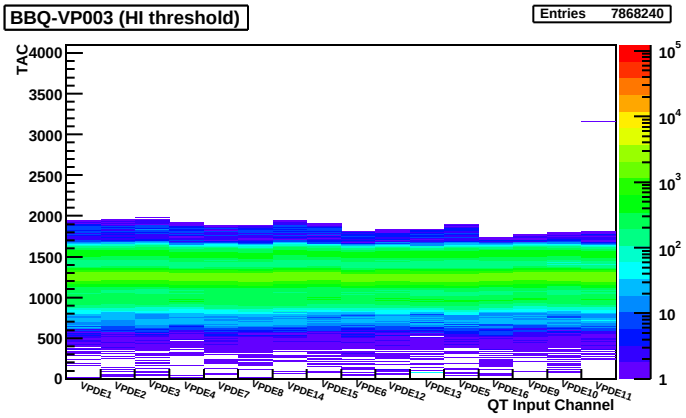

BBQ-VP001 (LO threshold)
Entries 7868240
ADC
QT Input Channel

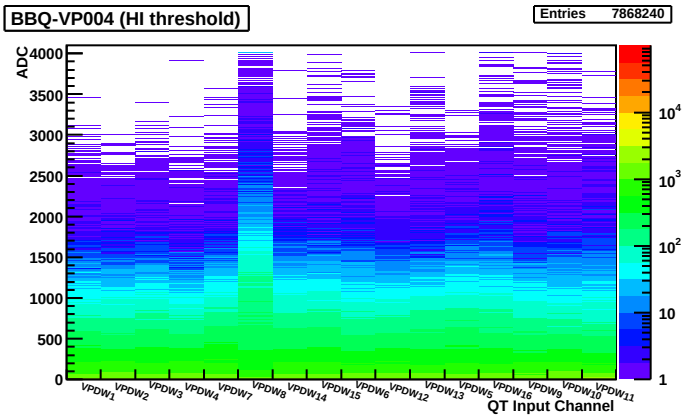

BBQ-VP001 (LO threshold)
Entries 7868240
TAC
QT Input Channel

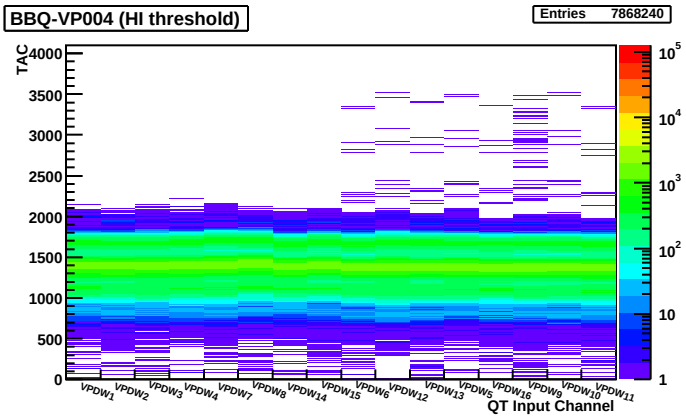

BBQ-VP002 (LO threshold)
Entries 7868240
ADC
QT Input Channel

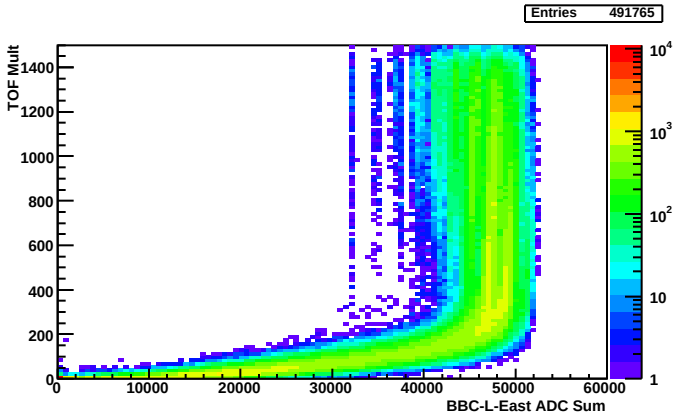

BBQ-VP002 (LO threshold)
Entries 7868240
TAC
QT Input Channel

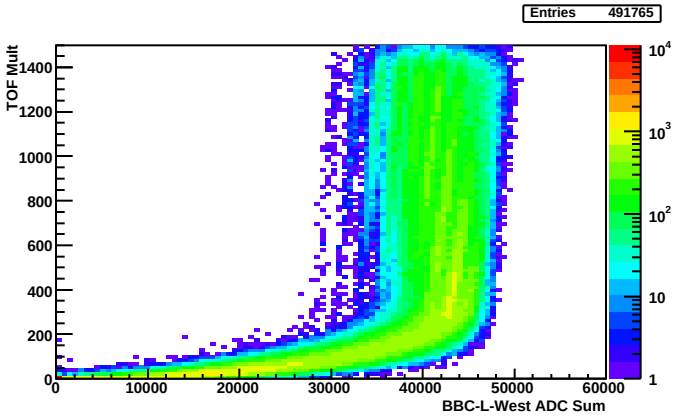

BBQ-VP003 (HI threshold)
Entries 7868240
ADC
QT Input Channel

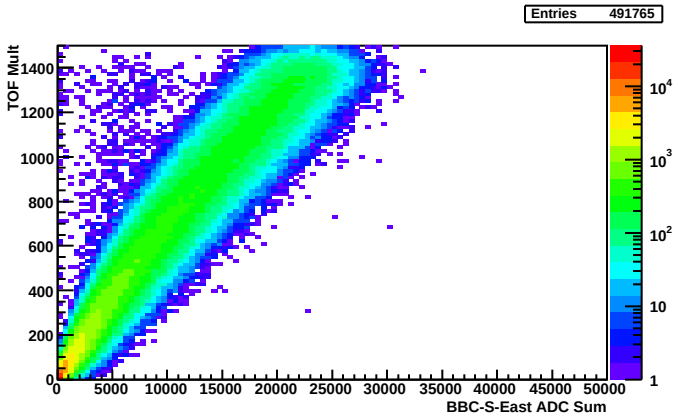

BBQ-VP003 (HI threshold)
Entries 7868240
TAC
QT Input Channel

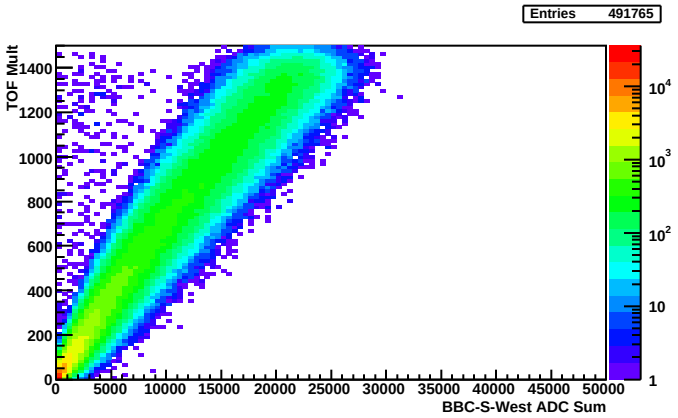

BBQ-VP004 (HI threshold)
Entries 7868240
ADC
QT Input Channel

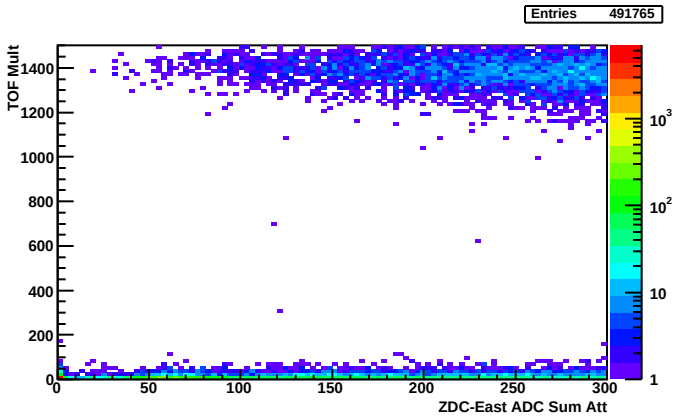

BBQ-VP004 (HI threshold)
Entries 7868240
TAC
QT Input Channel

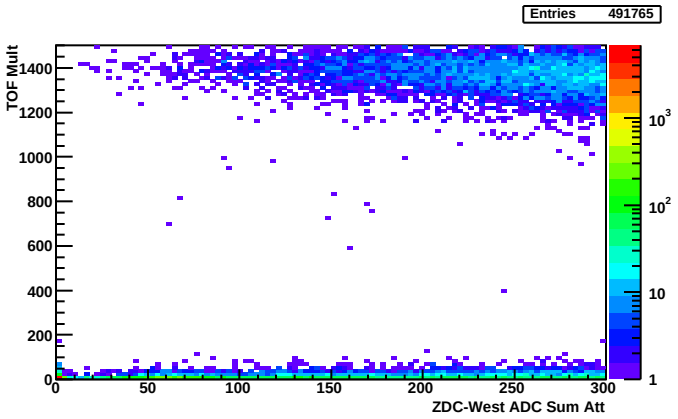

Entries 491765
TOF Mult
BBC-L-East ADC Sum

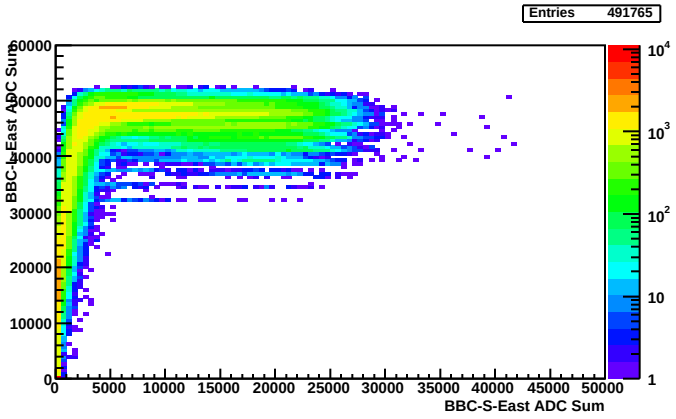

Entries 491765
TOF Mult
BBC-L-West ADC Sum

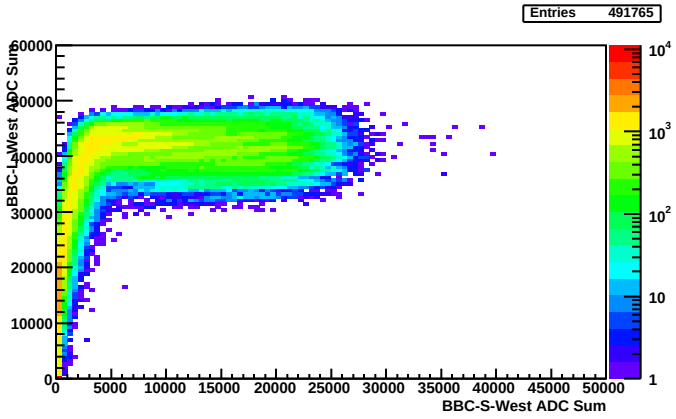

Entries 491765
TOF Mult
BBC-S-East ADC Sum

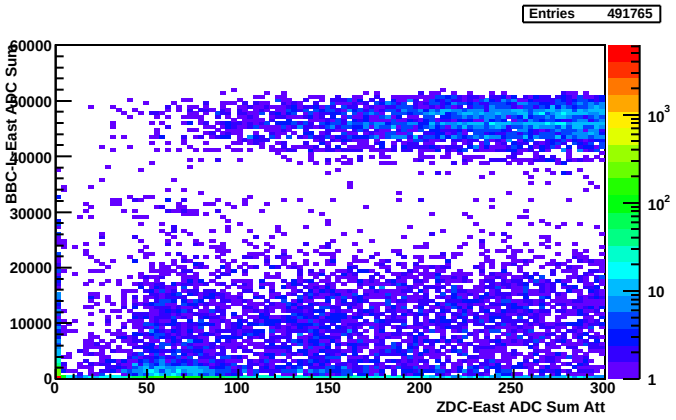

Entries 491765
TOF Mult
BBC-S-West ADC Sum

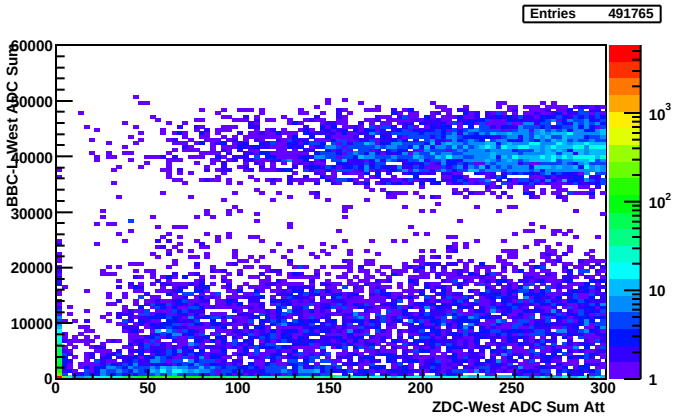

Entries 491765
TOF Mult
ZDC-East ADC Sum Att

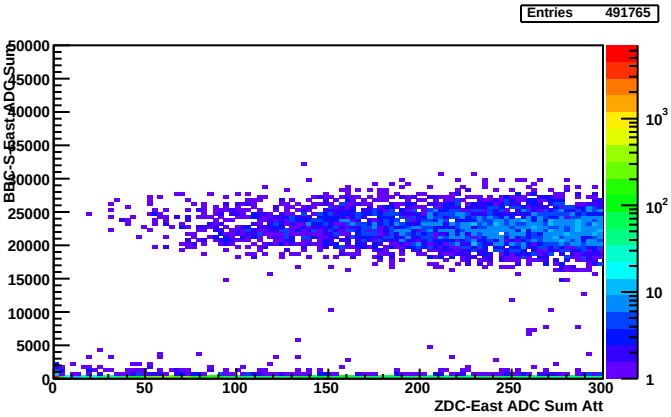

Entries 491765
TOF Mult
ZDC-West ADC Sum Att

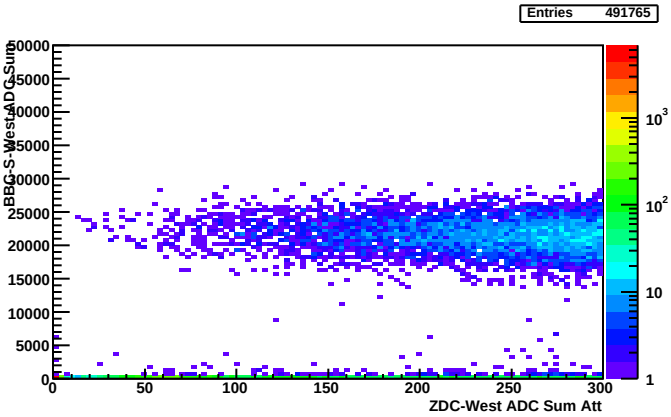

Entries 491765
BBC-L-East ADC Sum
BBC-S-East ADC Sum

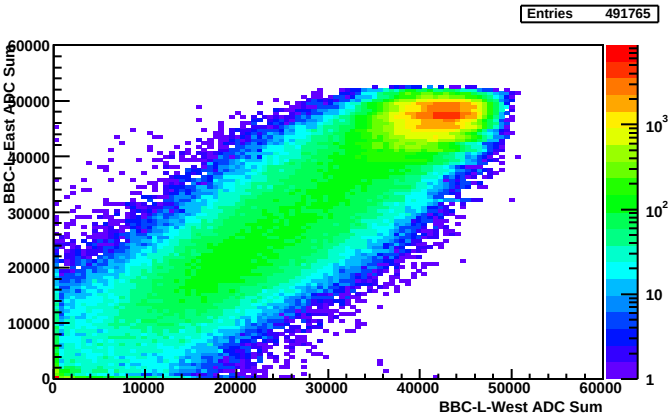

Entries 491765
BBC-L-West ADC Sum
BBC-S-West ADC Sum

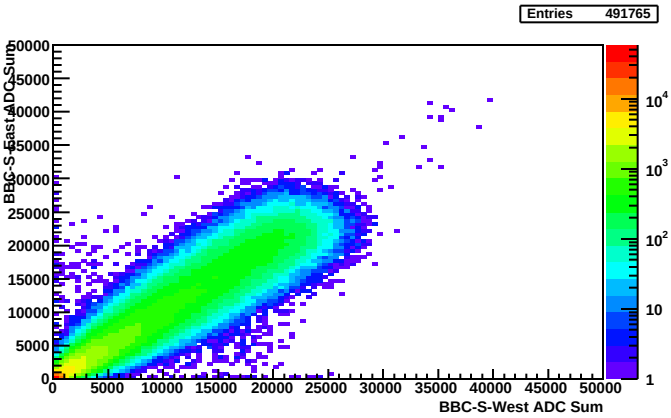

Entries 491765
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

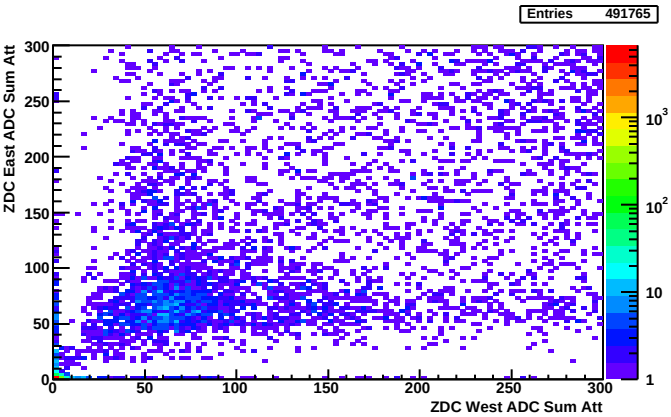

Entries 491765
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

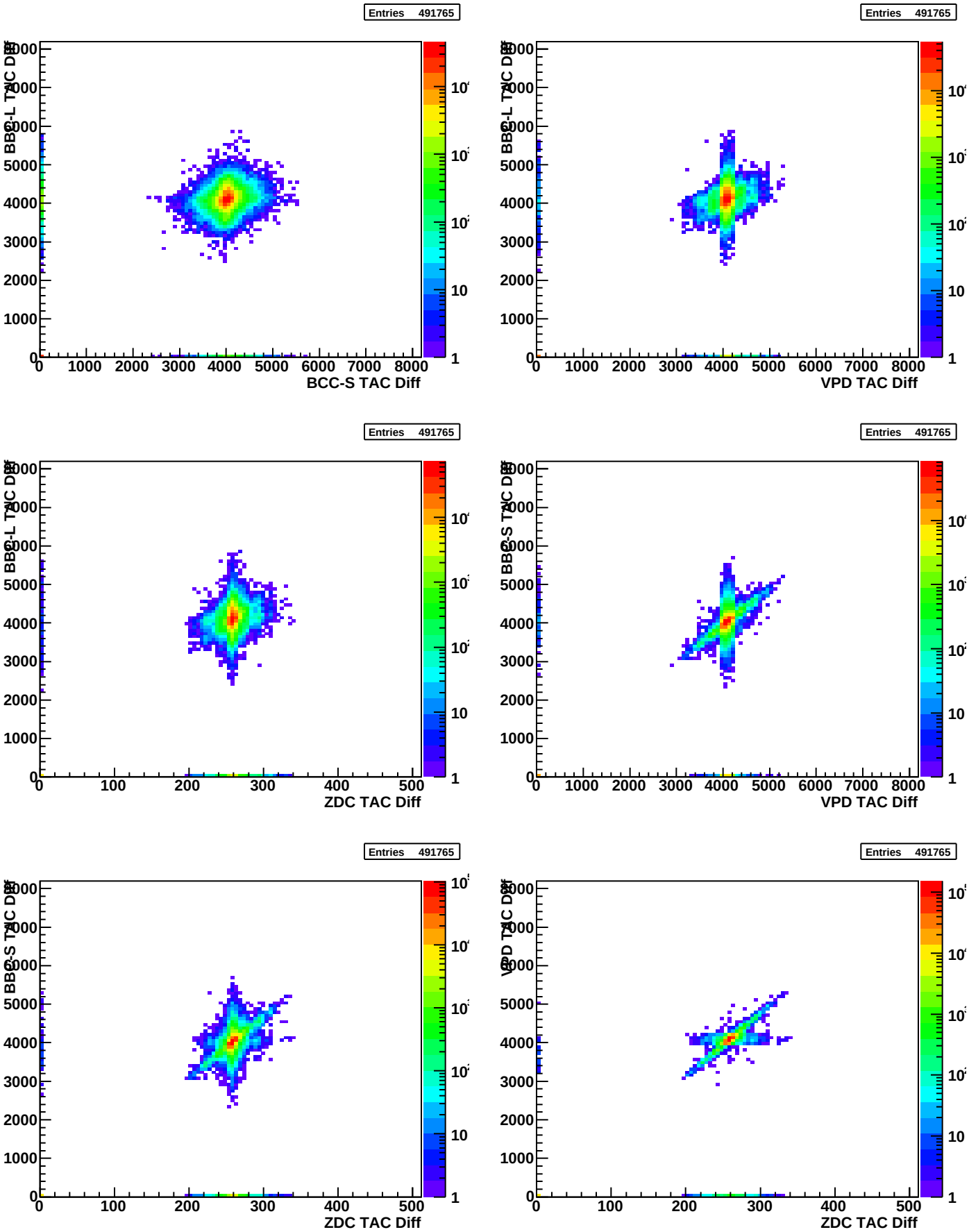

Entries 491765
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

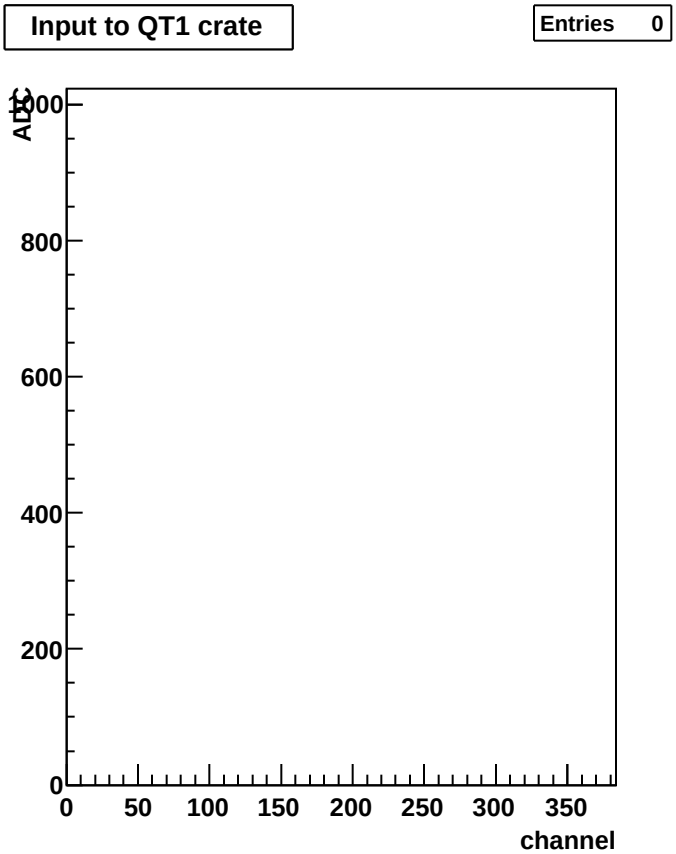

Entries 491765
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

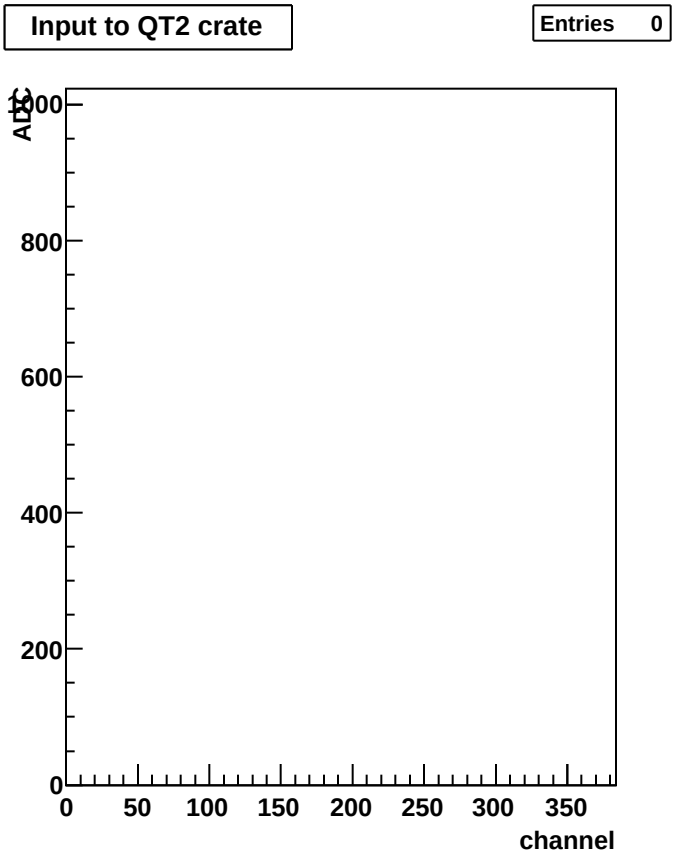

Entries 491765
BBC-L-East ADC Sum
BBC-L-West ADC Sum

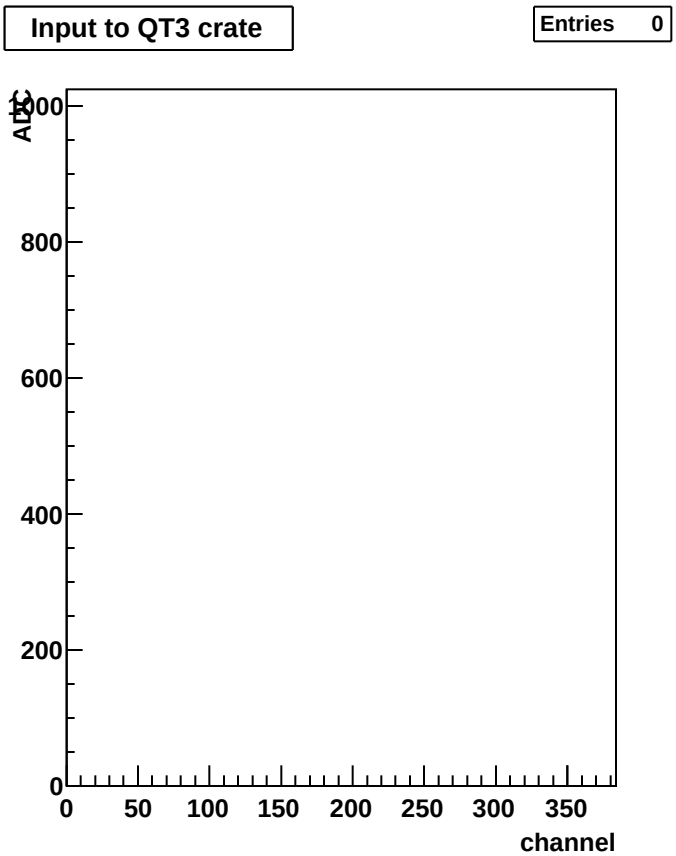

Entries 491765
BBC-S-East ADC Sum
BBC-S-West ADC Sum

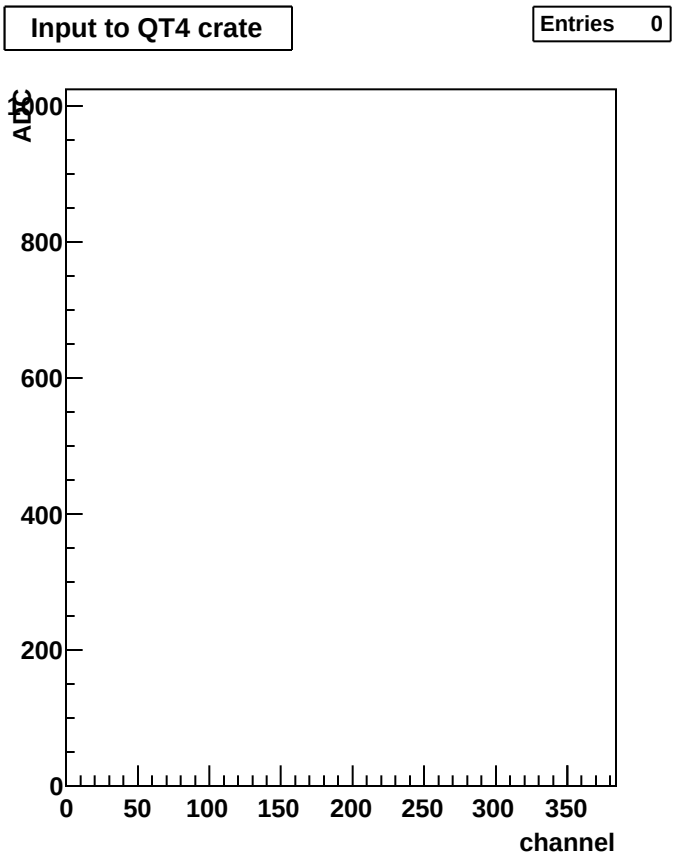

Entries 491765
ZDC East ADC Sum Att
ZDC West ADC Sum Att

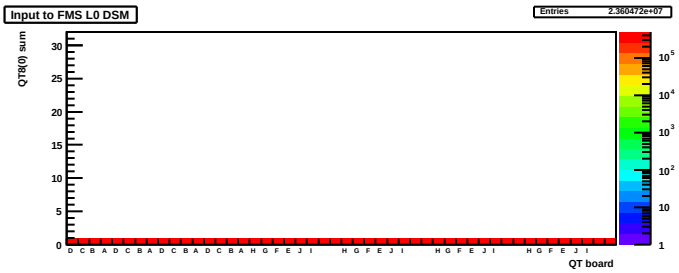

Entries 491765
BBC-L TAC Diff
BCC-S TAC Diff
Entries 491765
BBC-L TAC Diff
VPD TAC Diff
Entries 491765
BBC-L TAC Diff
ZDC TAC Diff
Entries 491765
BBC-S TAC Diff
VPD TAC Diff
Entries 491765
BBC-S TAC Diff
ZDC TAC Diff
Entries 491765
VPD TAC Diff
ZDC TAC Diff

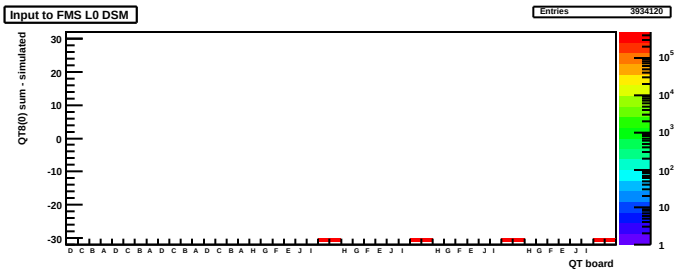

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

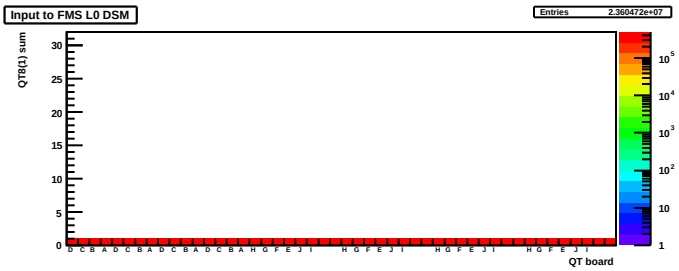

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

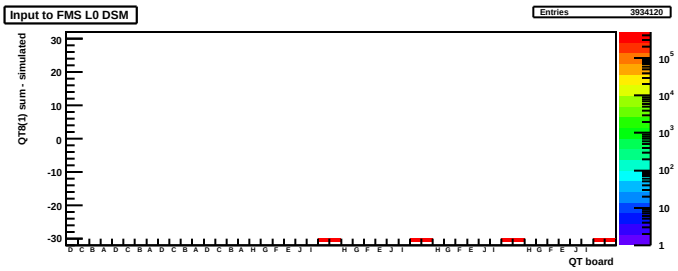

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

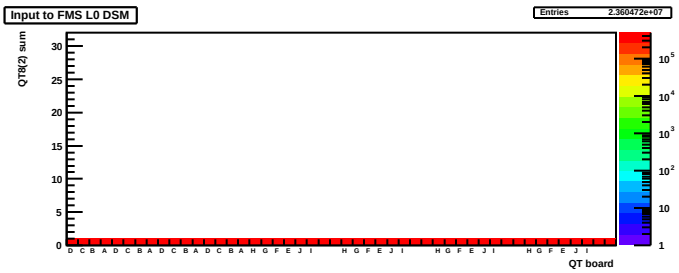

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

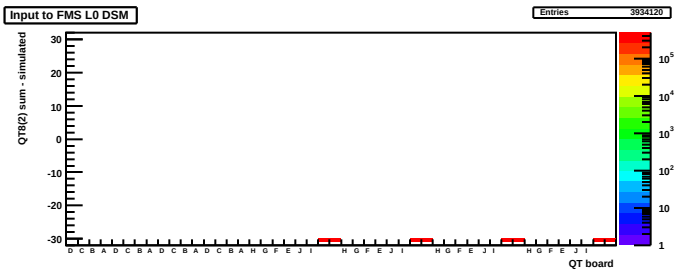

Input to FMS L0 DSM
Entries 2.360472e+07
QT8(0) sum
QT board

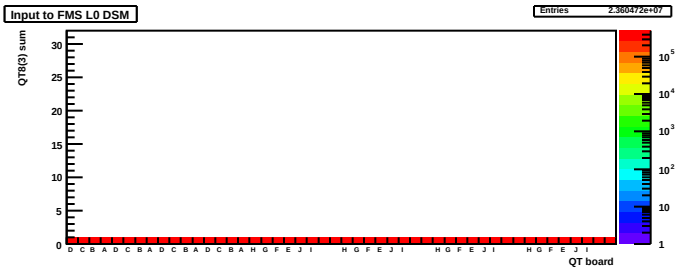

Input to FMS L0 DSM
Entries 3934120
QT8(0) sum - simulated
QT board

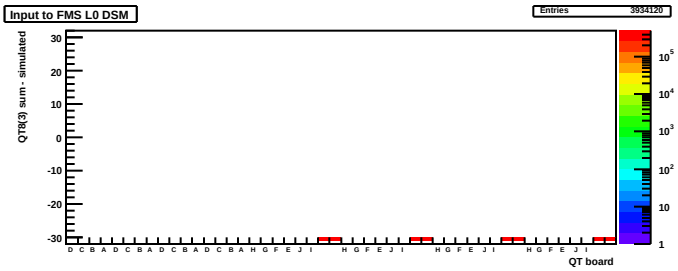

Input to FMS L0 DSM
Entries 2.360472e+07
QT8(1) sum
QT board

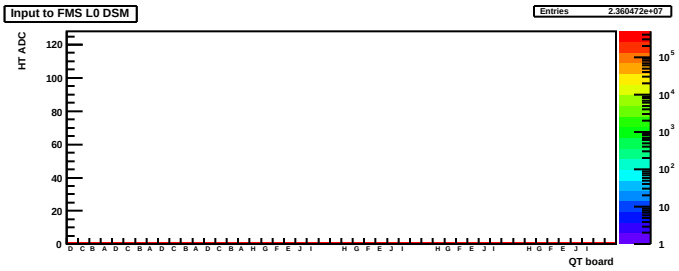

Input to FMS L0 DSM
Entries 3934120
QT8(1) sum - simulated
QT board

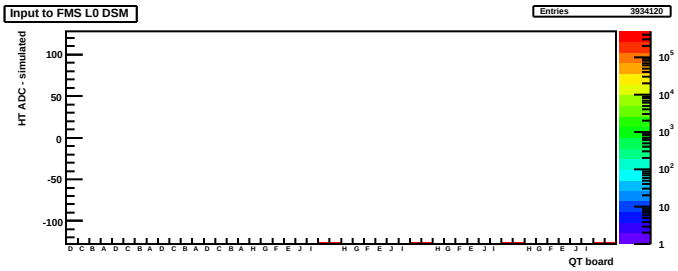

Input to FMS L0 DSM
Entries 2.360472e+07
QT8(2) sum
QT board

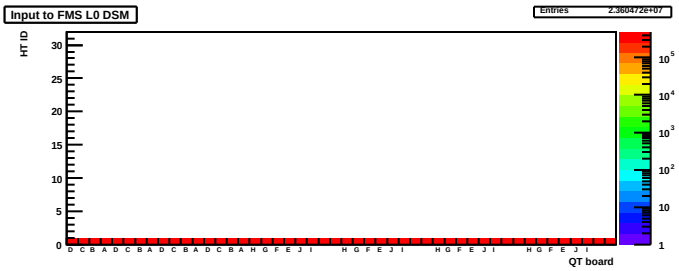

Input to FMS L0 DSM
Entries 3934120
QT8(2) sum - simulated
QT board

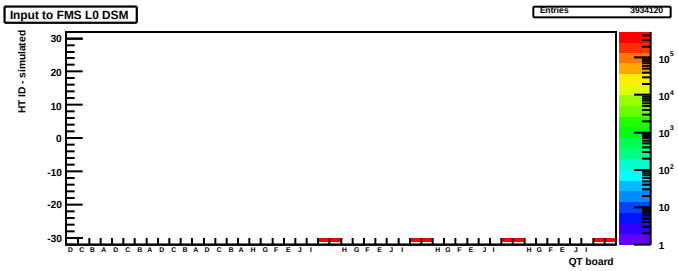

Input to FMS L0 DSM
Entries 2.360472e+07
QT8(3) sum
QT board

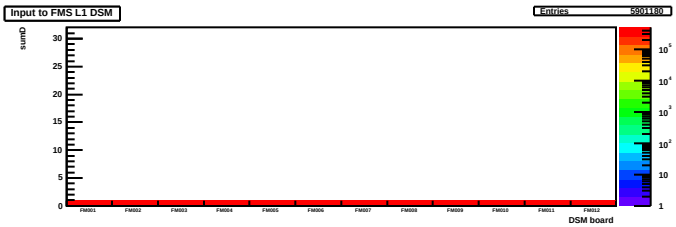

Input to FMS L0 DSM
Entries 3934120
QT8(3) sum - simulated
QT board

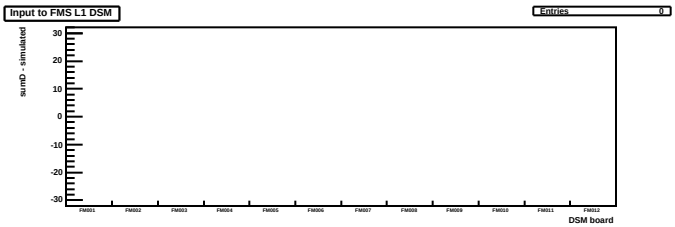

Input to FMS L0 DSM
Entries 2.360472e+07
HT ADC
QT board

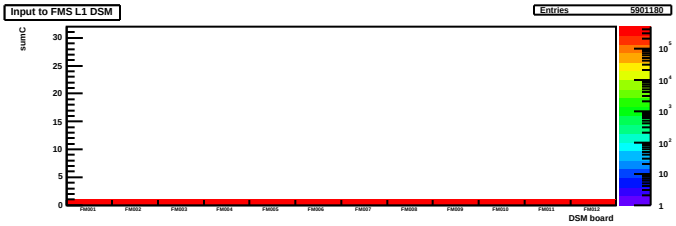

Input to FMS L0 DSM
Entries 3934120
HT ADC - simulated
QT board

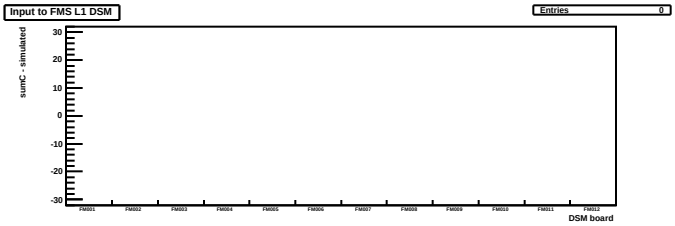

Input to FMS L0 DSM
Entries 2.360472e+07
HT ID
QT board

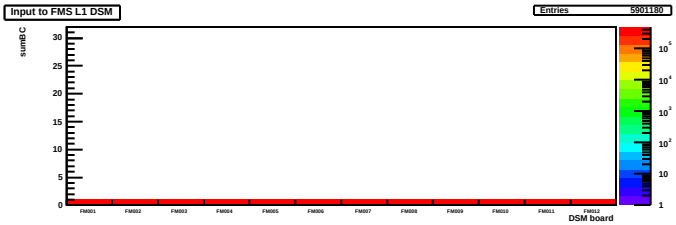

Input to FMS L0 DSM
Entries 3934120
HT ID - simulated
QT board

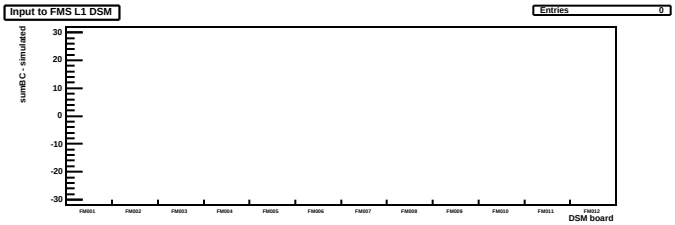

Input to FMS L1 DSM
Entries 5901180
sumD
DSM board

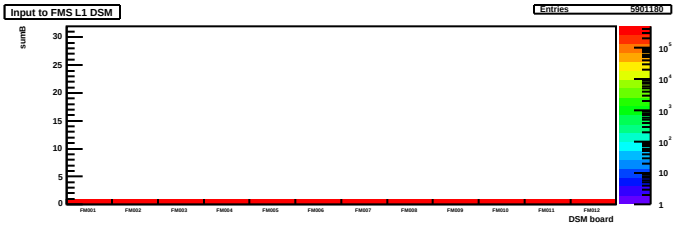

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

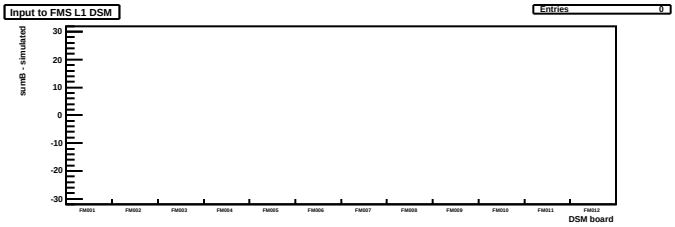

Input to FMS L1 DSM
Entries 5901180
sumC
DSM board

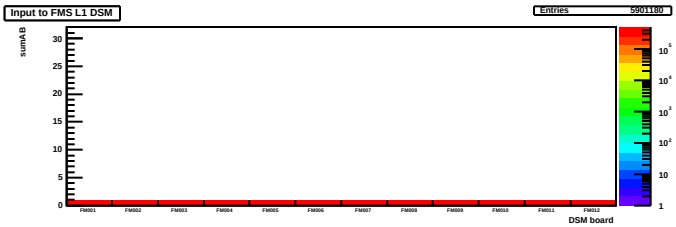

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

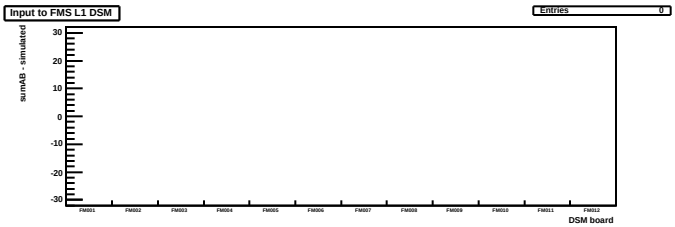

Input to FMS L1 DSM
Entries 5901180
sumBC
DSM board

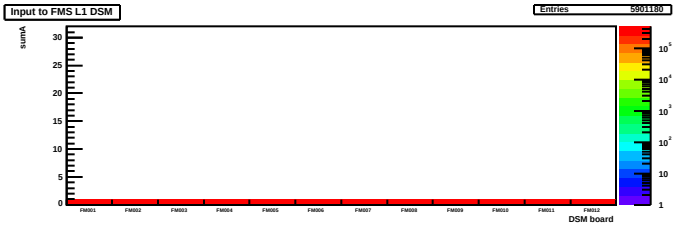

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

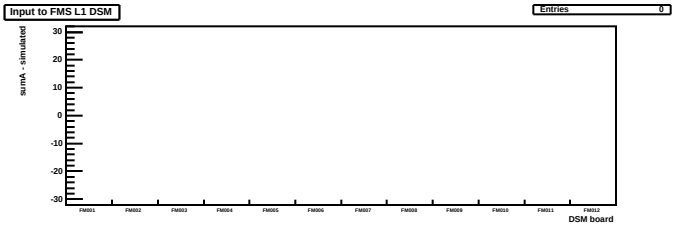

Input to FMS L1 DSM
Entries 5901180
sumB
DSM board

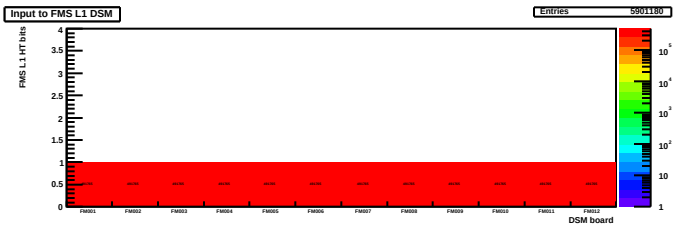

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

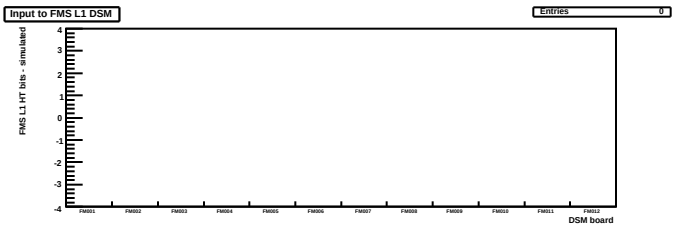

Input to FMS L1 DSM
Entries 5901180
sumAB
DSM board

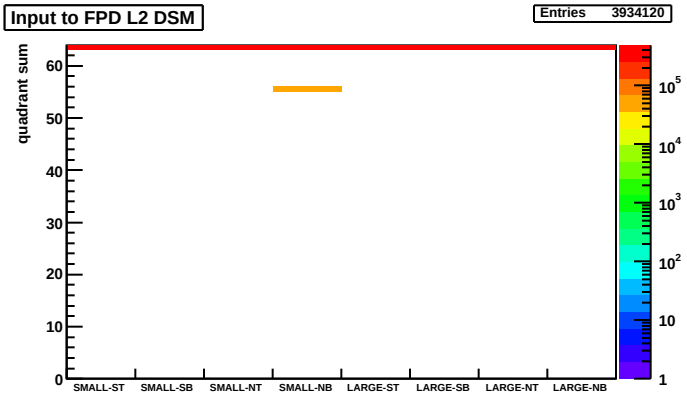

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

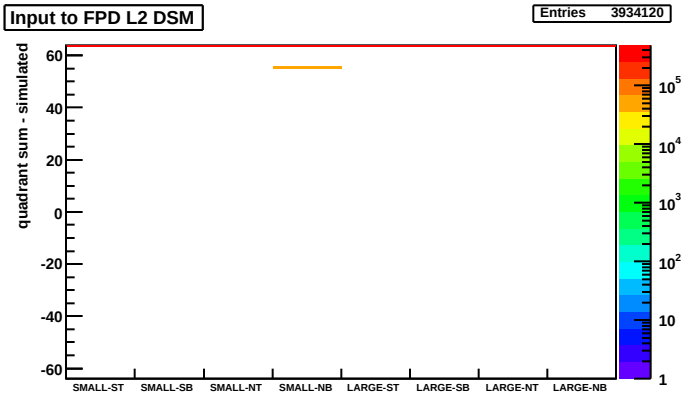

Input to FMS L1 DSM
Entries 5901180
sumA
DSM board

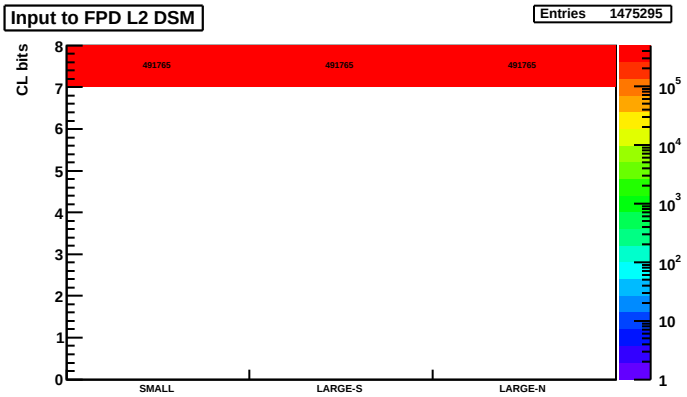

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

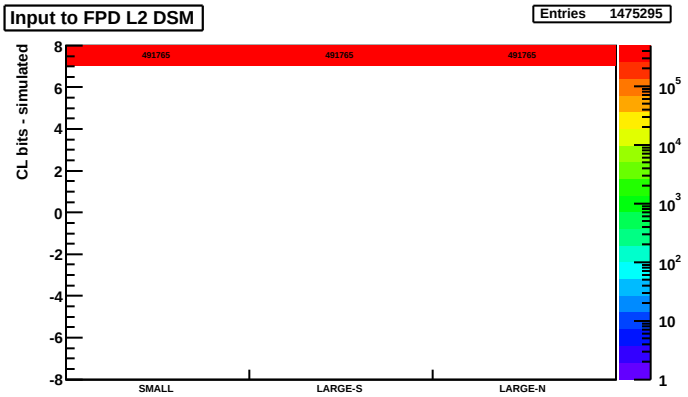

Input to FMS L1 DSM
Entries 5901180
FMS L1 HT bits
DSM board

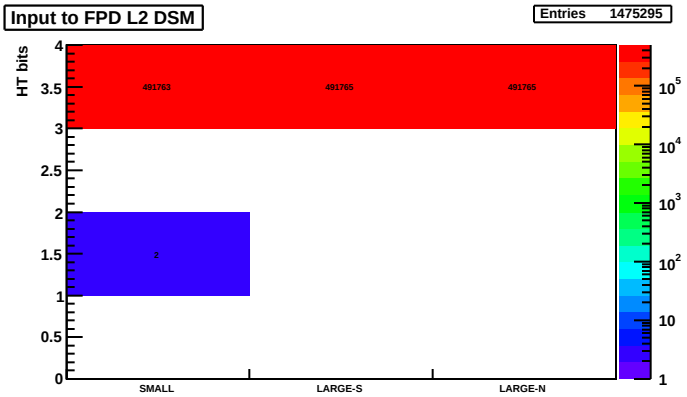

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

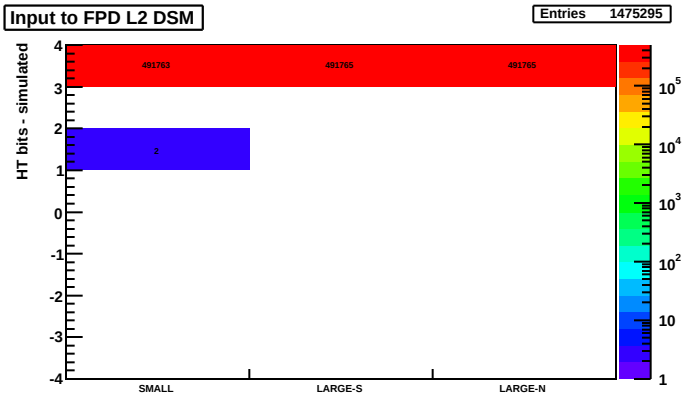

Input to FPD L2 DSM
Entries 3934120
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

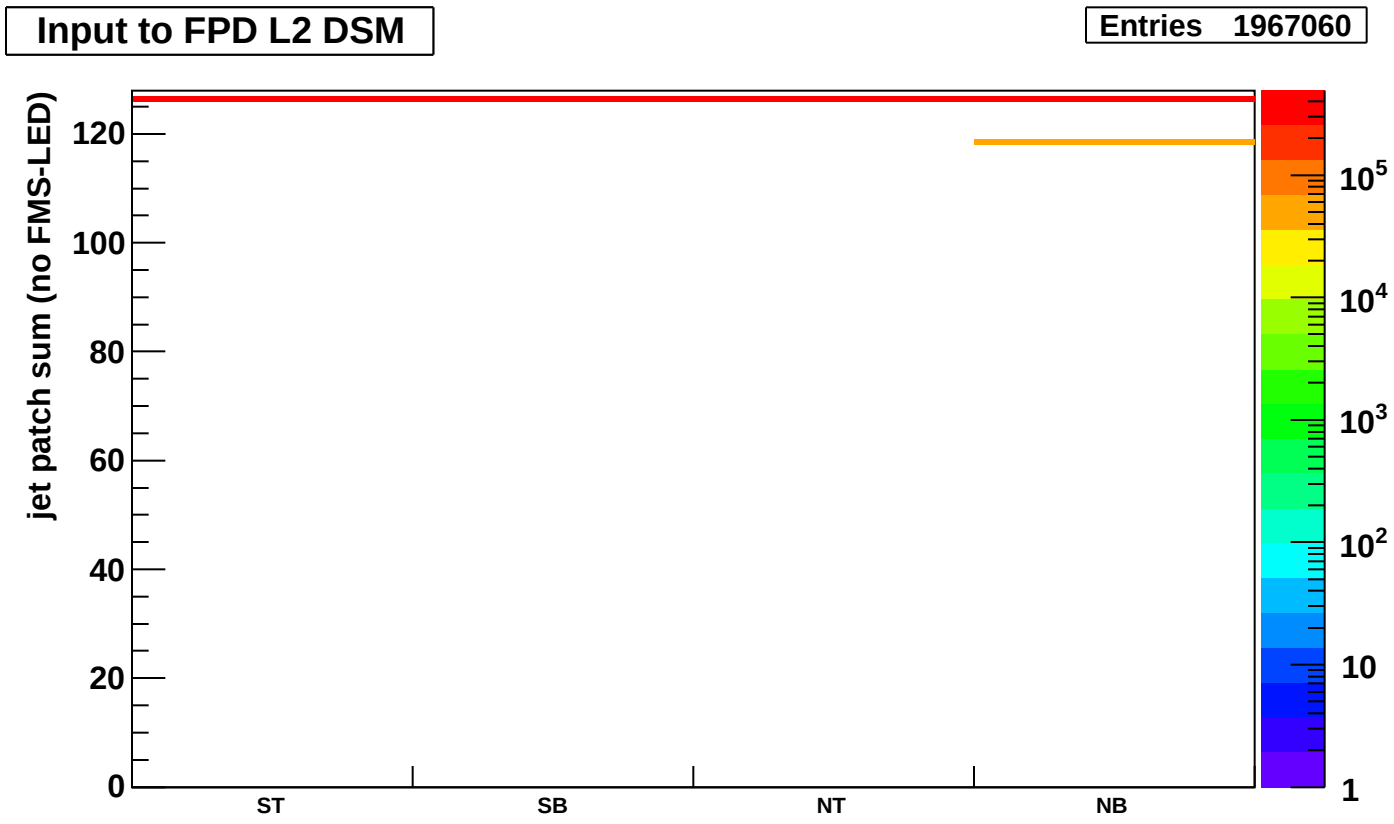

Input to FPD L2 DSM
Entries 3934120
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

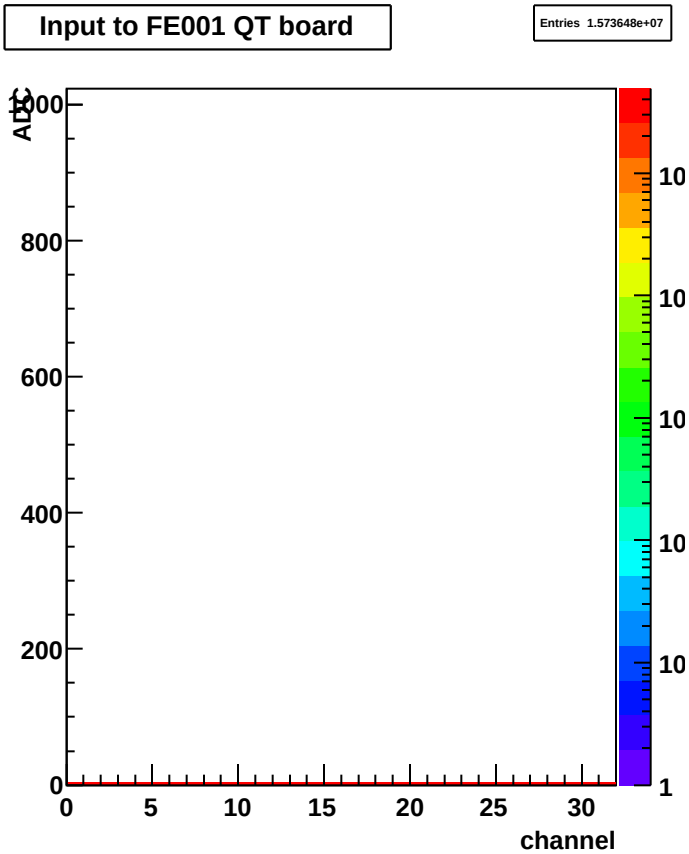

Input to FPD L2 DSM
Entries 1475295
CL bits
491765
491765
491765
SMALL
LARGE-S
LARGE-N

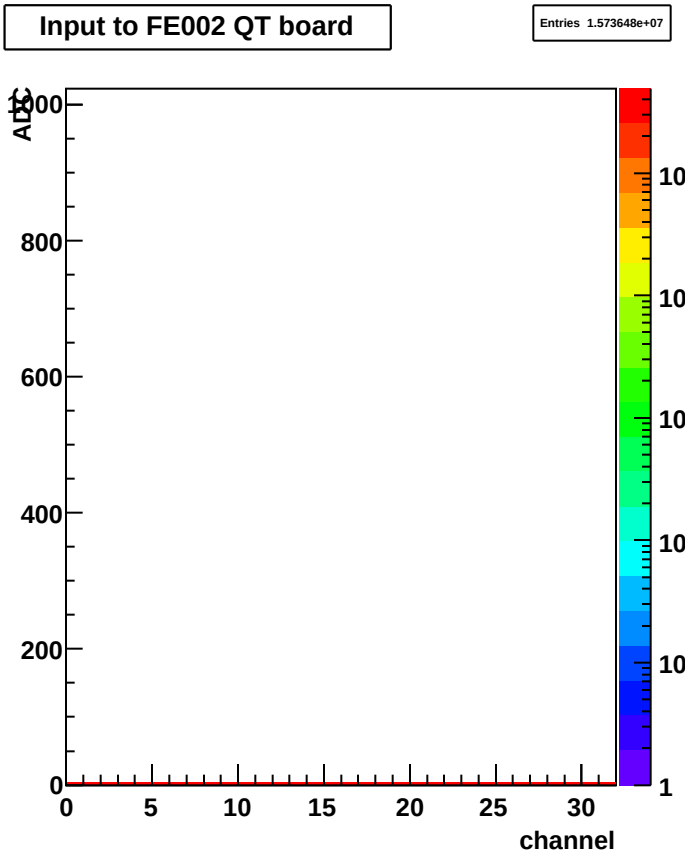

Input to FPD L2 DSM
Entries 1475295
CL bits - simulated
491765
491765
491765
SMALL
LARGE-S
LARGE-N

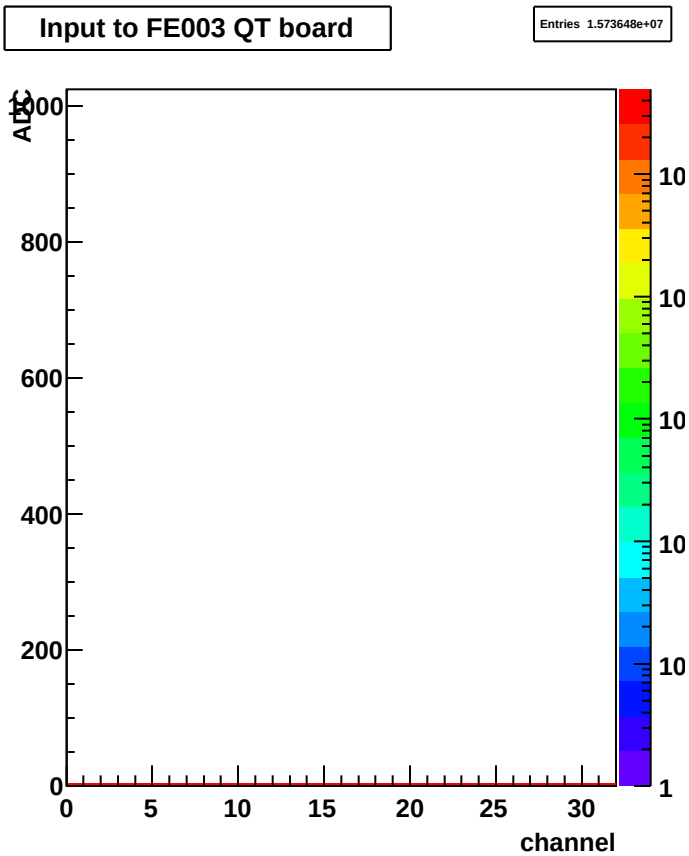

Input to FPD L2 DSM
Entries 1475295
HT bits
491763
491765
491765
2
SMALL
LARGE-S
LARGE-N

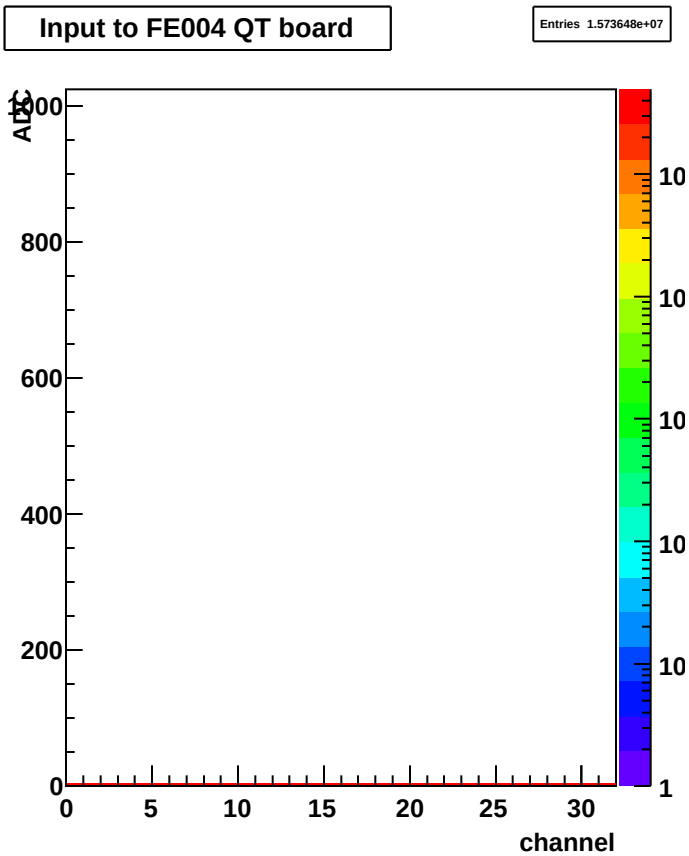

Input to FPD L2 DSM
Entries 1475295
HT bits - simulated
491763
491765
491765
2
SMALL
LARGE-S
LARGE-N

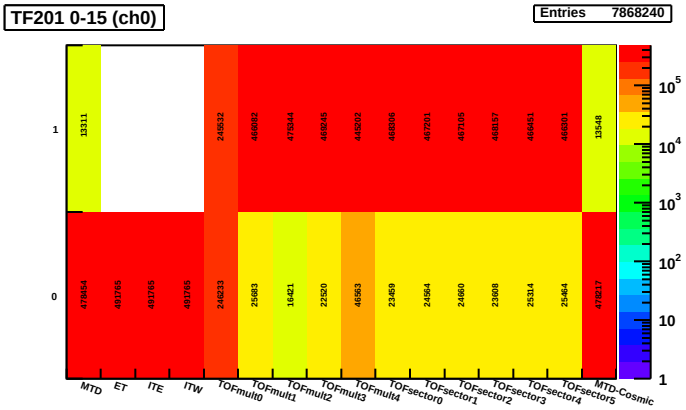

Input to FPD L2 DSM
Entries 1967060
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

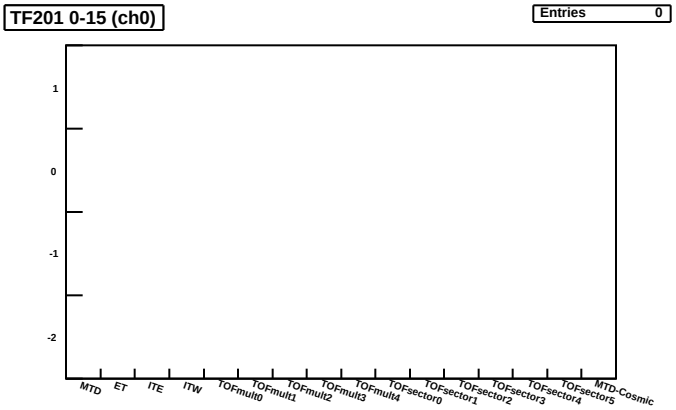

Input to FE001 QT board
Entries 1.573648e+07
ADC
channel

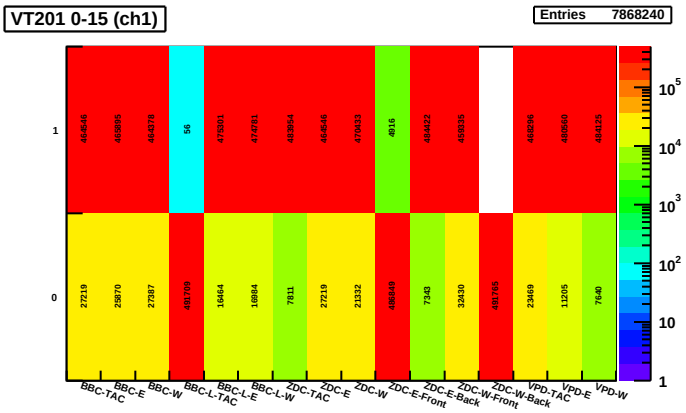

Input to FE002 QT board
Entries 1.573648e+07
ADC
channel

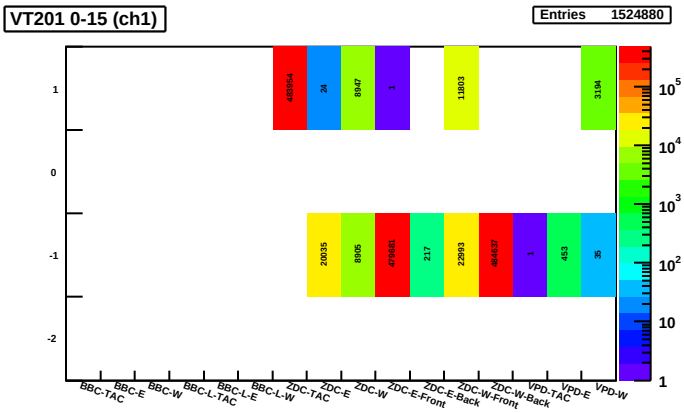

Input to FE003 QT board
Entries 1.573648e+07
ADC
channel

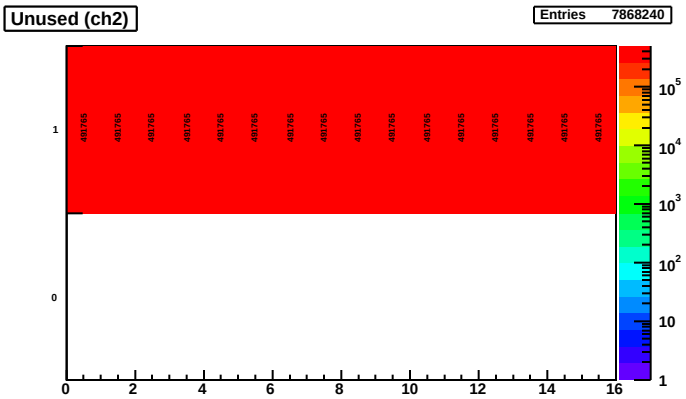

Input to FE004 QT board
Entries 1.573648e+07
ADC
channel

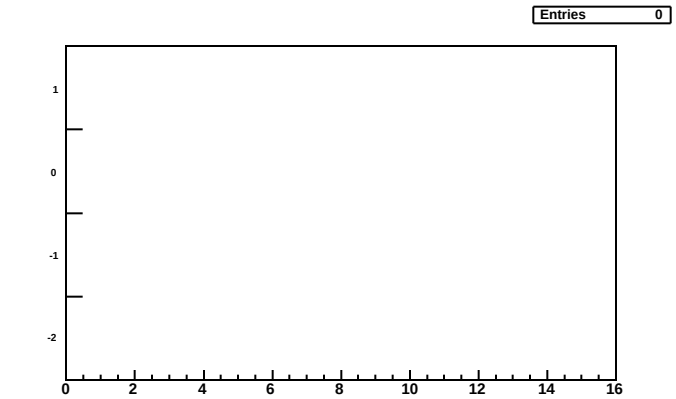

TF201 0-15 (ch0)
Entries 7868240
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

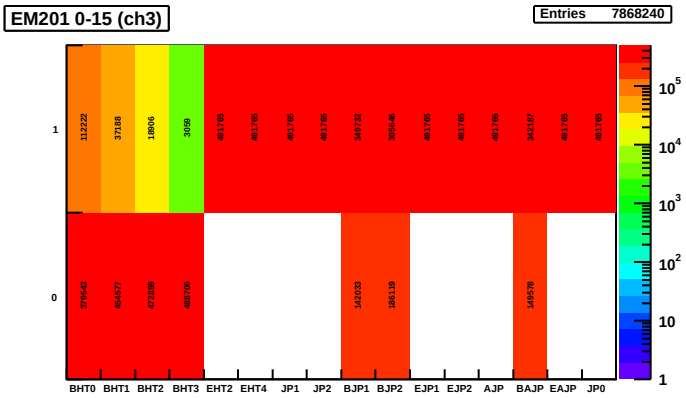

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

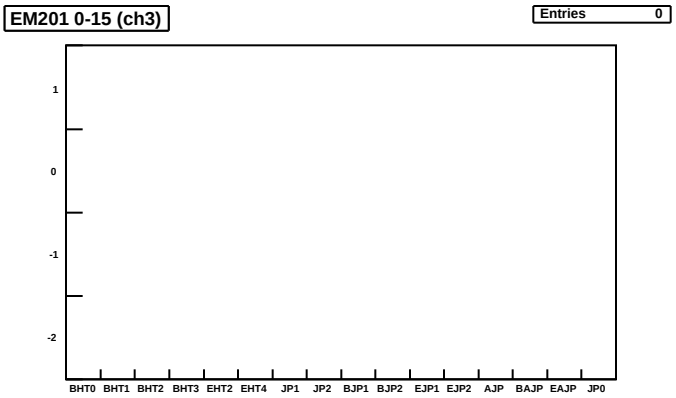

VT201 0-15 (ch1)
Entries 7868240
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

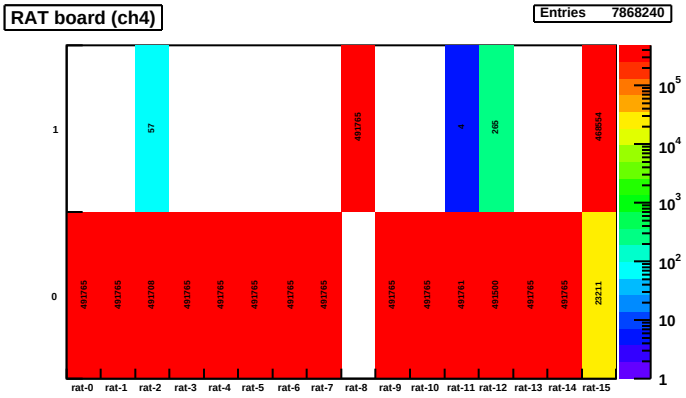

VT201 0-15 (ch1)
Entries 1524880
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

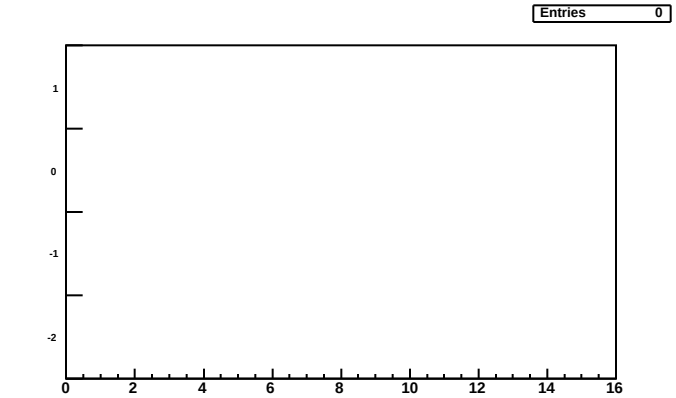

Unused (ch2)
Entries 7868240

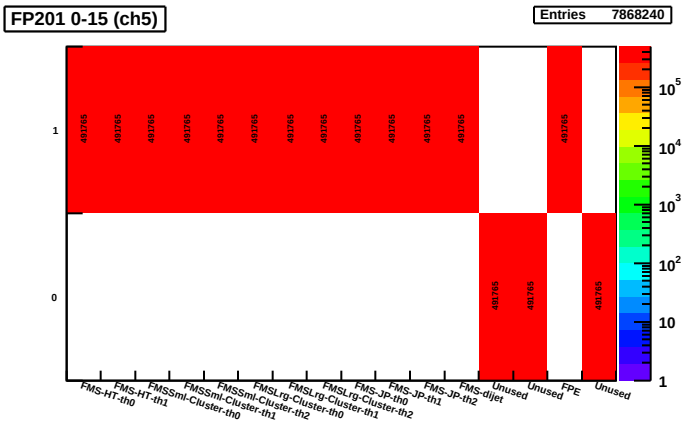

Entries 0

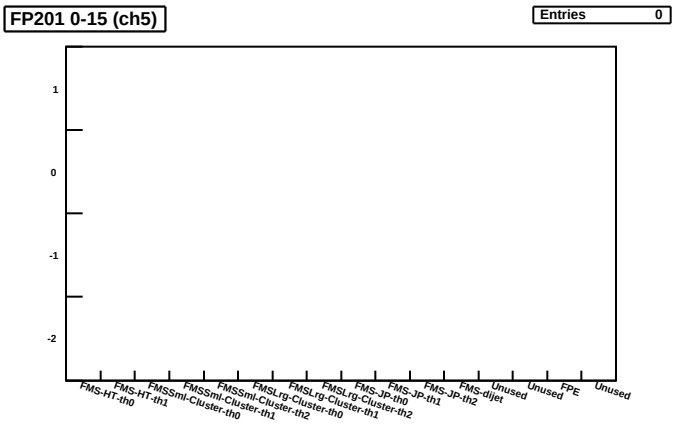

EM201 0-15 (ch3)
Entries 7868240
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

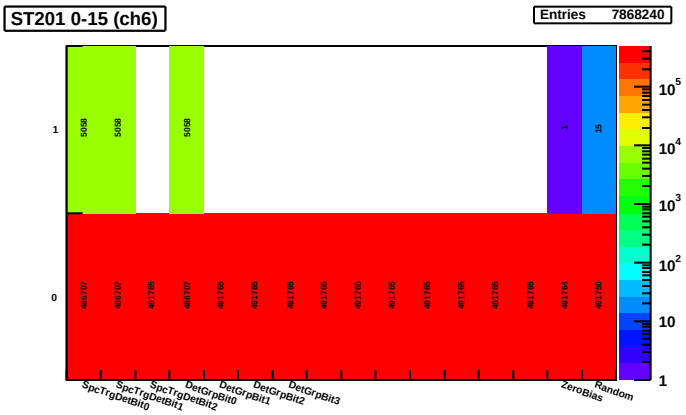

EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

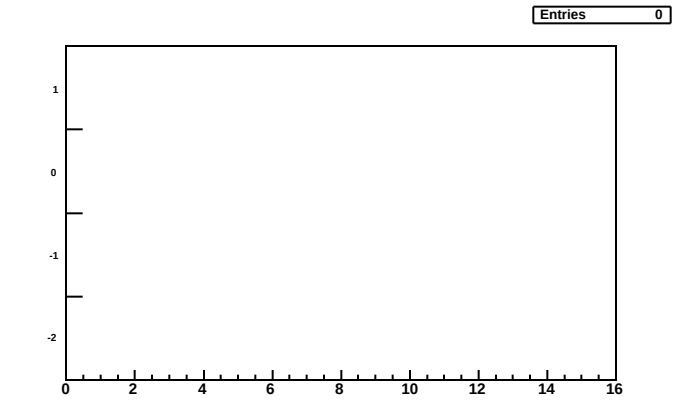

RAT board (ch4)
Entries 7868240
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

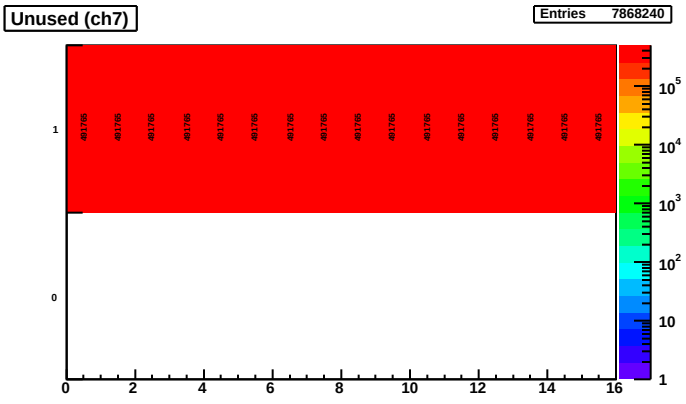

Entries 0

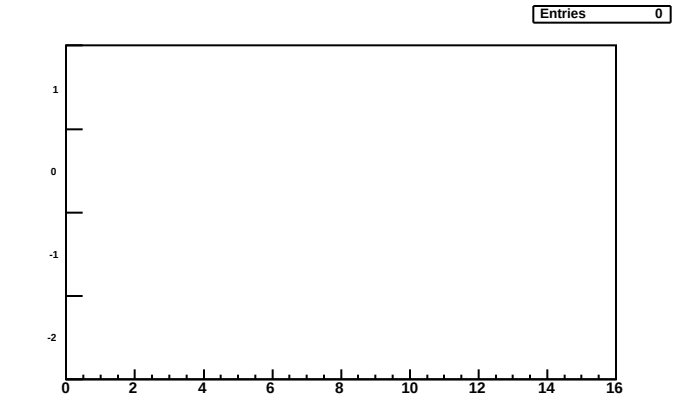

FP201 0-15 (ch5)
Entries 7868240
FMS-HT-th0 FMS-HT-th1 FMSSml-Cluster-th0 FMSSml-Cluster-th1 FMSSml-Cluster-th2 FMSLrg-Cluster-th0 FMSLrg-Cluster-th1 FMSLrg-Cluster-th2 FMS-JP-th0 FMS-JP-th1 FMS-JP-th2 FMS-diJet Unused Unused FPE Unused


FP201 0-15 (ch5)
Entries 0
FMS-HT-th0 FMS-HT-th1 FMSSml-Cluster-th0 FMSSml-Cluster-th1 FMSSml-Cluster-th2 FMSLrg-Cluster-th0 FMSLrg-Cluster-th1 FMSLrg-Cluster-th2 FMS-JP-th0 FMS-JP-th1 FMS-JP-th2 FMS-diJet Unused Unused FPE Unused


ST201 0-15 (ch6)
Entries 7868240
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random


Entries 0


Unused (ch7)
Entries 7868240


Entries 0