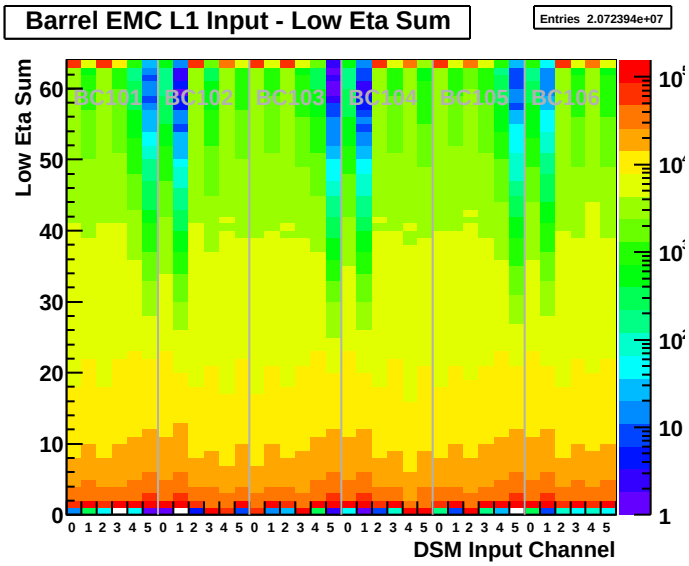
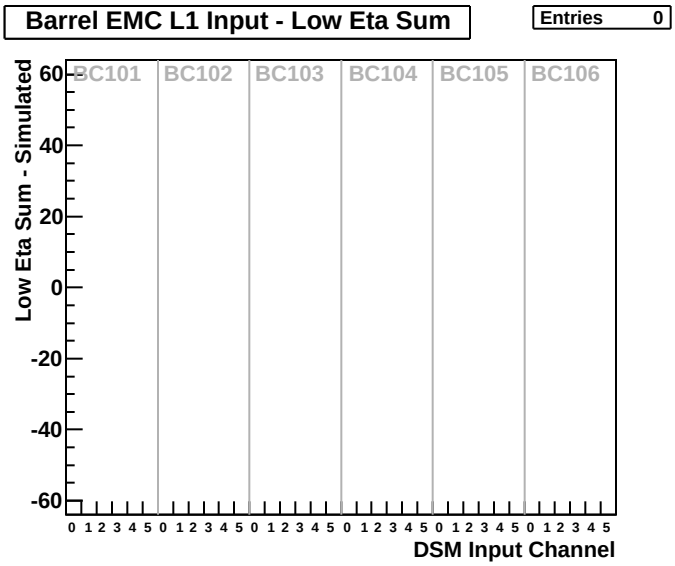
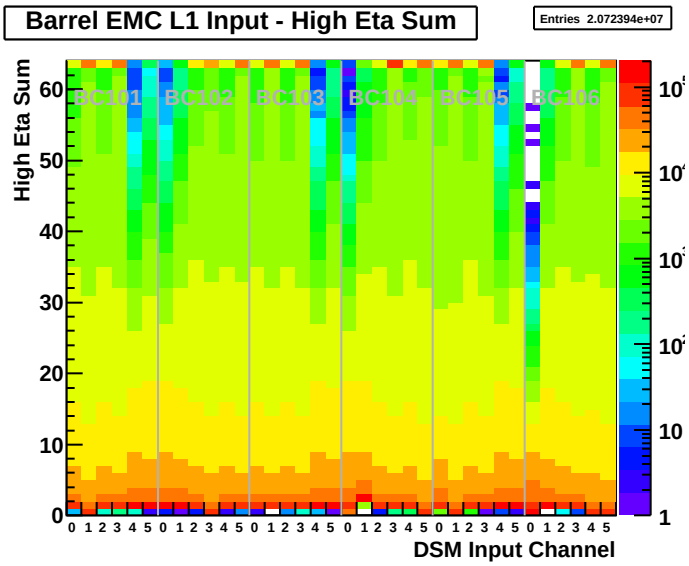
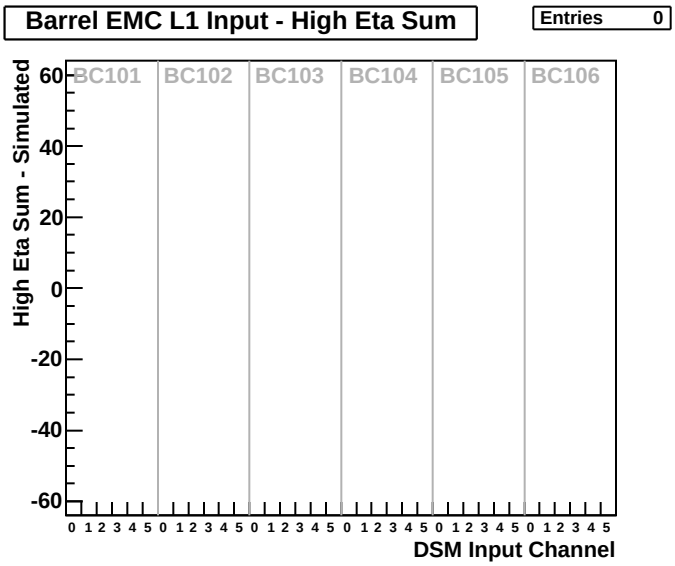
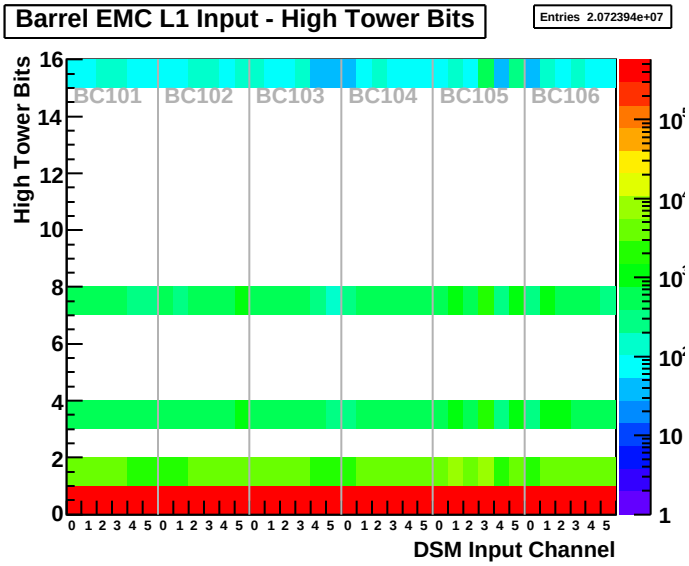
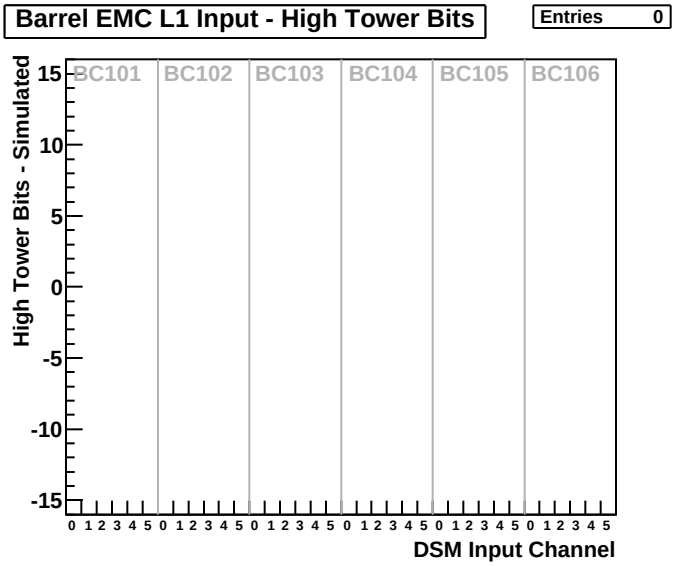

Barrel EMC L1 Input - Low Eta Sum
Entries 2.072394e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

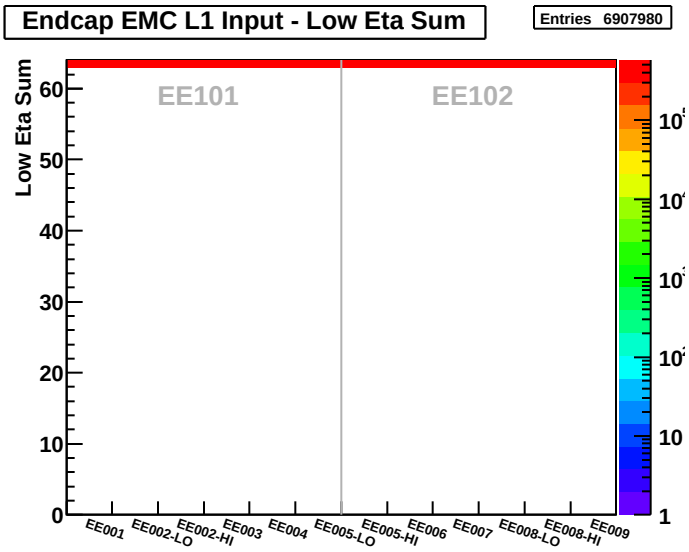

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

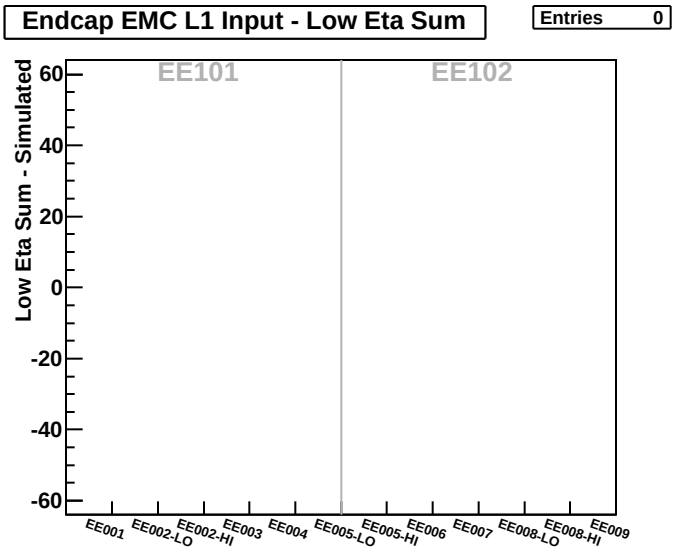

Barrel EMC L1 Input - High Eta Sum
Entries 2.072394e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

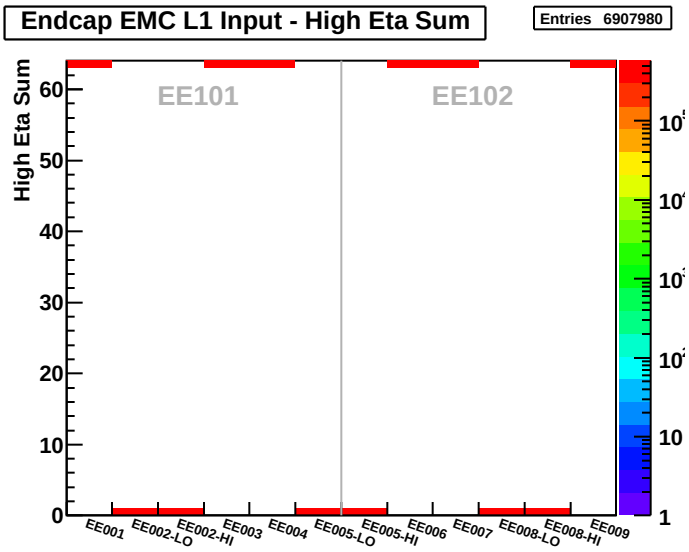

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

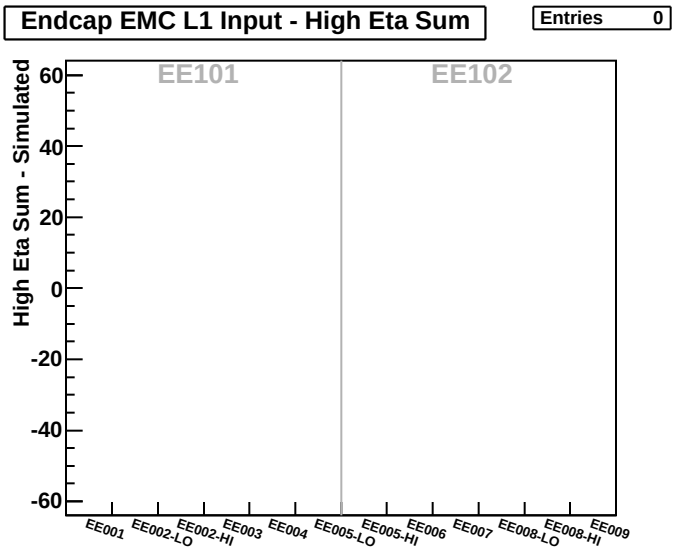

Barrel EMC L1 Input - High Tower Bits
Entries 2.072394e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

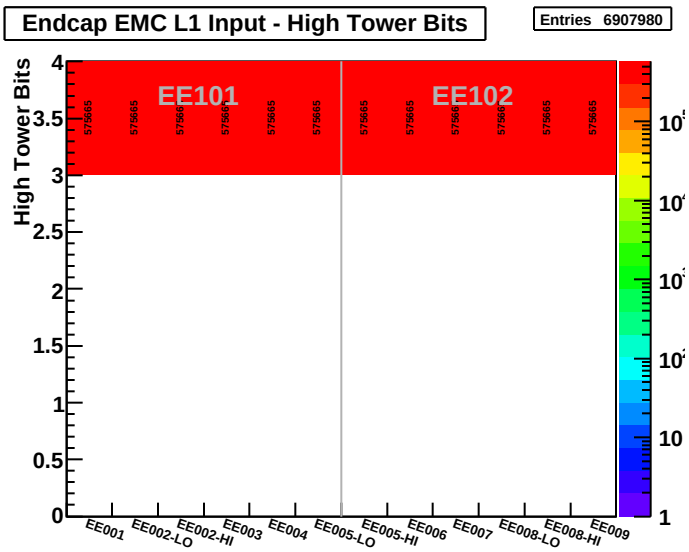

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

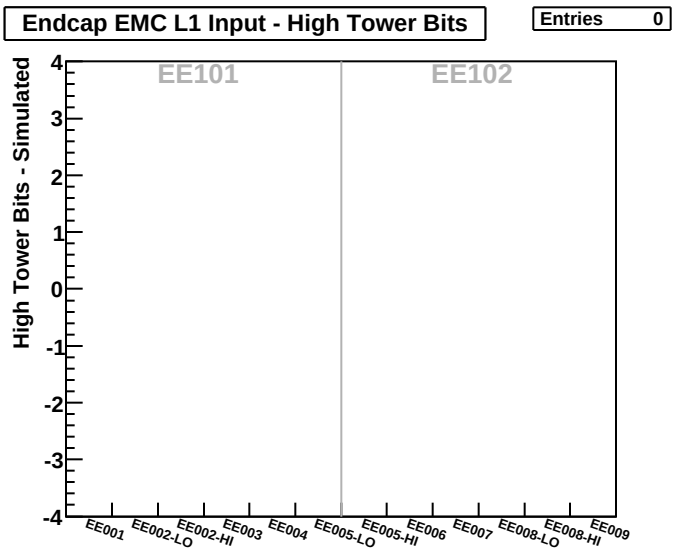

Endcap EMC L1 Input - Low Eta Sum
Entries 6907980
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

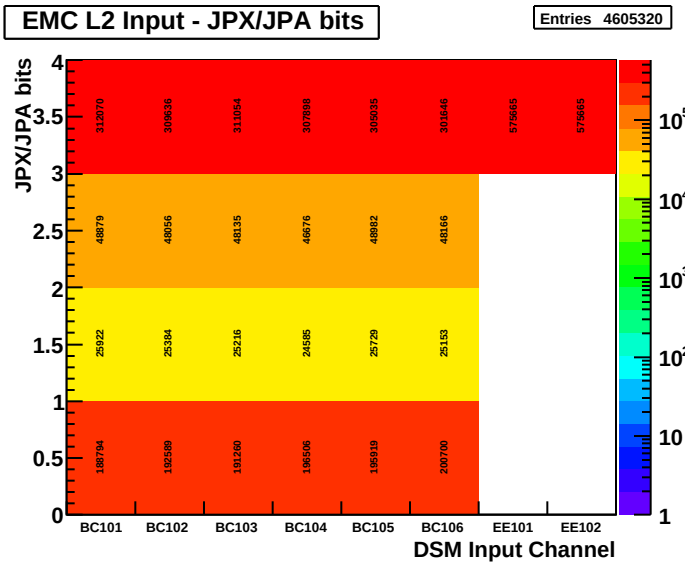

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

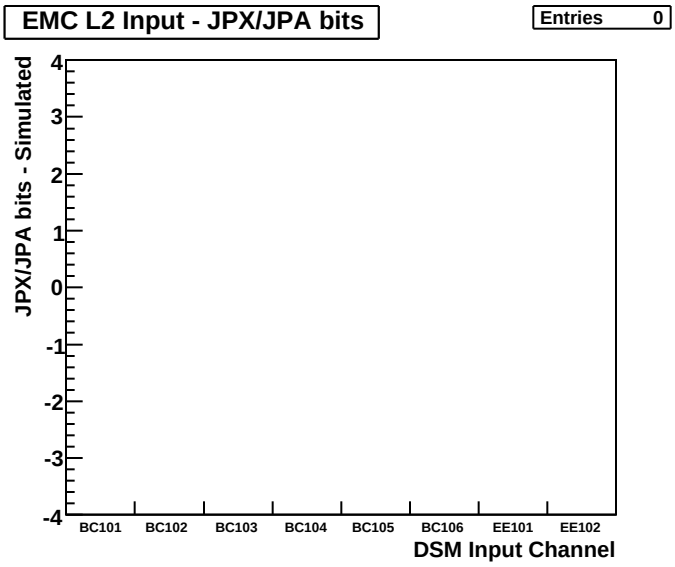

Endcap EMC L1 Input - High Eta Sum
Entries 6907980
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

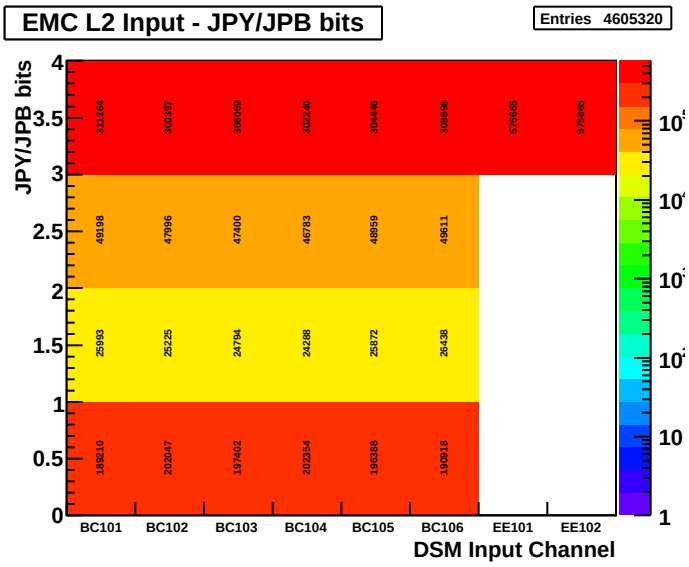

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

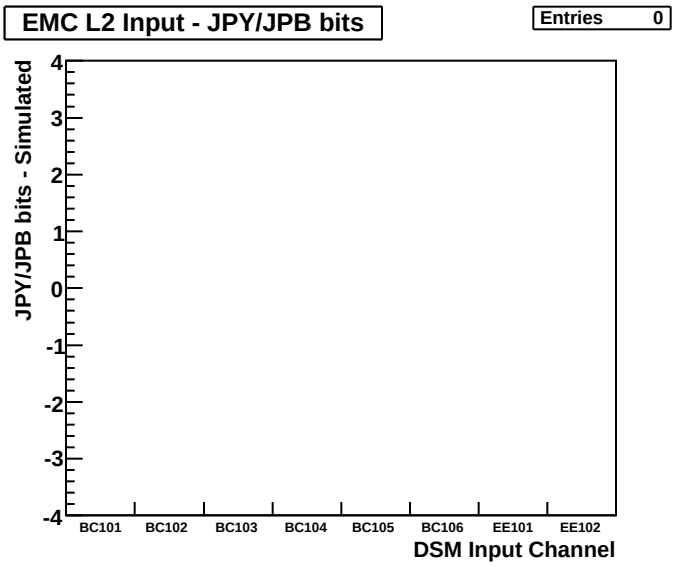

Endcap EMC L1 Input - High Tower Bits
Entries 6907980
High Tower Bits
EE101
EE102
575665
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

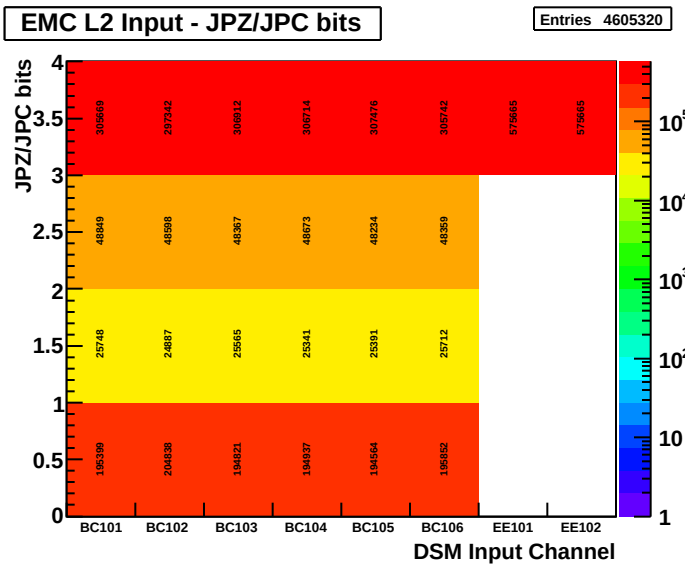

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

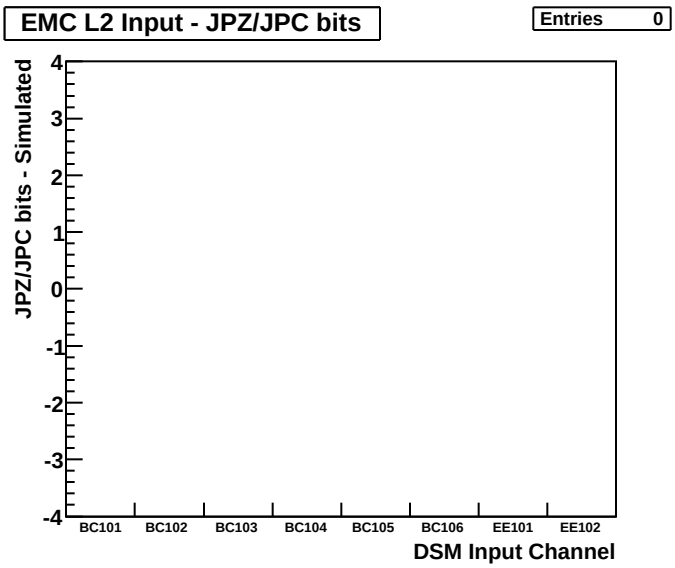

EMC L2 Input - JPX/JPA bits
Entries 4605320
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
312070 309636 311054 307898 305035 301646 575665 575665
48879 48056 48135 46676 48892 48166
25922 25384 25216 24585 25729 25153
188794 192589 191260 196506 195919 200700

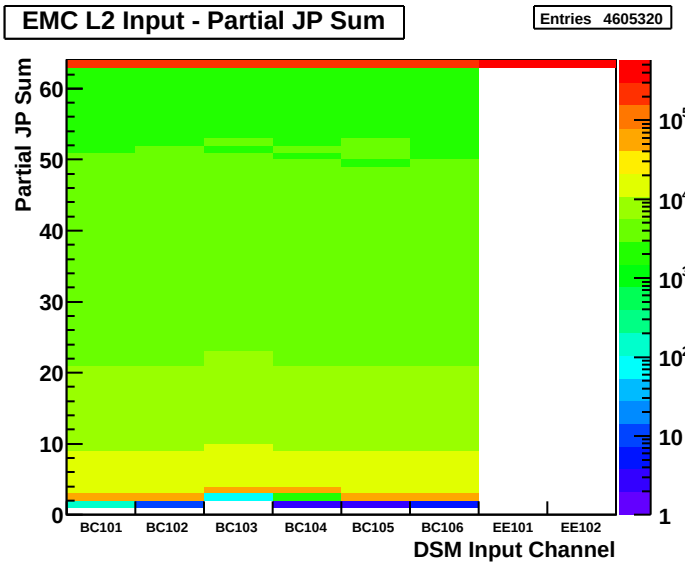

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

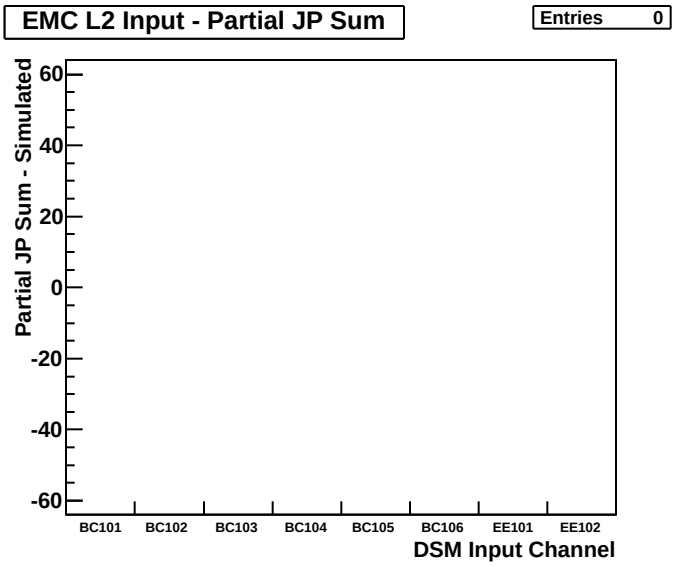

EMC L2 Input - JPY/JPB bits
Entries 4605320
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
311264 300397 306089 302240 304446 308698 575665 575665
49198 47996 47400 46783 48959 49611
25993 25225 24794 24288 25872 26438
189210 202047 197402 202354 196388 190918

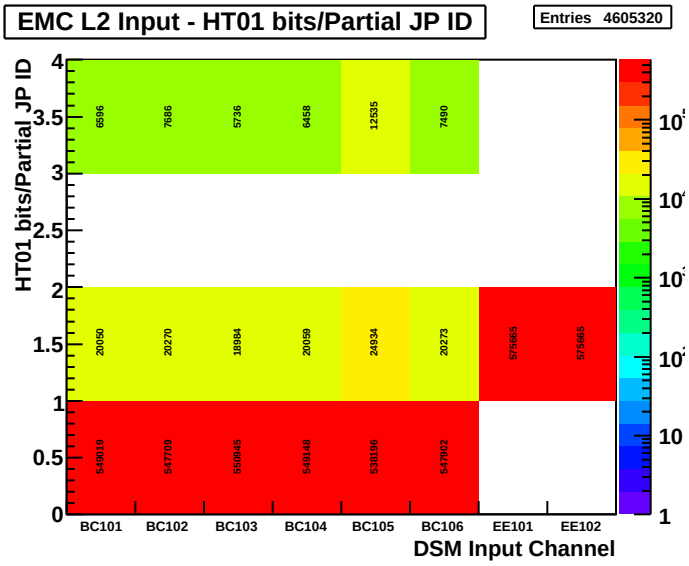

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

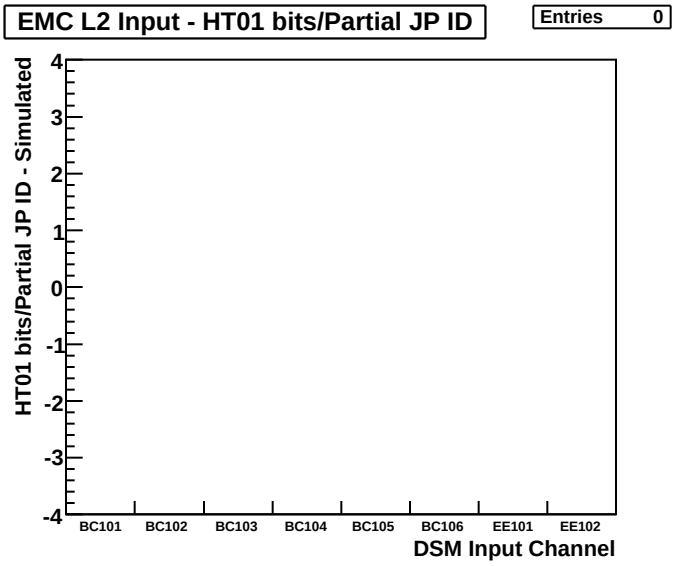

EMC L2 Input - JPZ/JPC bits
Entries 4605320
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
305669 297342 308912 306714 307476 305742 575665 575665
48849 48598 48367 48673 48234 48359
25748 24887 25565 25341 25391 25712
195399 204838 194821 194937 194564 195852

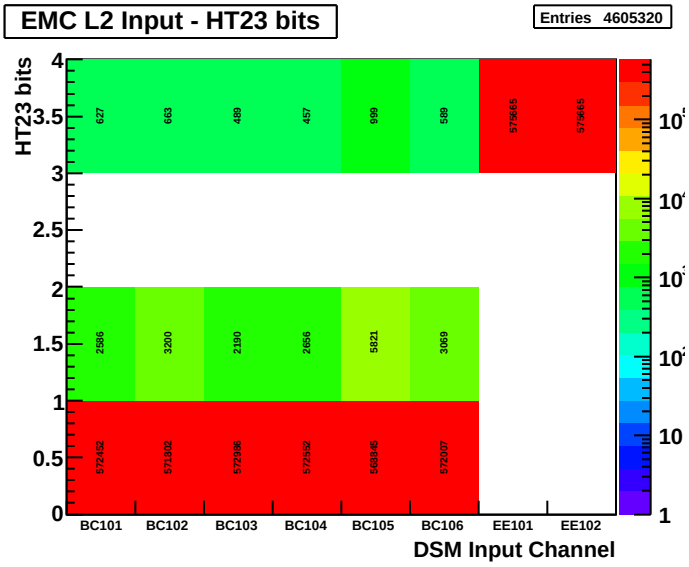

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

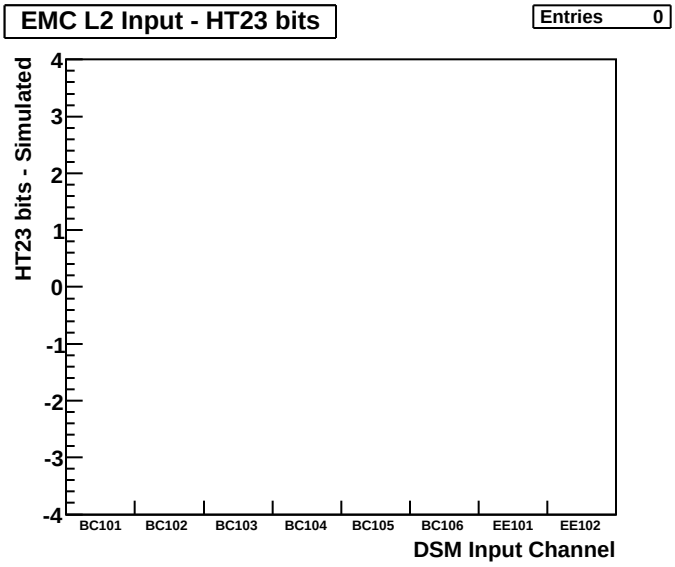

EMC L2 Input - Partial JP Sum
Entries 4605320
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

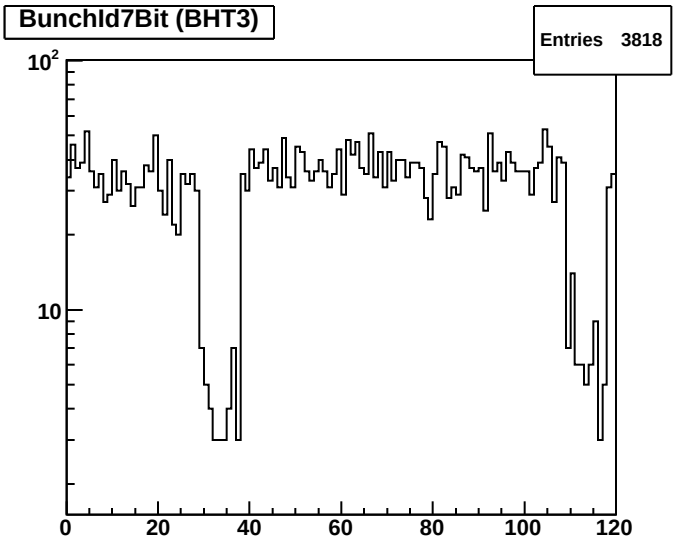

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

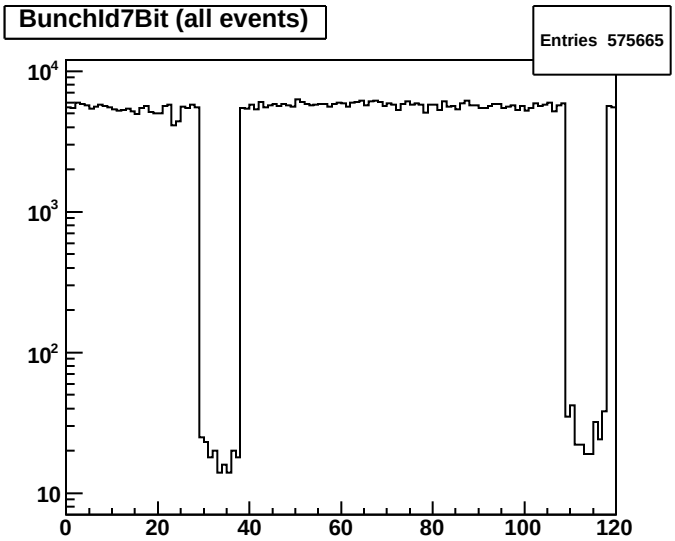

EMC L2 Input - HT01 bits/Partial JP ID
Entries 4605320
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

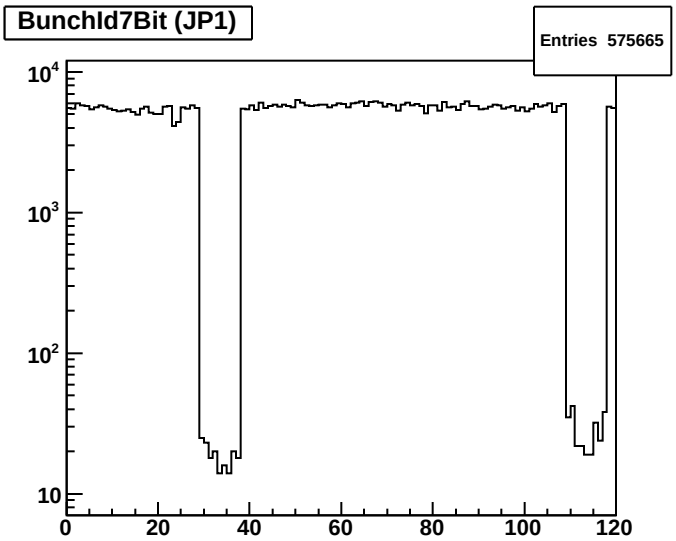

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

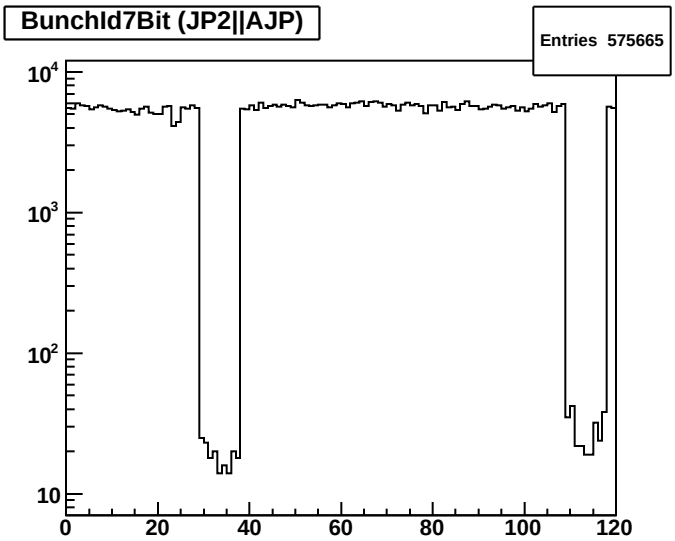

EMC L2 Input - HT23 bits
Entries 4605320
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

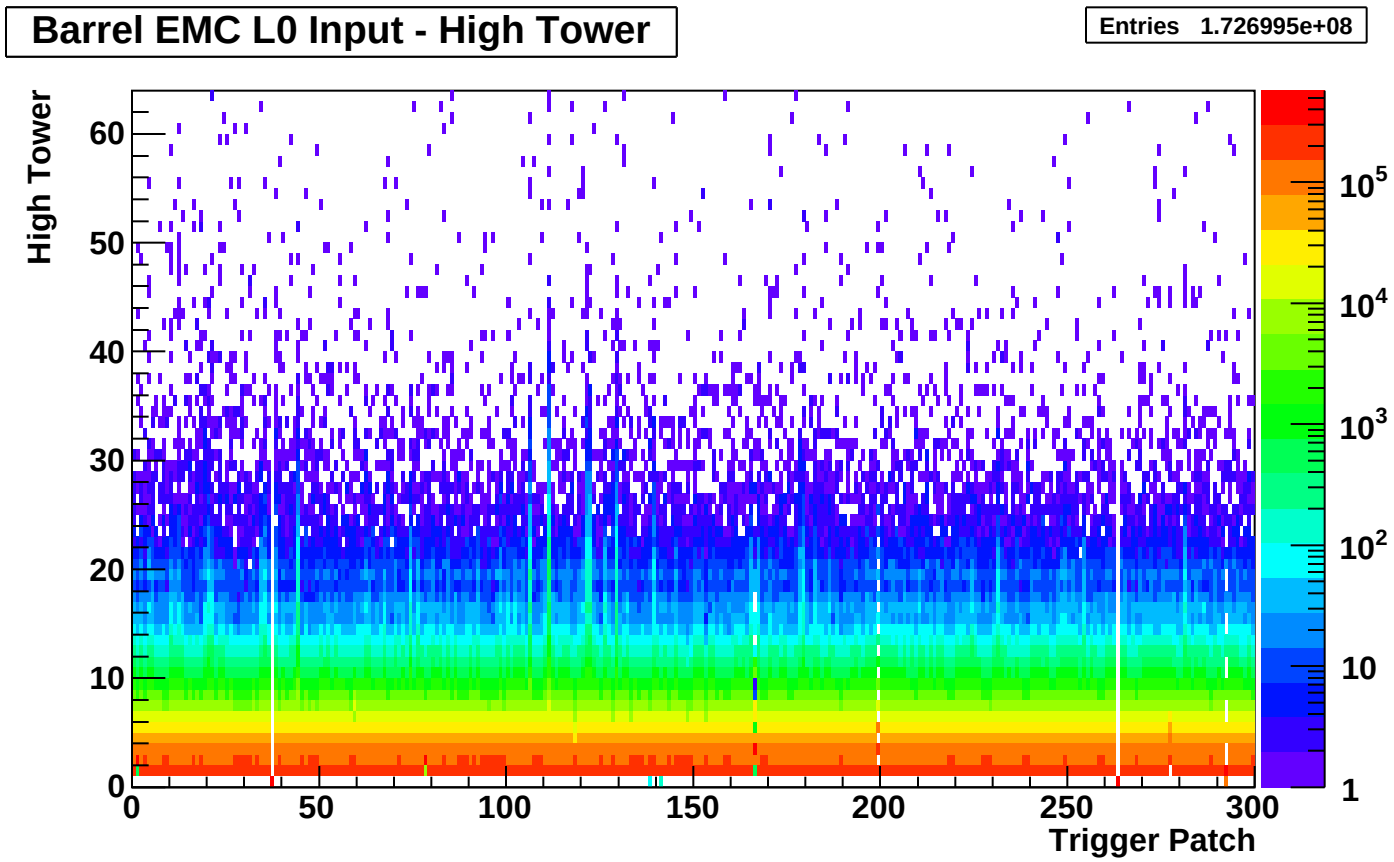

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

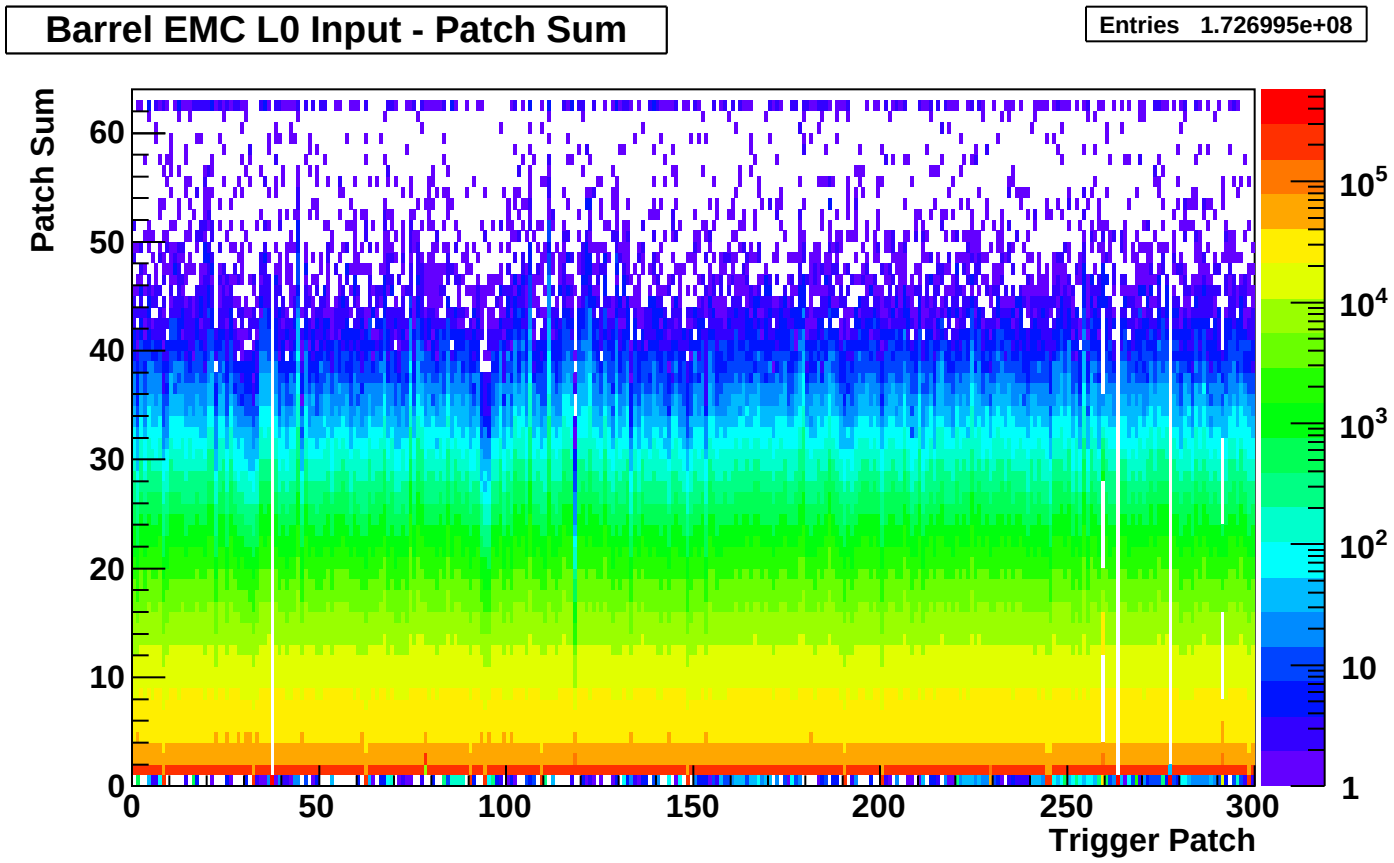

BunchId7Bit (BHT3)
Entries 3818
10^2
10
0
20
40
60
80
100
120

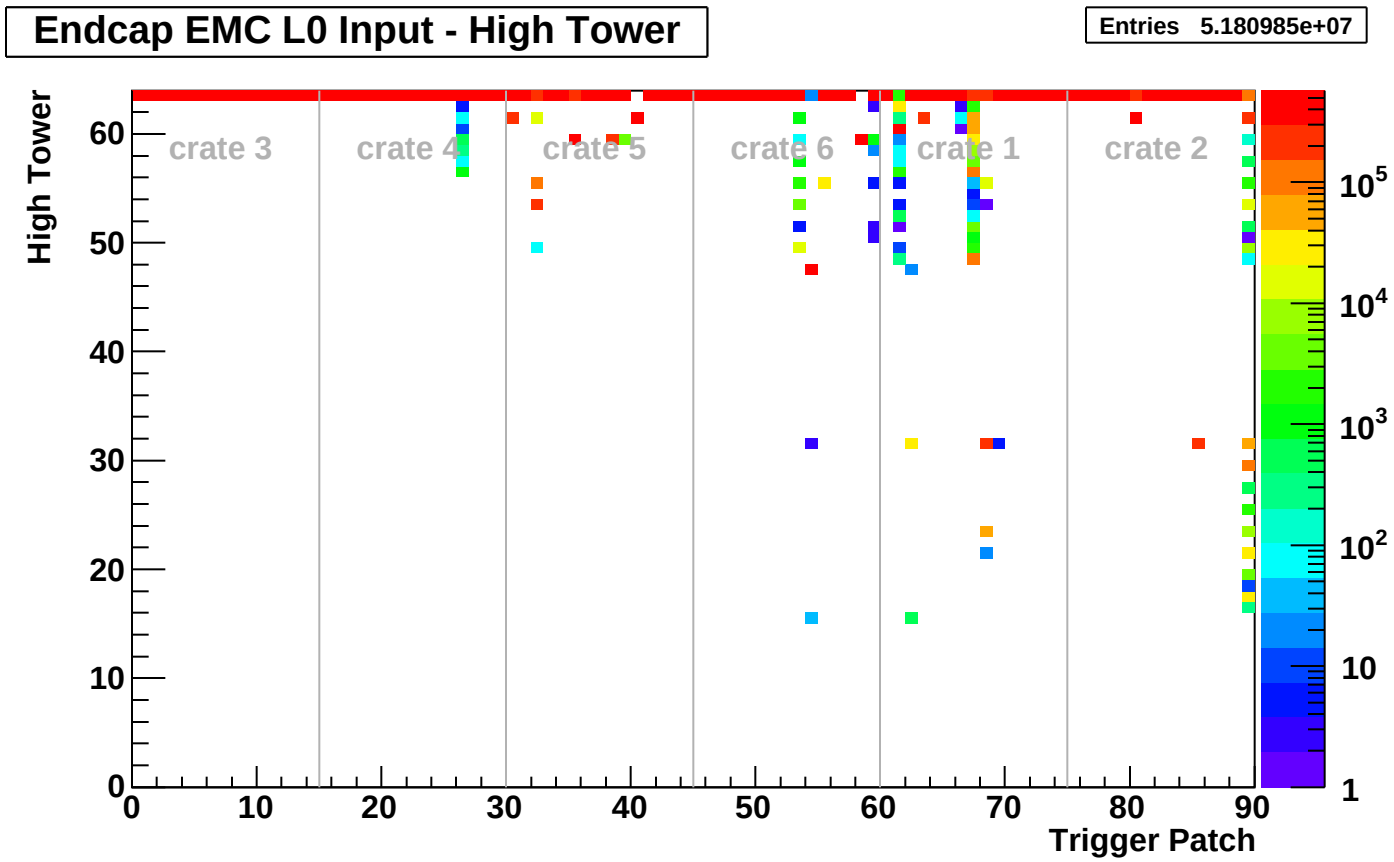

BunchId7Bit (all events)
Entries 575665
10^4
10^3
10^2
10
0
20
40
60
80
100
120

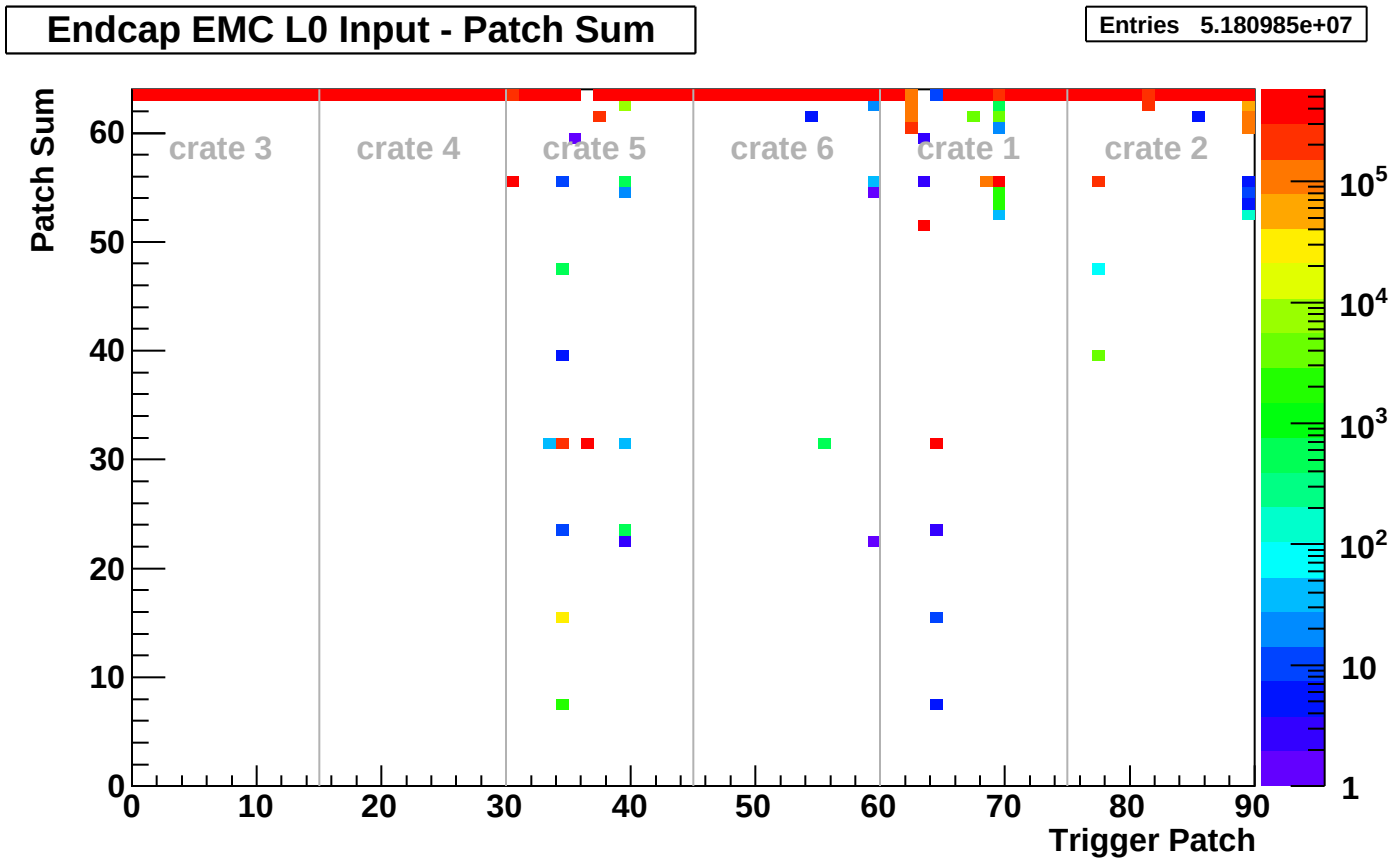

BunchId7Bit (JP1)
Entries 575665
10^4
10^3
10^2
10
0
20
40
60
80
100
120

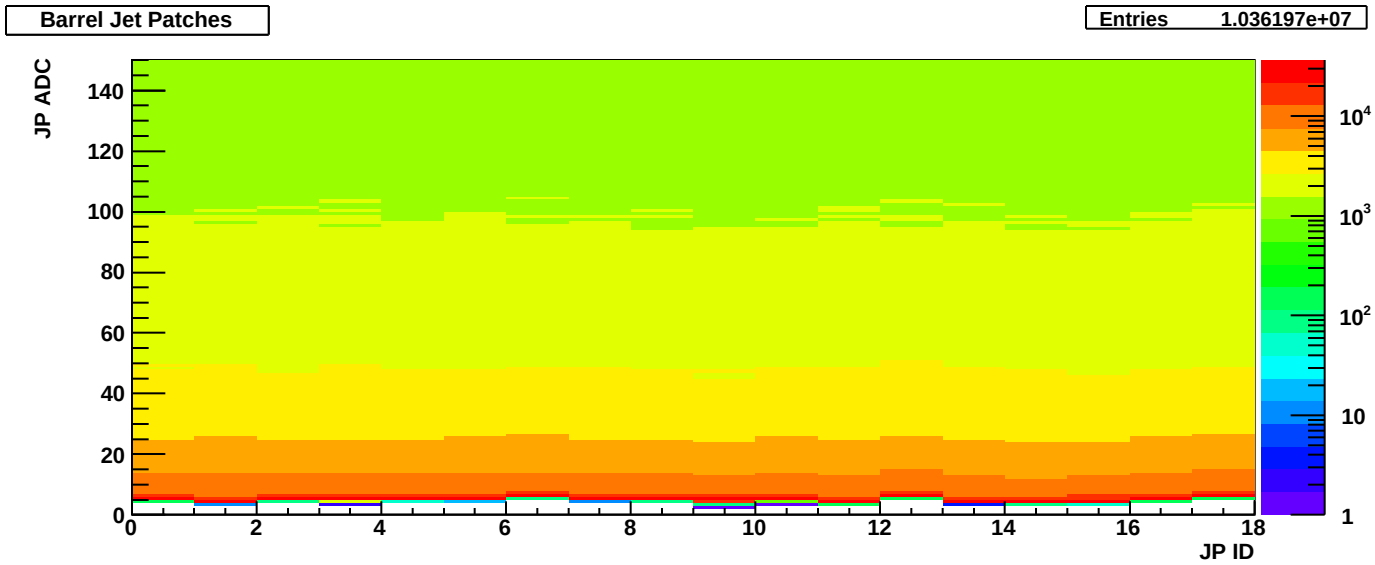

BunchId7Bit (JP2||AJP)
Entries 575665
10^4
10^3
10^2
10
0
20
40
60
80
100
120

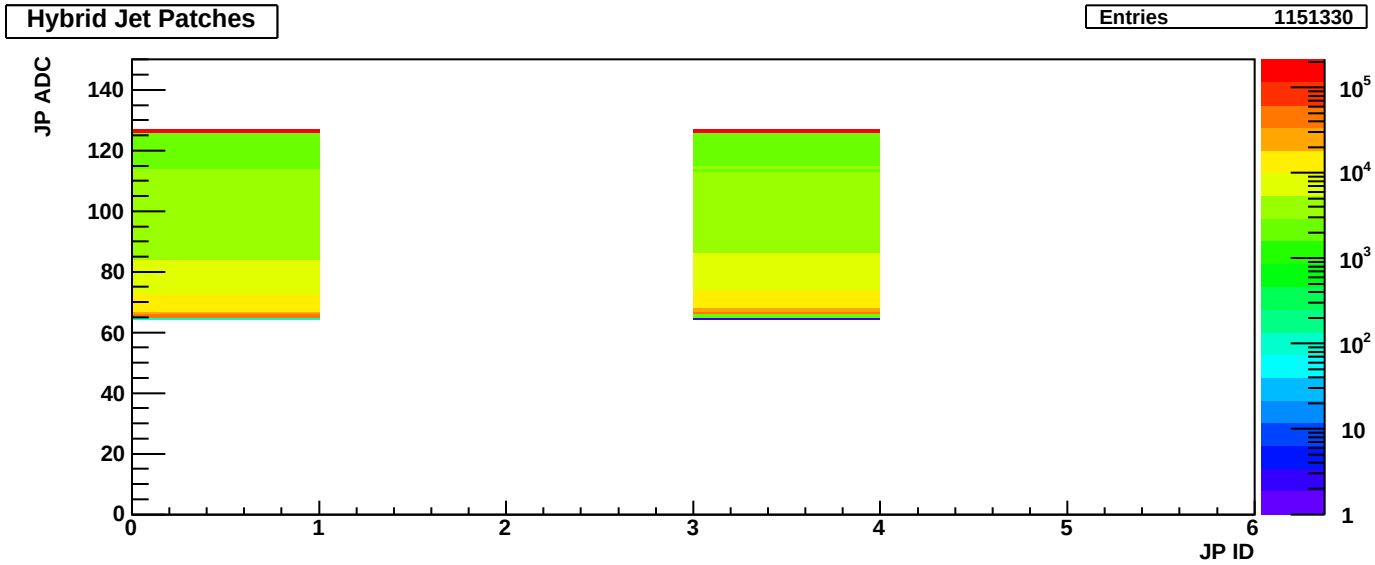

Barrel EMC L0 Input - High Tower
Entries 1.726995e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

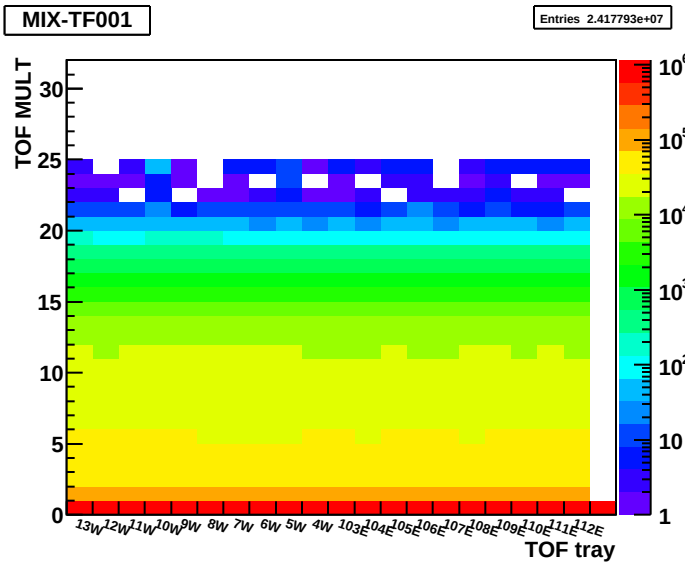

Barrel EMC L0 Input - Patch Sum
Entries 1.726995e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

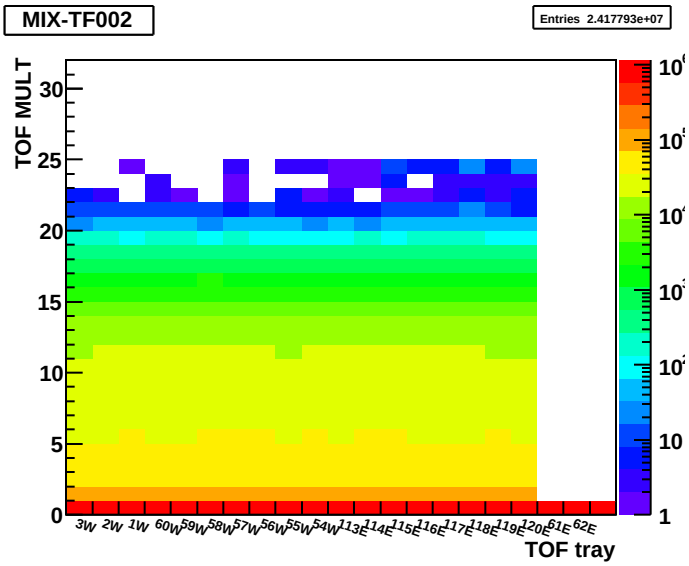

Endcap EMC L0 Input - High Tower
Entries 5.180985e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

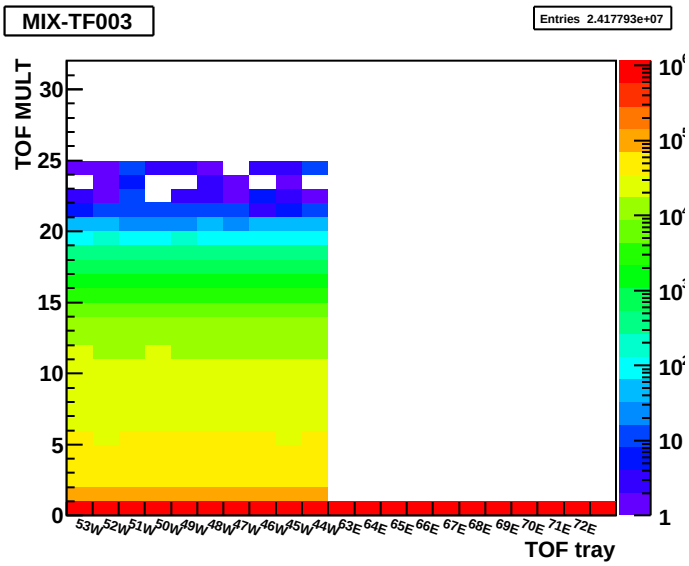

Endcap EMC L0 Input - Patch Sum
Entries 5.180985e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

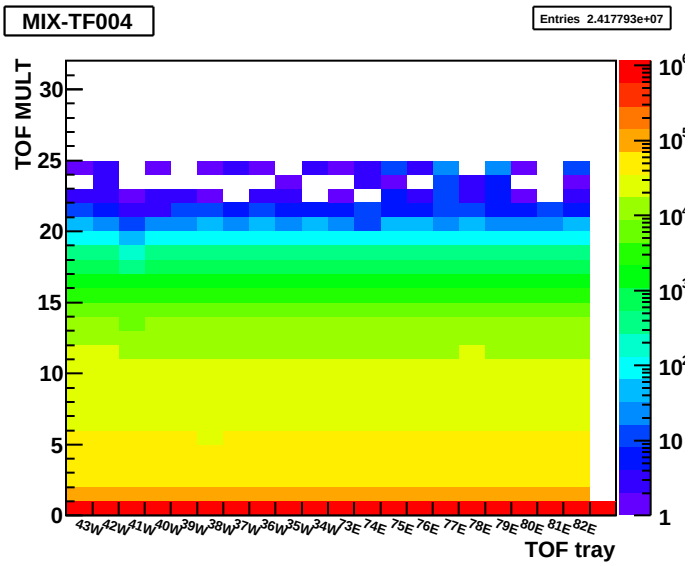

Barrel Jet Patches
Entries 1.036197e+07
JP ADC
JP ID

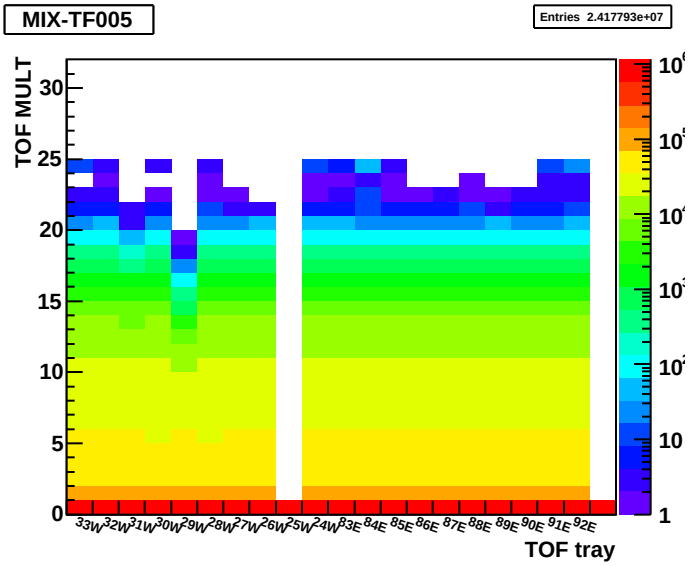

Hybrid Jet Patches
Entries 1151330
JP ADC
JP ID

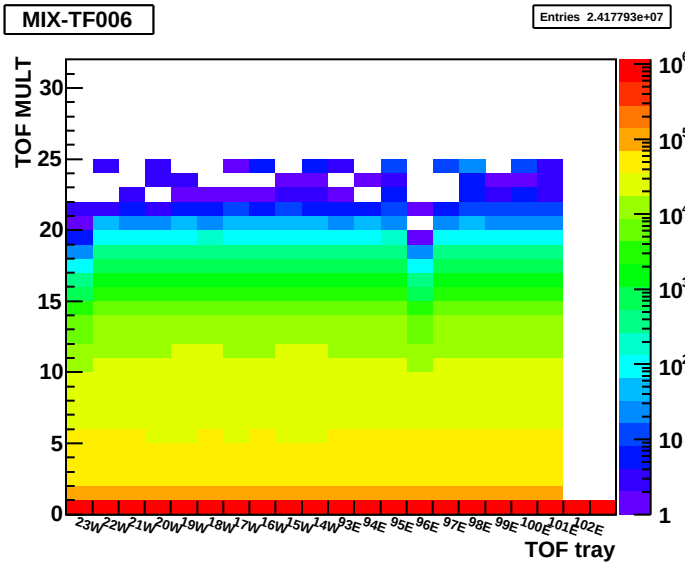

MIX-TF001
Entries 2.417793e+07
TOF MULT
TOF tray

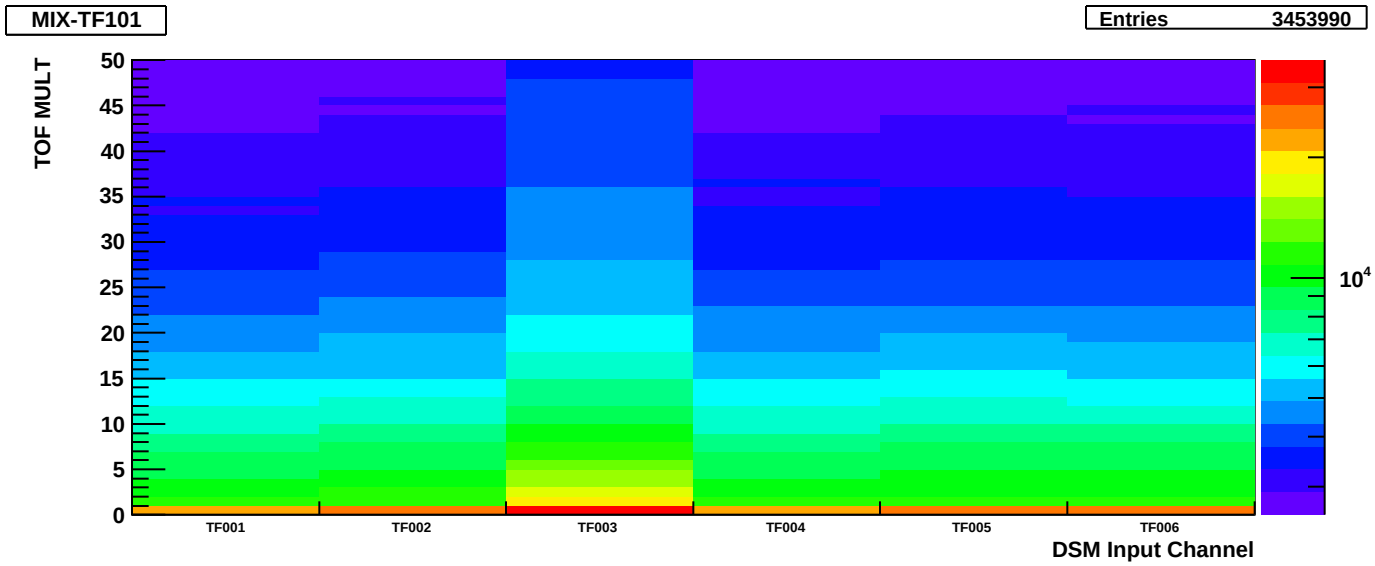

MIX-TF002
Entries 2.417793e+07
TOF MULT
TOF tray

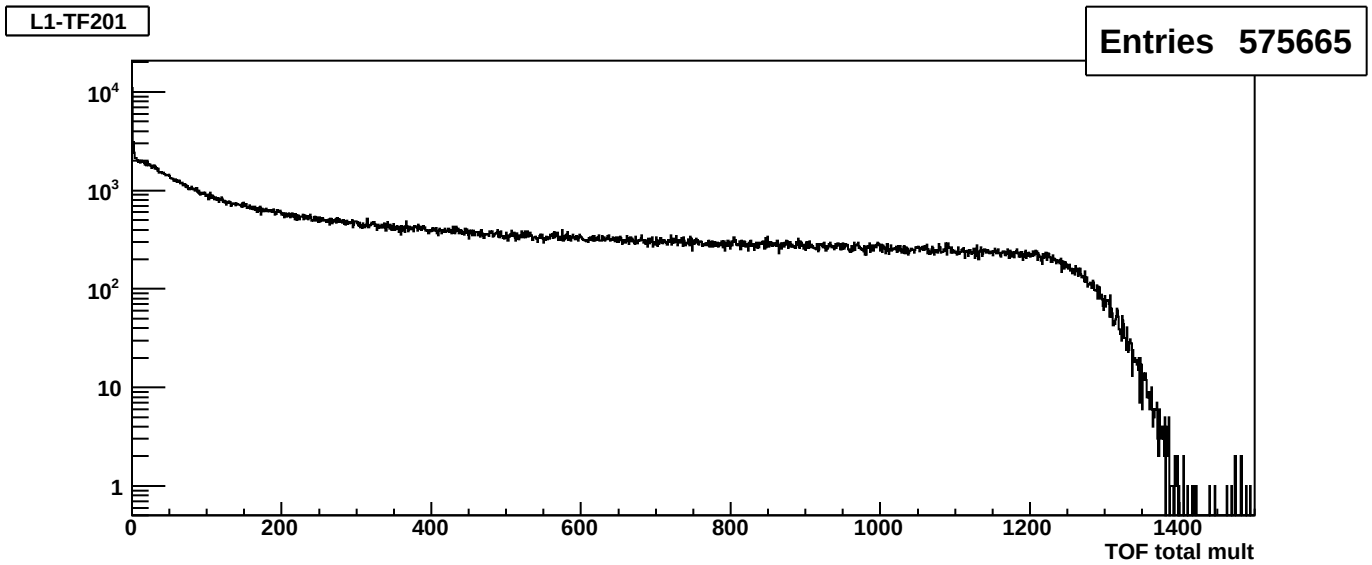

MIX-TF003
Entries 2.417793e+07
TOF MULT
TOF tray

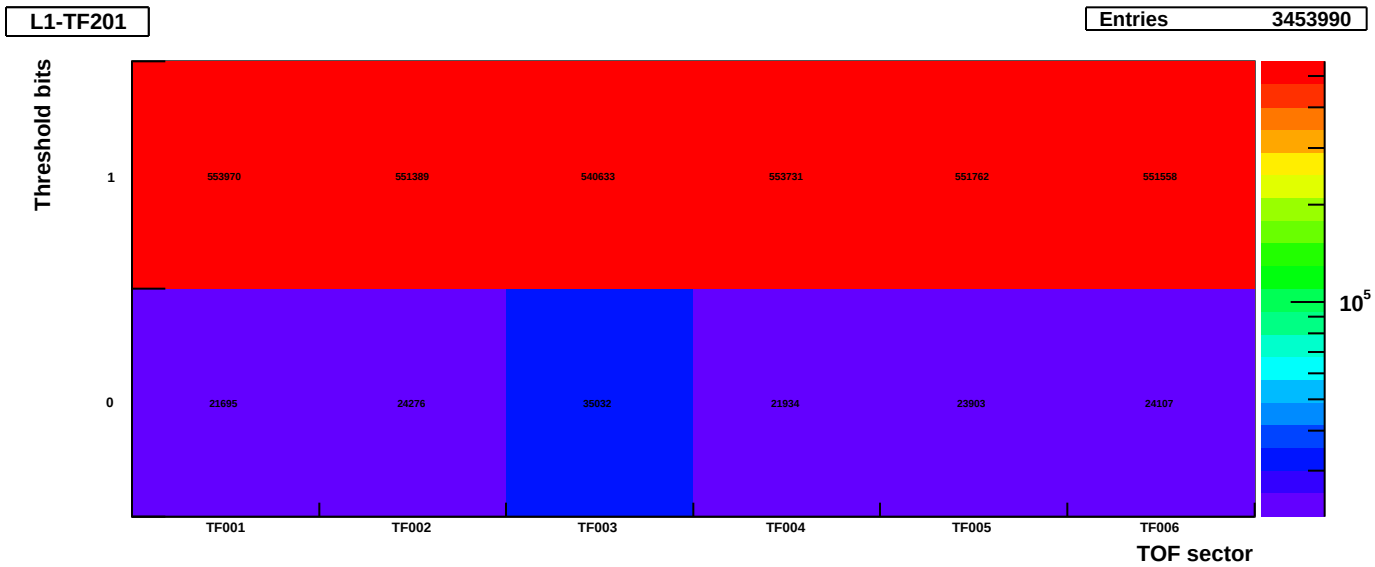

MIX-TF004
Entries 2.417793e+07
TOF MULT
TOF tray

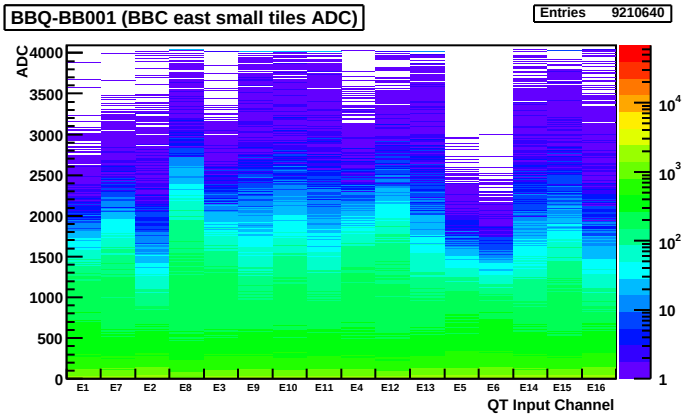

MIX-TF005
Entries 2.417793e+07
TOF MULT
TOF tray

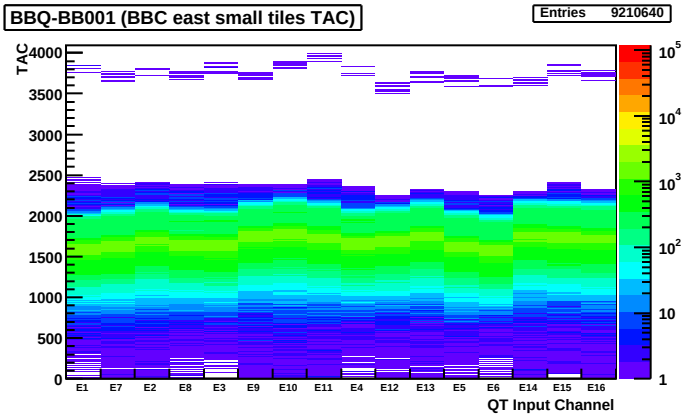

MIX-TF006
Entries 2.417793e+07
TOF MULT
TOF tray

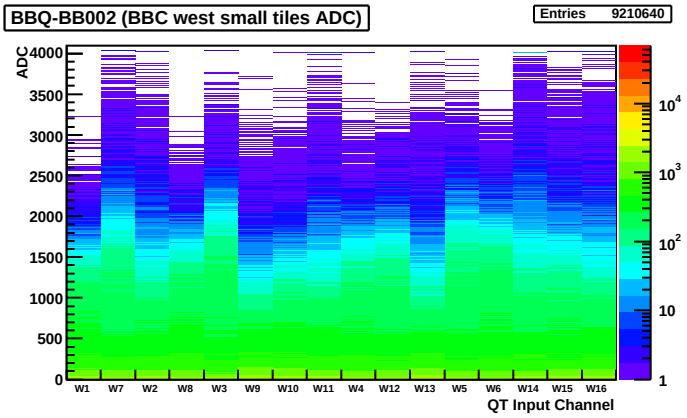

MIX-TF101
Entries 3453990
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

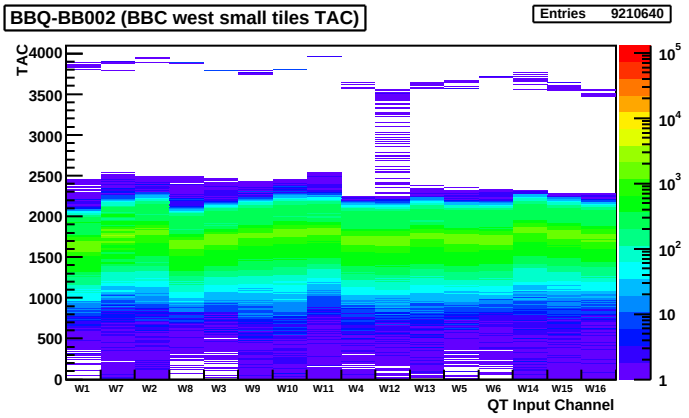

L1-TF201
Entries 575665
10^4
10^3
10^2
10
1
0
200
400
600
800
1000
1200
1400
TOF total mult

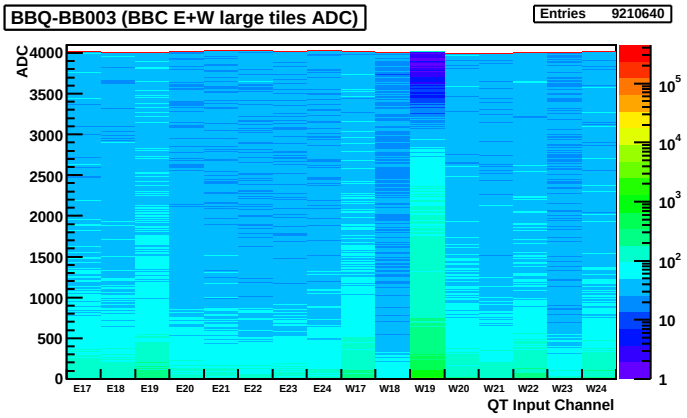

L1-TF201
Entries 3453990
Threshold bits
1
0
553970
551389
540633
553731
551762
551558
21695
24276
35032
21934
23903
24107
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

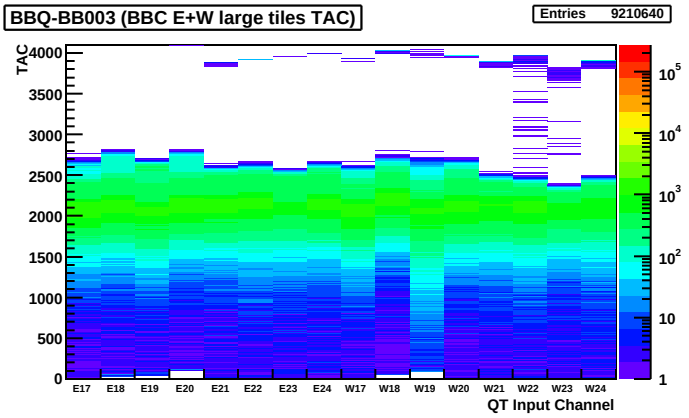

BBQ-BB001 (BBC east small tiles ADC)
Entries 9210640
ADC
QT Input Channel

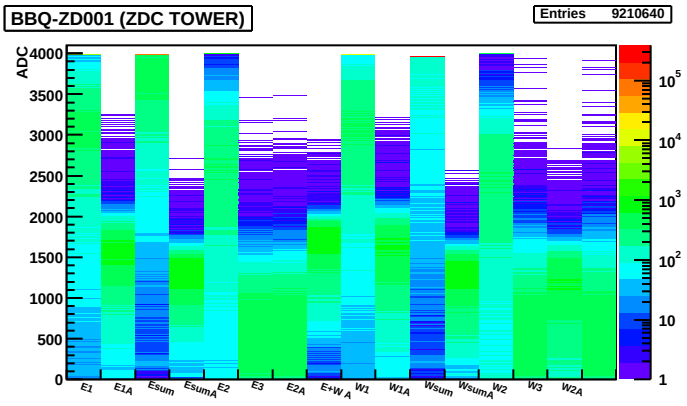

BBQ-BB001 (BBC east small tiles TAC)
Entries 9210640
TAC
QT Input Channel

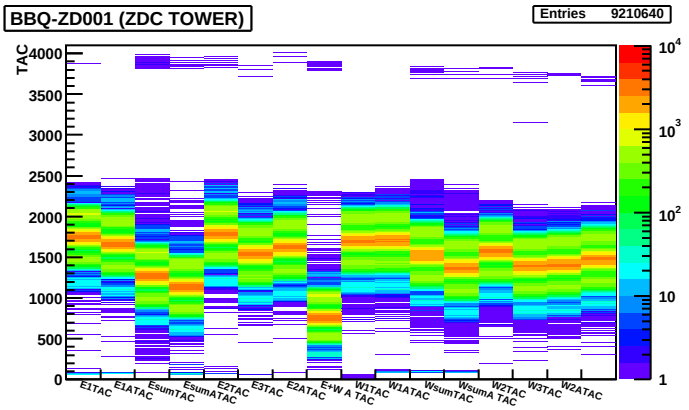

BBQ-BB002 (BBC west small tiles ADC)
Entries 9210640
ADC
QT Input Channel

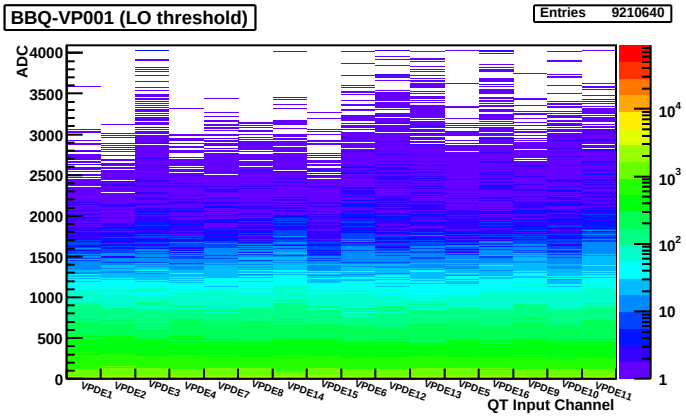

BBQ-BB002 (BBC west small tiles TAC)
Entries 9210640
TAC
QT Input Channel

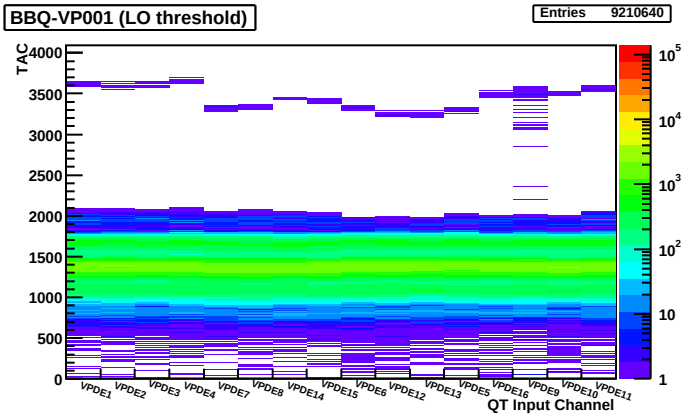

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 9210640
ADC
QT Input Channel

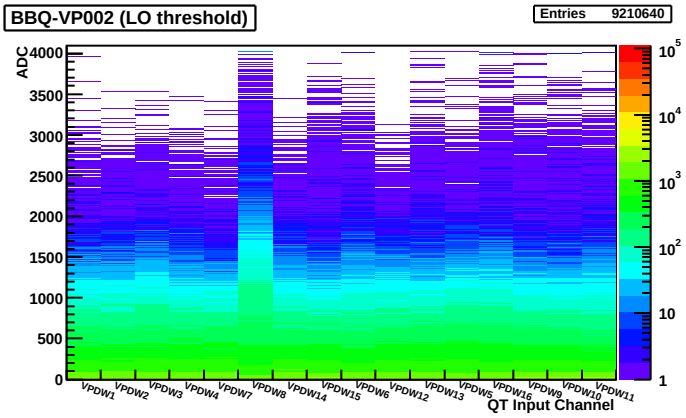

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 9210640
TAC
QT Input Channel

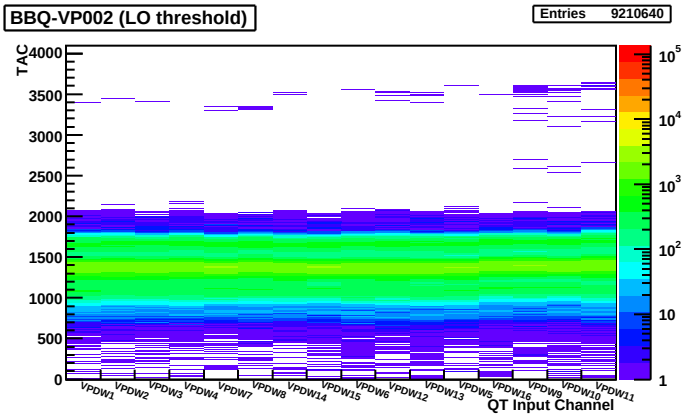

BBQ-ZD001 (ZDC TOWER)
Entries 9210640
ADC

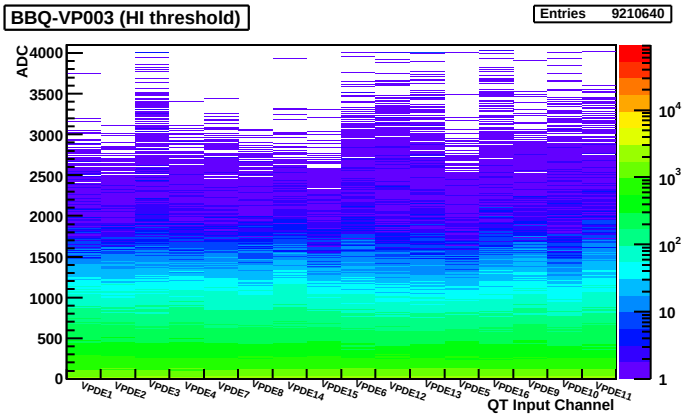

BBQ-ZD001 (ZDC TOWER)
Entries 9210640
TAC

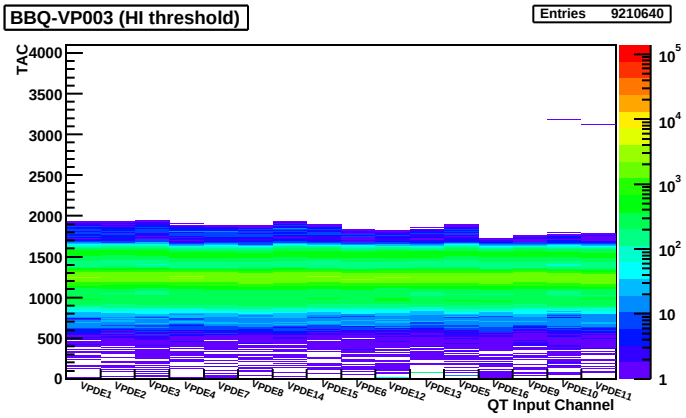

BBQ-VP001 (LO threshold)
Entries 9210640
ADC
QT Input Channel

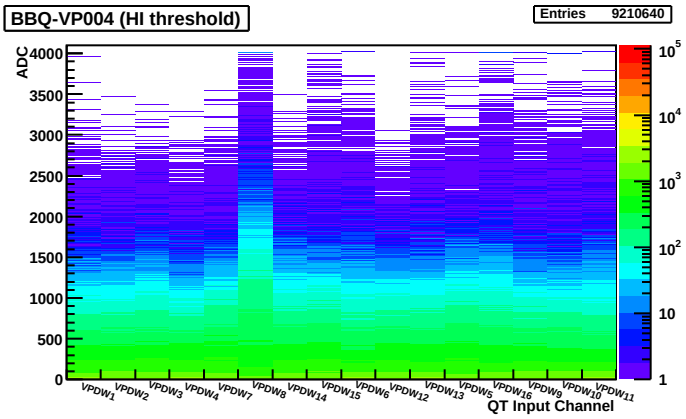

BBQ-VP001 (LO threshold)
Entries 9210640
TAC
QT Input Channel

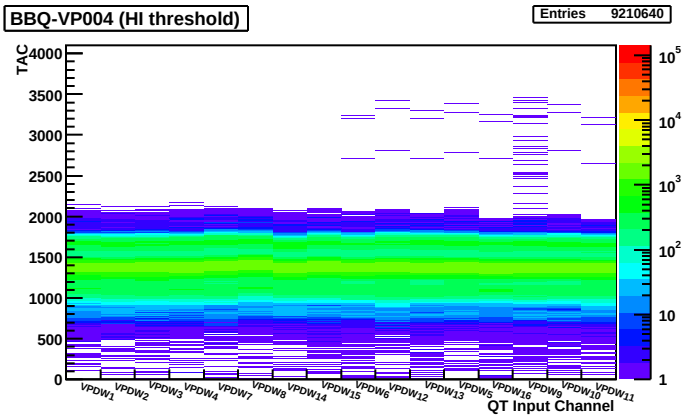

BBQ-VP002 (LO threshold)
Entries 9210640
ADC
QT Input Channel

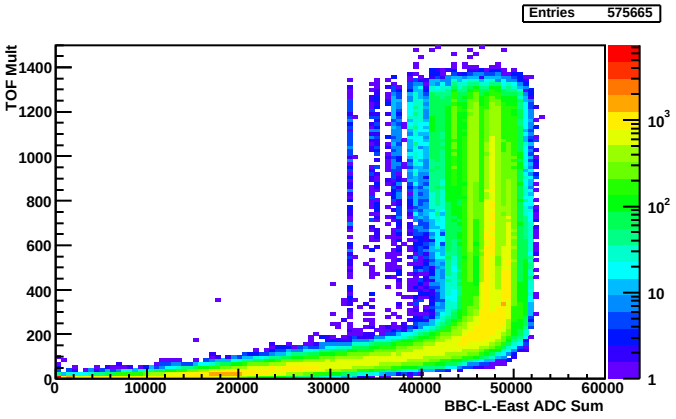

BBQ-VP002 (LO threshold)
Entries 9210640
TAC
QT Input Channel

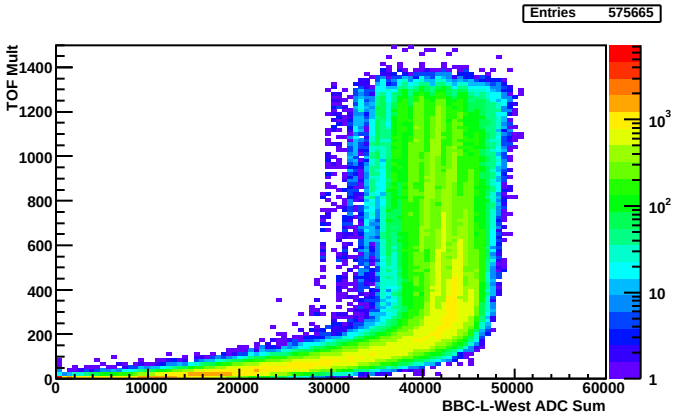

BBQ-VP003 (HI threshold)
Entries 9210640
ADC
QT Input Channel

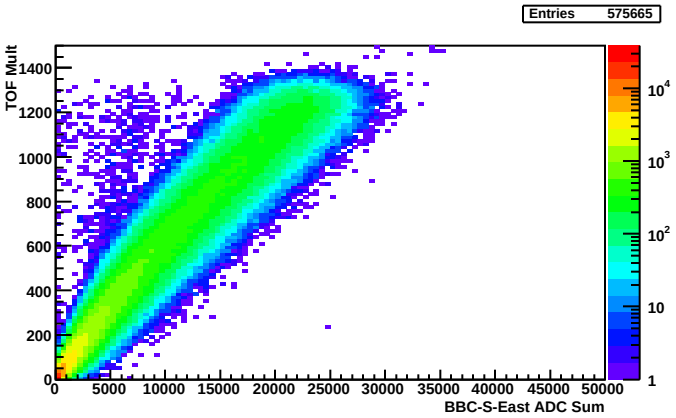

BBQ-VP003 (HI threshold)
Entries 9210640
TAC
QT Input Channel

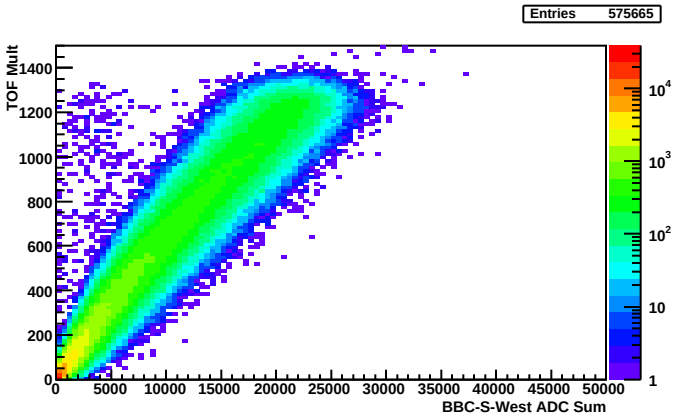

BBQ-VP004 (HI threshold)
Entries 9210640
ADC
QT Input Channel

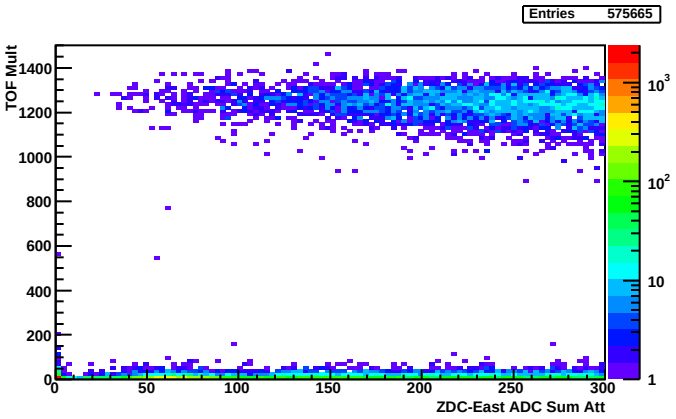

BBQ-VP004 (HI threshold)
Entries 9210640
TAC
QT Input Channel

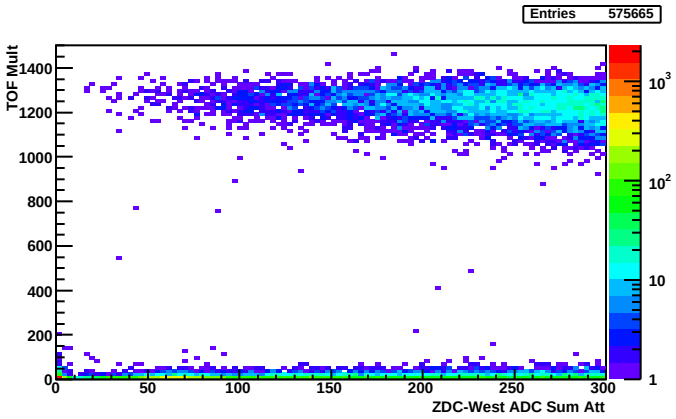

Entries 575665
TOF Mult
BBC-L-East ADC Sum

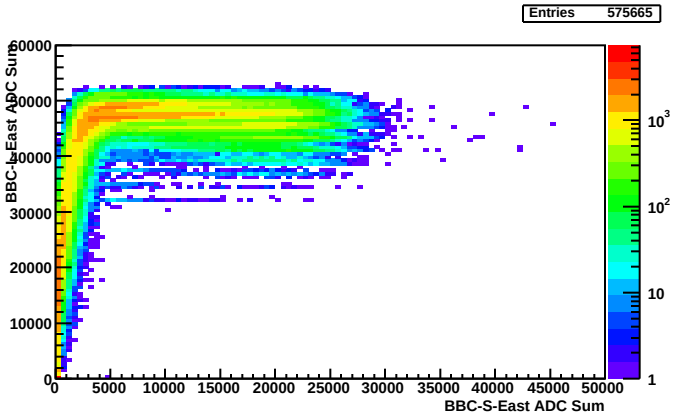

Entries 575665
TOF Mult
BBC-L-West ADC Sum

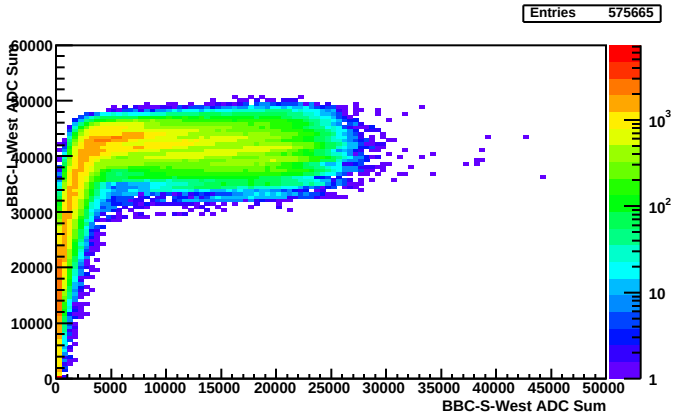

Entries 575665
TOF Mult
BBC-S-East ADC Sum

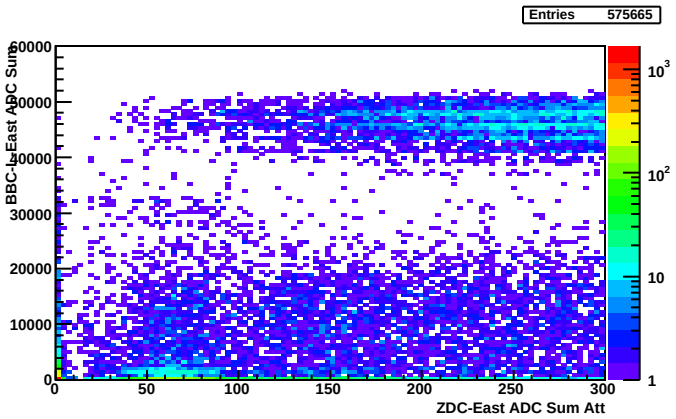

Entries 575665
TOF Mult
BBC-S-West ADC Sum

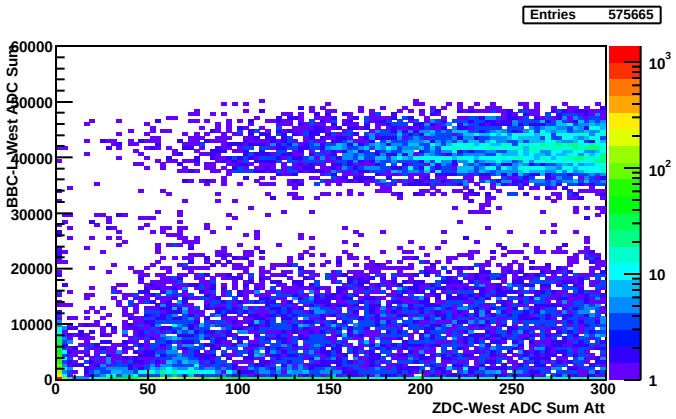

Entries 575665
TOF Mult
ZDC-East ADC Sum Att


Entries 575665
TOF Mult
ZDC-West ADC Sum Att


Entries 575665
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 575665
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 575665
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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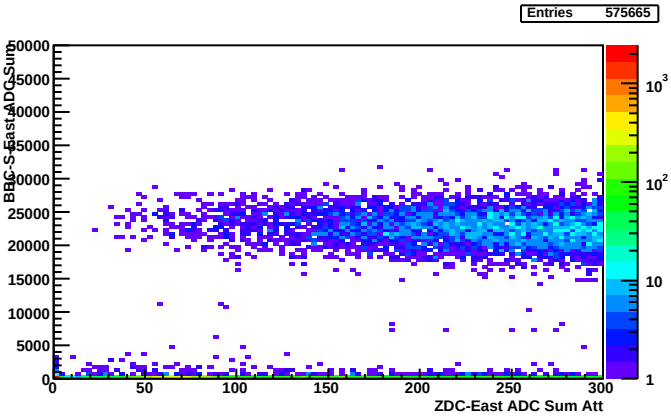
BBC-L-West ADC Sum
ZDC-West ADC Sum Att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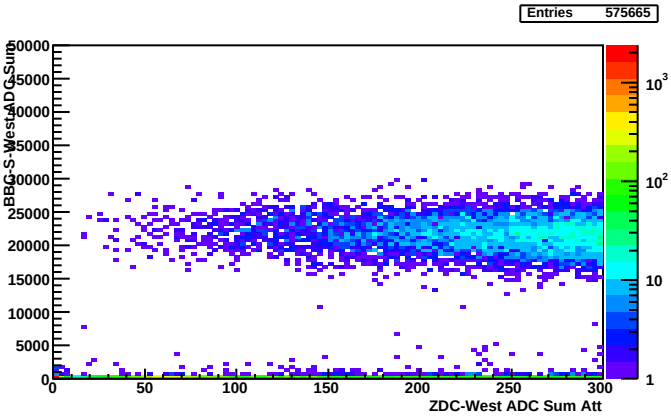
BBC-S-East ADC Sum
ZDC-East ADC Sum Att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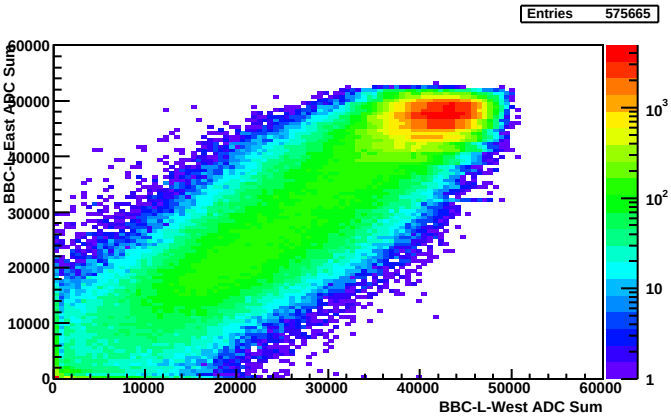
BBC-S-West ADC Sum
ZDC-West ADC Sum Att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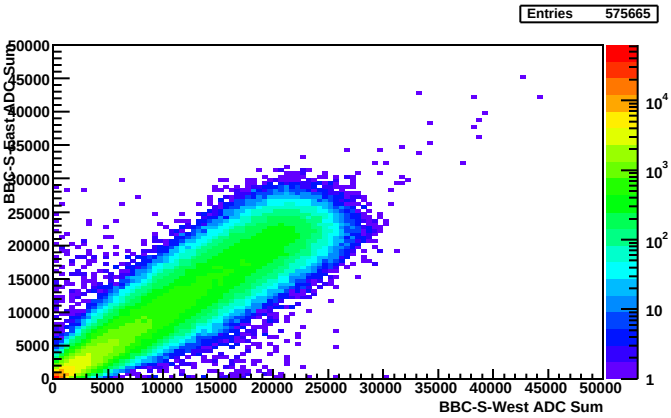
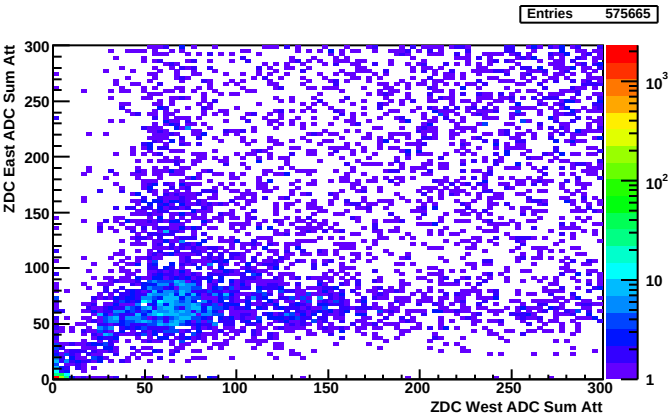
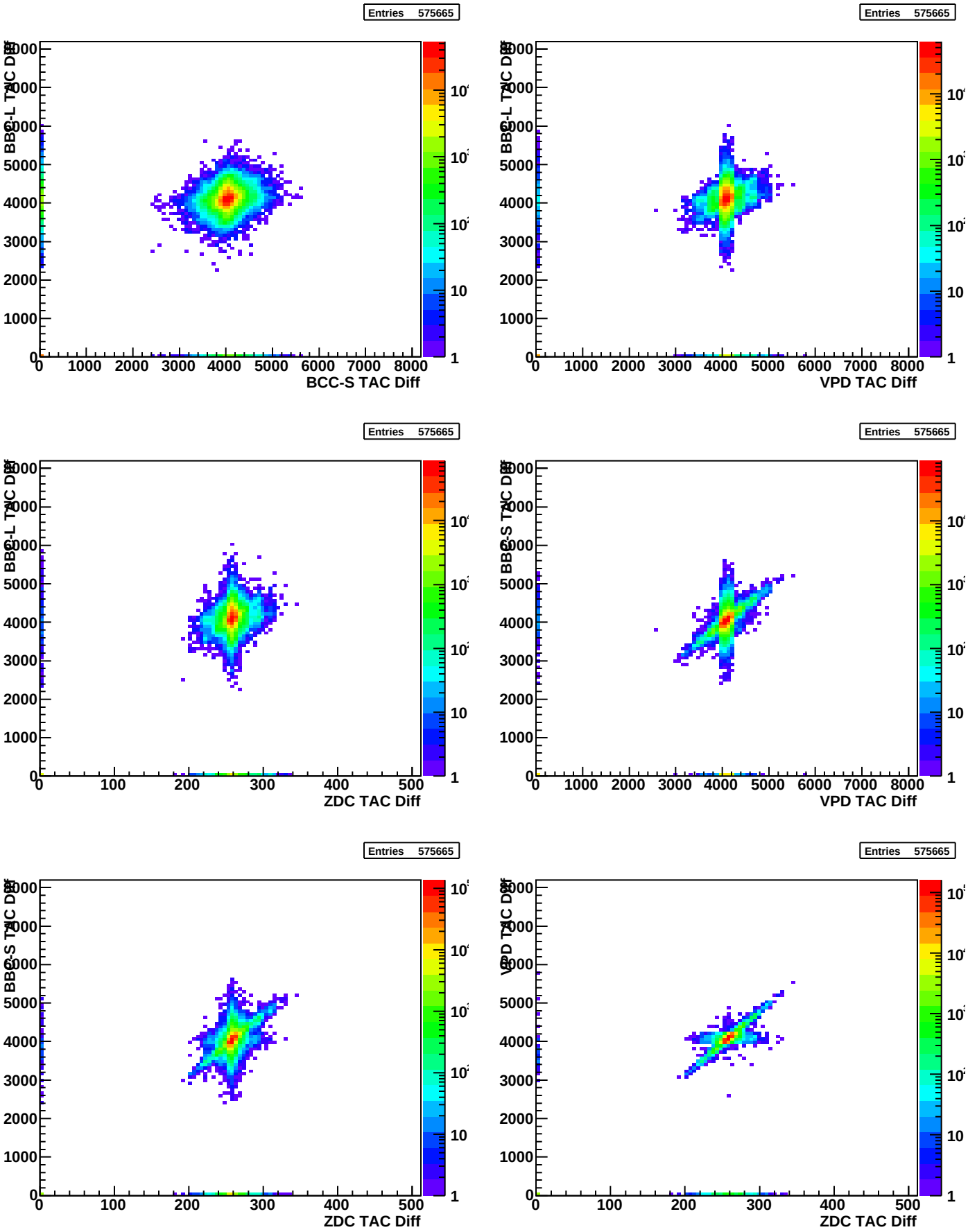
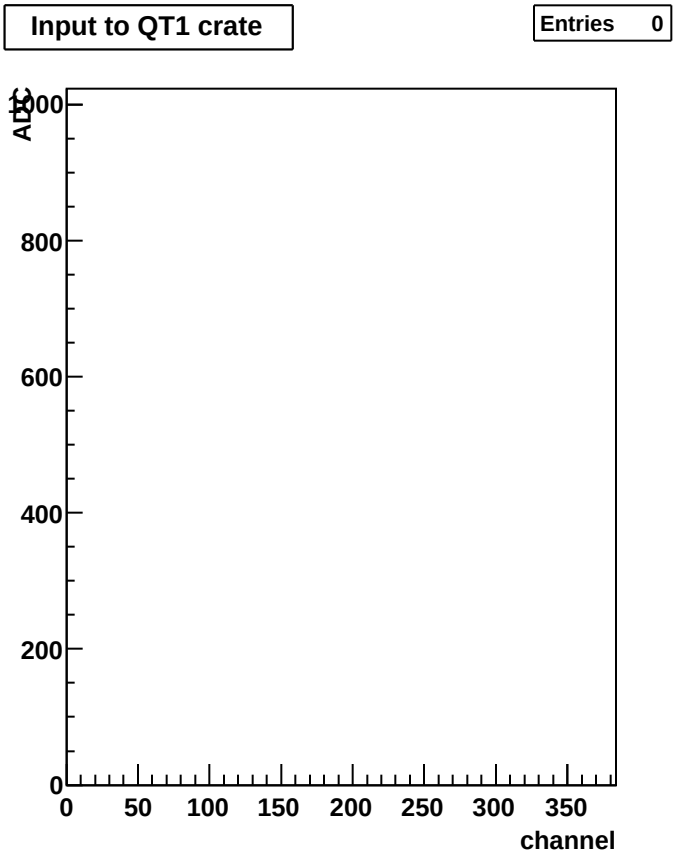
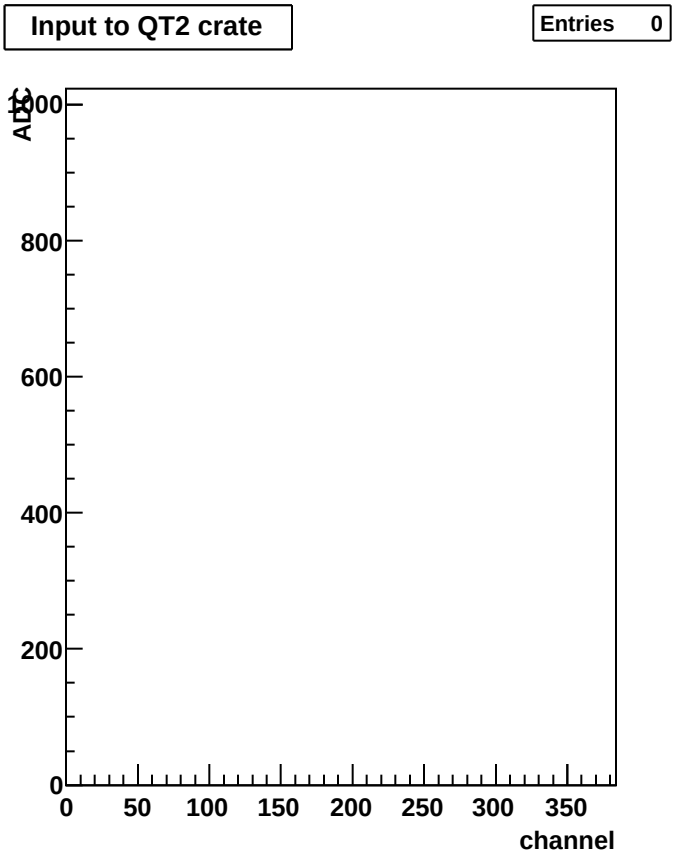
BBC-L-East ADC Sum
BBC-L-West ADC Sum

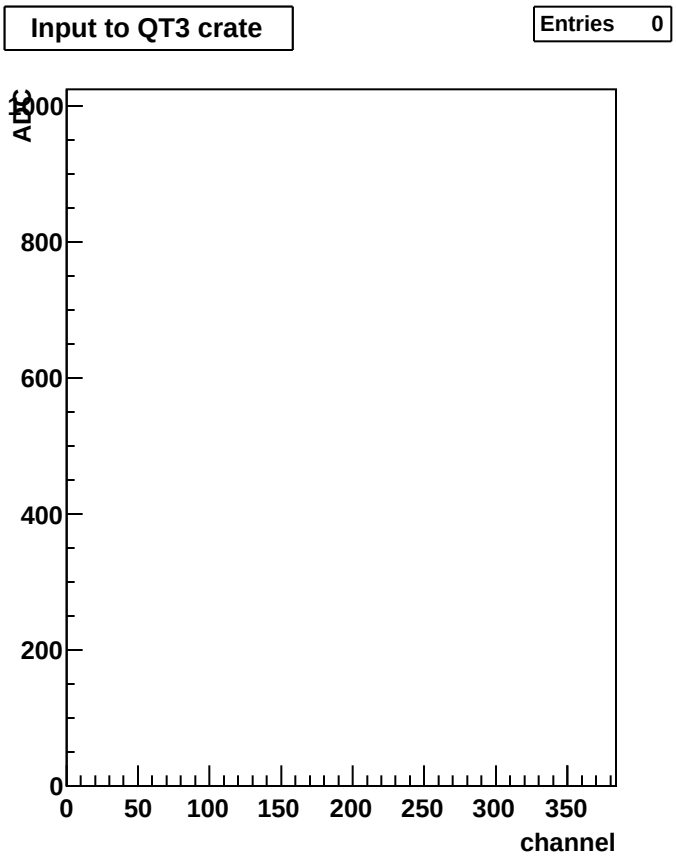

Entries 575665
BBC-S-East ADC Sum
BBC-S-West ADC Sum

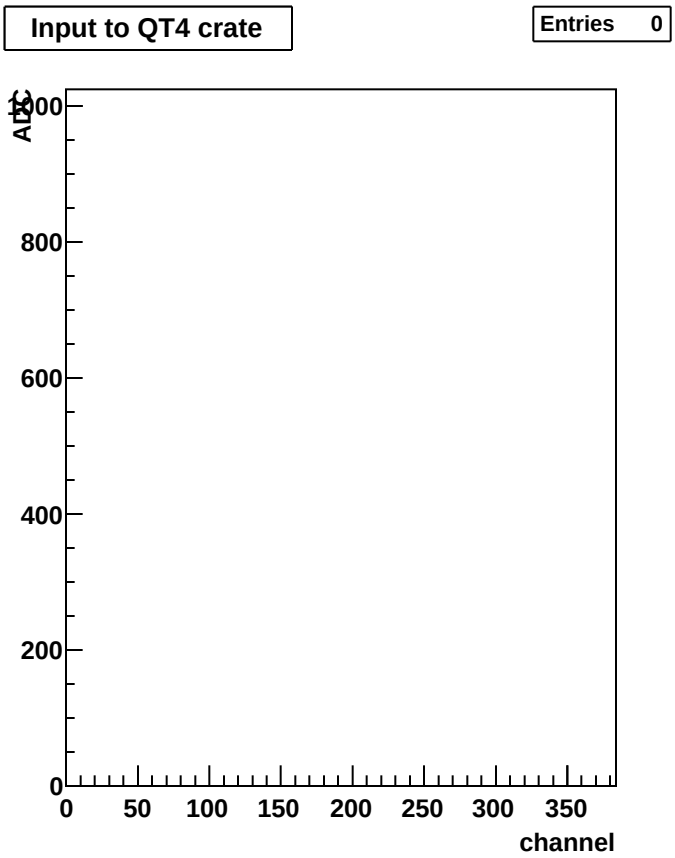

Entries 575665
ZDC East ADC Sum Att
ZDC West ADC Sum Att

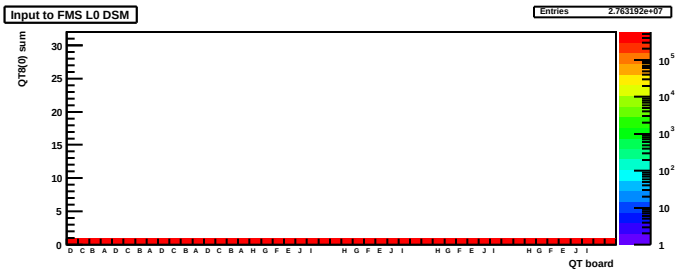

Entries 575665
BBC-L TAC Diff
BCC-S TAC Diff
Entries 575665
BBC-L TAC Diff
VPD TAC Diff
Entries 575665
BBC-L TAC Diff
ZDC TAC Diff
Entries 575665
BBC-S TAC Diff
VPD TAC Diff
Entries 575665
BBC-S TAC Diff
ZDC TAC Diff
Entries 575665
VPD TAC Diff
ZDC TAC Diff

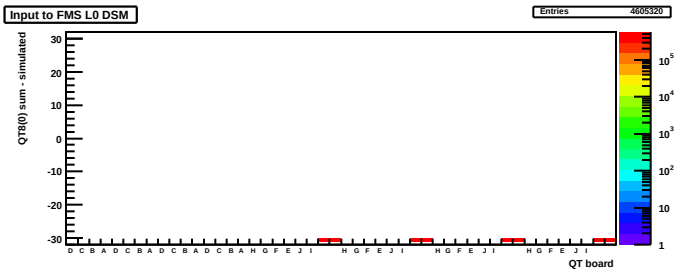

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

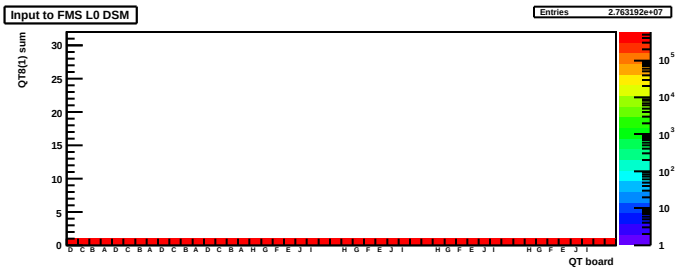

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

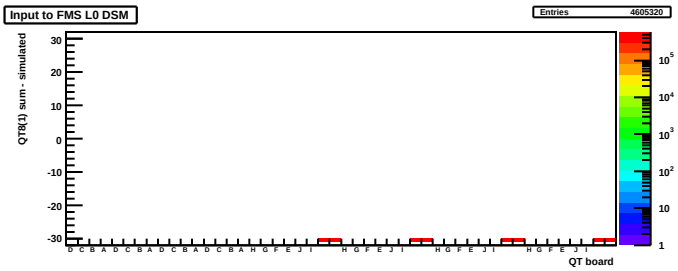

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

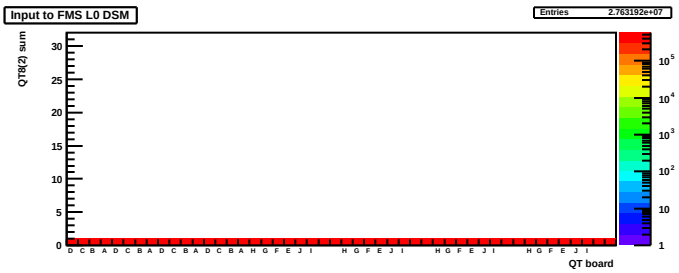

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

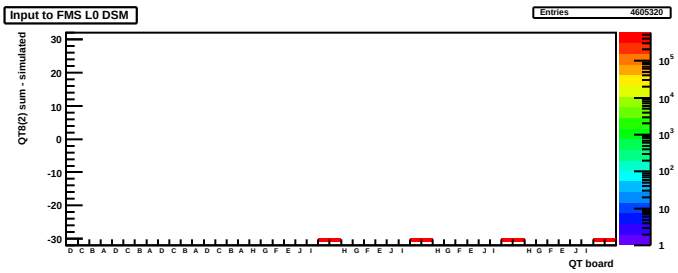

Input to FMS L0 DSM
Entries 2.763192e+07
QT8(0) sum
QT board

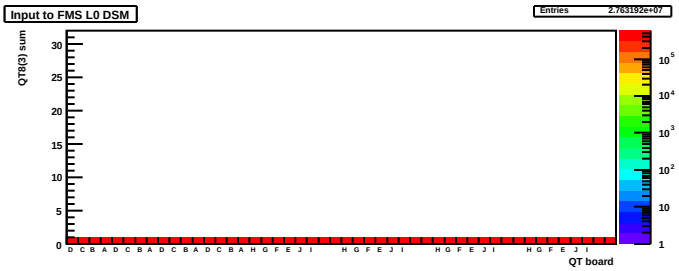

Input to FMS L0 DSM
Entries 4605320
QT8(0) sum - simulated
QT board

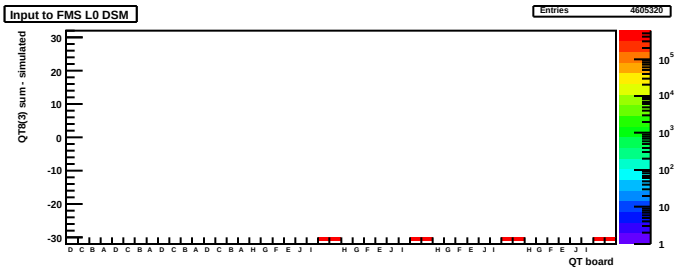

Input to FMS L0 DSM
Entries 2.763192e+07
QT8(1) sum
QT board

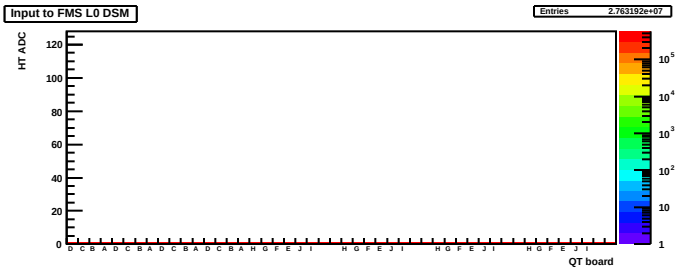

Input to FMS L0 DSM
Entries 4605320
QT8(1) sum - simulated
QT board

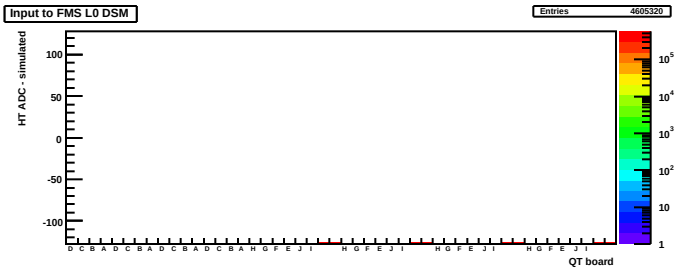

Input to FMS L0 DSM
Entries 2.763192e+07
QT8(2) sum
QT board

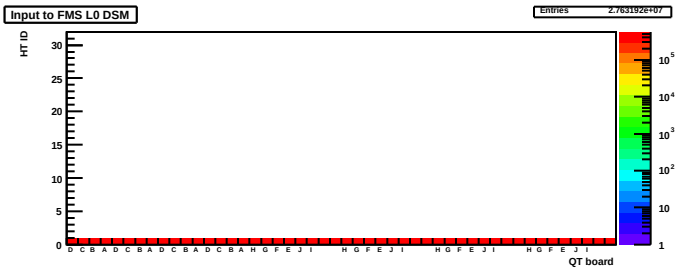

Input to FMS L0 DSM
Entries 4605320
QT8(2) sum - simulated
QT board

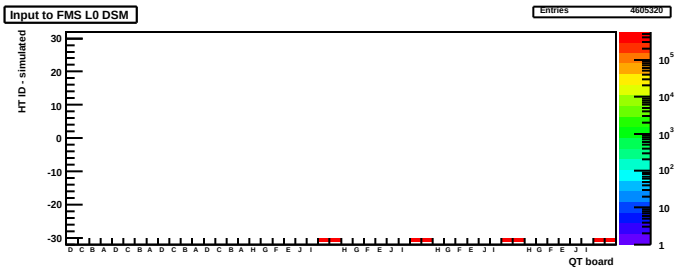

Input to FMS L0 DSM
Entries 2.763192e+07
QT8(3) sum
QT board

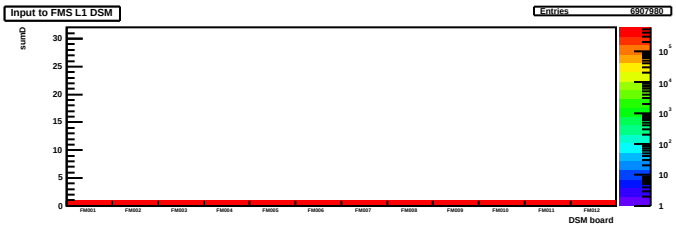

Input to FMS L0 DSM
Entries 4605320
QT8(3) sum - simulated
QT board

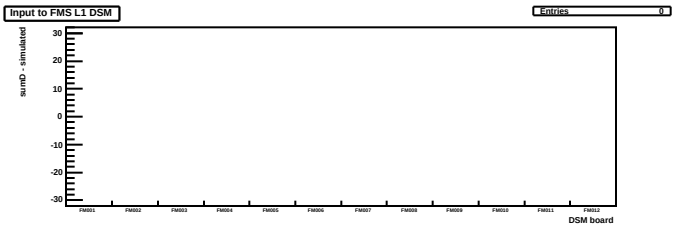

Input to FMS L0 DSM
Entries 2.763192e+07
HT ADC
QT board

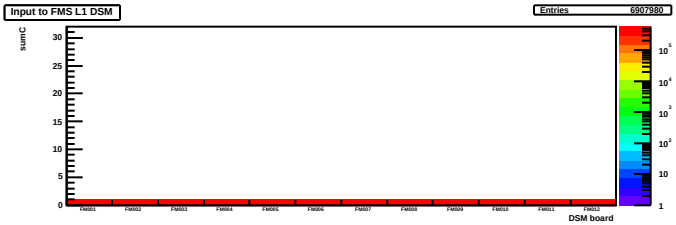

Input to FMS L0 DSM
Entries 4605320
HT ADC - simulated
QT board

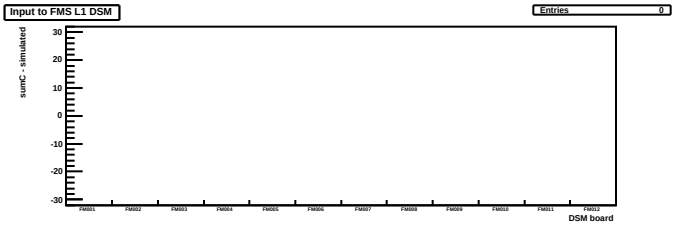

Input to FMS L0 DSM
Entries 2.763192e+07
HT ID
QT board

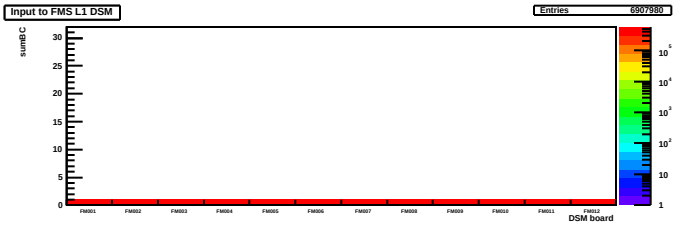

Input to FMS L0 DSM
Entries 4605320
HT ID - simulated
QT board

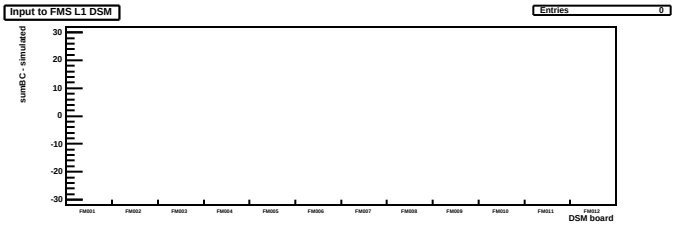

Input to FMS L1 DSM
Entries 6907980
sumD
DSM board

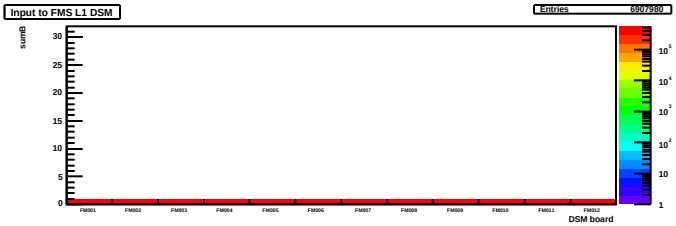

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

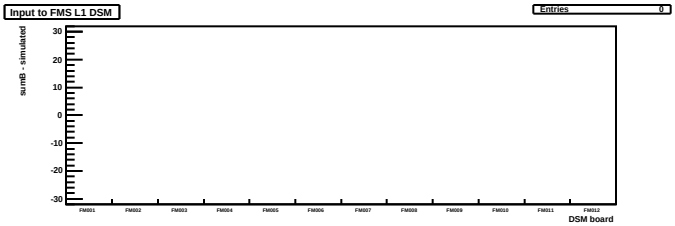

Input to FMS L1 DSM
Entries 6907980
sumC
DSM board

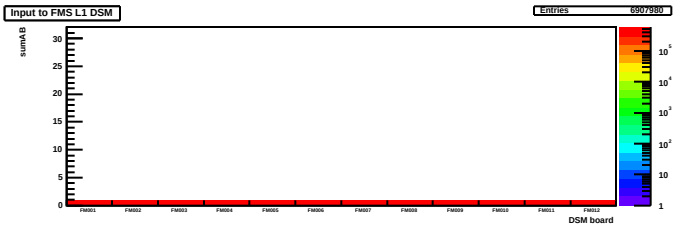

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

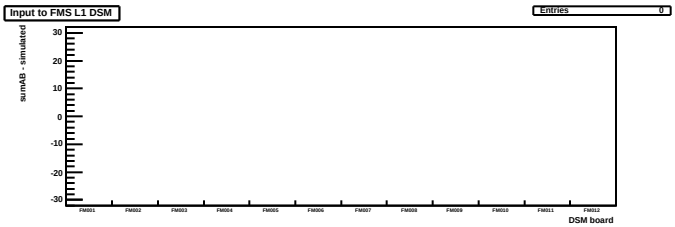

Input to FMS L1 DSM
Entries 6907980
sumBC
DSM board

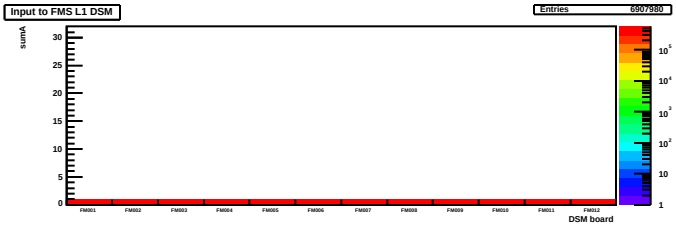

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

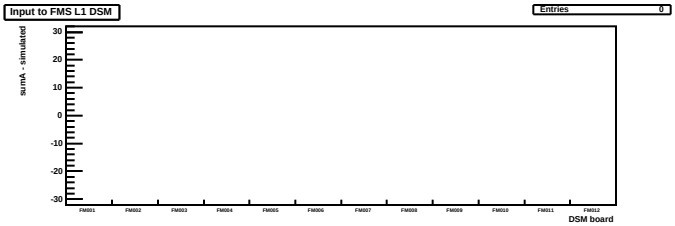

Input to FMS L1 DSM
Entries 6907980
sumB
DSM board

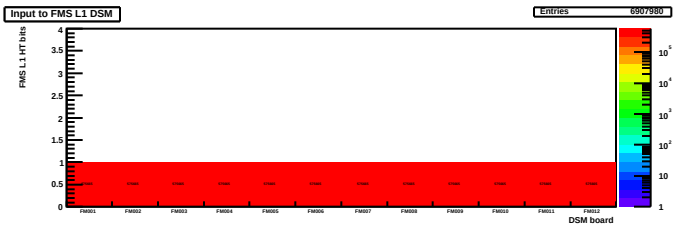

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

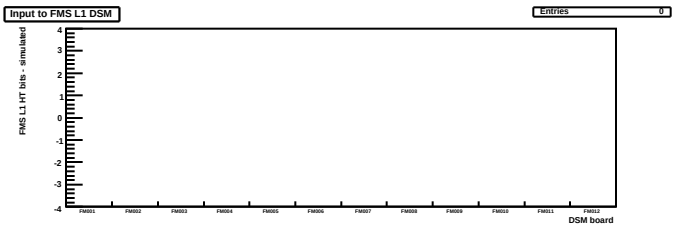

Input to FMS L1 DSM
Entries 6907980
sumAB
DSM board

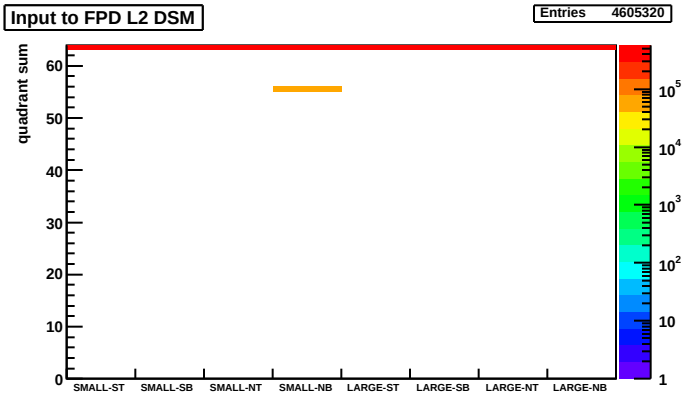

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

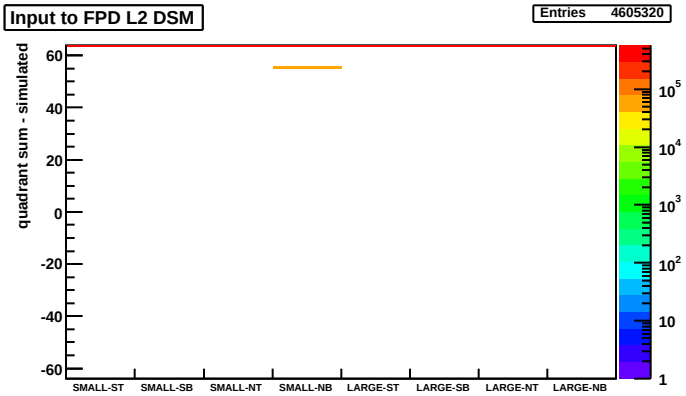

Input to FMS L1 DSM
Entries 6907980
sumA
DSM board

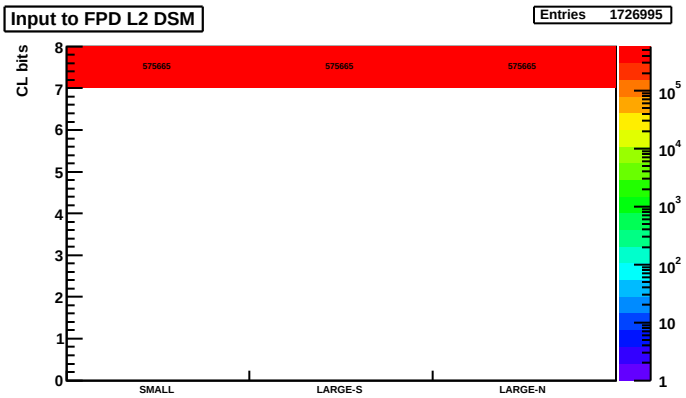

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

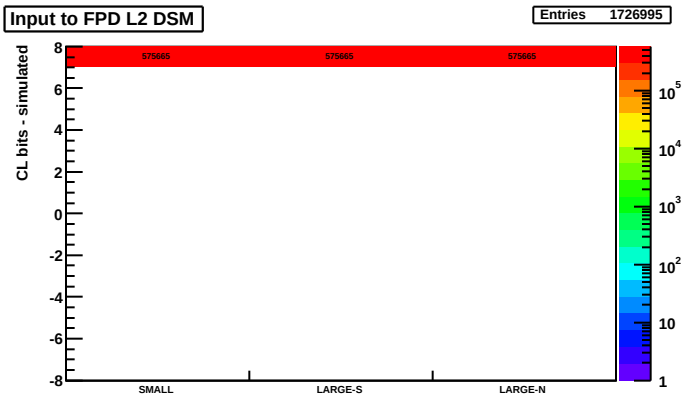

Input to FMS L1 DSM
Entries 6907980
FMS L1 HT bits
DSM board

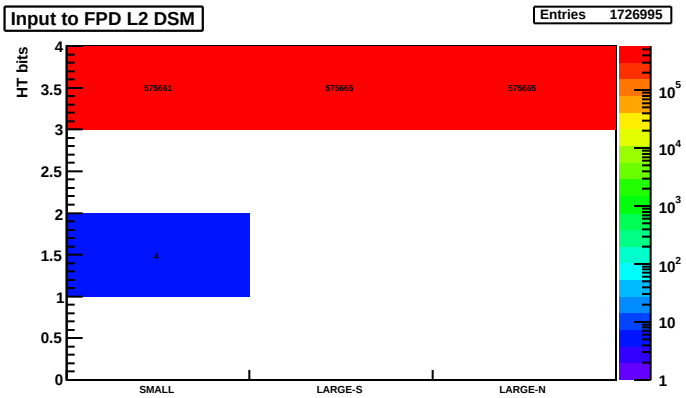

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

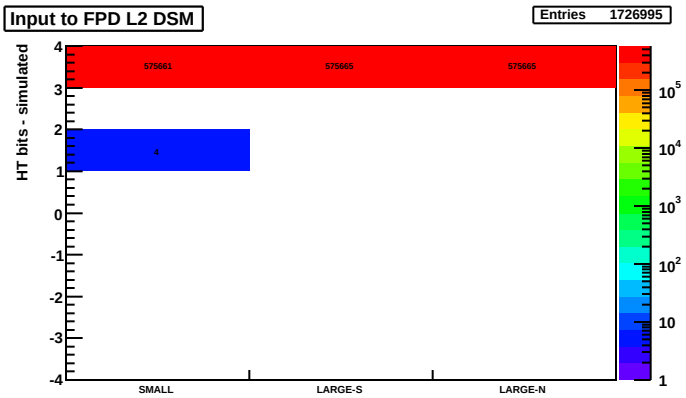

Input to FPD L2 DSM
Entries 4605320
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

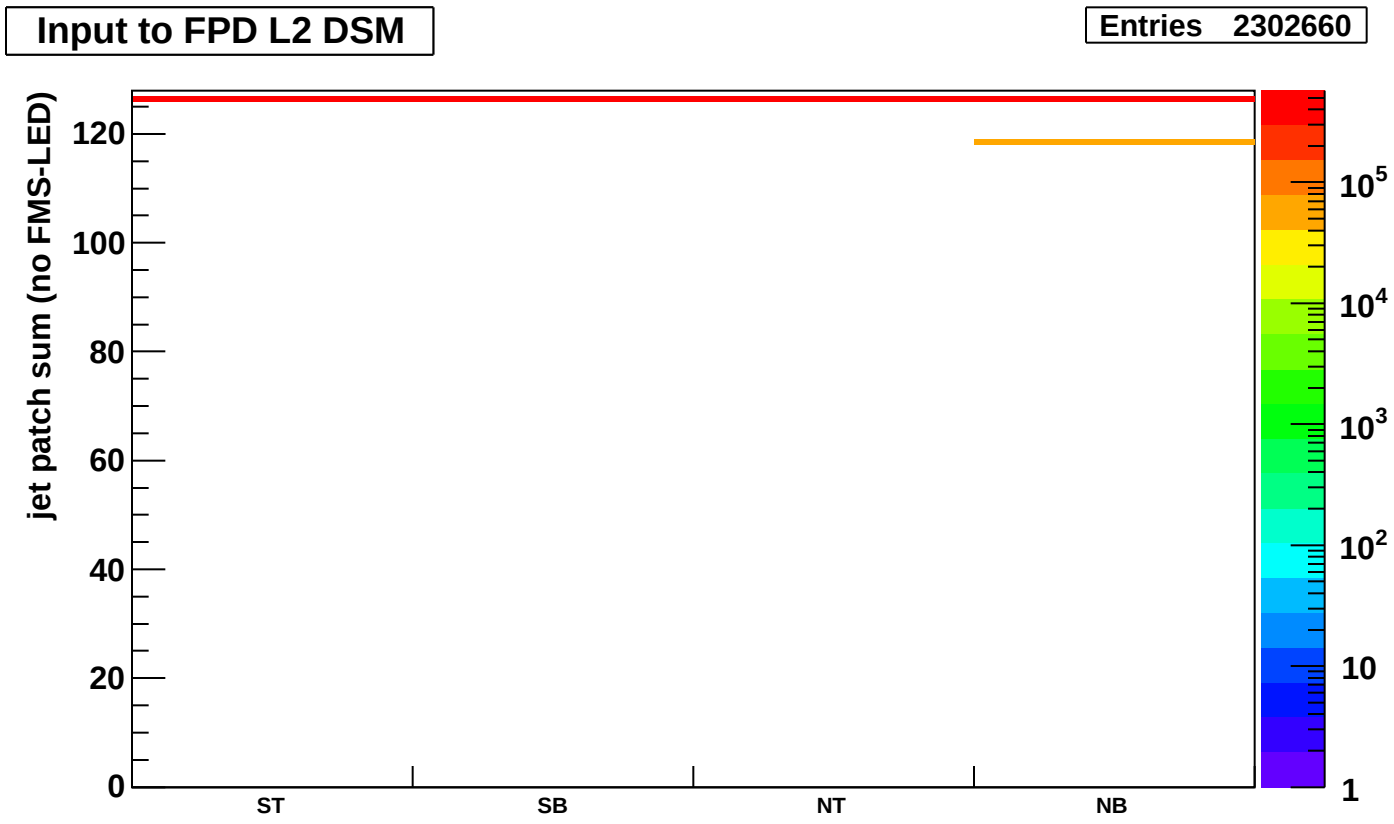

Input to FPD L2 DSM
Entries 4605320
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

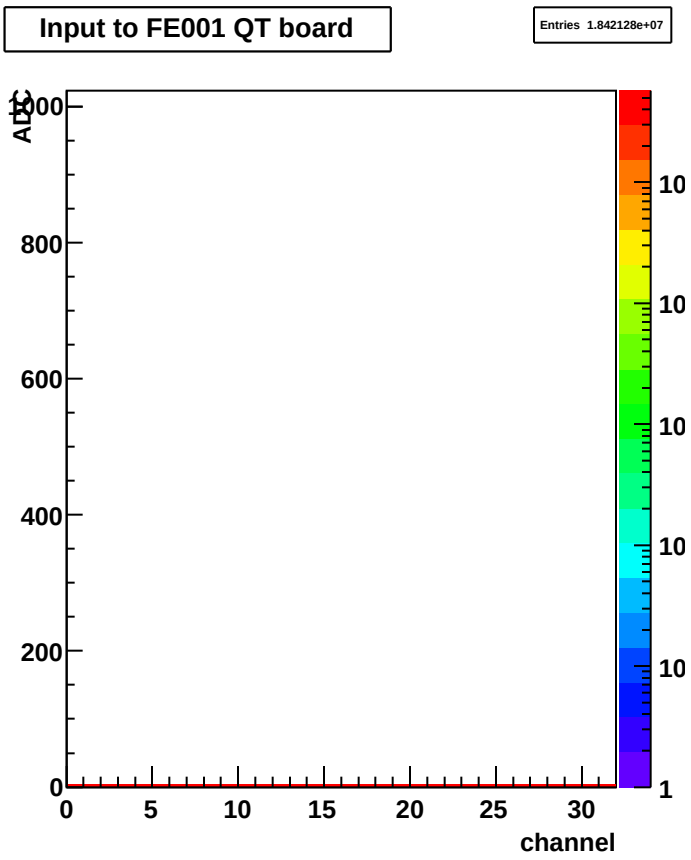

Input to FPD L2 DSM
Entries 1726995
CL bits
575665
575665
575665
SMALL
LARGE-S
LARGE-N

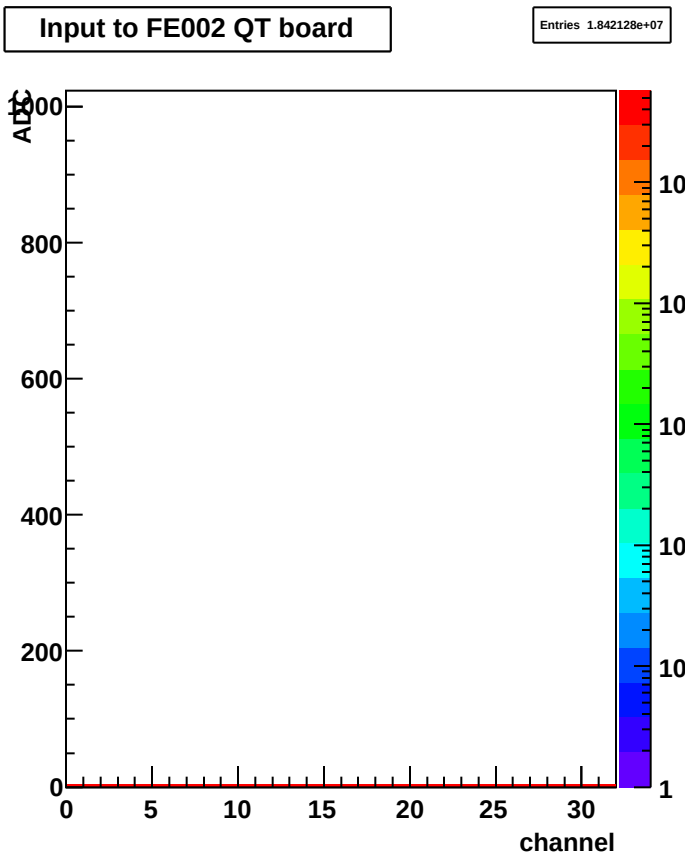

Input to FPD L2 DSM
Entries 1726995
CL bits - simulated
575665
575665
575665
SMALL
LARGE-S
LARGE-N

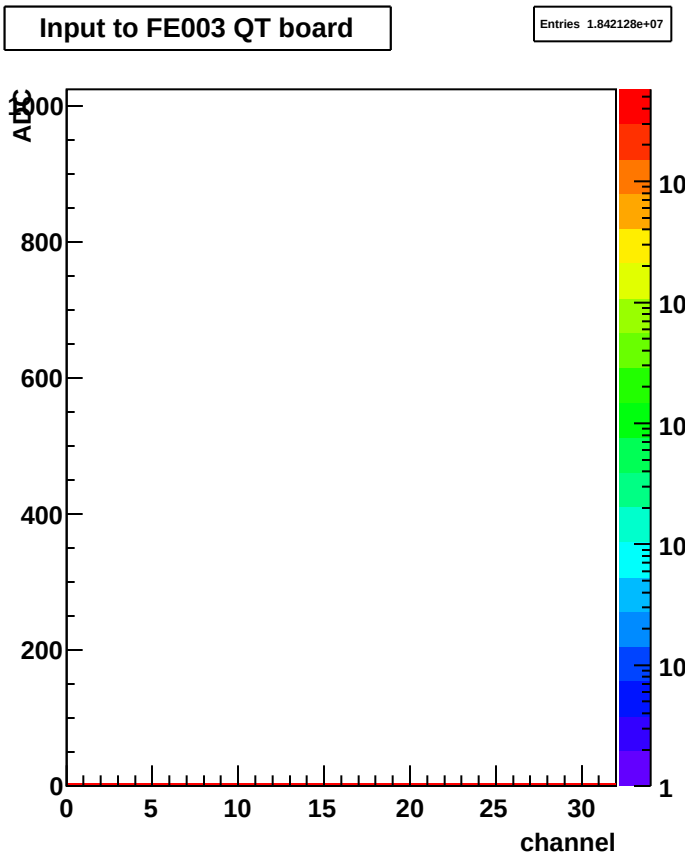

Input to FPD L2 DSM
Entries 1726995
HT bits
575661
575665
575665
4
SMALL
LARGE-S
LARGE-N

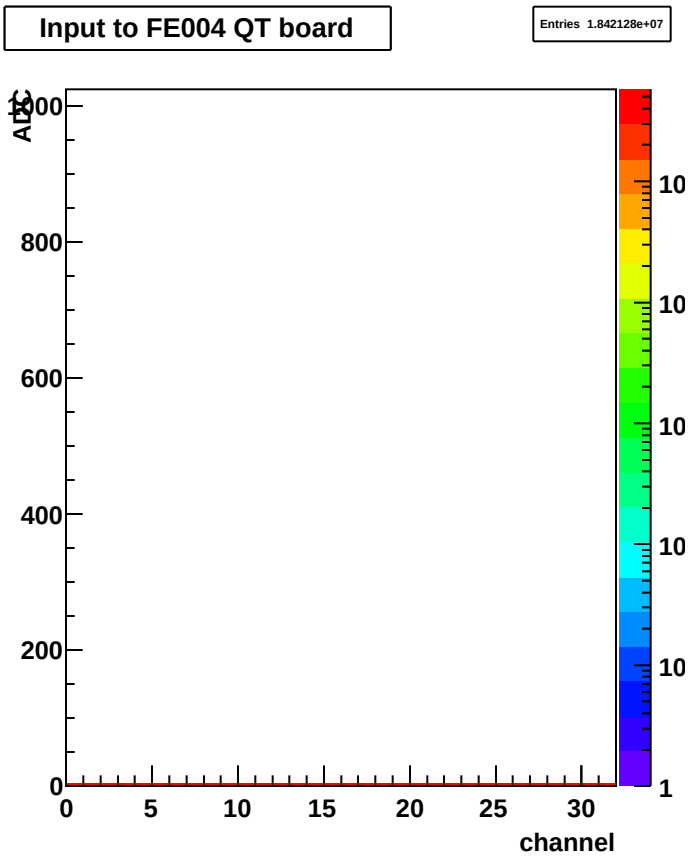

Input to FPD L2 DSM
Entries 1726995
HT bits - simulated
575661
575665
575665
4
SMALL
LARGE-S
LARGE-N


Input to FPD L2 DSM
Entries 2302660
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

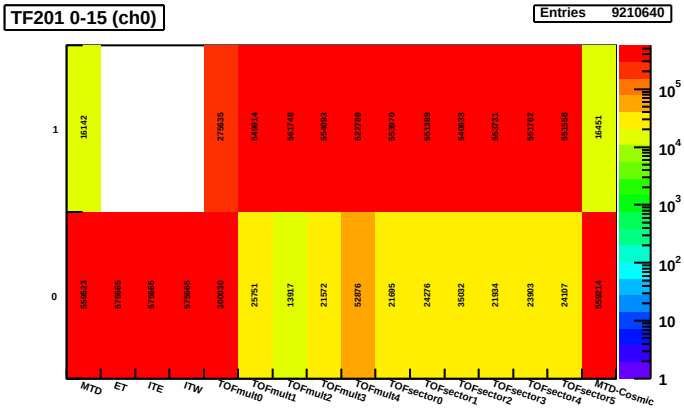

Input to FE001 QT board
Entries 1.842128e+07
ADC
channel


Input to FE002 QT board
Entries 1.842128e+07
ADC
channel

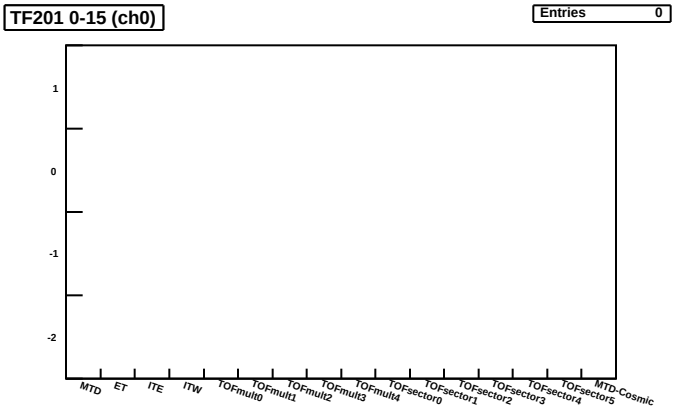

Input to FE003 QT board
Entries 1.842128e+07
ADC
channel


Input to FE004 QT board
Entries 1.842128e+07
ADC
channel

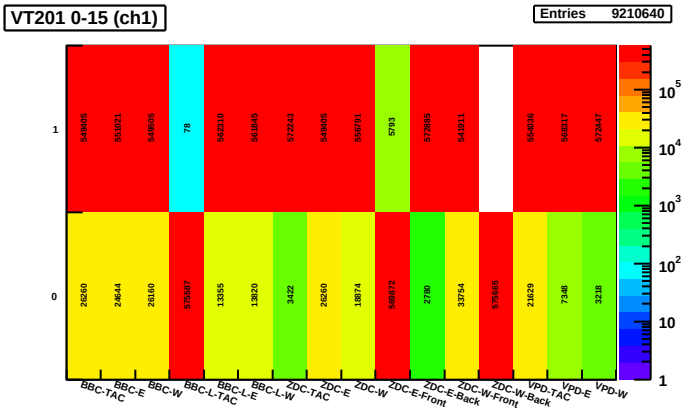
TF201 0-15 (ch0)



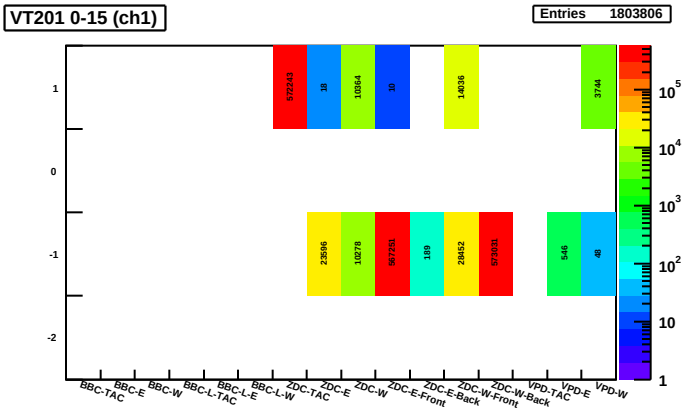
TF201 0-15 (ch0)



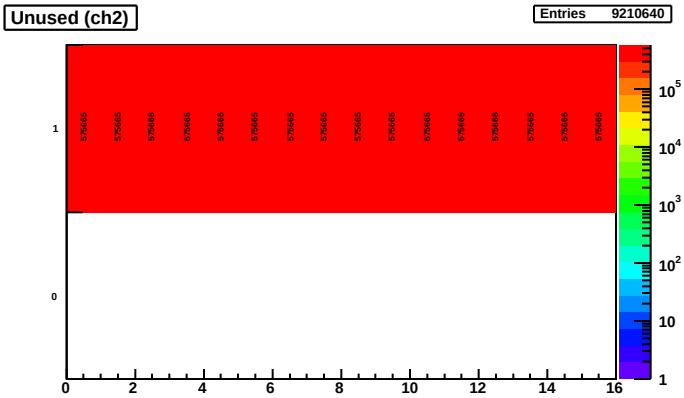
VT201 0-15 (ch1)

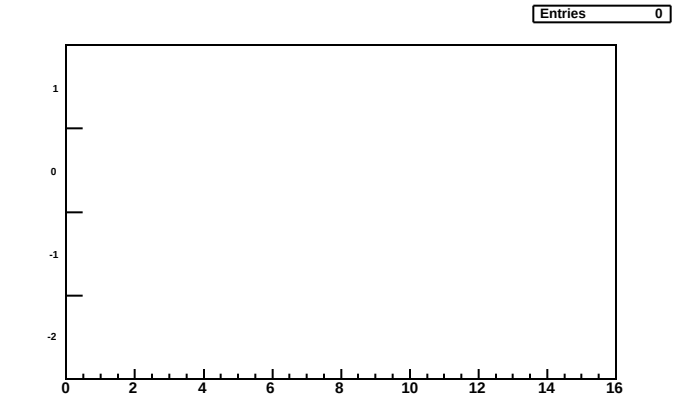


VT201 0-15 (ch1)

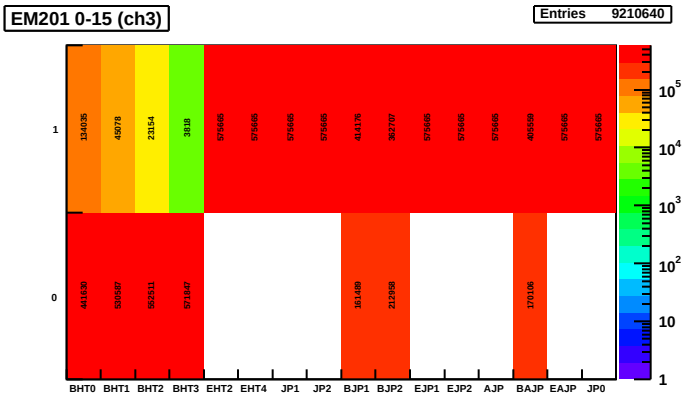


Unused (ch2)

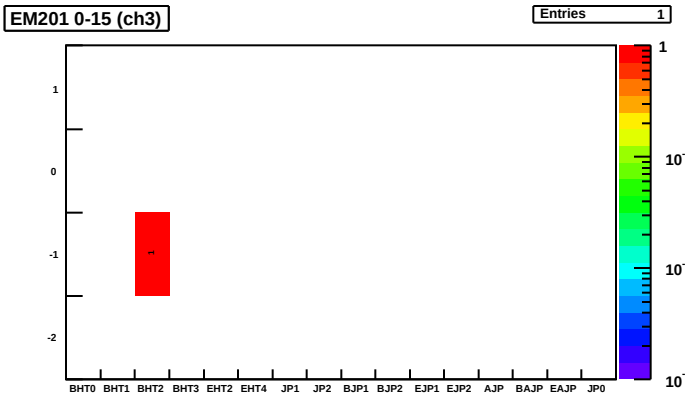




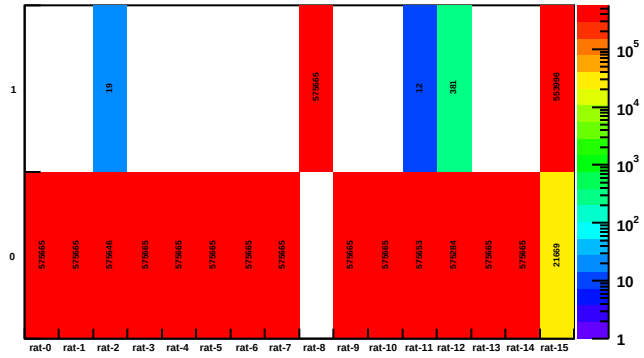
EM201 0-15 (ch3)

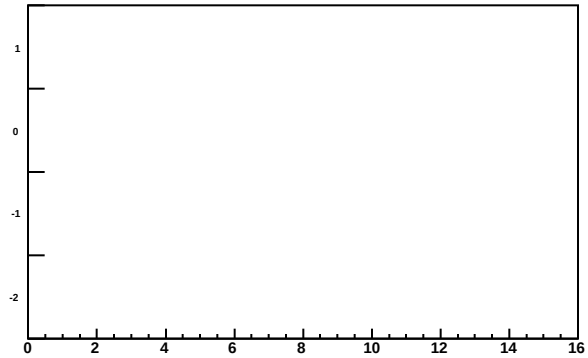


EM201 0-15 (ch3)

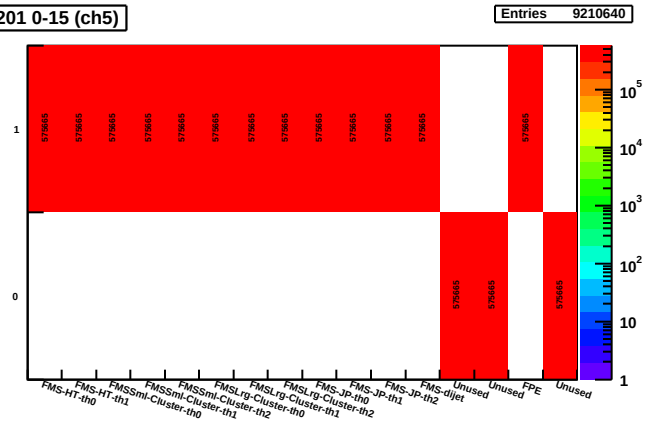


RAT board (ch4)

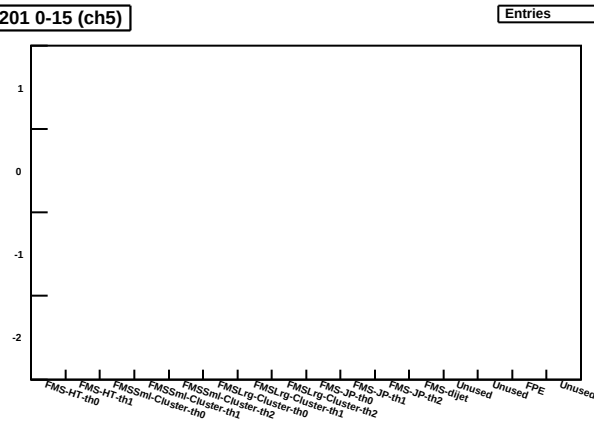




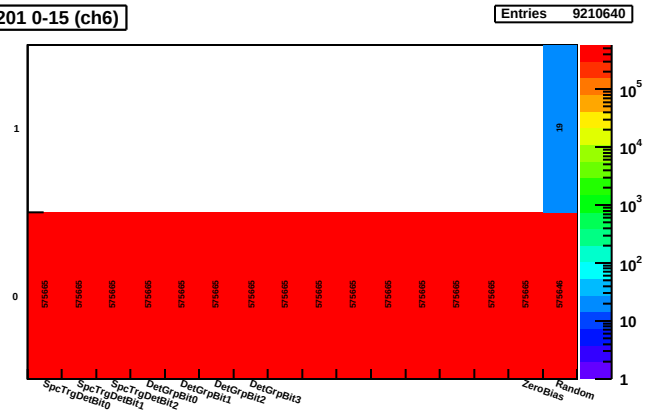
FP201 0-15 (ch5)

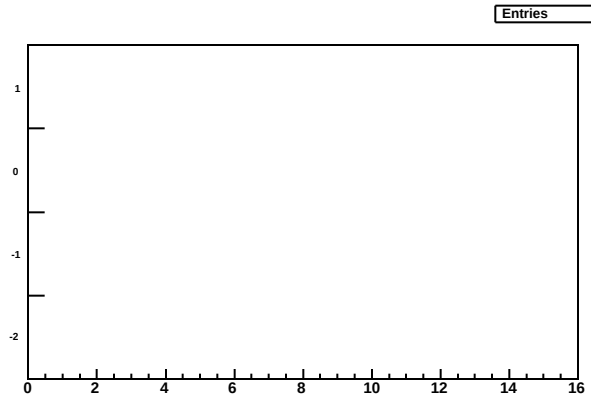
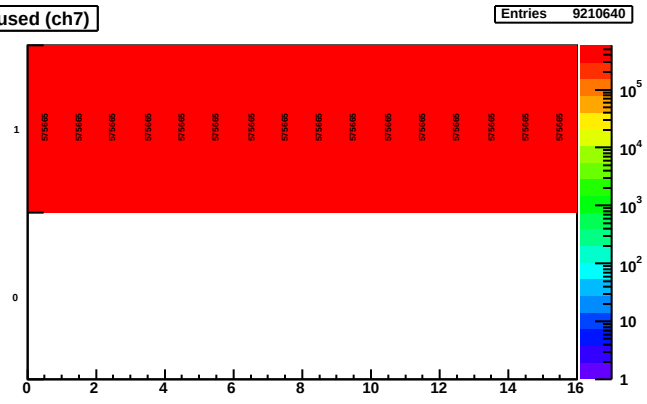
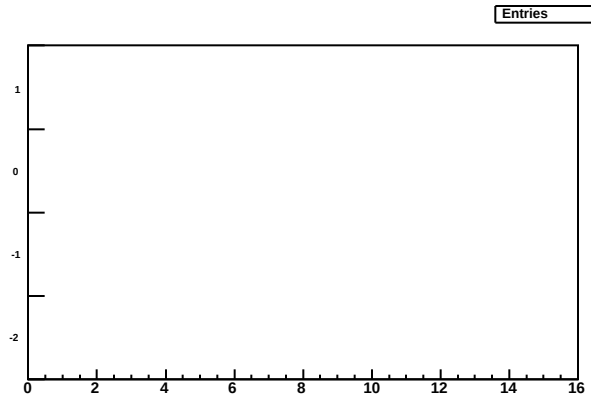


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)