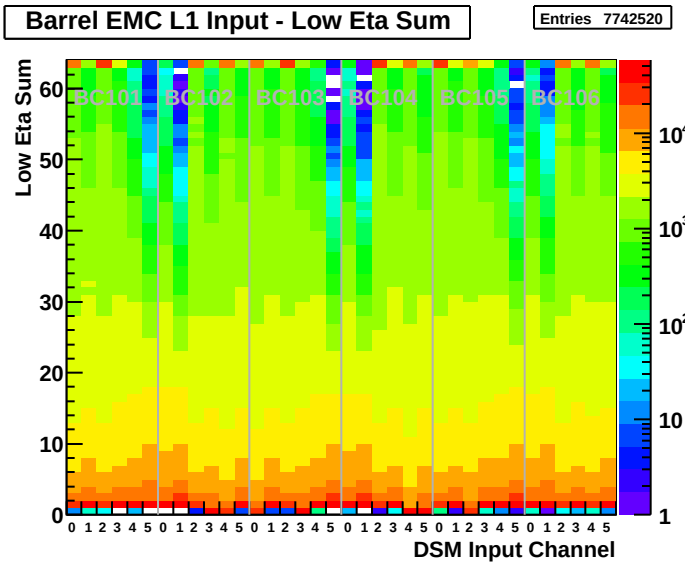
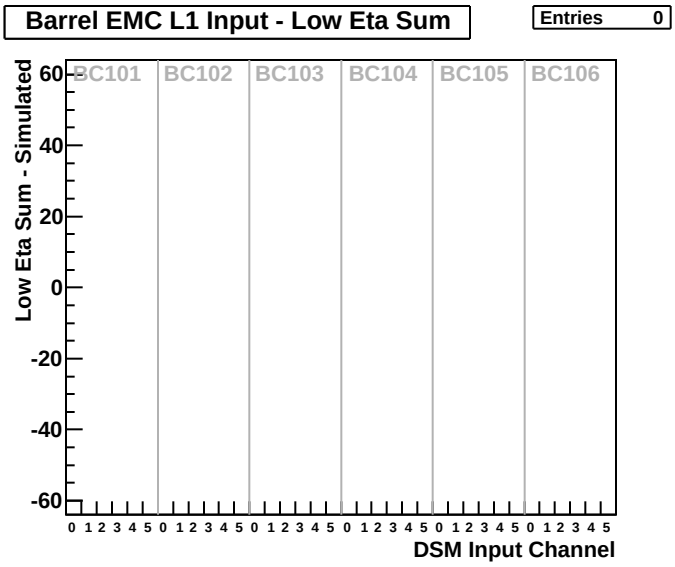
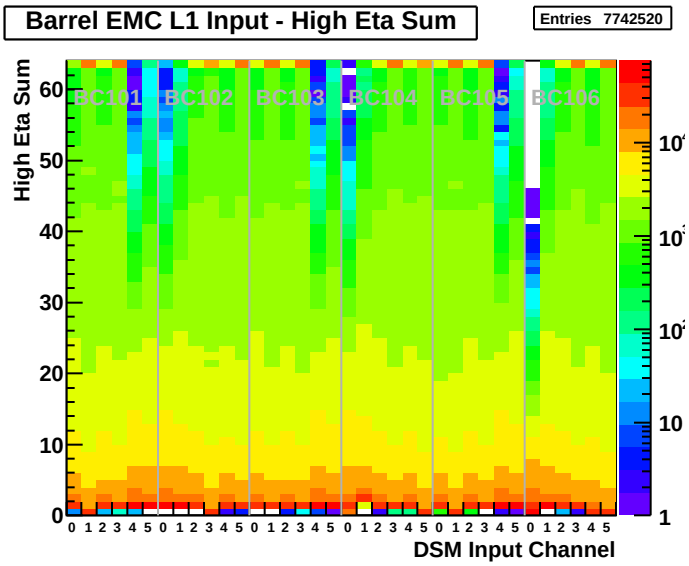
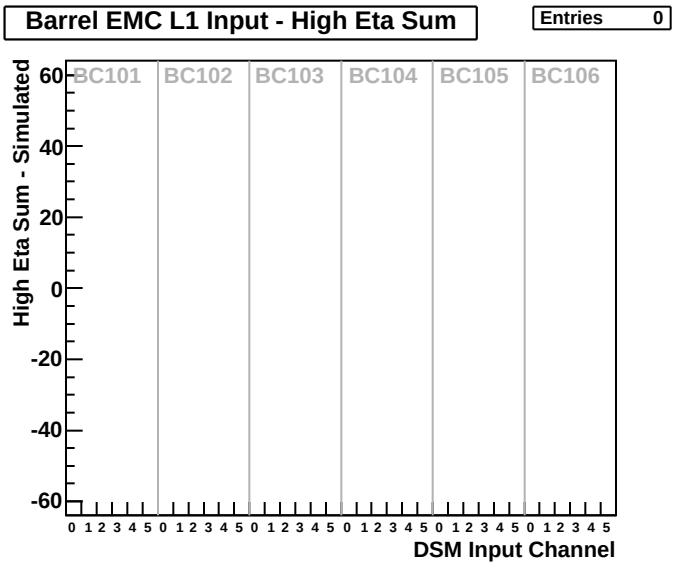
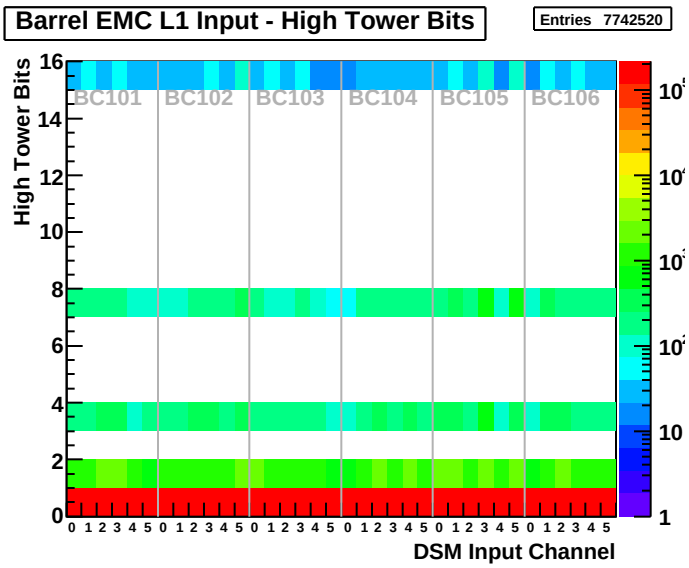
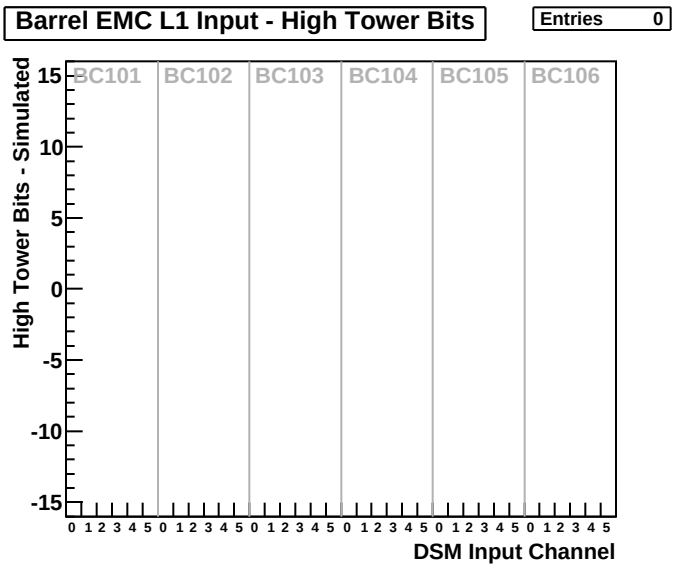

Barrel EMC L1 Input - Low Eta Sum
Entries 7742520
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

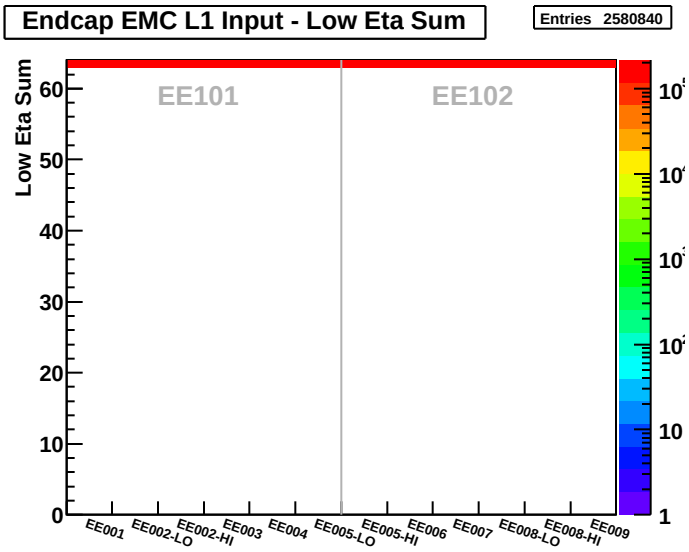

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

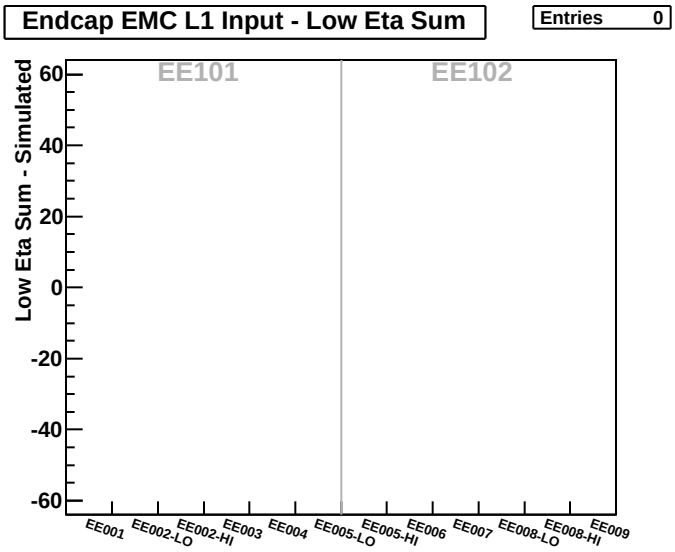

Barrel EMC L1 Input - High Eta Sum
Entries 7742520
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

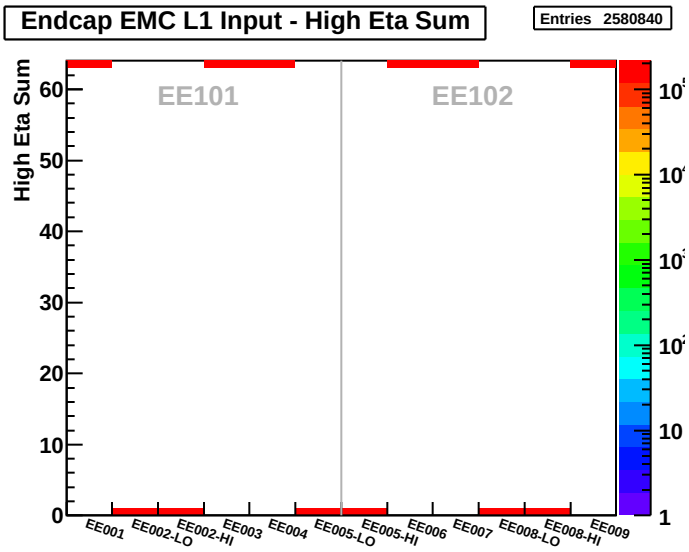

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

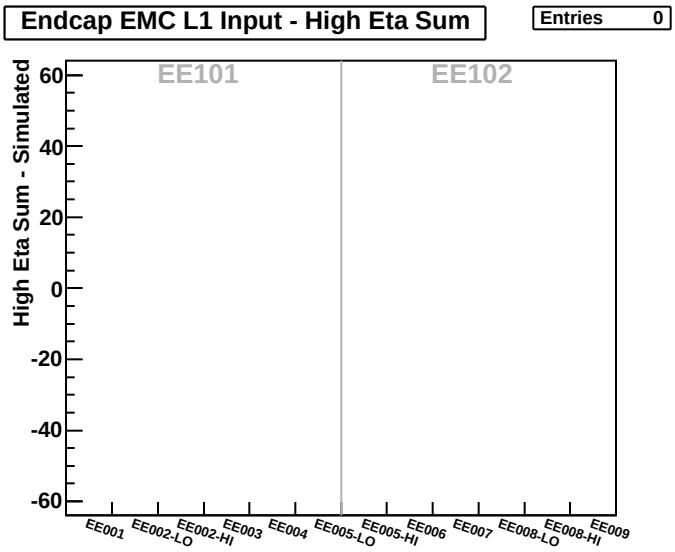

Barrel EMC L1 Input - High Tower Bits
Entries 7742520
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

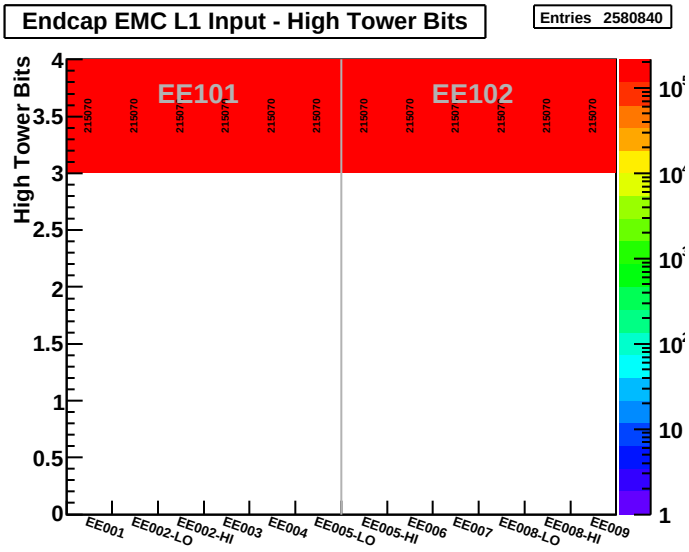

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

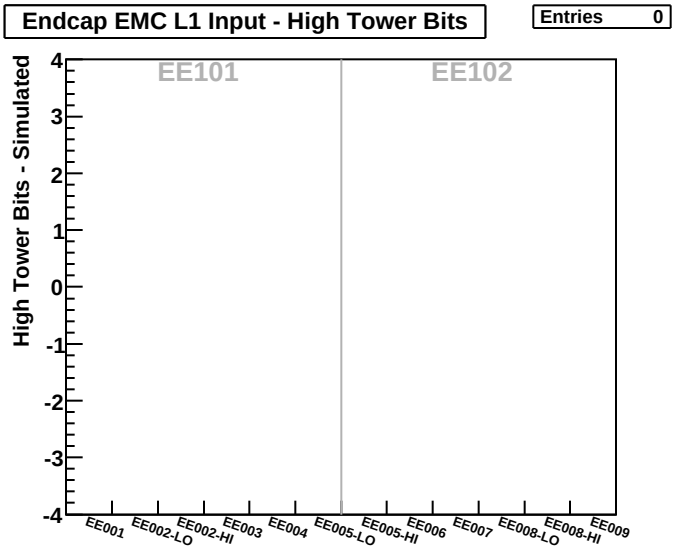

Endcap EMC L1 Input - Low Eta Sum
Entries 2580840
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

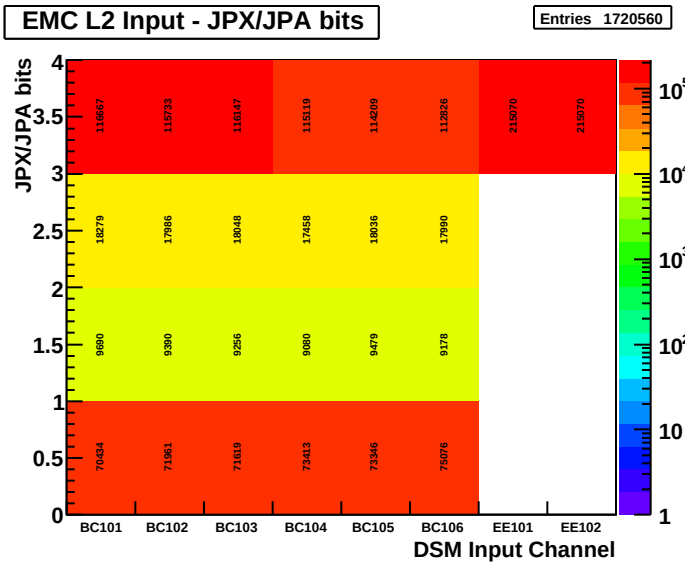

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

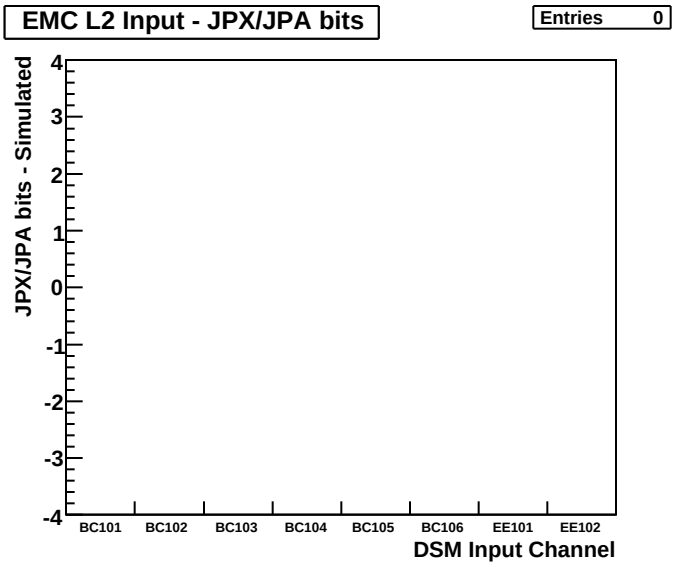

Endcap EMC L1 Input - High Eta Sum
Entries 2580840
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

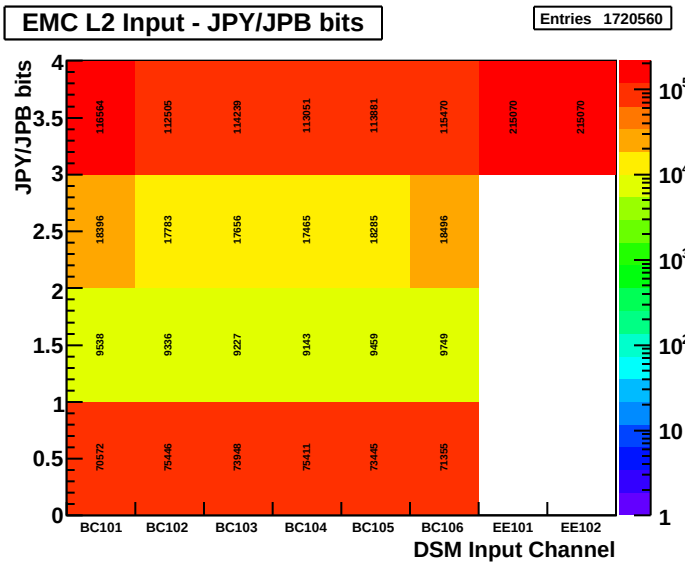

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

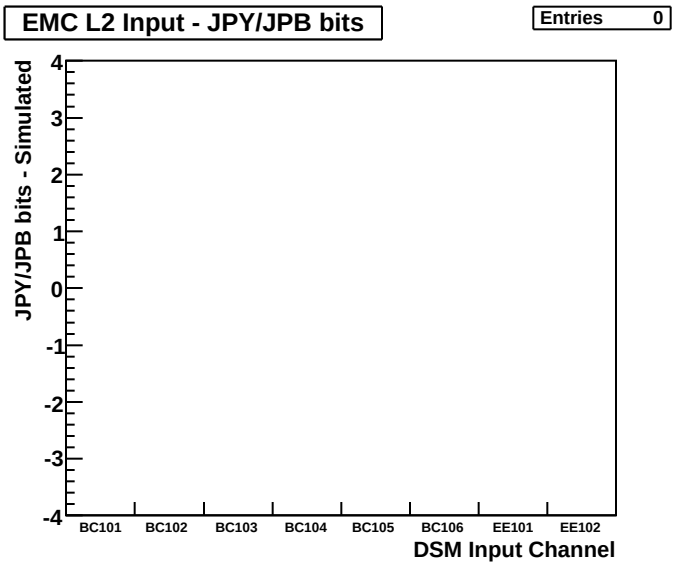

Endcap EMC L1 Input - High Tower Bits
Entries 2580840
High Tower Bits
EE101
EE102
215070
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

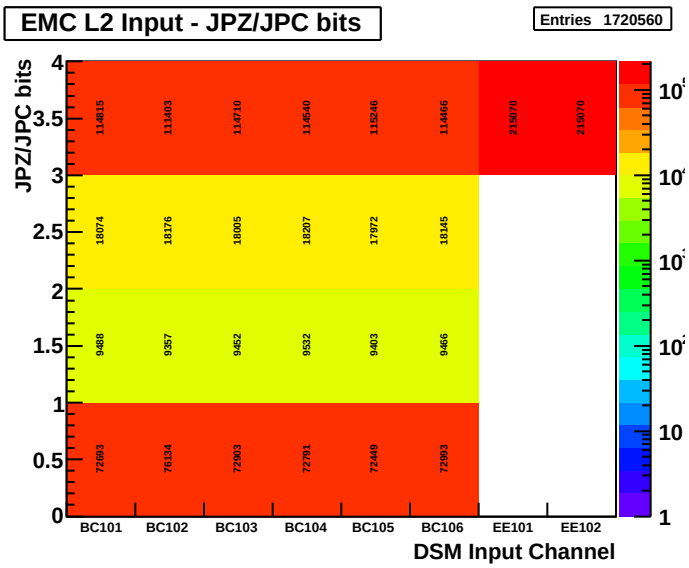

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

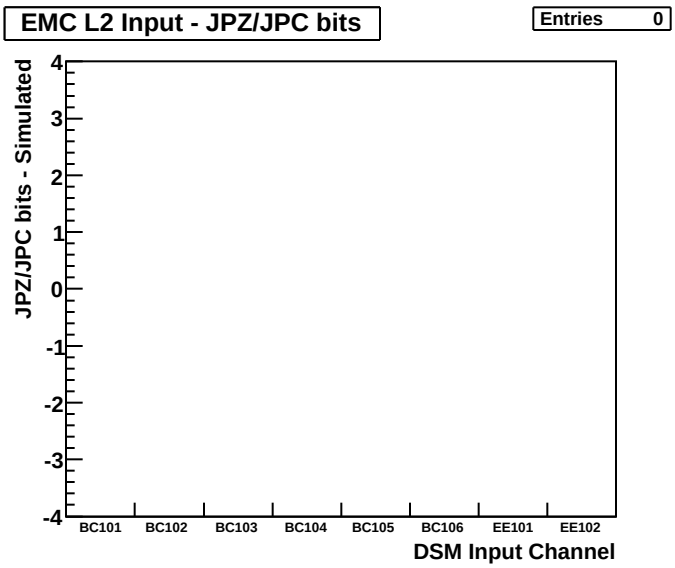

EMC L2 Input - JPX/JPA bits
Entries 1720560
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
116667 115733 116147 115119 114209 112826 215070 215070
18279 17986 18048 17458 18036 17990
9660 9390 9256 9080 9479 9178
70434 71961 71619 73413 73346 75076

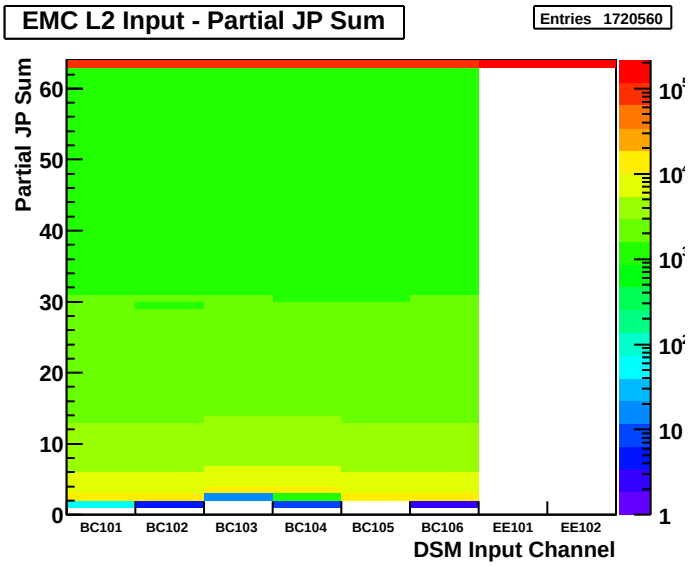

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

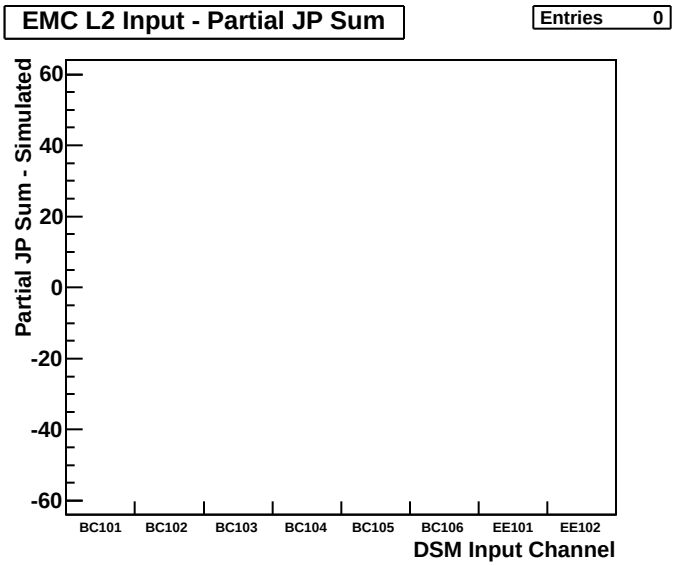

EMC L2 Input - JPY/JPB bits
Entries 1720560
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
116564 112505 114239 113051 113881 115470 215070 215070
18396 17783 17656 17465 18285 18496
9538 9336 9227 9143 9459 9749
70572 75446 73948 75411 73445 71355

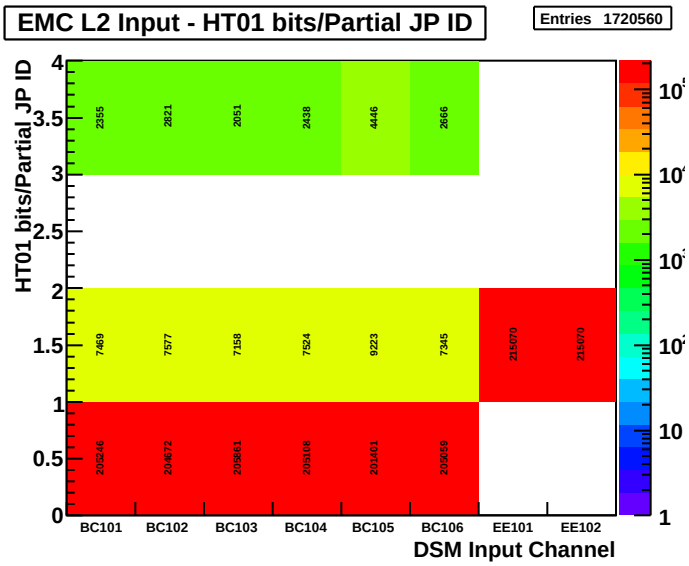

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

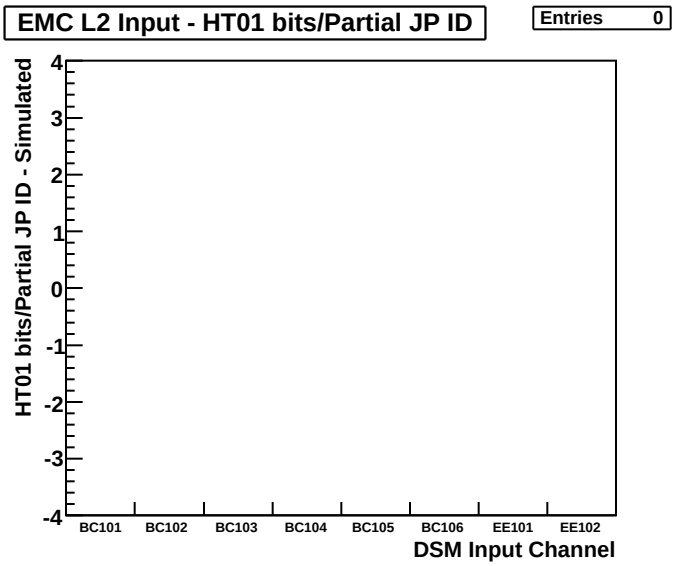

EMC L2 Input - JPZ/JPC bits
Entries 1720560
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
114815 111403 114710 114540 115246 114466 215070 215070
18074 18176 18005 18207 17972 18145
9488 9357 9452 9532 9403 9466
72693 76134 72803 72791 72449 72993

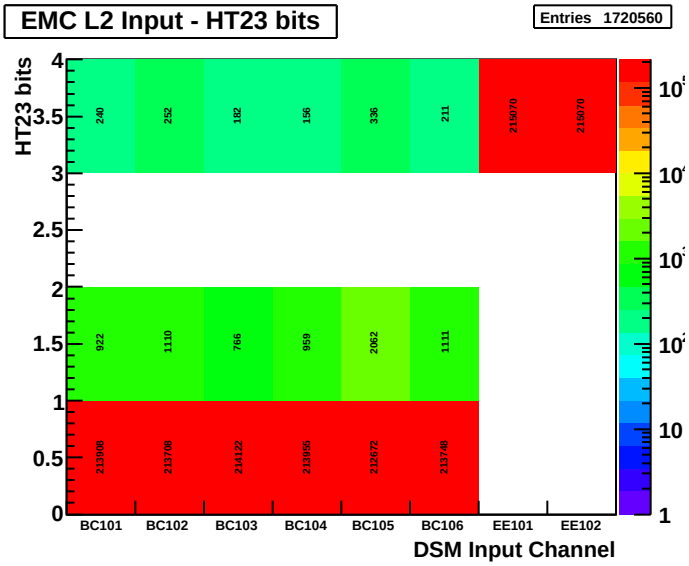

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

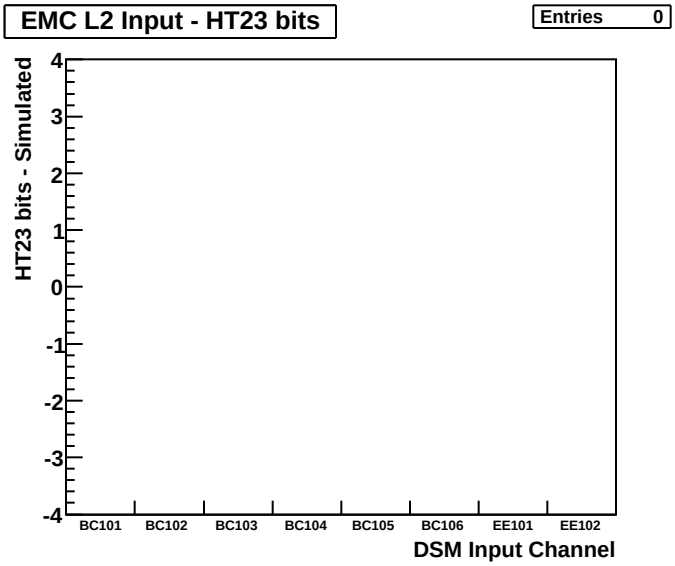

EMC L2 Input - Partial JP Sum
Entries 1720560
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

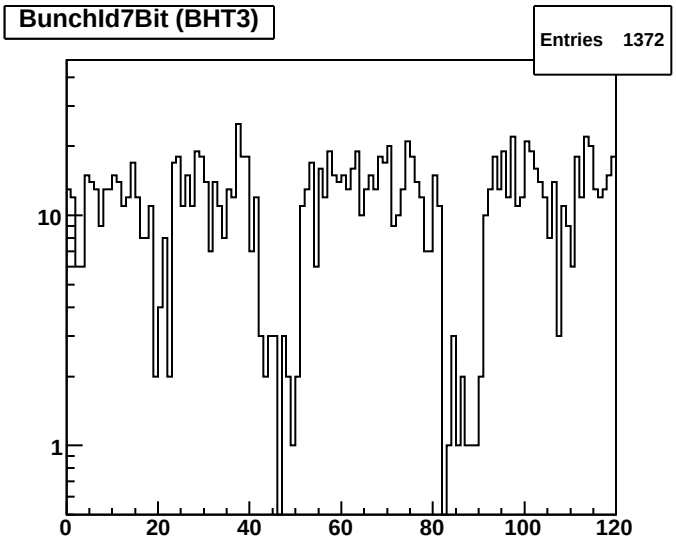

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

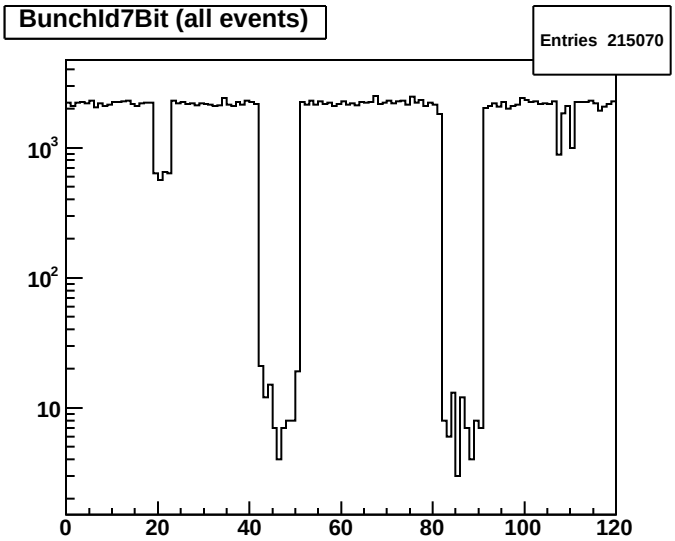

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1720560
HT01 bits/Partial JP ID
2355 2821 2051 2438 4446 2666
7469 7577 7158 7524 9223 7345 215070 215070
205246 204672 205861 205108 201401 205059
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

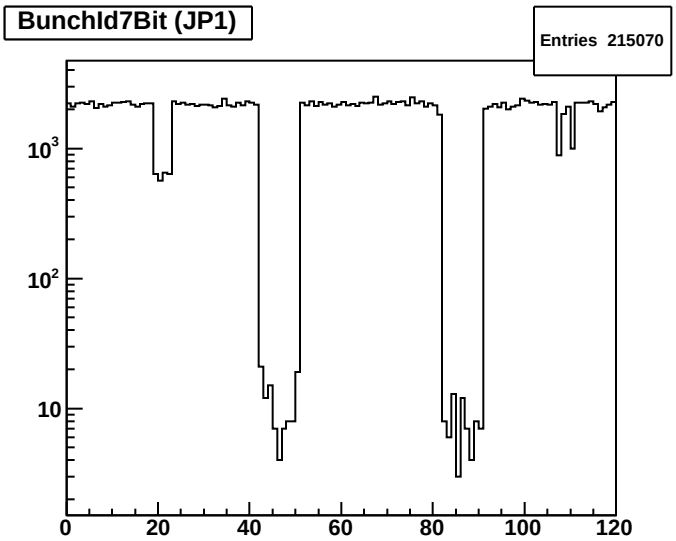

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

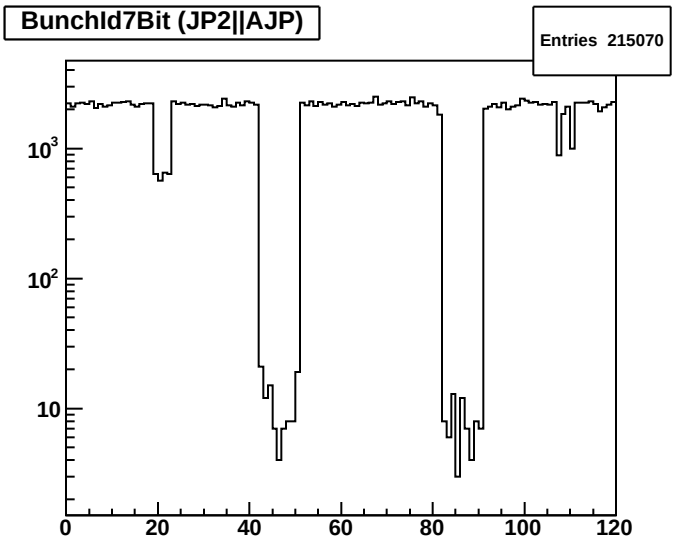

EMC L2 Input - HT23 bits
Entries 1720560
HT23 bits
240 252 182 156 336 211 215070 215070
922 1110 766 959 2062 1111
213908 213708 214122 213955 212672 213748
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

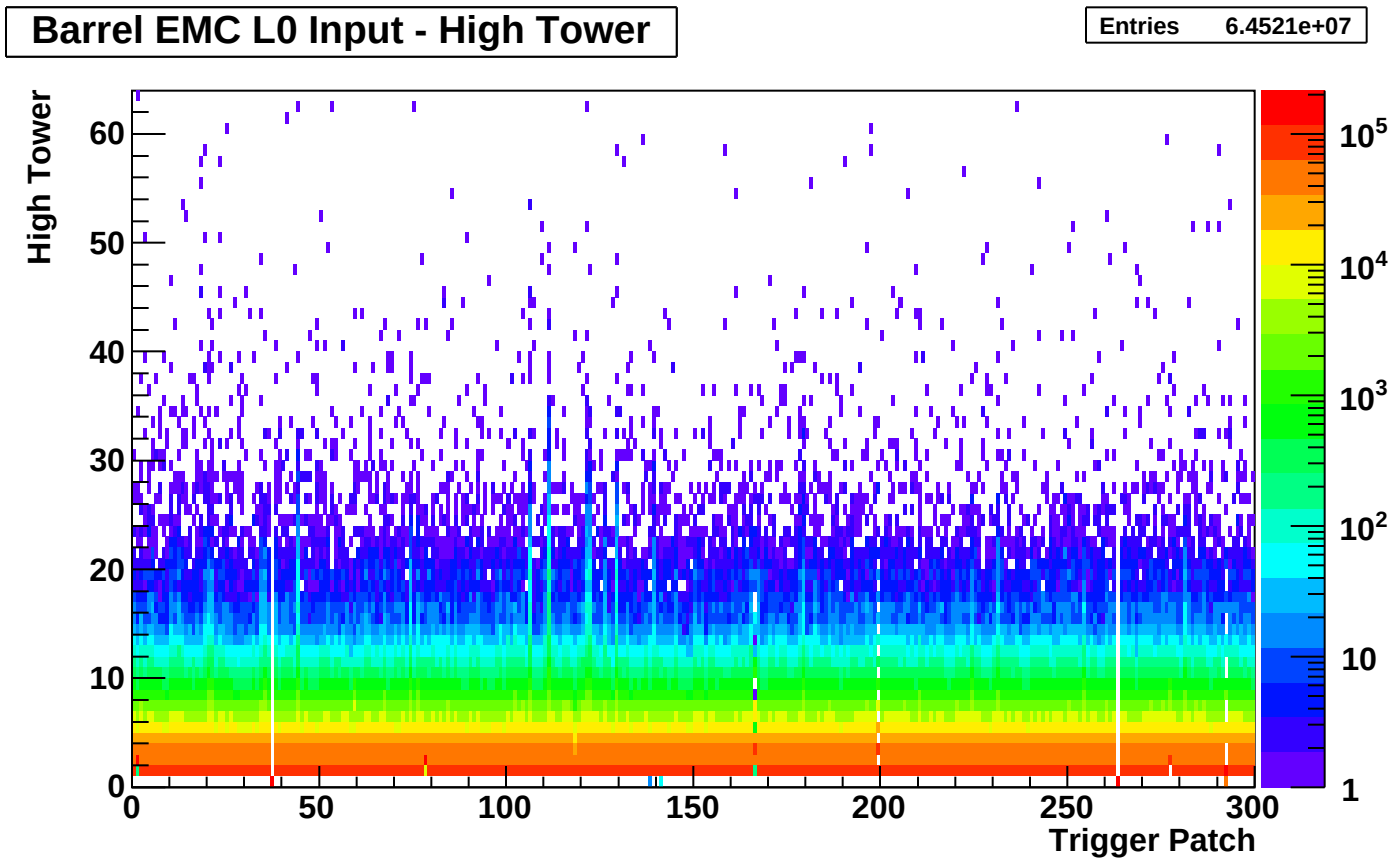

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

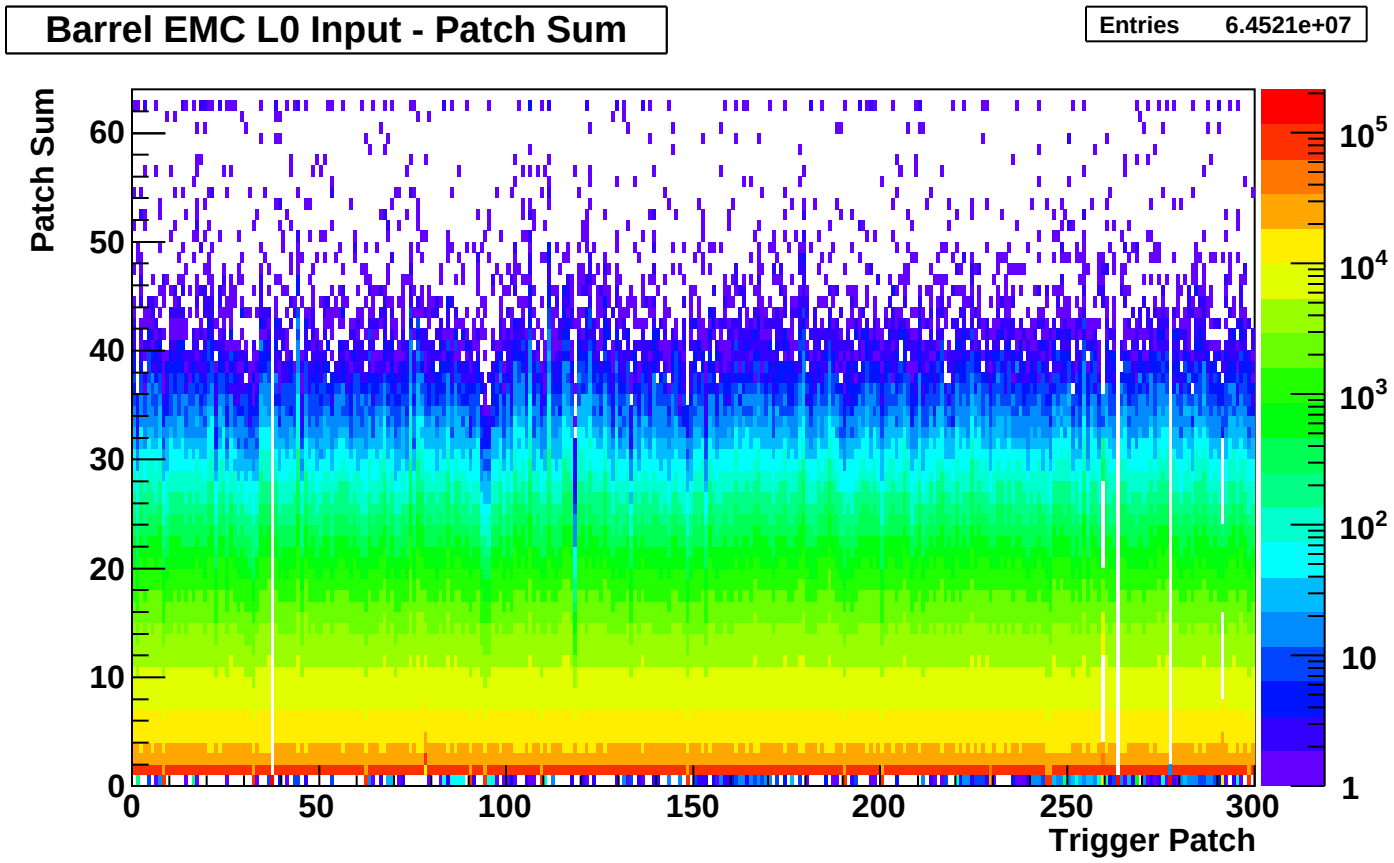

BunchId7Bit (BHT3)
Entries 1372
10
1
0
20
40
60
80
100
120

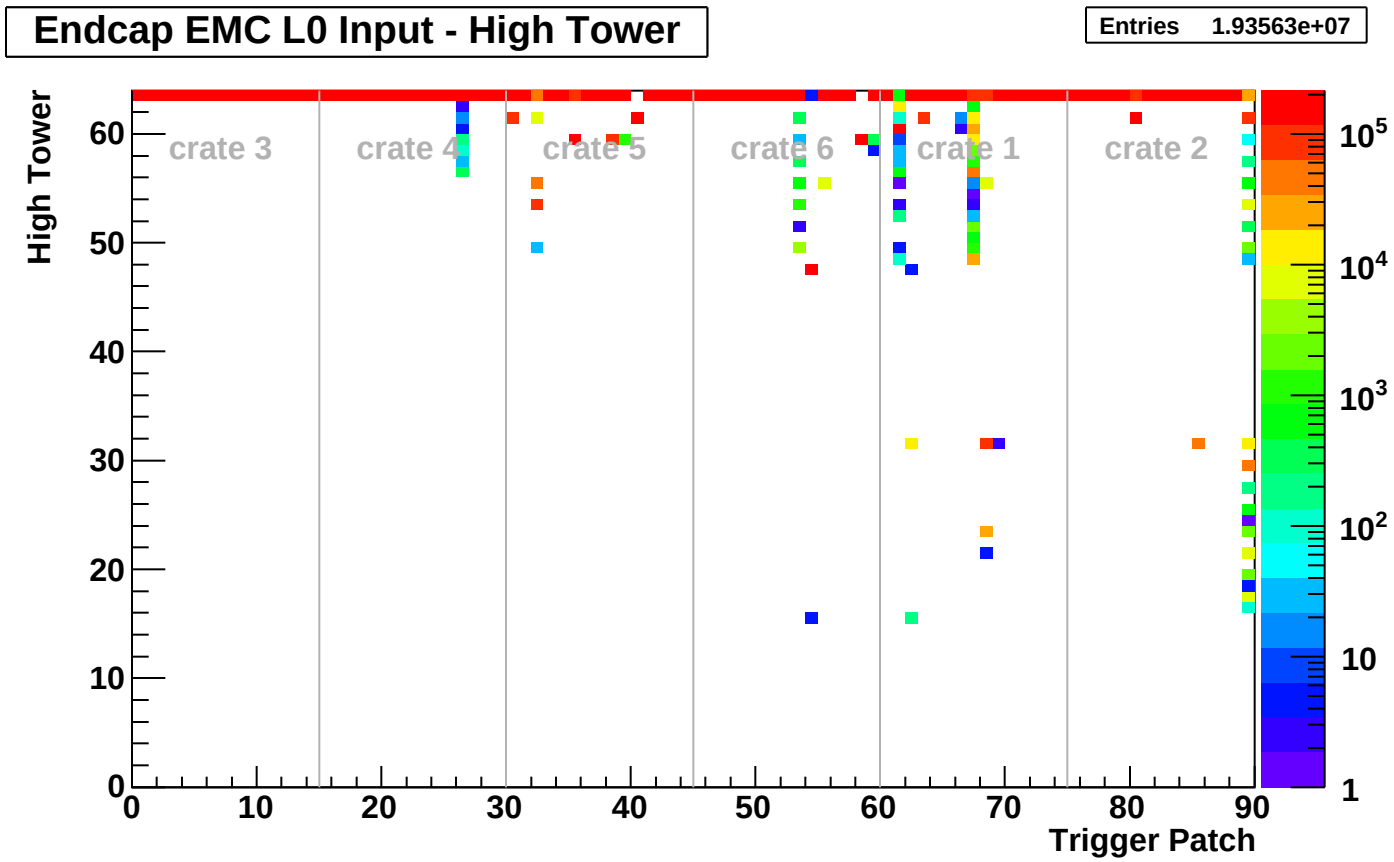

BunchId7Bit (all events)
Entries 215070
10^3
10^2
10
0
20
40
60
80
100
120

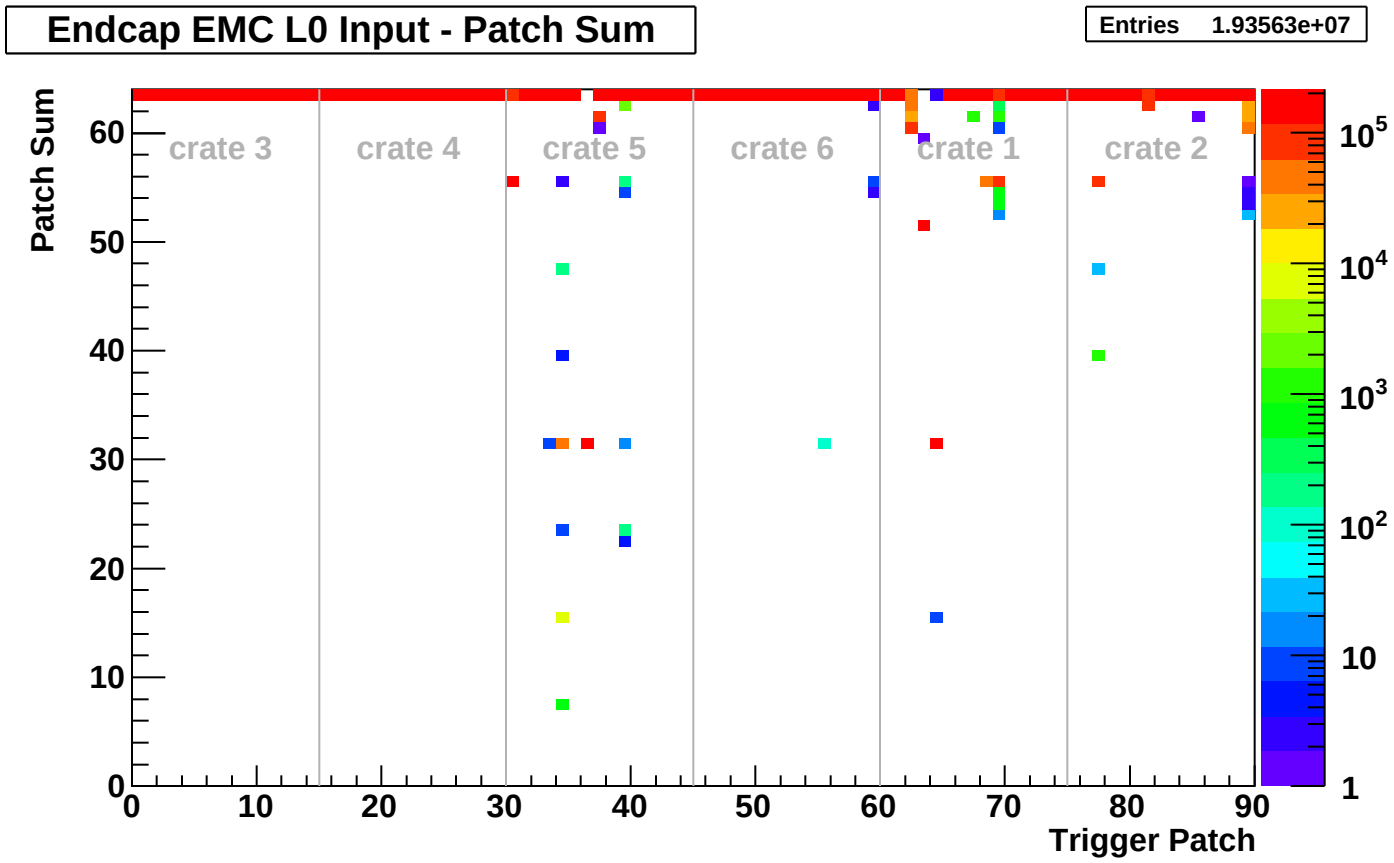

BunchId7Bit (JP1)
Entries 215070
10^3
10^2
10
0
20
40
60
80
100
120

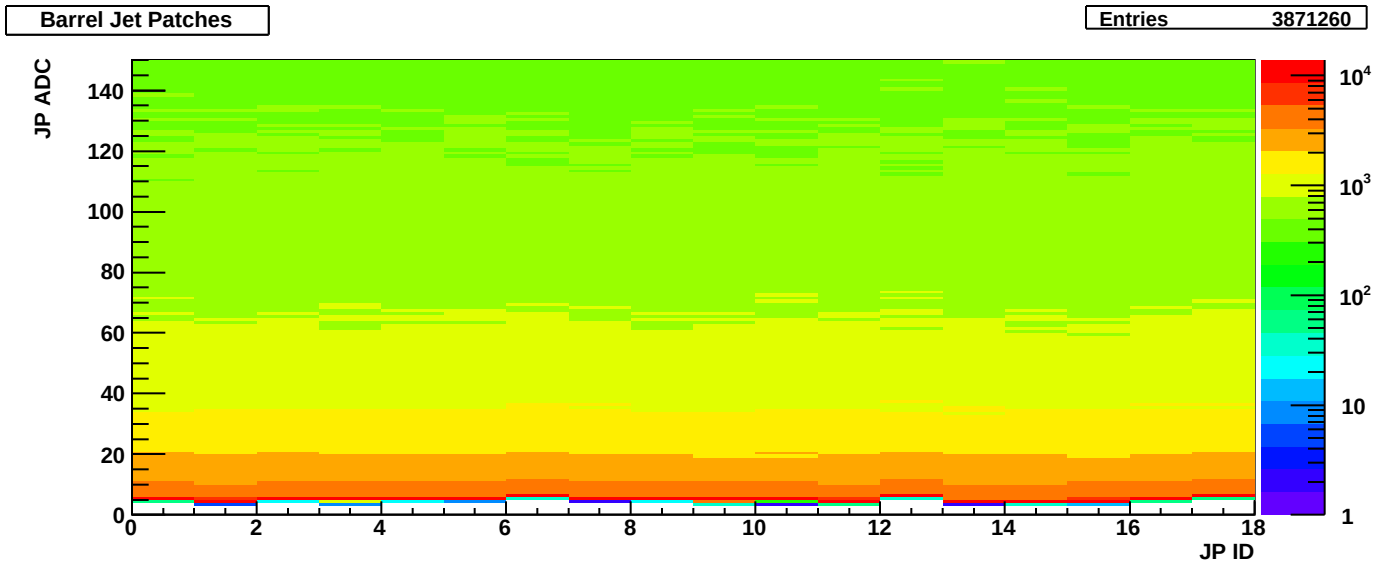

BunchId7Bit (JP2||AJP)
Entries 215070
10^3
10^2
10
0
20
40
60
80
100
120

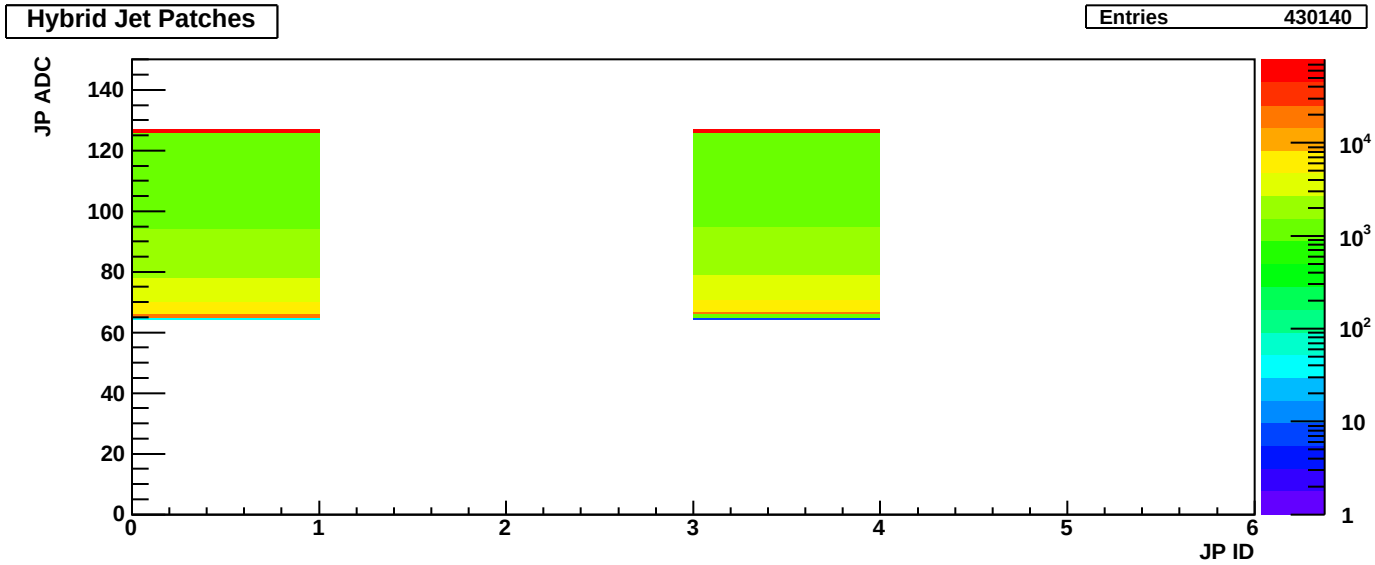

Barrel EMC L0 Input - High Tower
Entries 6.4521e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

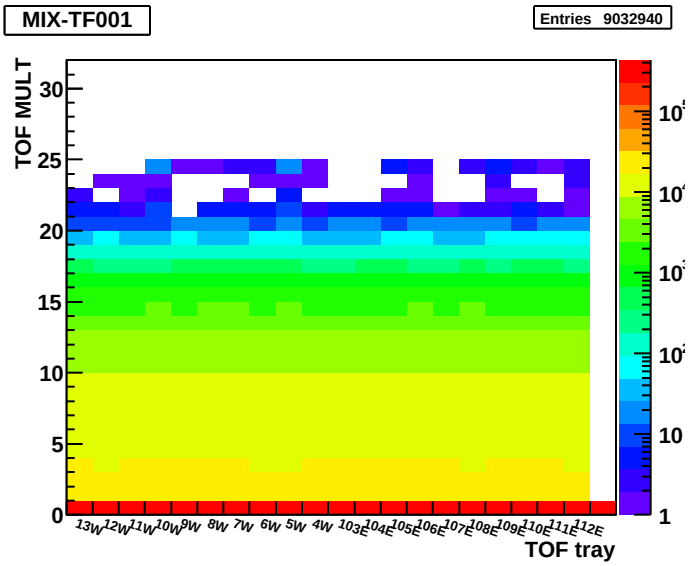

Barrel EMC L0 Input - Patch Sum
Entries 6.4521e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

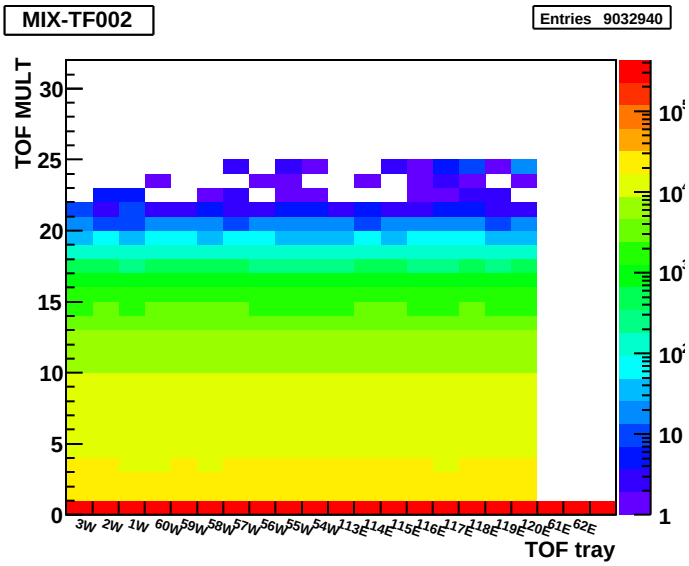

Endcap EMC L0 Input - High Tower
Entries 1.93563e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

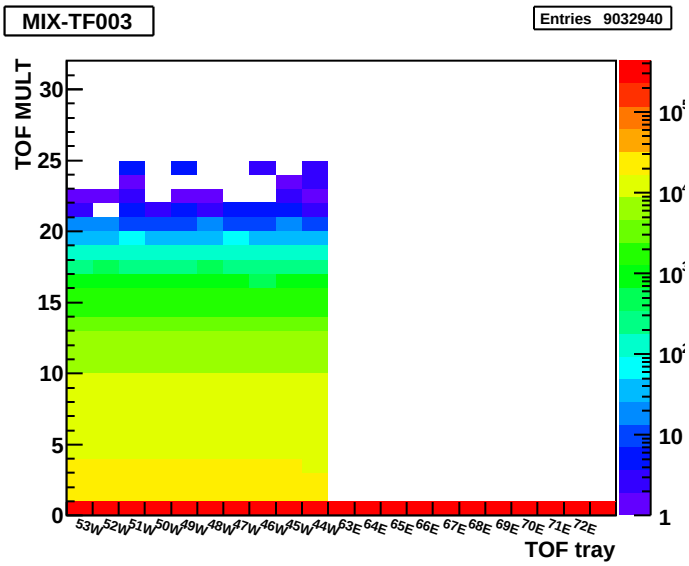

Endcap EMC L0 Input - Patch Sum
Entries 1.93563e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

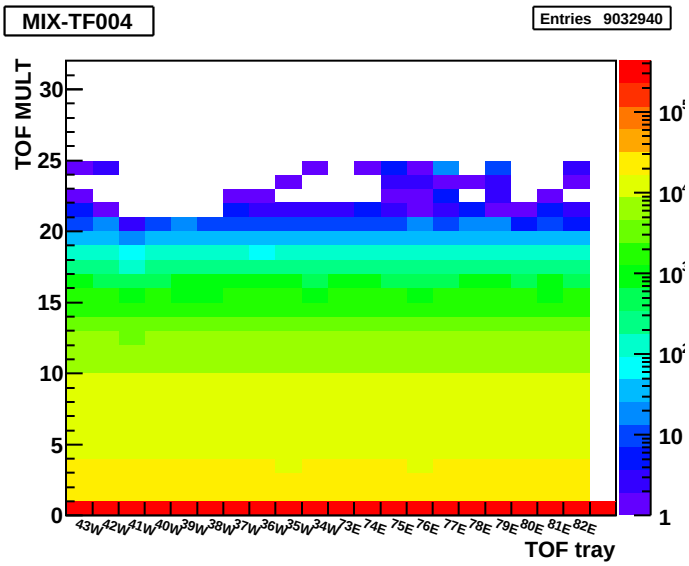

Barrel Jet Patches
Entries 3871260
JP ADC
JP ID

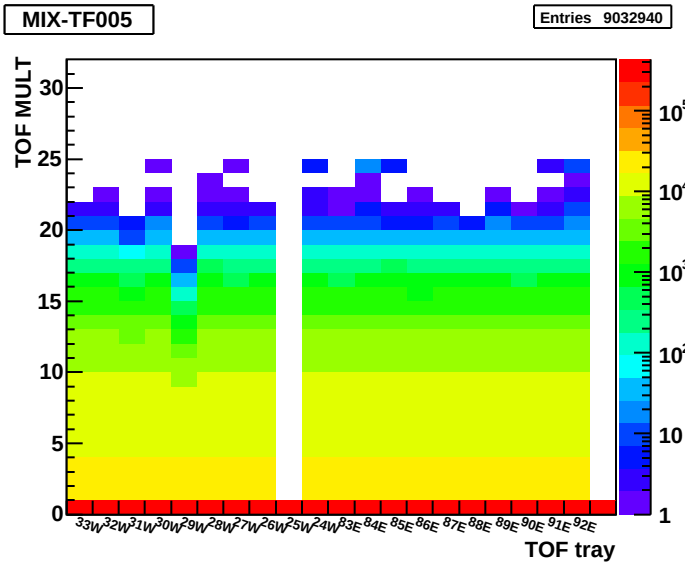

Hybrid Jet Patches
Entries 430140
JP ADC
JP ID

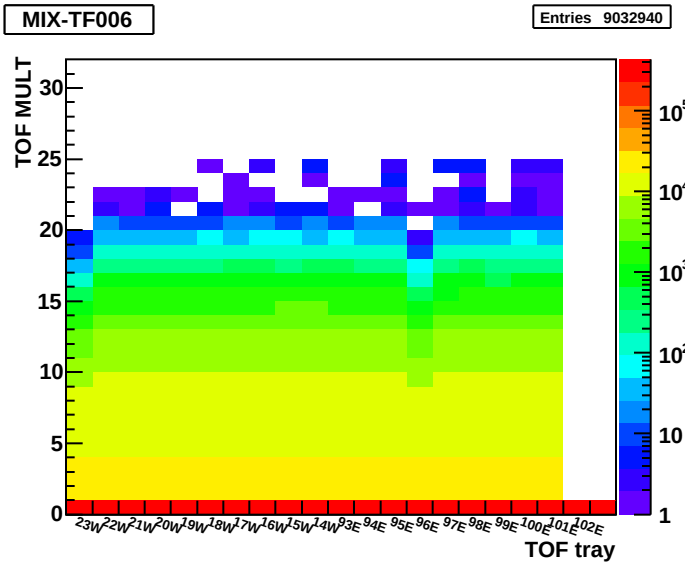

MIX-TF001
Entries 9032940
TOF MULT
TOF tray

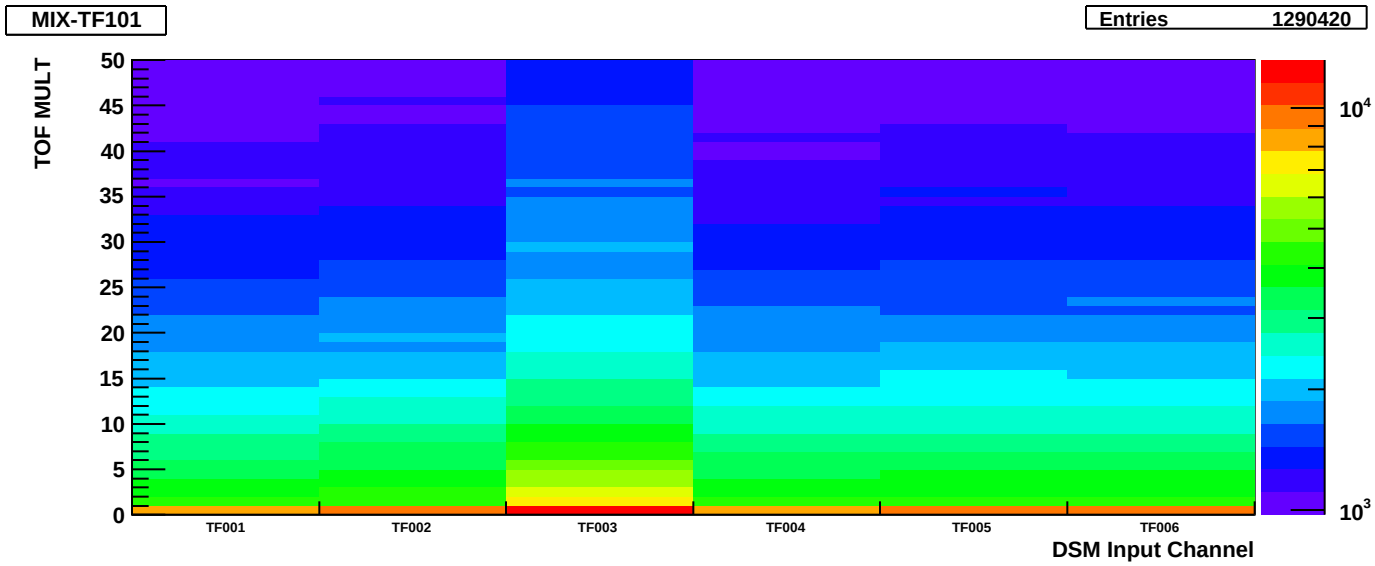

MIX-TF002
Entries 9032940
TOF MULT
TOF tray

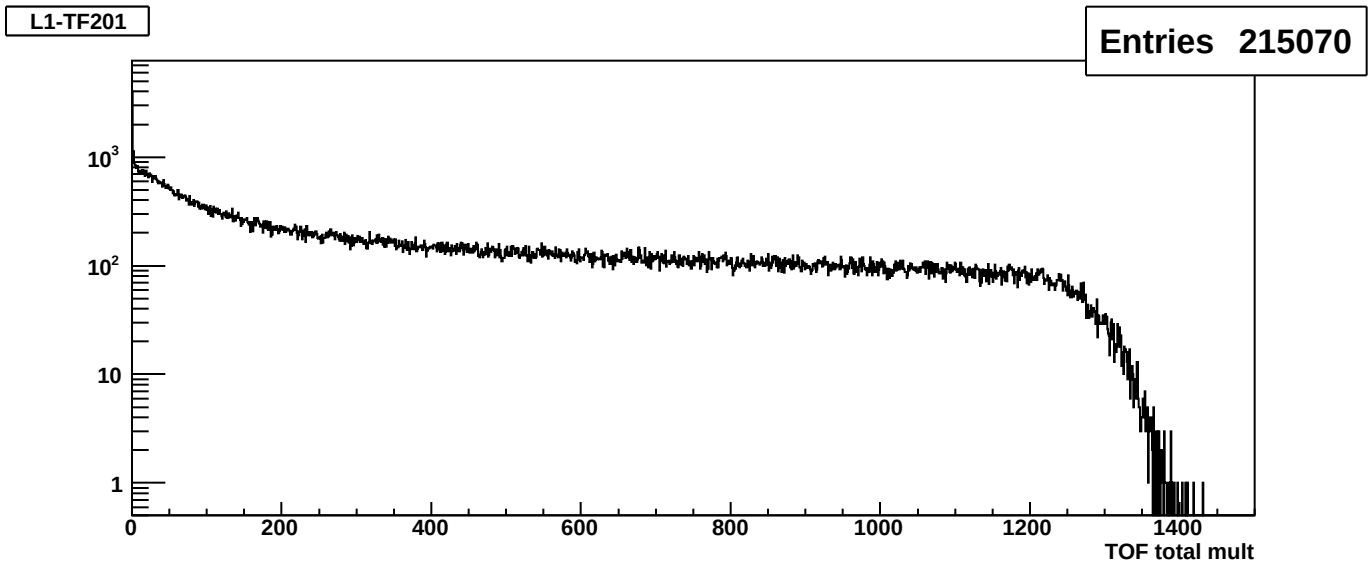

MIX-TF003
Entries 9032940
TOF MULT
TOF tray

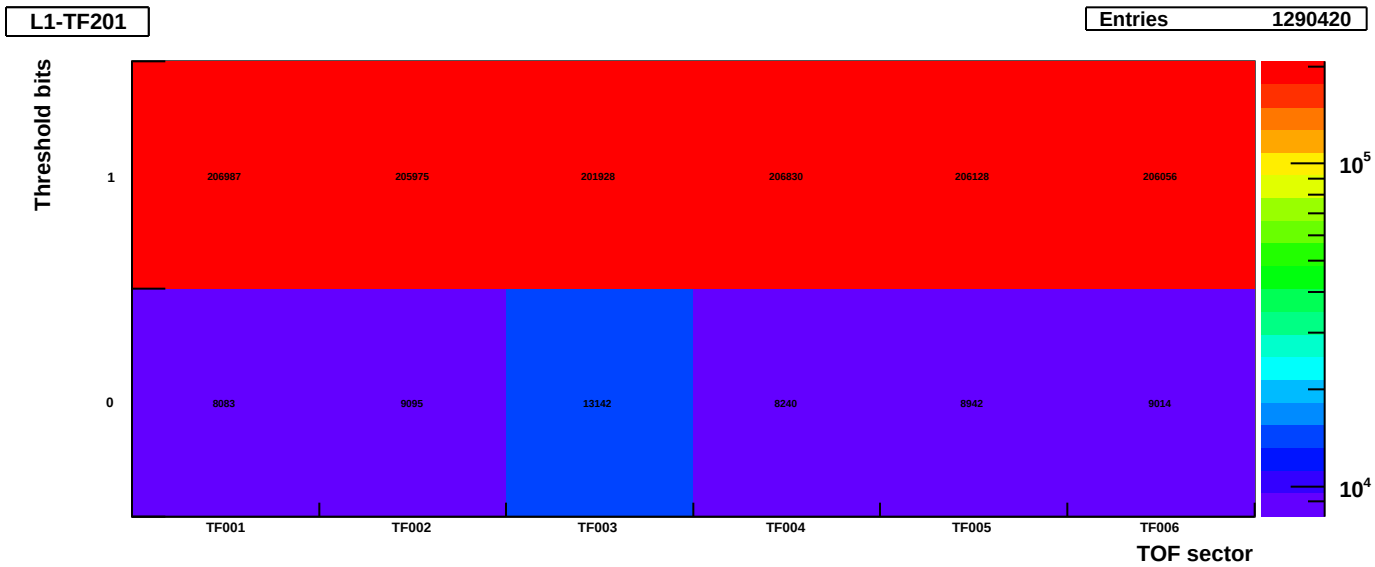

MIX-TF004
Entries 9032940
TOF MULT
TOF tray

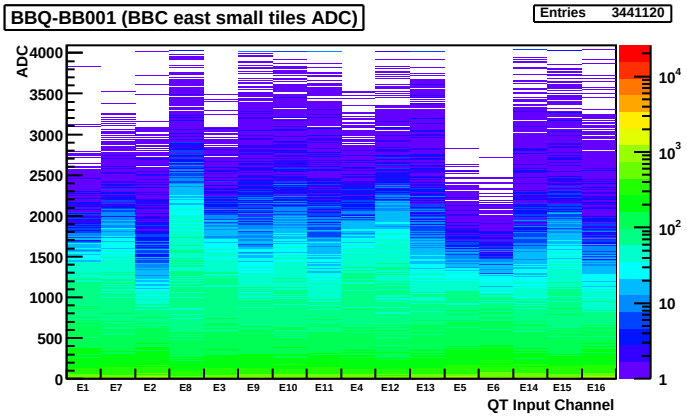

MIX-TF005
Entries 9032940
TOF MULT
TOF tray

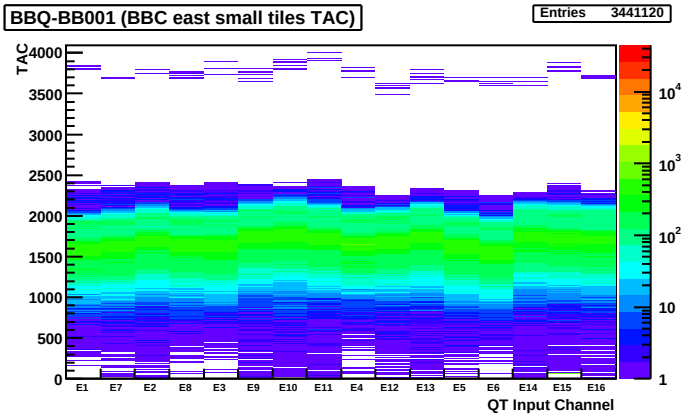

MIX-TF006
Entries 9032940
TOF MULT
TOF tray

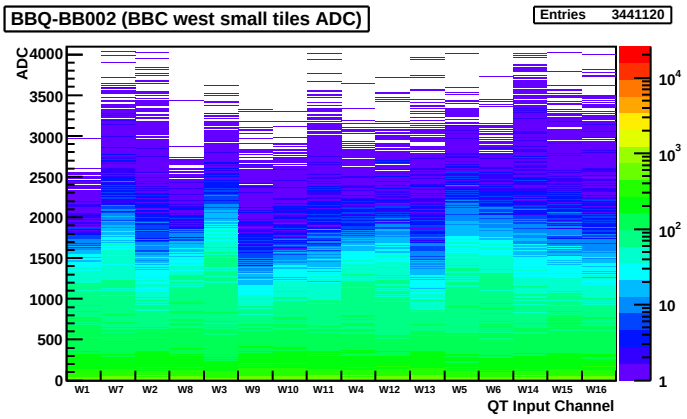

MIX-TF101
Entries 1290420
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3

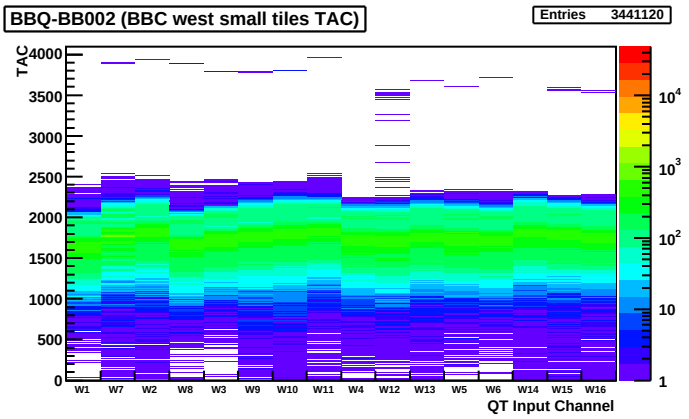

L1-TF201
Entries 215070
10^3
10^2
10
1
0
200
400
600
800
1000
1200
1400
TOF total mult

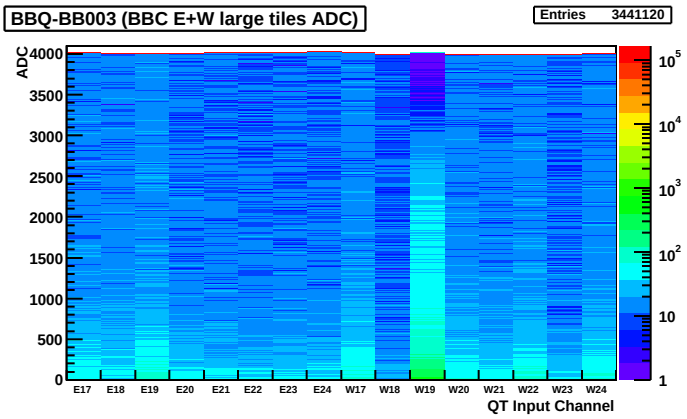

L1-TF201
Entries 1290420
Threshold bits
1
0
206987
205975
201928
206830
206128
206056
8083
9095
13142
8240
8942
9014
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5
10^4

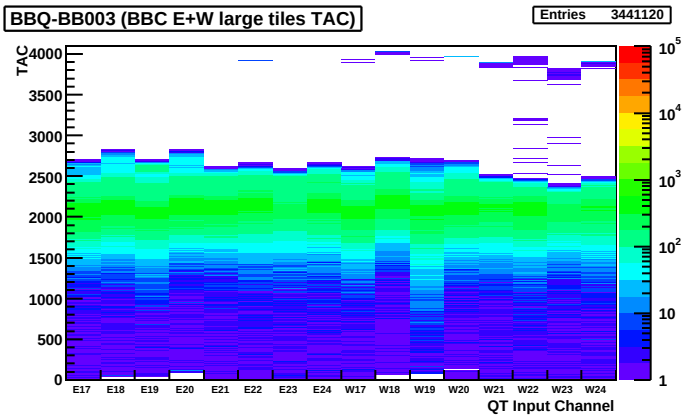

BBQ-BB001 (BBC east small tiles ADC)
Entries 3441120
ADC
QT Input Channel

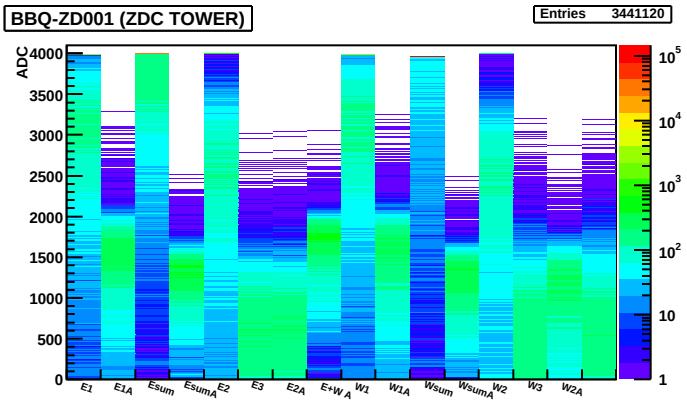

BBQ-BB001 (BBC east small tiles TAC)
Entries 3441120
TAC
QT Input Channel

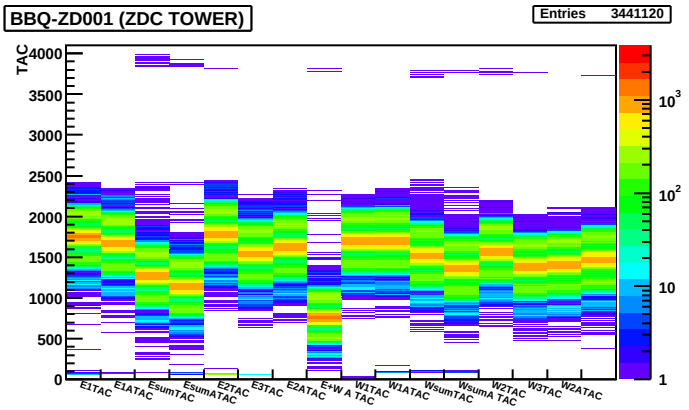

BBQ-BB002 (BBC west small tiles ADC)
Entries 3441120
ADC
QT Input Channel

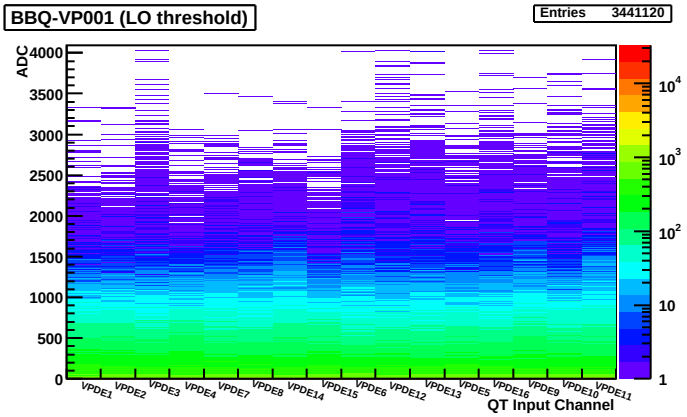

BBQ-BB002 (BBC west small tiles TAC)
Entries 3441120
TAC
QT Input Channel

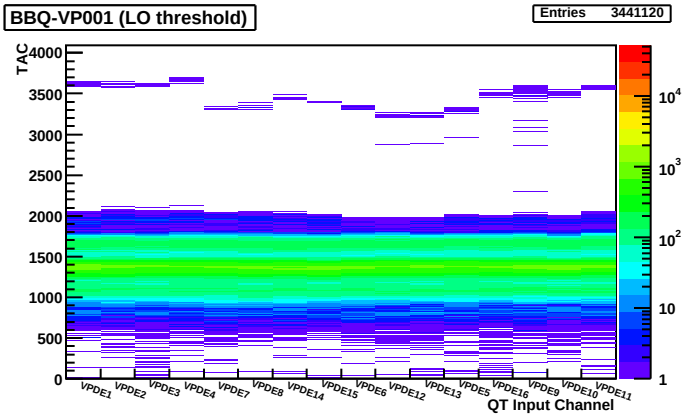

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3441120
ADC
QT Input Channel

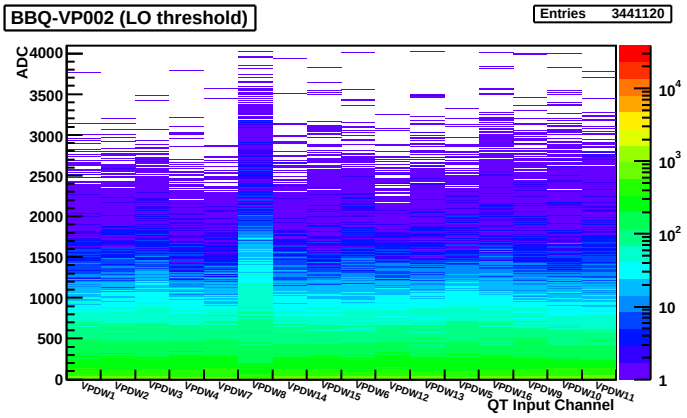

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3441120
TAC
QT Input Channel

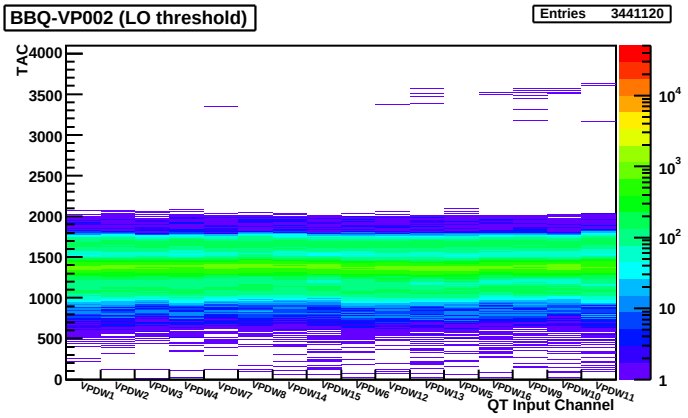

BBQ-ZD001 (ZDC TOWER)
Entries 3441120
ADC

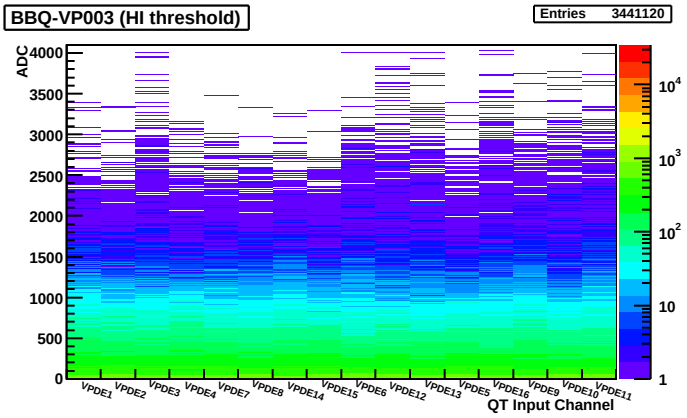

BBQ-ZD001 (ZDC TOWER)
Entries 3441120
TAC

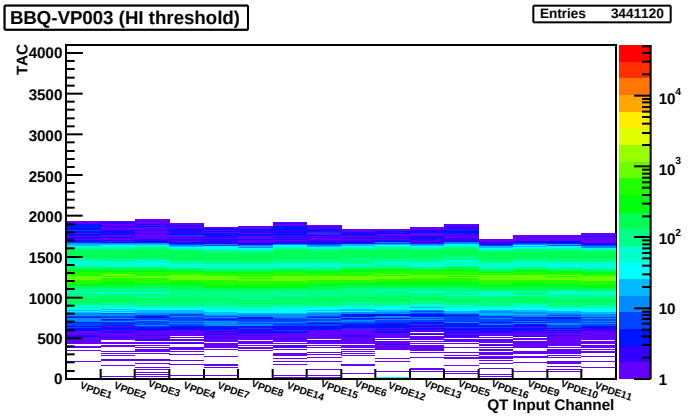

BBQ-VP001 (LO threshold)
Entries 3441120
ADC
QT Input Channel

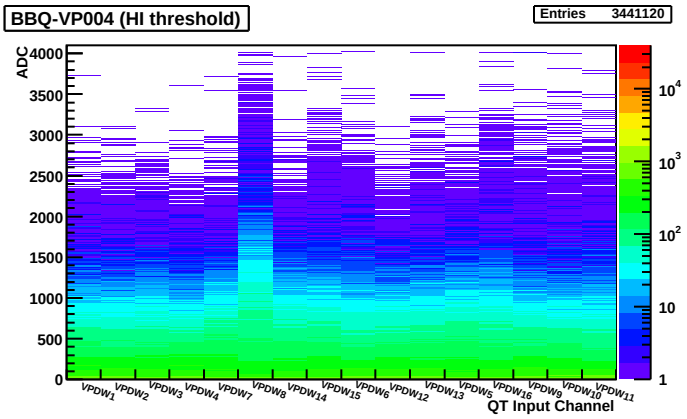

BBQ-VP001 (LO threshold)
Entries 3441120
TAC
QT Input Channel

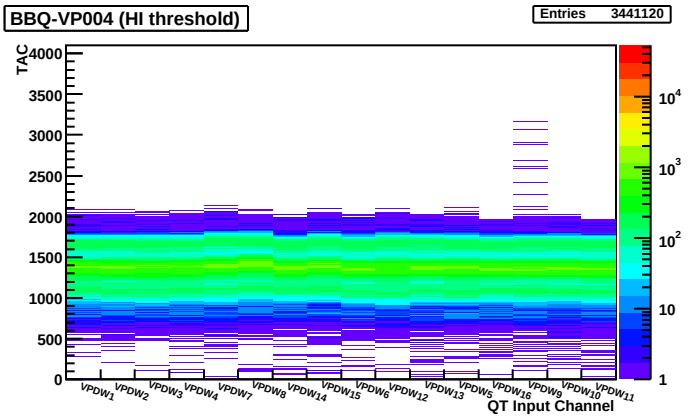

BBQ-VP002 (LO threshold)
Entries 3441120
ADC
QT Input Channel

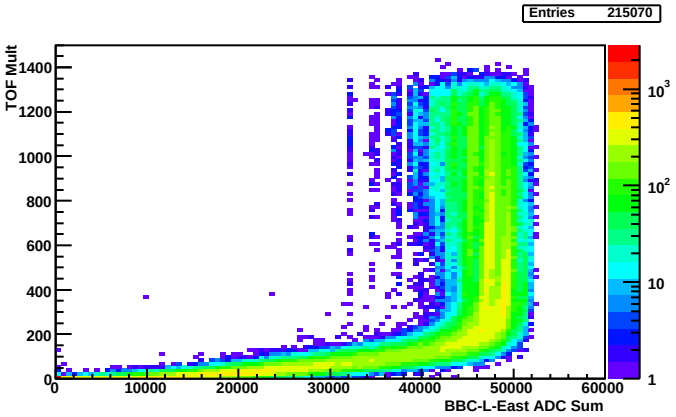

BBQ-VP002 (LO threshold)
Entries 3441120
TAC
QT Input Channel

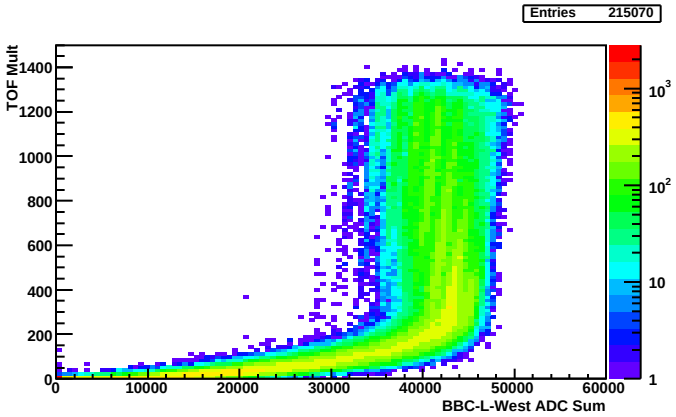

BBQ-VP003 (HI threshold)
Entries 3441120
ADC
QT Input Channel

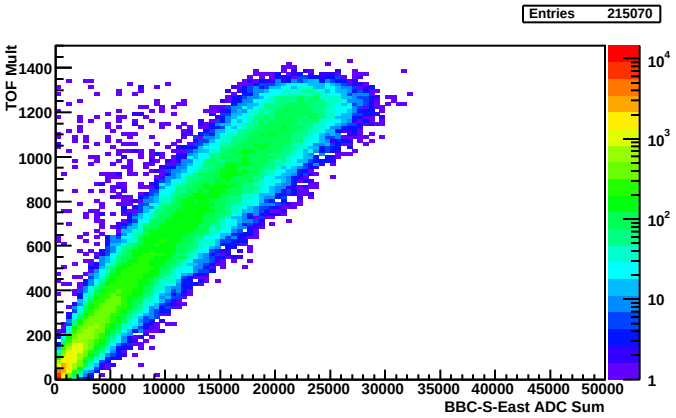

BBQ-VP003 (HI threshold)
Entries 3441120
TAC
QT Input Channel

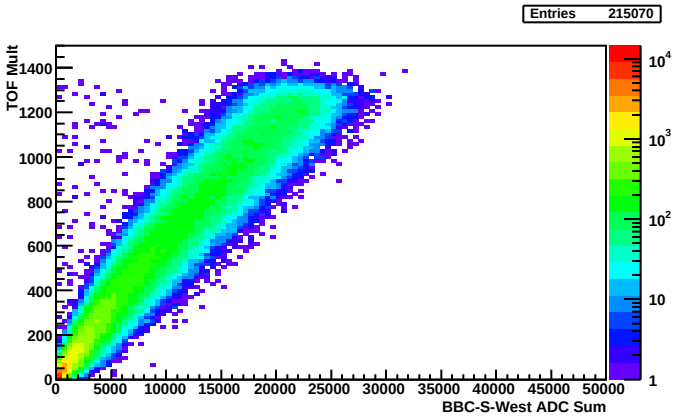

BBQ-VP004 (HI threshold)
Entries 3441120
ADC
QT Input Channel

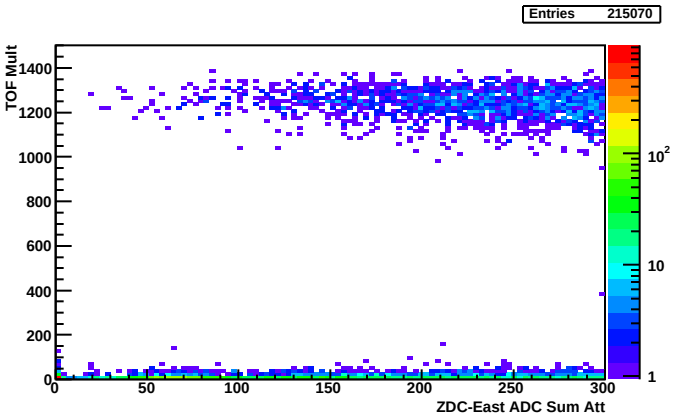

BBQ-VP004 (HI threshold)
Entries 3441120
TAC
QT Input Channel

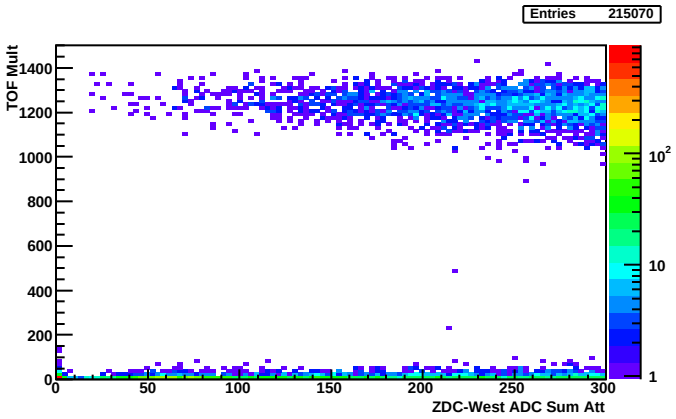

Entries 215070
TOF Mult
BBC-L-East ADC Sum

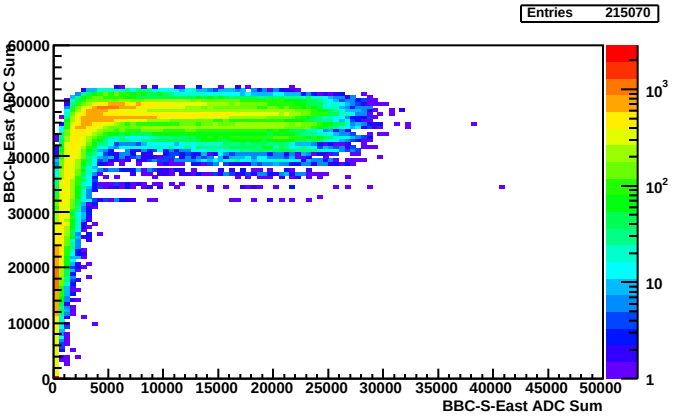

Entries 215070
TOF Mult
BBC-L-West ADC Sum

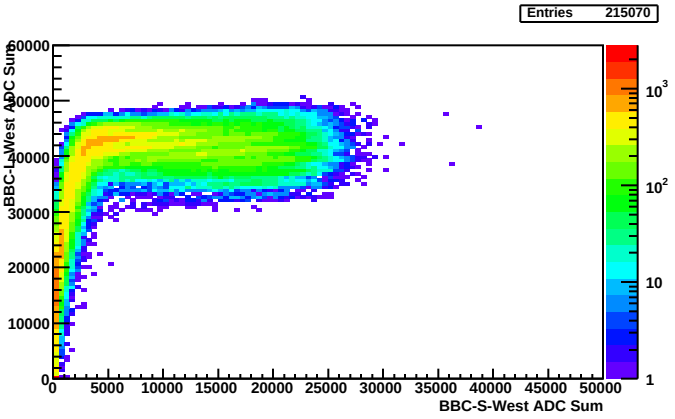

Entries 215070
TOF Mult
BBC-S-East ADC Sum

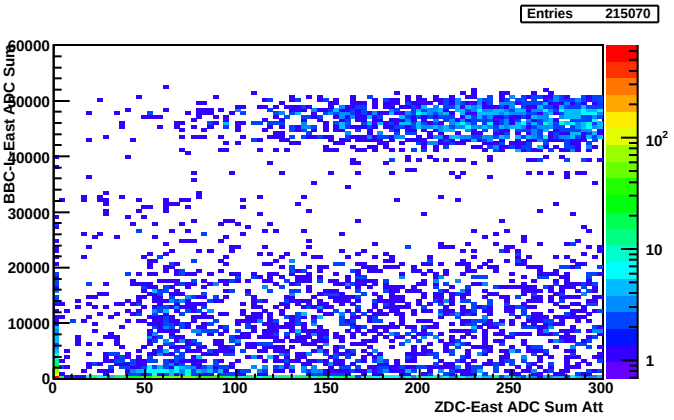

Entries 215070
TOF Mult
BBC-S-West ADC Sum

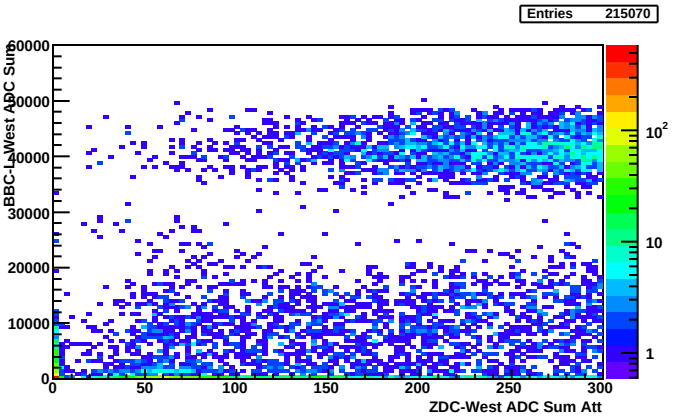

Entries 215070
TOF Mult
ZDC-East ADC Sum Att


Entries 215070
TOF Mult
ZDC-West ADC Sum Att


Entries 215070
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 215070
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 215070
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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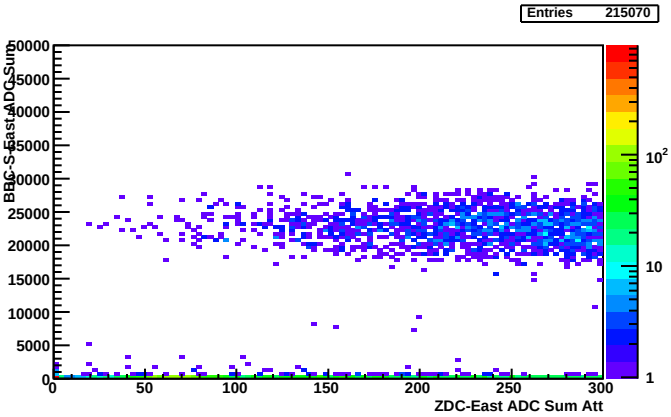
BBC-L-West ADC Sum
ZDC-West ADC Sum Att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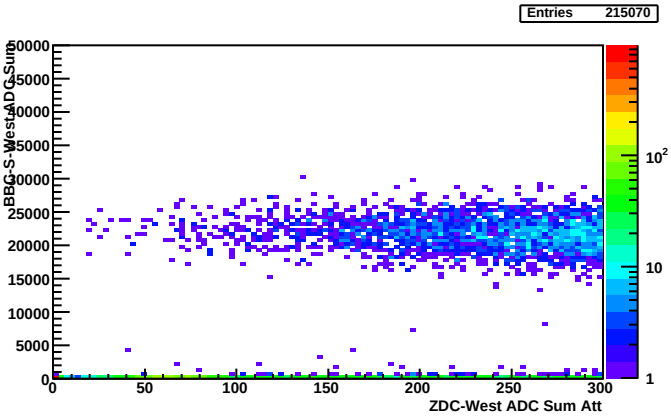
BBC-S-East ADC Sum
ZDC-East ADC Sum Att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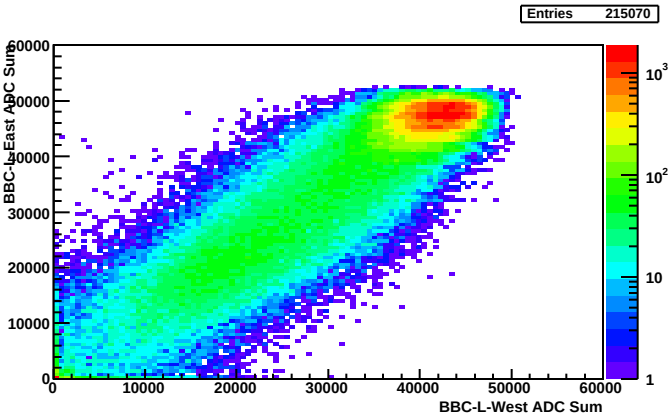
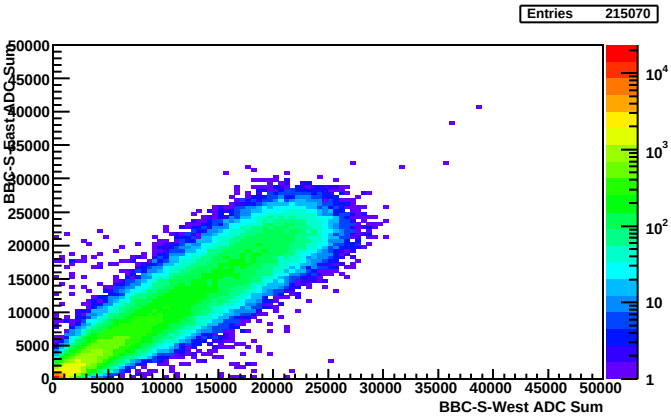
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 215070
BBC-L-East ADC Sum
BBC-L-West ADC Sum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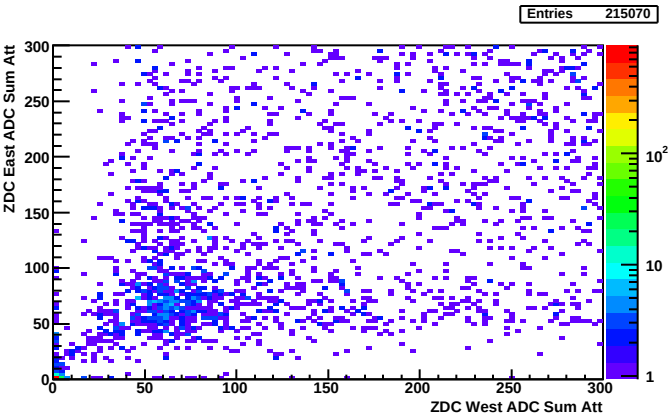
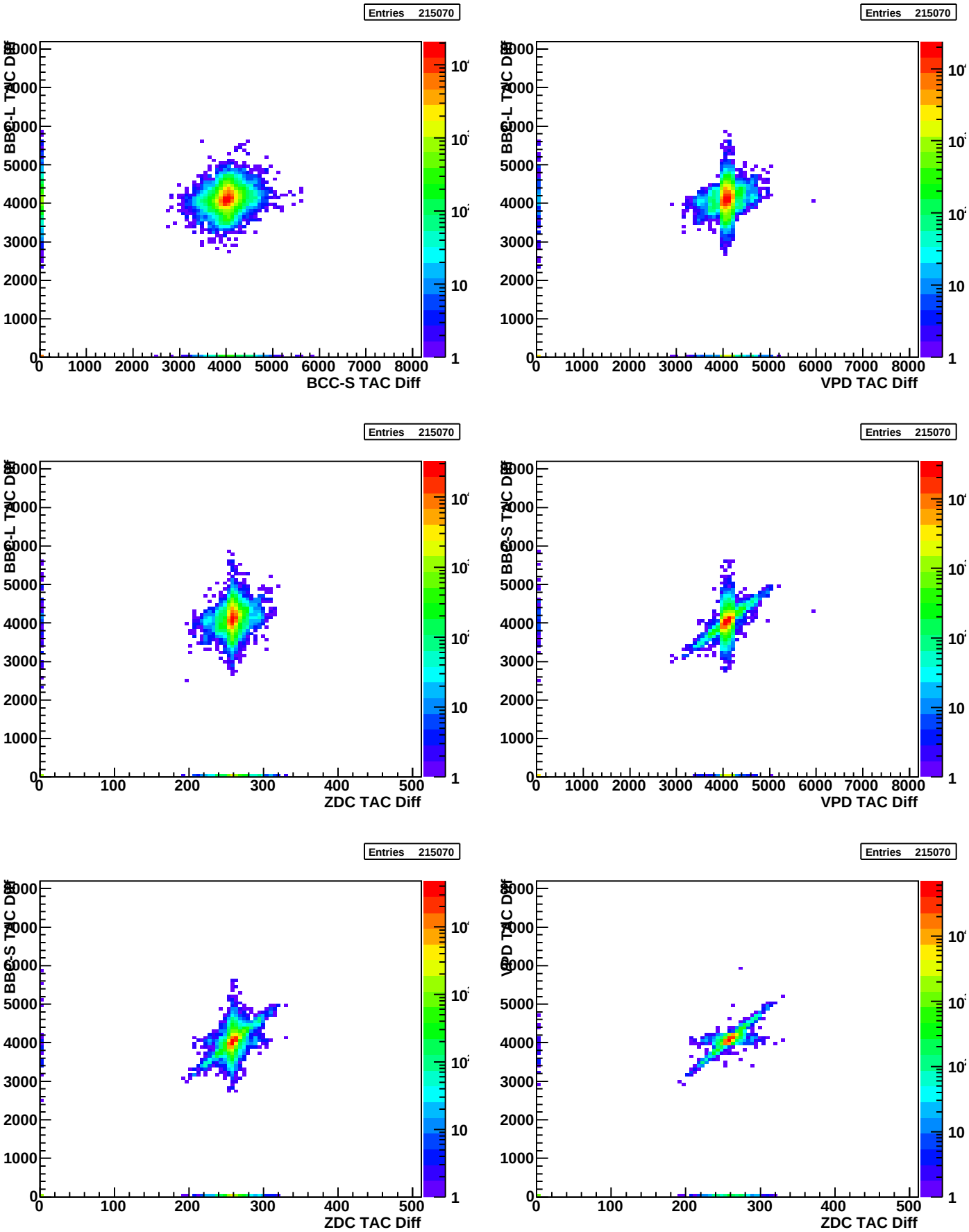
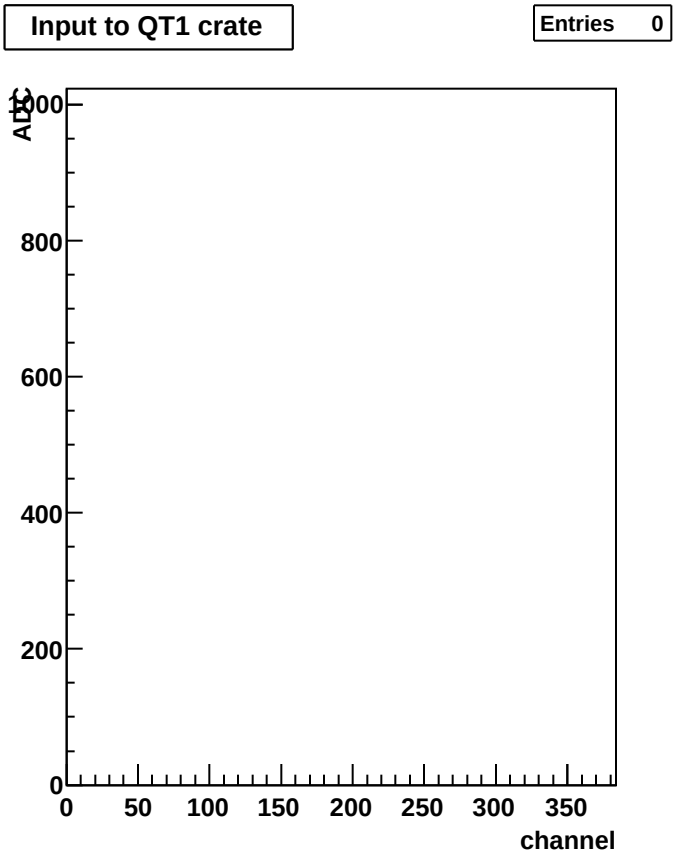
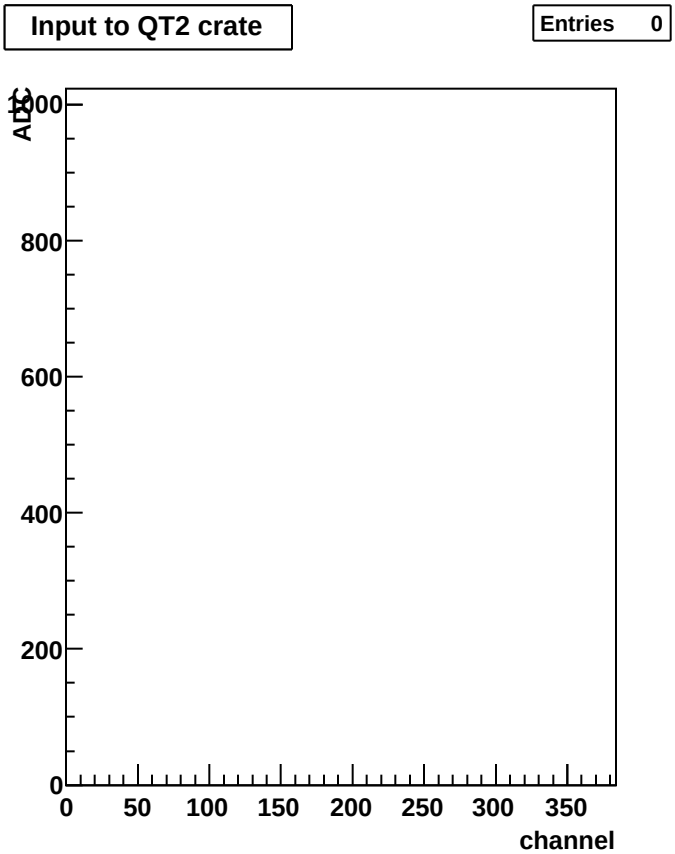
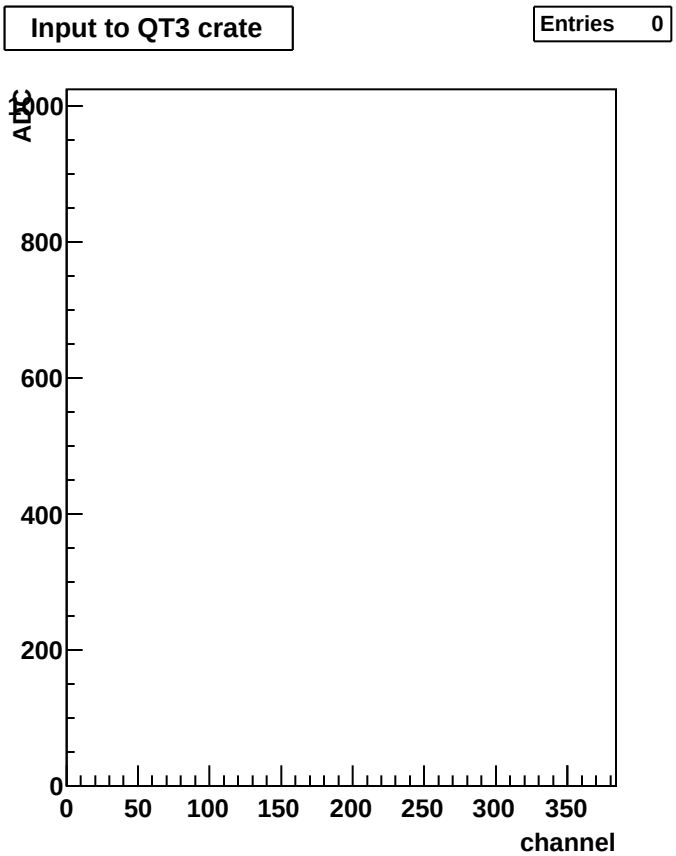
BBC-S-East ADC Sum
BBC-S-West ADC Sum

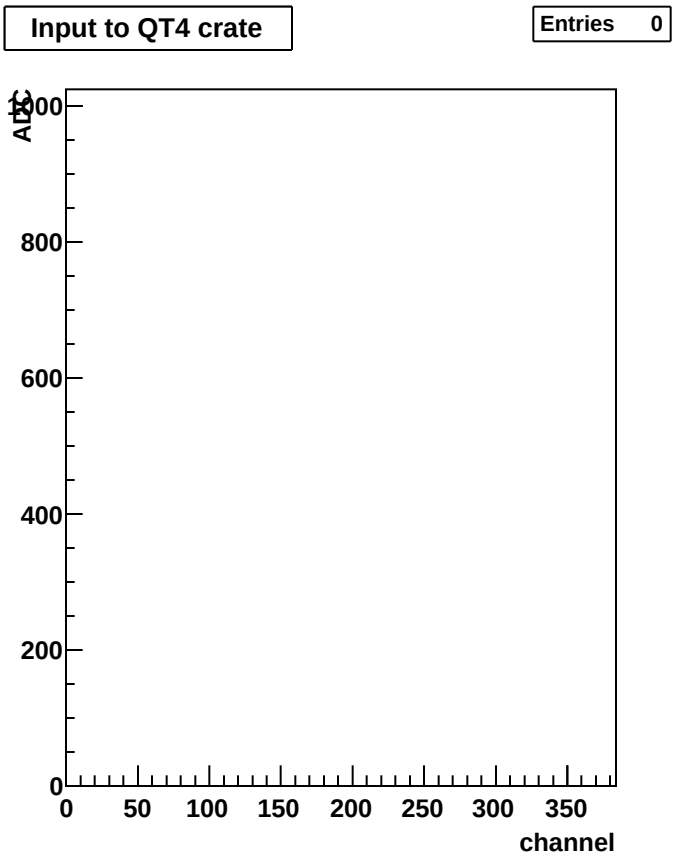

Entries 215070
ZDC East ADC Sum Att
ZDC West ADC Sum Att

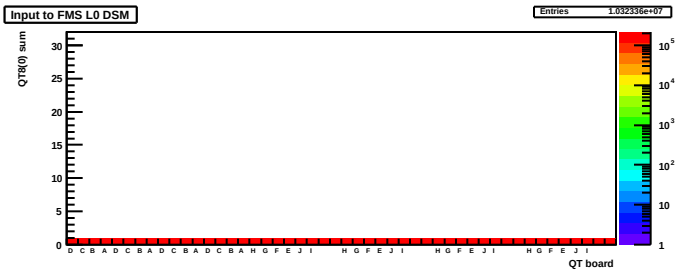

Entries 215070
BBC-L TAC Diff
BCC-S TAC Diff
Entries 215070
BBC-L TAC Diff
VPD TAC Diff
Entries 215070
BBC-L TAC Diff
ZDC TAC Diff
Entries 215070
BBC-S TAC Diff
VPD TAC Diff
Entries 215070
BBC-S TAC Diff
ZDC TAC Diff
Entries 215070
VPD TAC Diff
ZDC TAC Diff

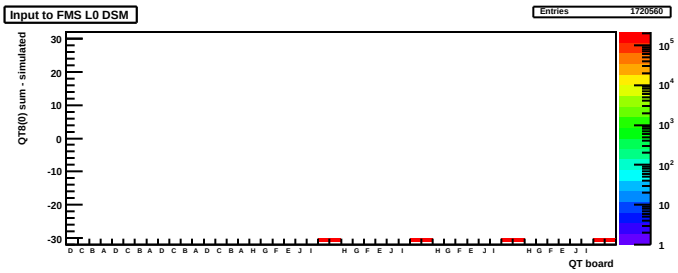

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

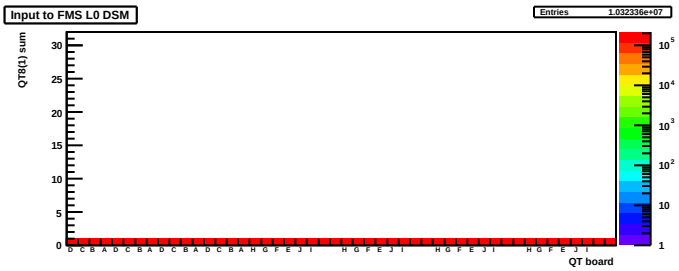

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

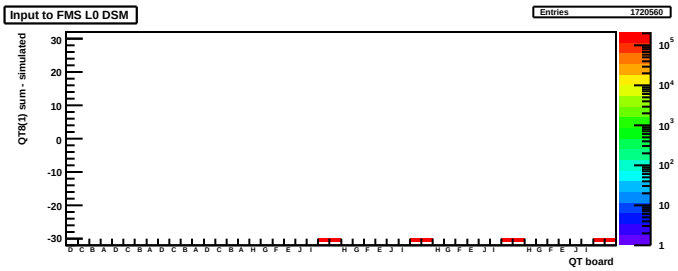

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

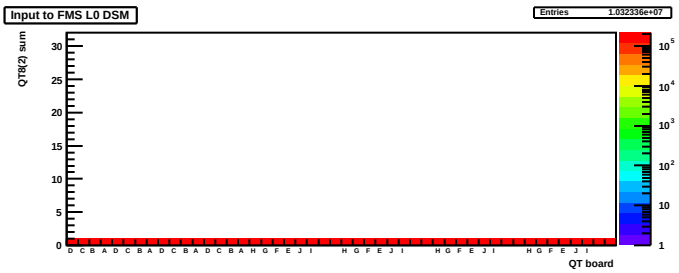

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

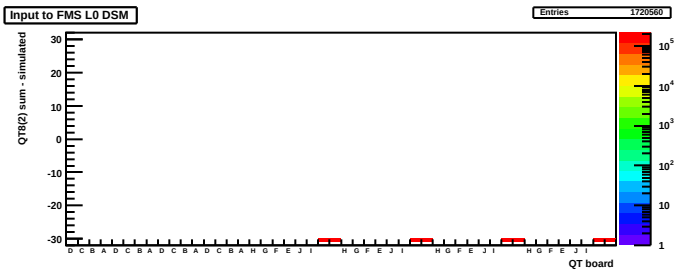

Input to FMS L0 DSM
Entries 1.032336e+07
QT8(0) sum
QT board

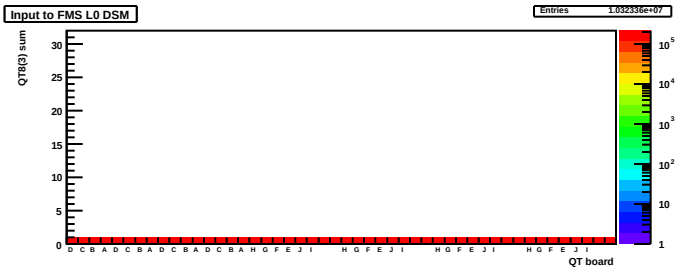

Input to FMS L0 DSM
Entries 1720560
QT8(0) sum - simulated
QT board

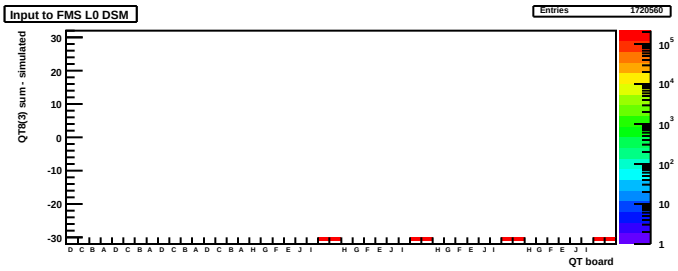

Input to FMS L0 DSM
Entries 1.032336e+07
QT8(1) sum
QT board

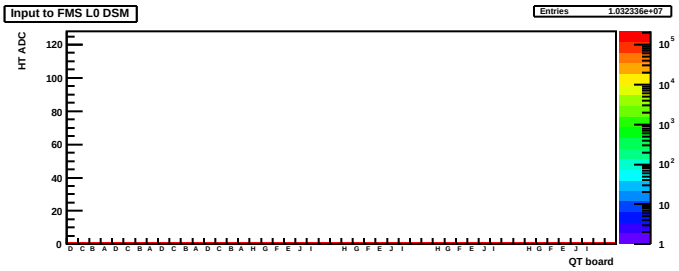

Input to FMS L0 DSM
Entries 1720560
QT8(1) sum - simulated
QT board

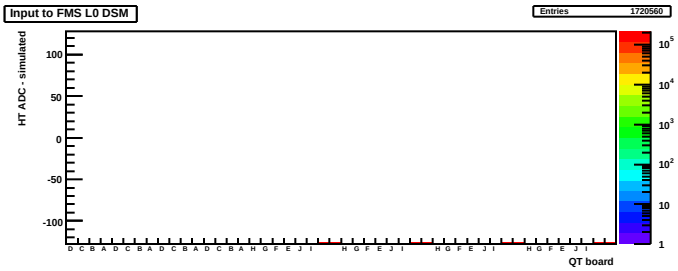

Input to FMS L0 DSM
Entries 1.032336e+07
QT8(2) sum
QT board

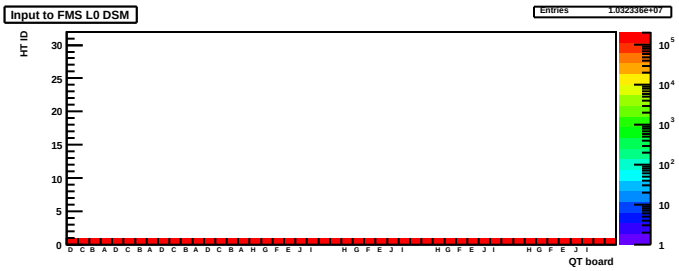

Input to FMS L0 DSM
Entries 1720560
QT8(2) sum - simulated
QT board

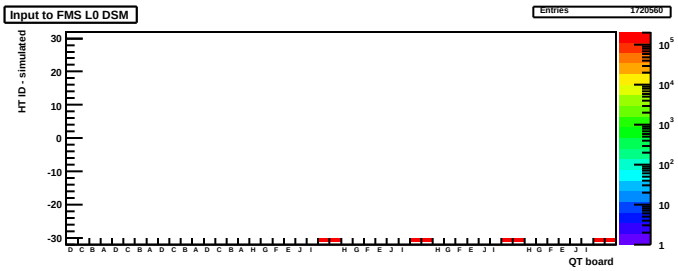

Input to FMS L0 DSM
Entries 1.032336e+07
QT8(3) sum
QT board

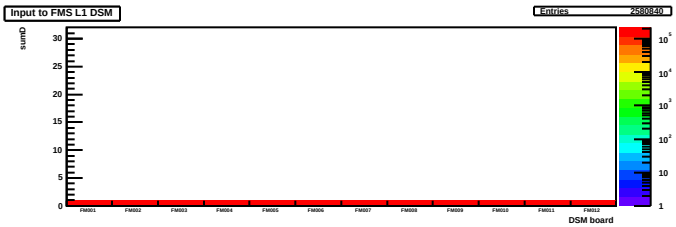

Input to FMS L0 DSM
Entries 1720560
QT8(3) sum - simulated
QT board

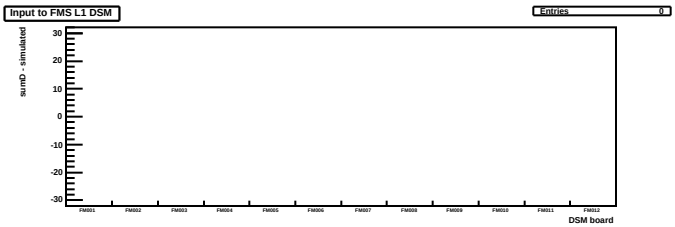

Input to FMS L0 DSM
Entries 1.032336e+07
HT ADC
QT board

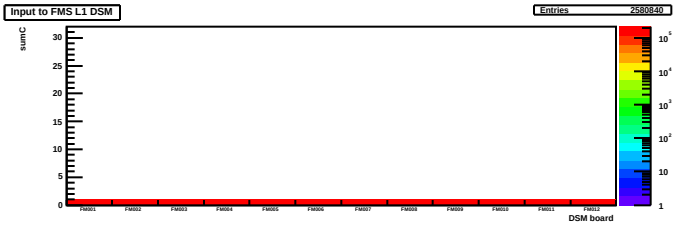

Input to FMS L0 DSM
Entries 1720560
HT ADC - simulated
QT board

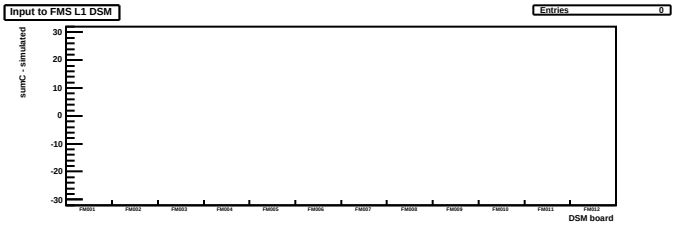

Input to FMS L0 DSM
Entries 1.032336e+07
HT ID
QT board

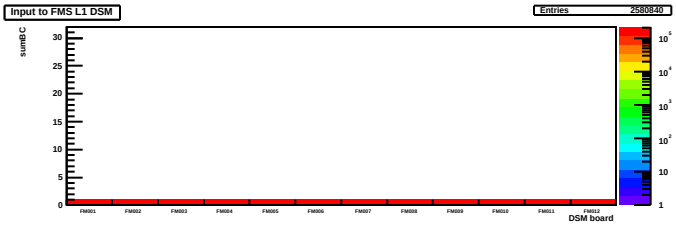

Input to FMS L0 DSM
Entries 1720560
HT ID - simulated
QT board

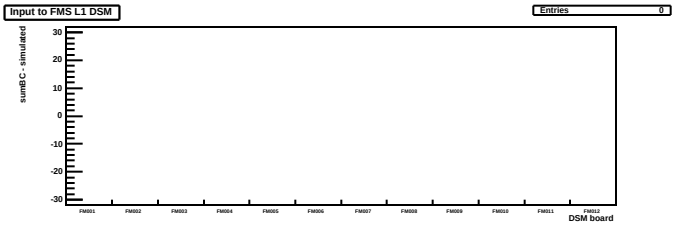

Input to FMS L1 DSM
Entries 2580840
sumD
DSM board

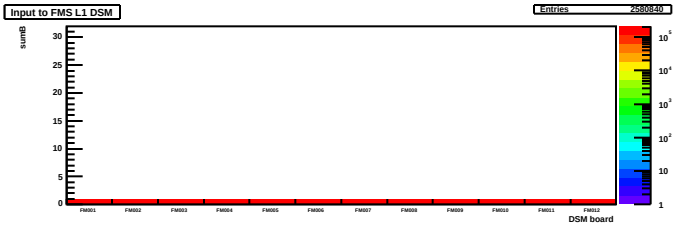

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

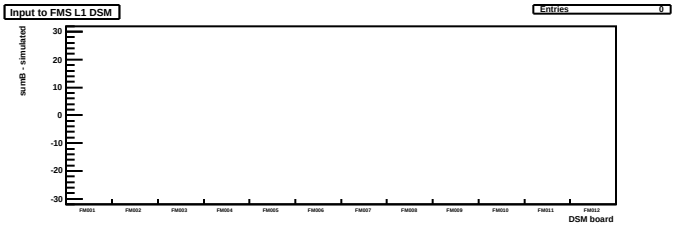

Input to FMS L1 DSM
Entries 2580840
sumC
DSM board

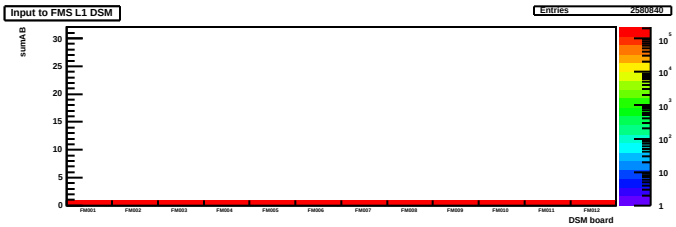

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

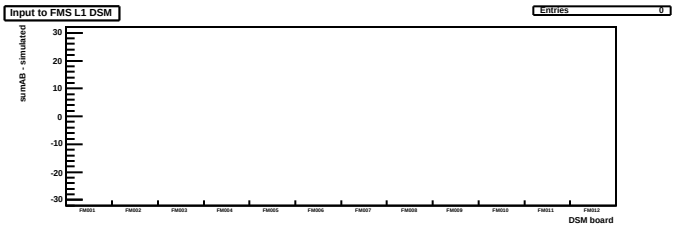

Input to FMS L1 DSM
Entries 2580840
sumBC
DSM board

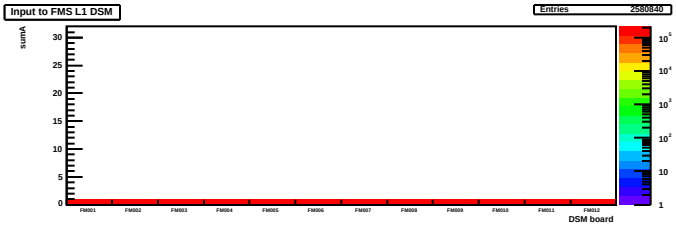

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

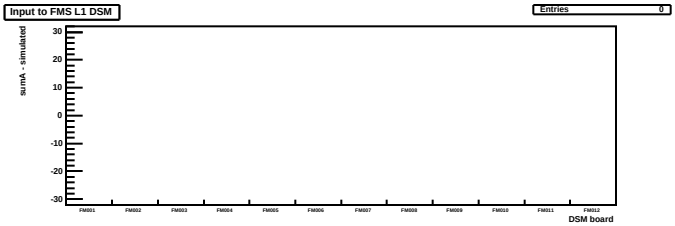

Input to FMS L1 DSM
Entries 2580840
sumB
DSM board

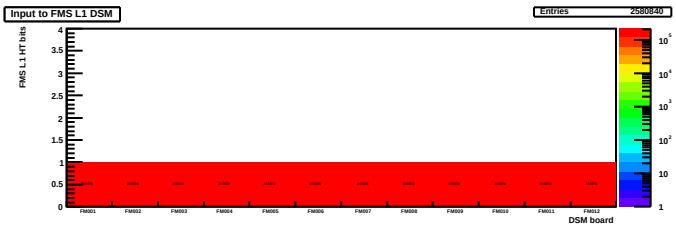

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

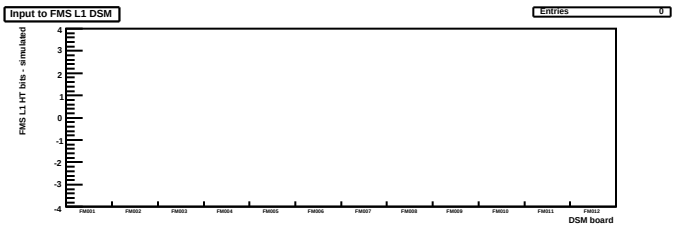

Input to FMS L1 DSM
Entries 2580840
sumAB
DSM board

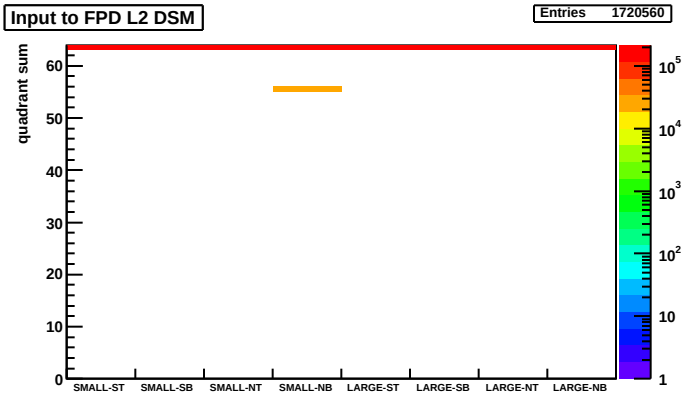

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

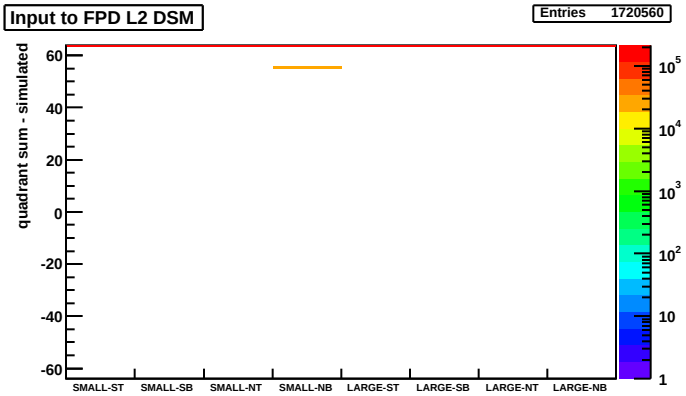

Input to FMS L1 DSM
Entries 2580840
sumA
DSM board

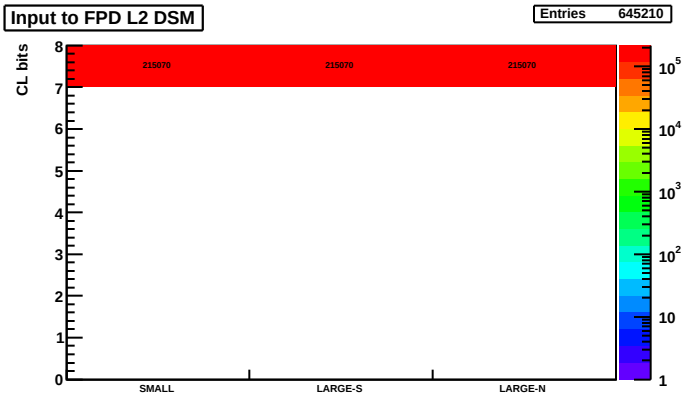

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

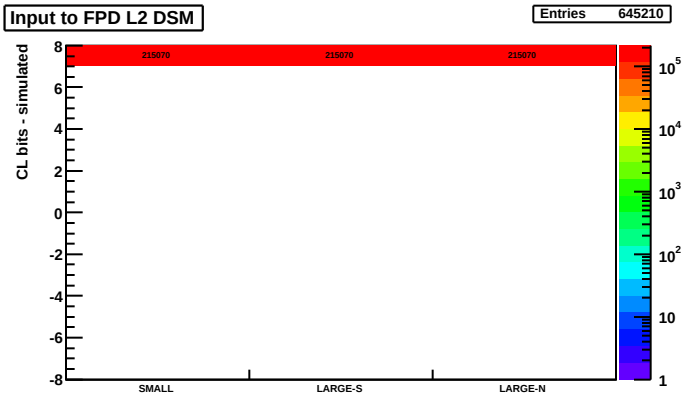

Input to FMS L1 DSM
Entries 2580840
FMS L1 HT bits
DSM board

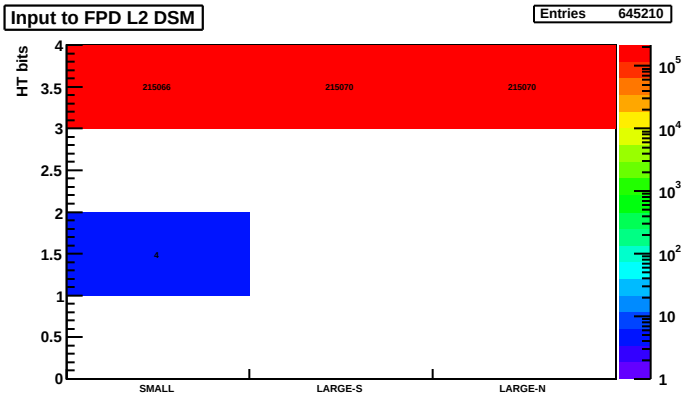

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

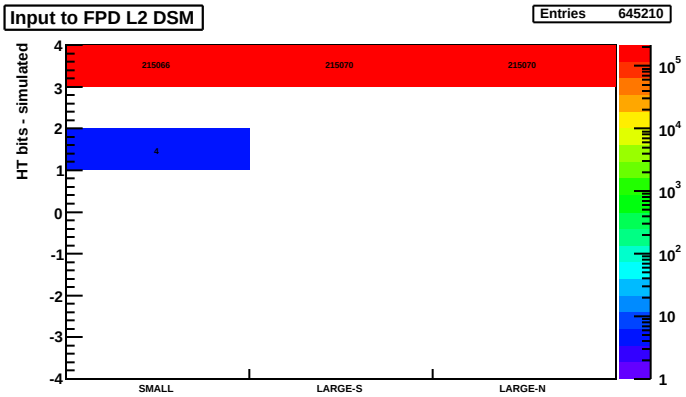

Input to FPD L2 DSM
Entries 1720560
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

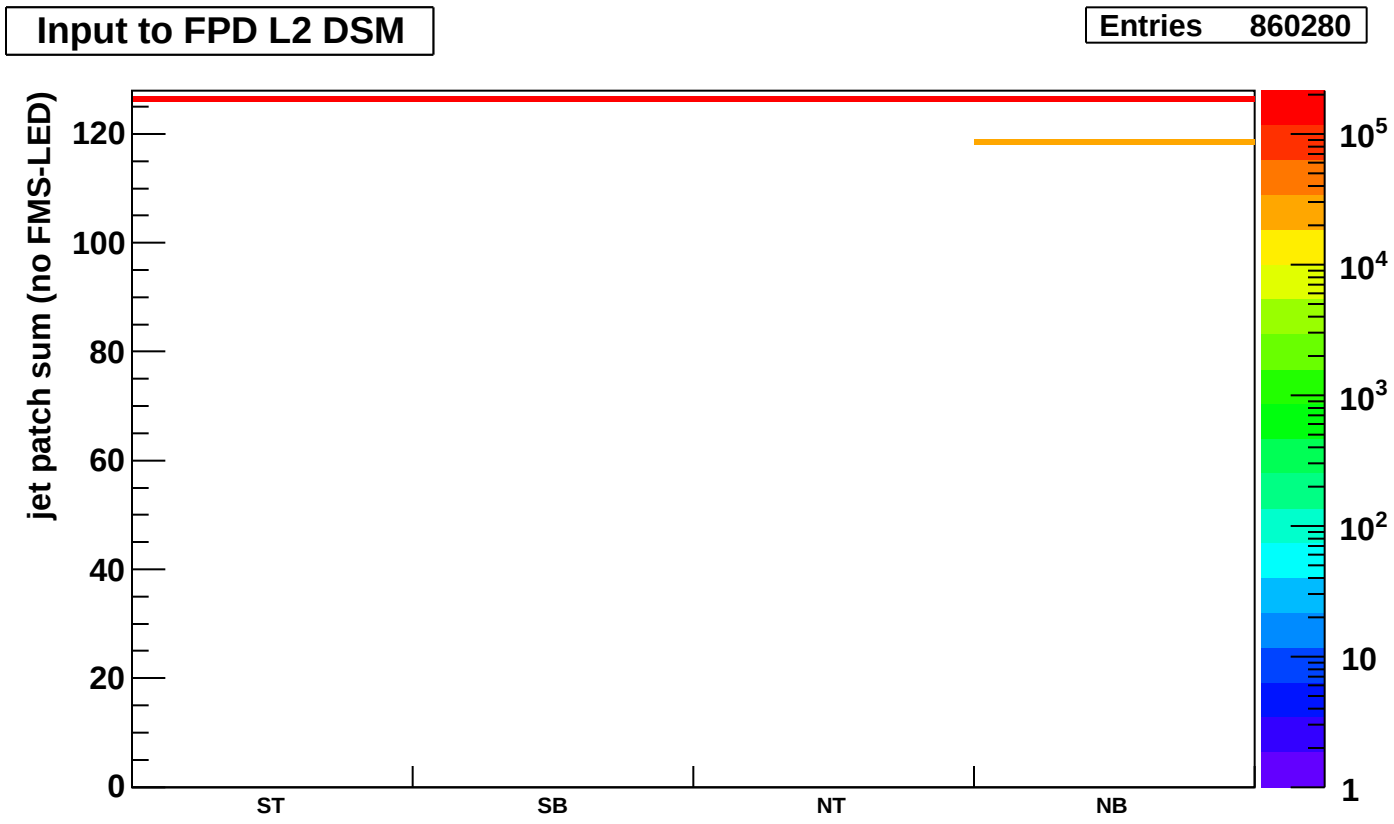

Input to FPD L2 DSM
Entries 1720560
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

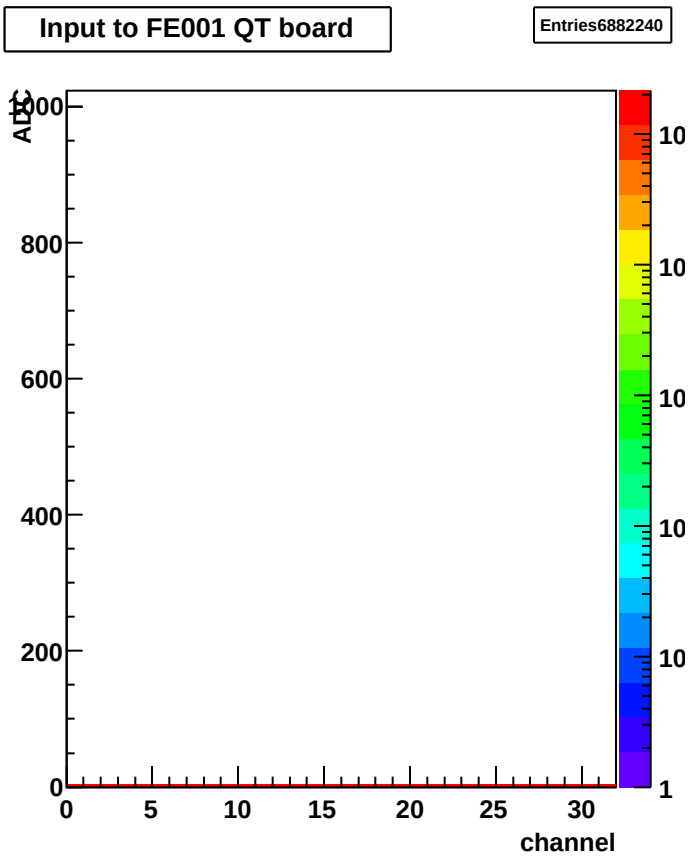

Input to FPD L2 DSM
Entries 645210
CL bits
215070
215070
215070
SMALL
LARGE-S
LARGE-N

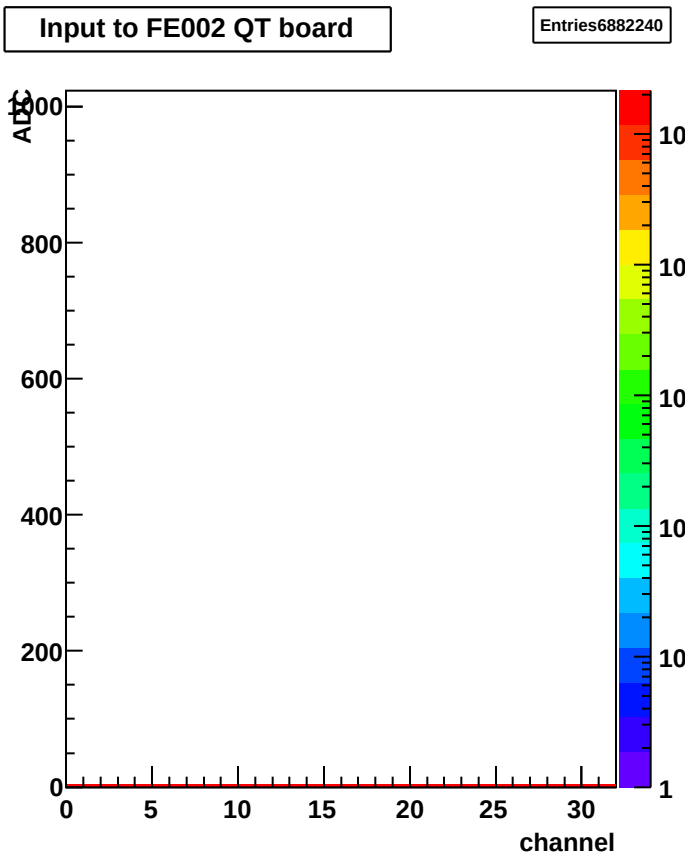

Input to FPD L2 DSM
Entries 645210
CL bits - simulated
215070
215070
215070
SMALL
LARGE-S
LARGE-N

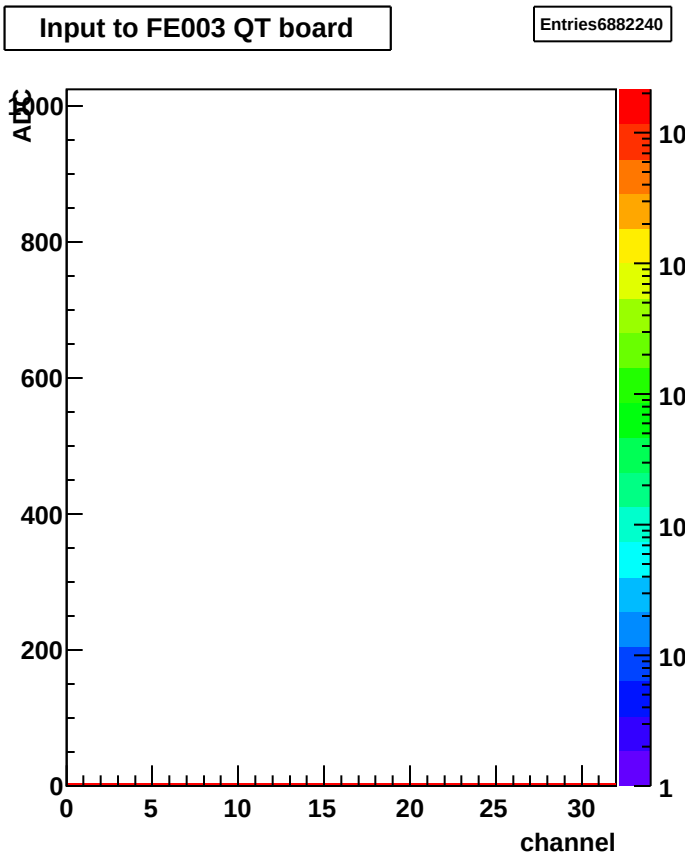

Input to FPD L2 DSM
Entries 645210
HT bits
215066
215070
215070
4
SMALL
LARGE-S
LARGE-N

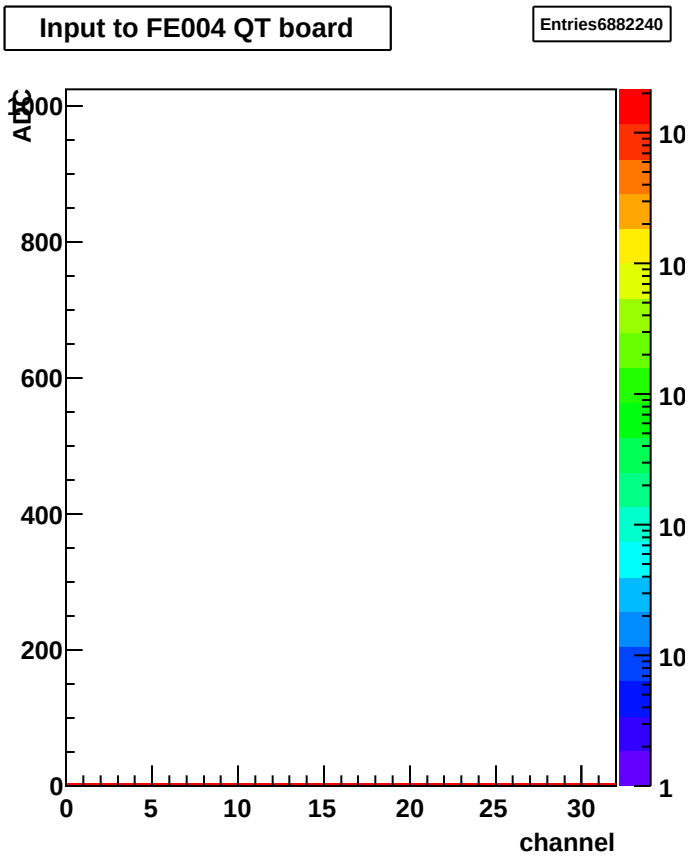

Input to FPD L2 DSM
Entries 645210
HT bits - simulated
215066
215070
215070
4
SMALL
LARGE-S
LARGE-N


Input to FPD L2 DSM
Entries 860280
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

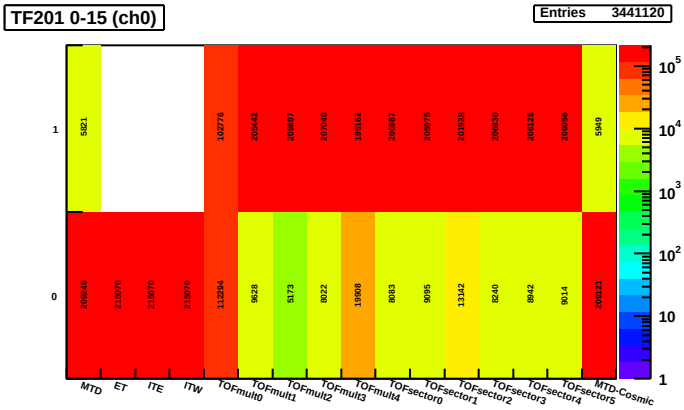

Input to FE001 QT board
Entries6882240
ADC
channel


Input to FE002 QT board
Entries6882240
ADC
channel

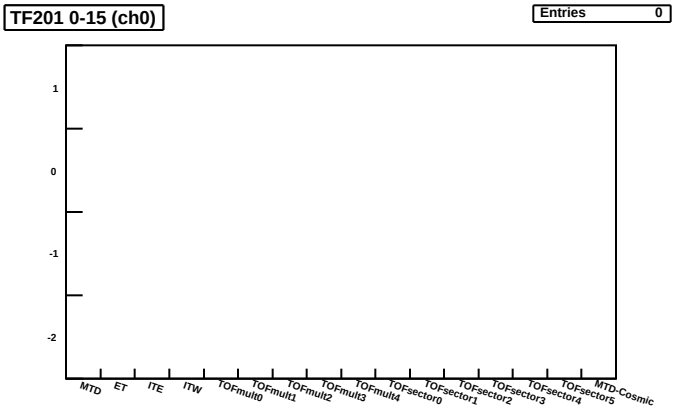

Input to FE003 QT board
Entries6882240
ADC
channel


Input to FE004 QT board
Entries6882240
ADC
channel

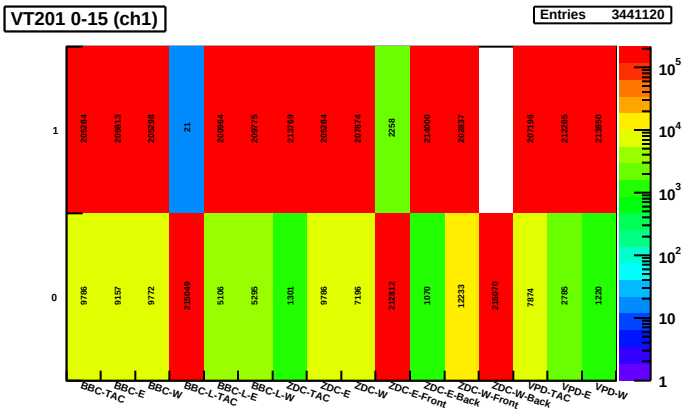
TF201 0-15 (ch0)



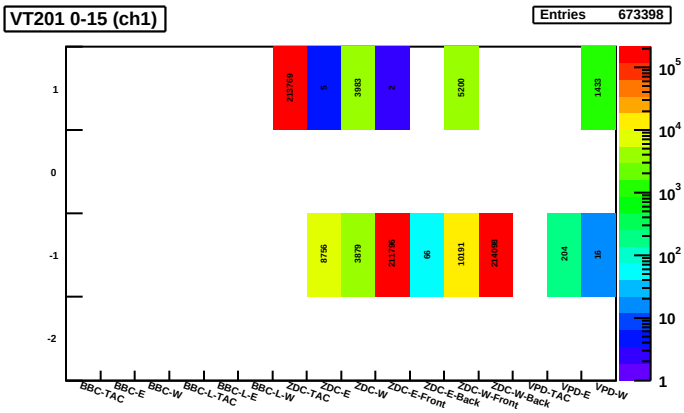
TF201 0-15 (ch0)



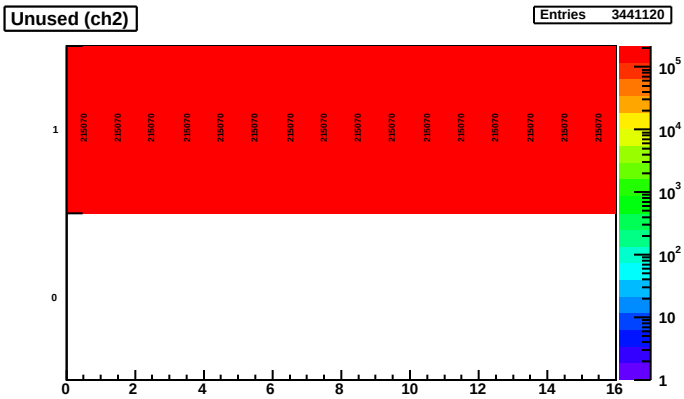
VT201 0-15 (ch1)

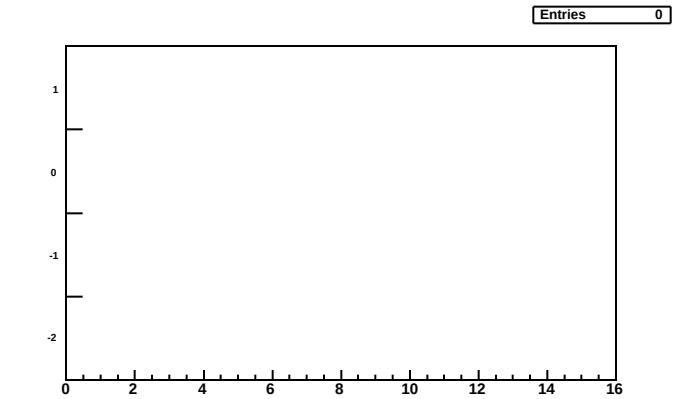


VT201 0-15 (ch1)

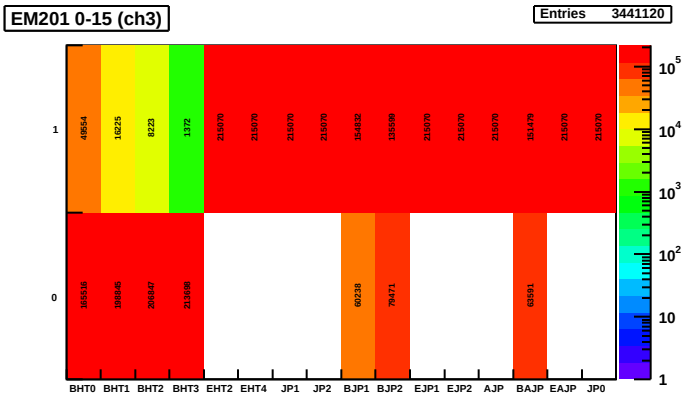


Unused (ch2)

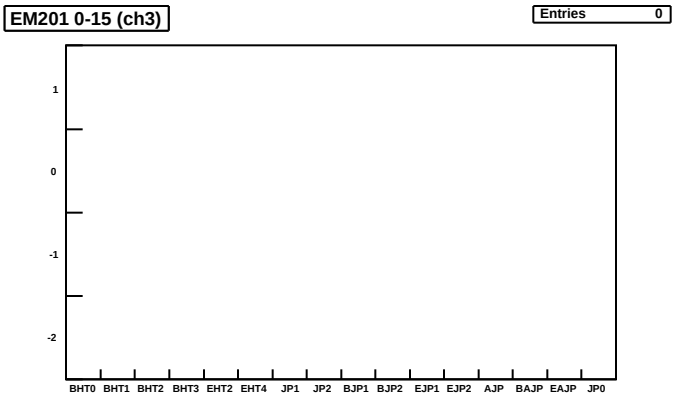




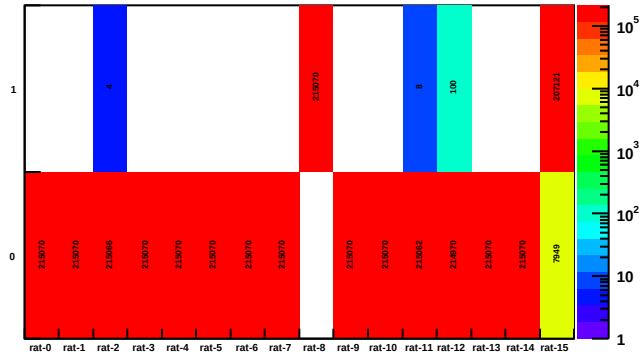
EM201 0-15 (ch3)

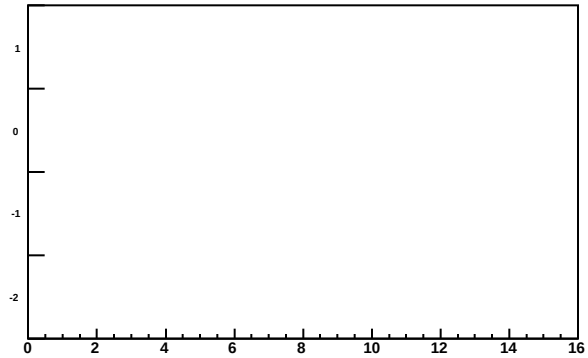


EM201 0-15 (ch3)

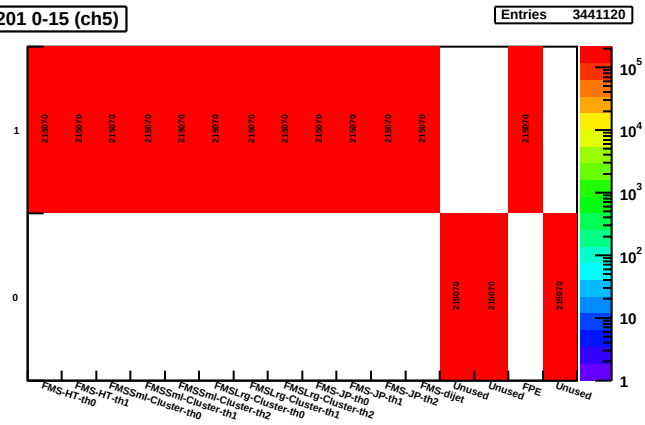


RAT board (ch4)

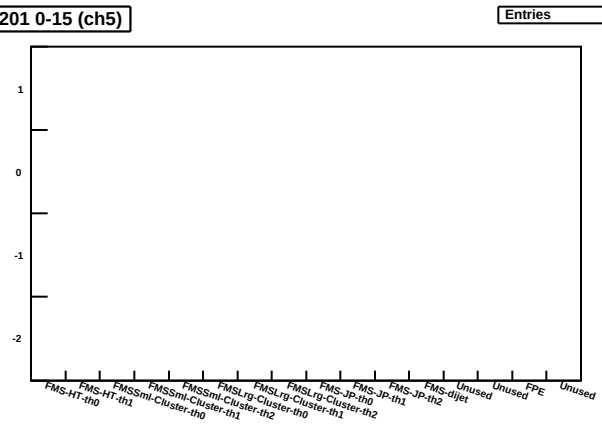




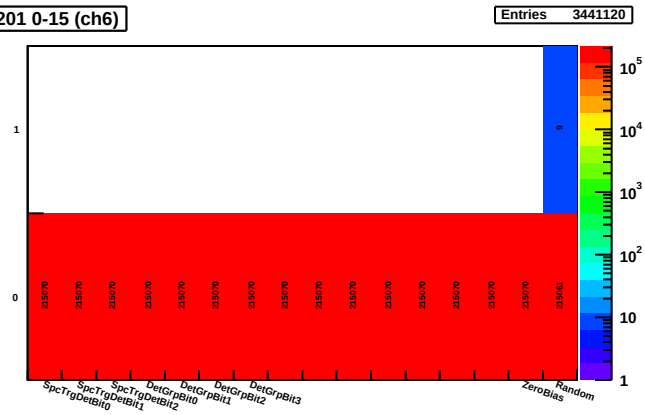
FP201 0-15 (ch5)

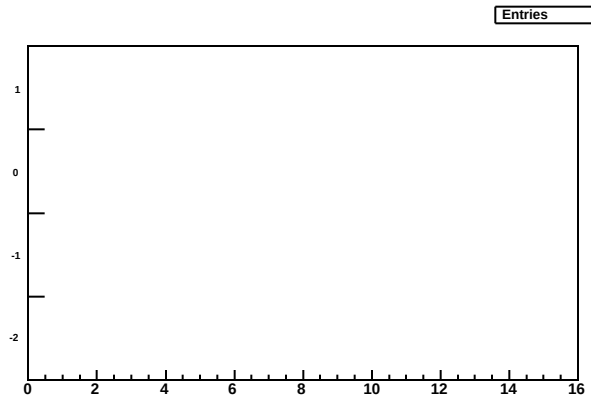
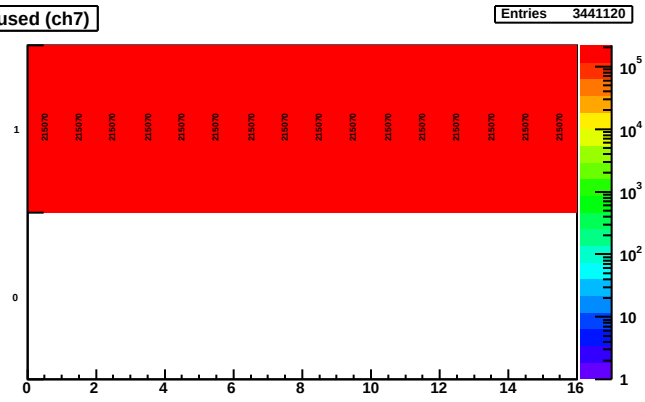
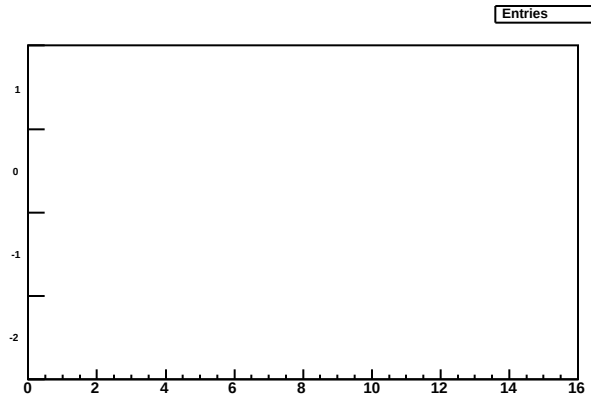


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)