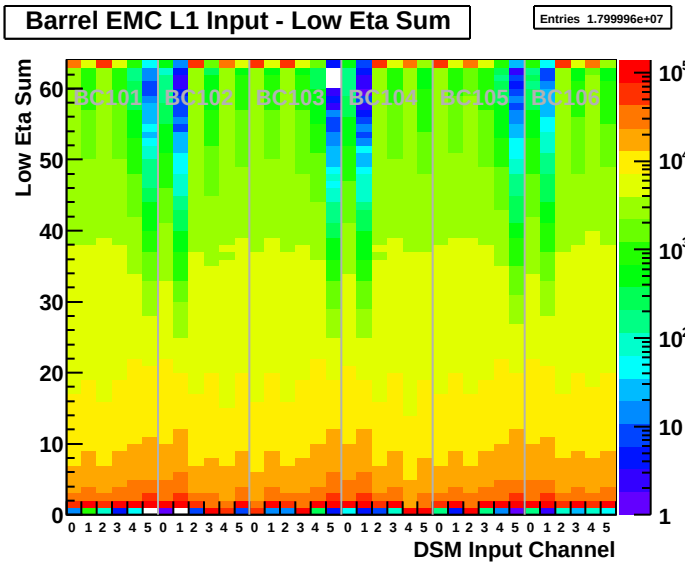
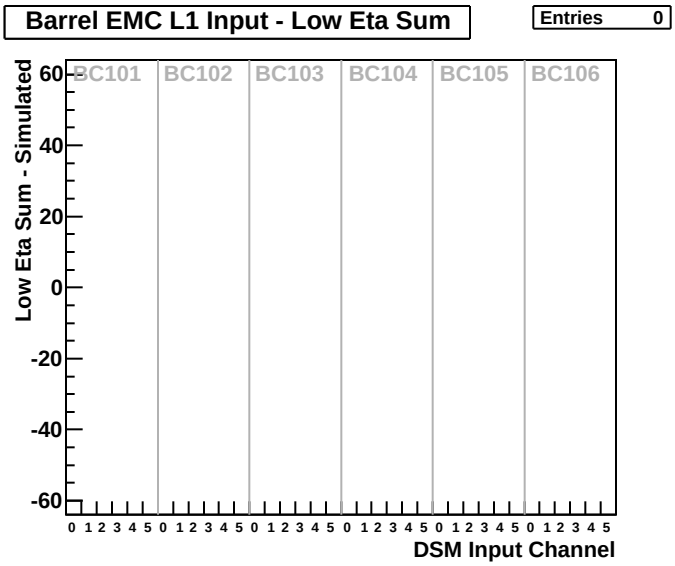
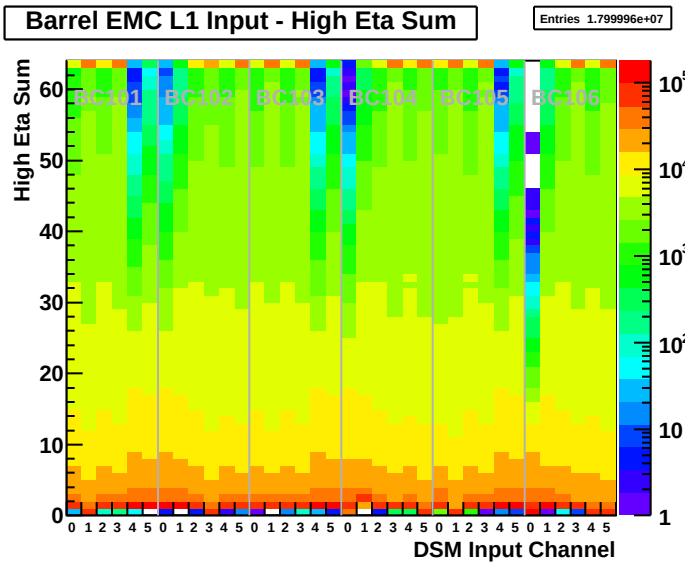
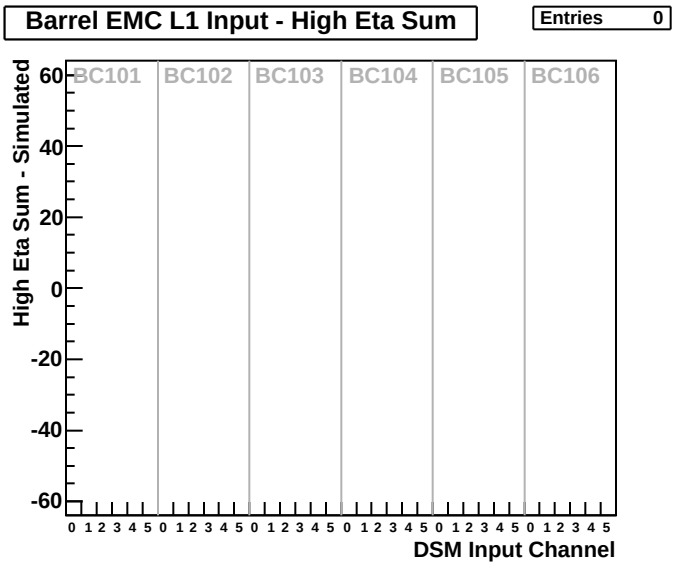
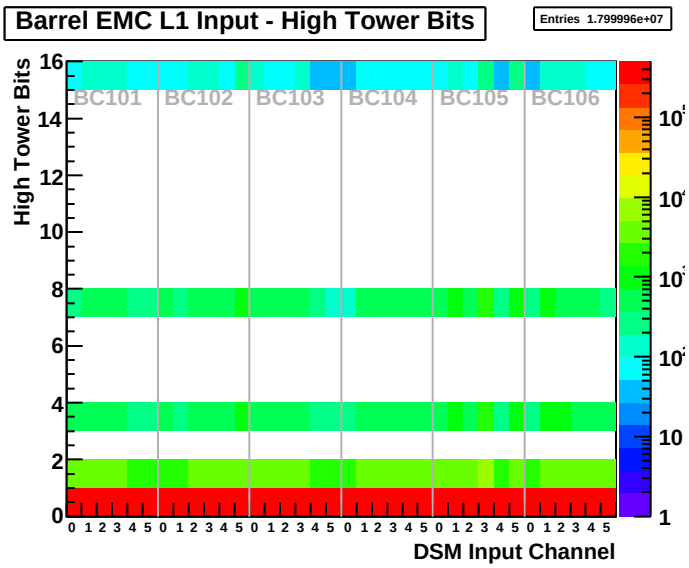
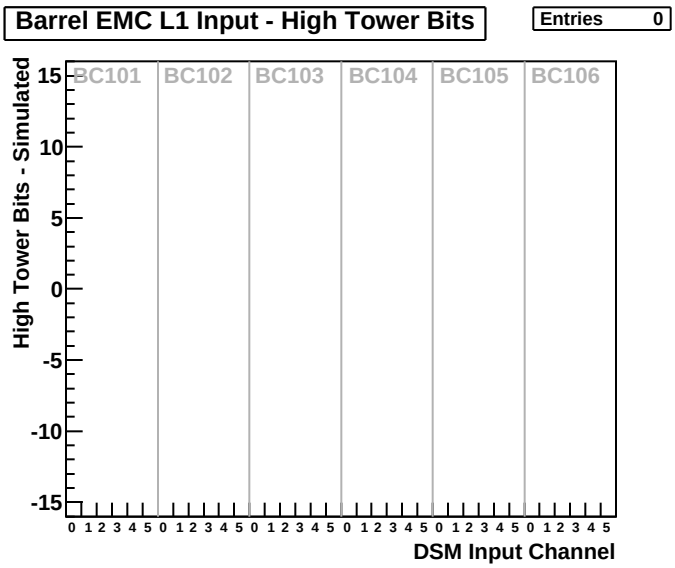

Barrel EMC L1 Input - Low Eta Sum
Entries 1.799996e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

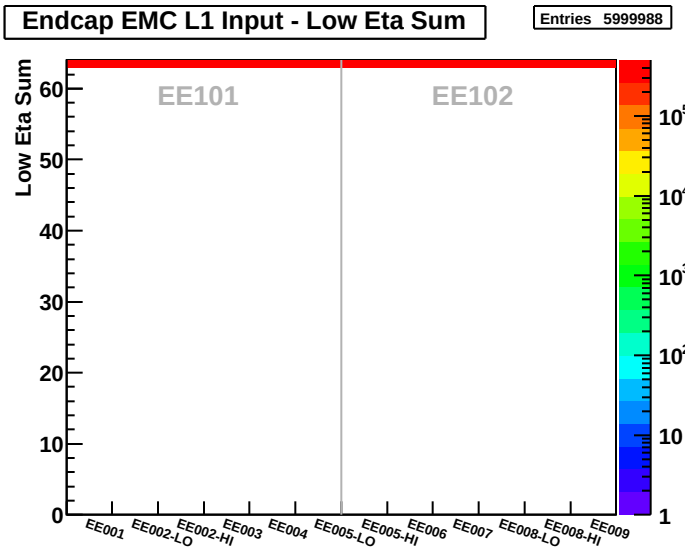

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

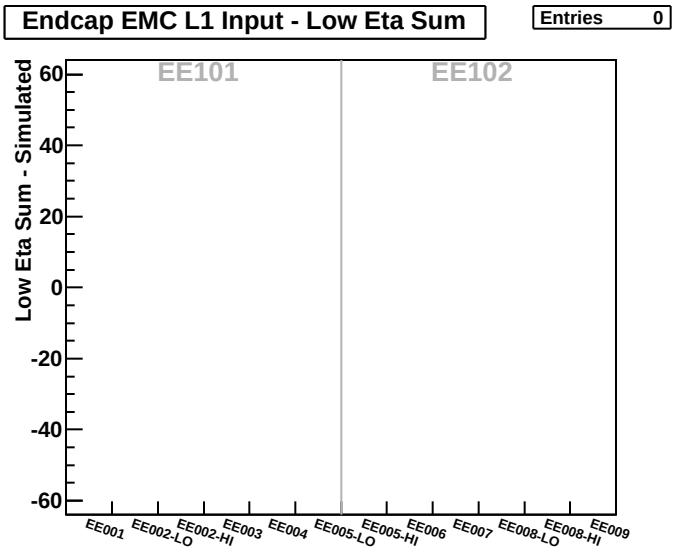

Barrel EMC L1 Input - High Eta Sum
Entries 1.799996e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

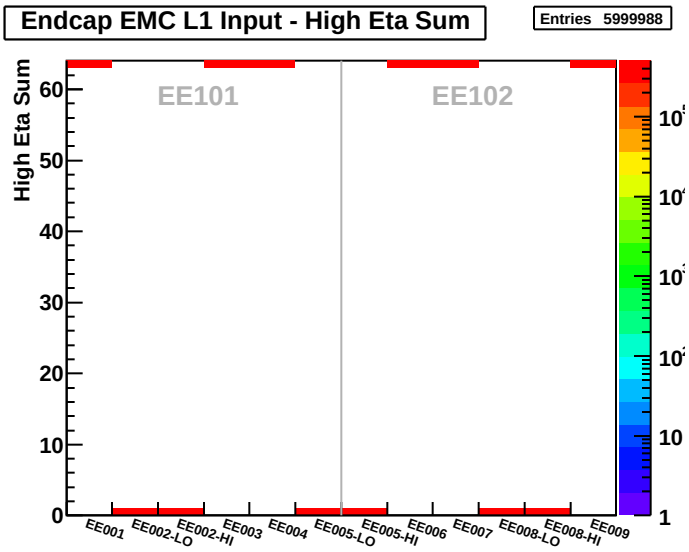

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

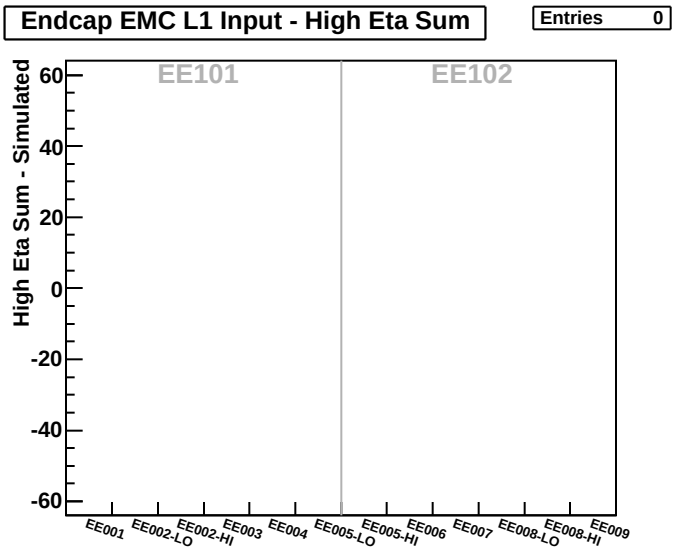

Barrel EMC L1 Input - High Tower Bits
Entries 1.799996e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

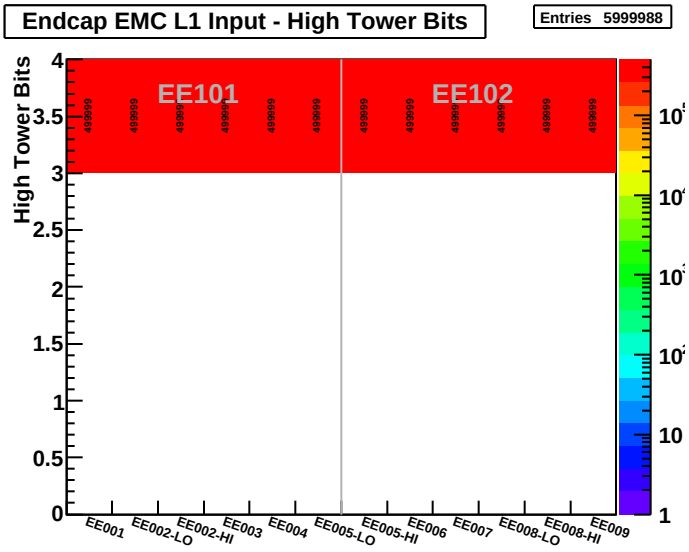

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

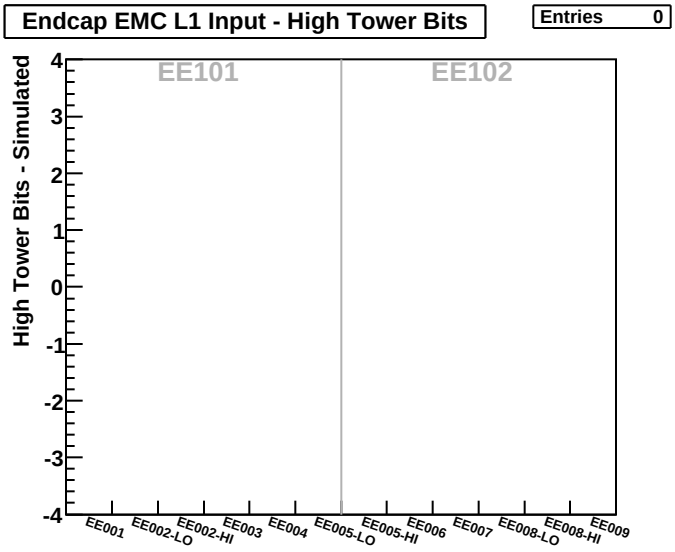

Endcap EMC L1 Input - Low Eta Sum
Entries 5999988
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

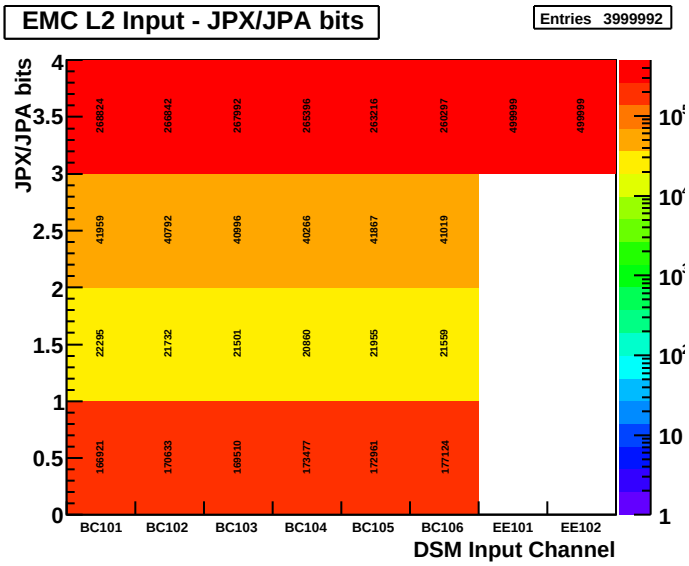

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

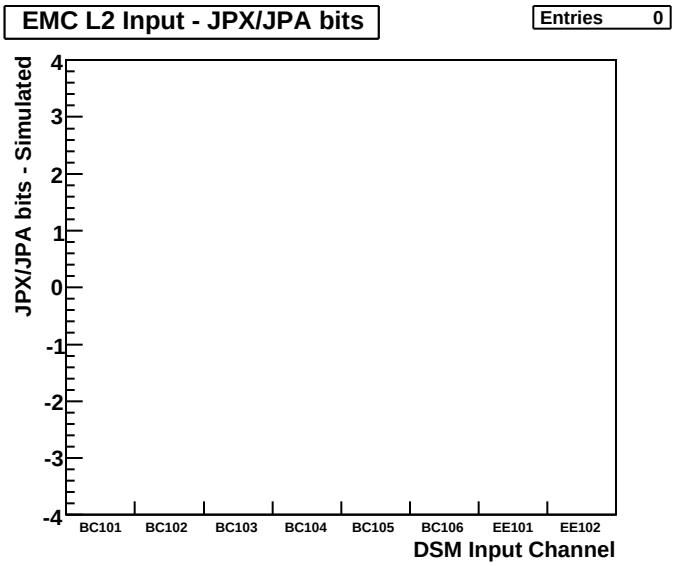

Endcap EMC L1 Input - High Eta Sum
Entries 5999988
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

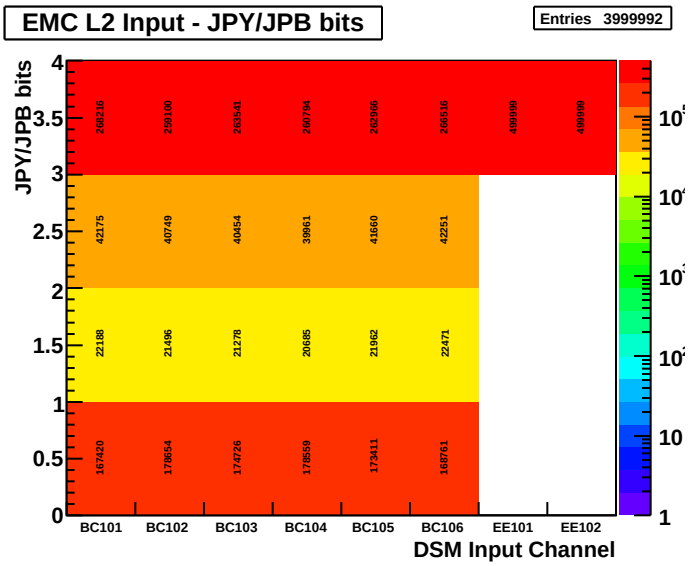

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

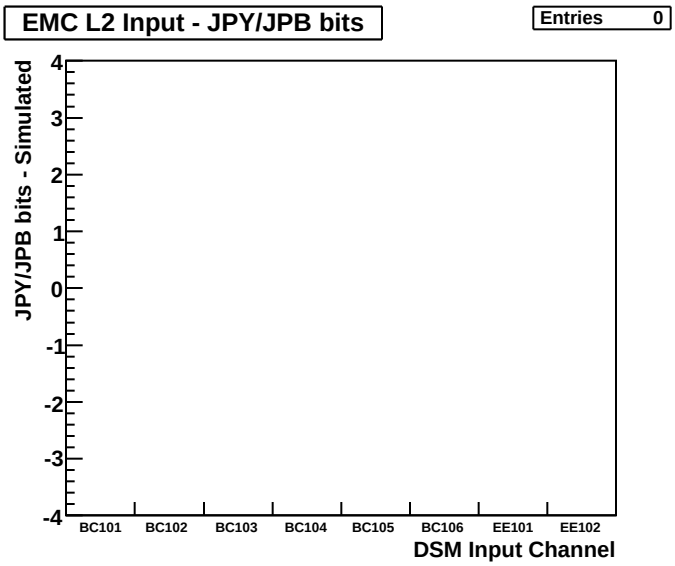

Endcap EMC L1 Input - High Tower Bits
Entries 5999988
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

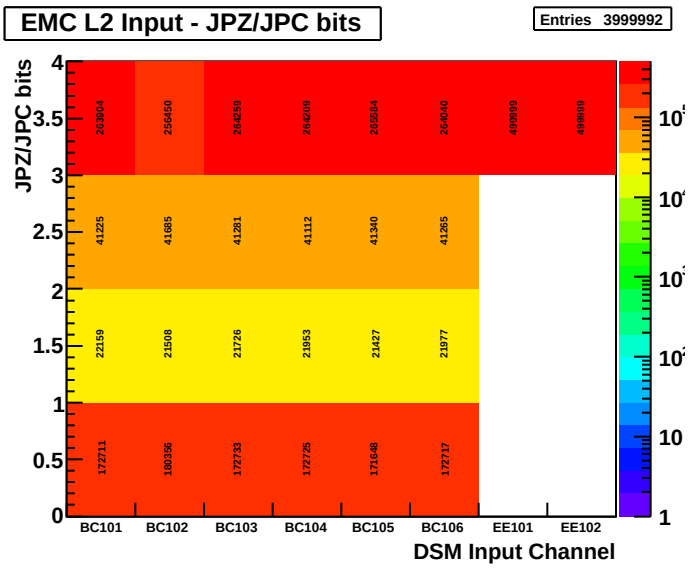

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

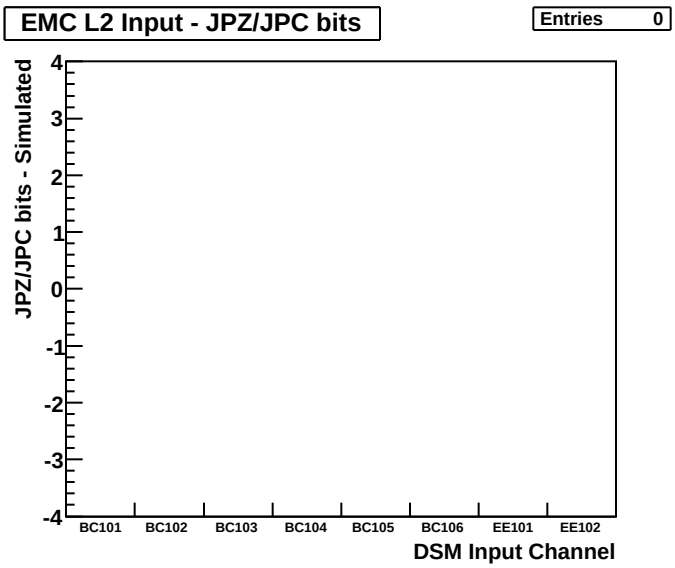

EMC L2 Input - JPX/JPA bits
Entries 3999992
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
258824 258842 257992 255396 253216 250297 499999 499999
41959 40792 40996 40266 41867 41019
22295 21732 21501 20860 21955 21559
166921 170633 169510 173477 172061 177124

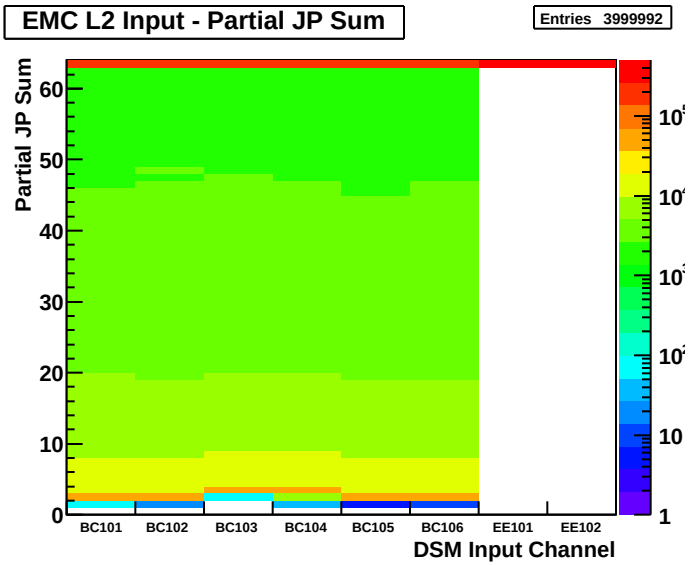

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

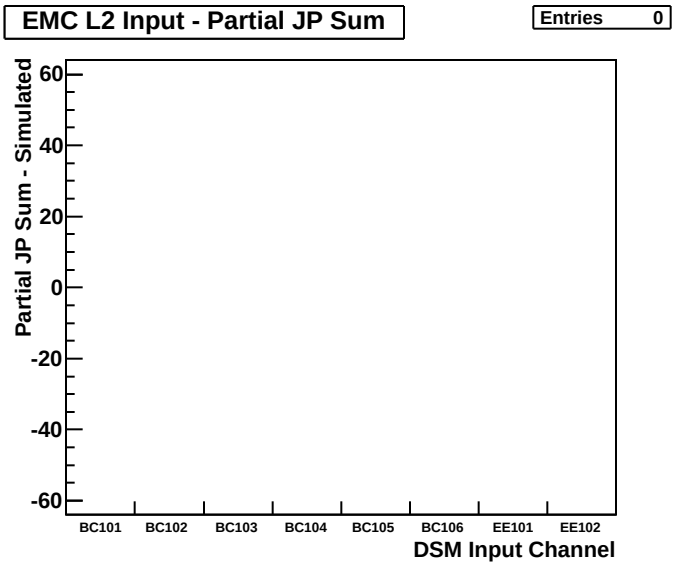

EMC L2 Input - JPY/JPB bits
Entries 3999992
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
268216 259100 263541 260704 262966 266516 499999 499999
42175 40749 40454 39961 41660 42251
22188 21496 21278 20685 21962 22471
167420 178654 174726 178559 173411 168761

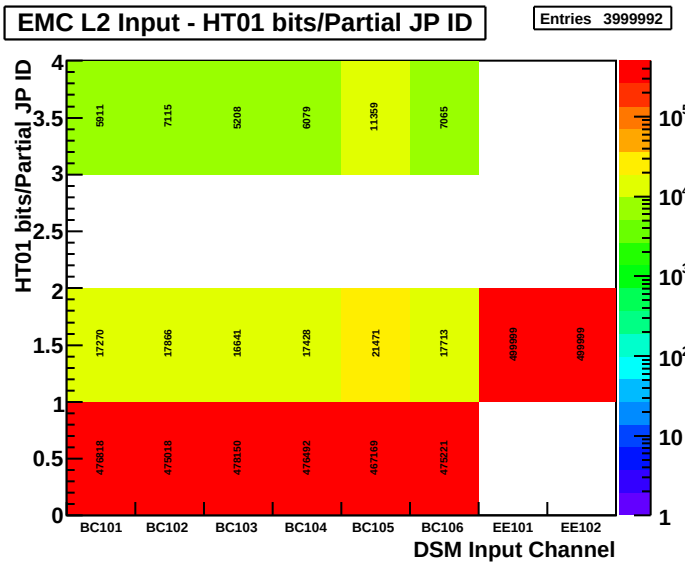

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

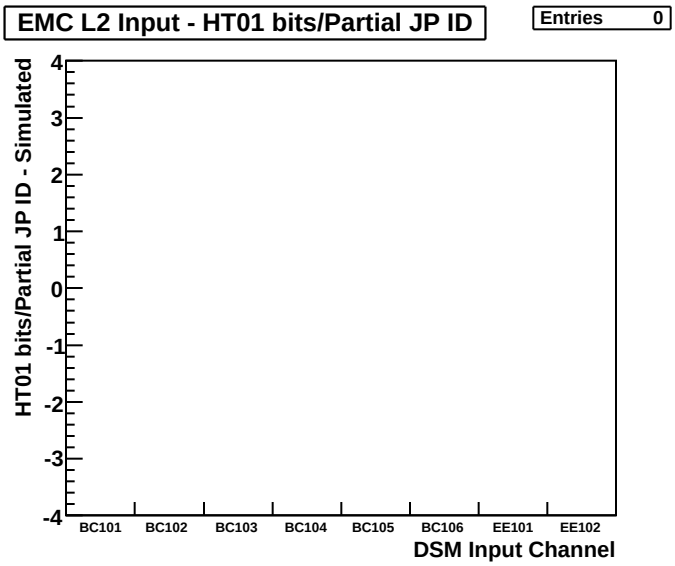

EMC L2 Input - JPZ/JPC bits
Entries 3999992
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
263904 256450 264259 264209 265584 264040 499999 499999
41225 41685 41281 41112 41340 41265
22159 21508 21726 21953 21427 21977
172711 180356 172733 172725 171648 172717

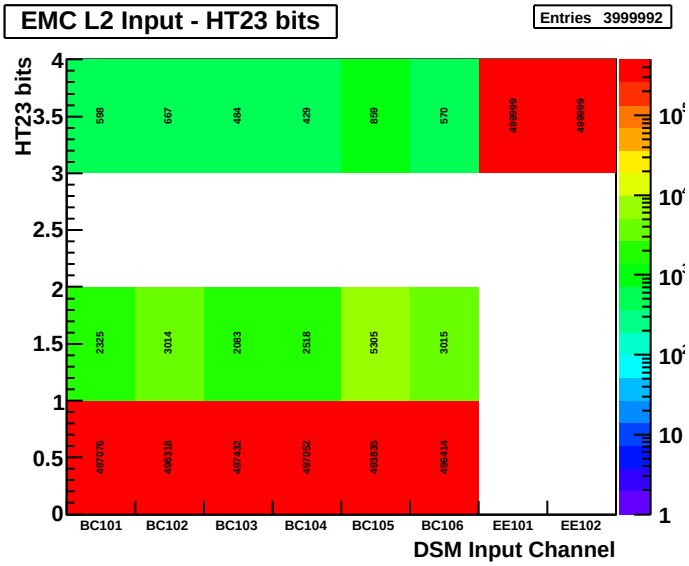

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

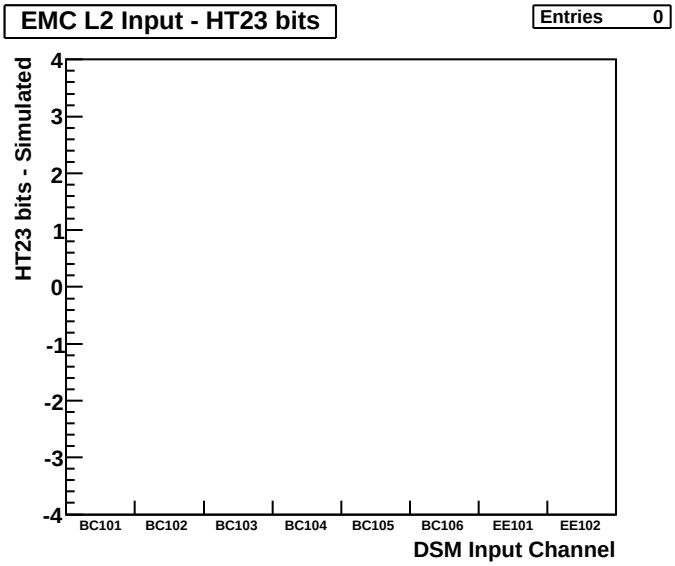

EMC L2 Input - Partial JP Sum
Entries 3999992
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

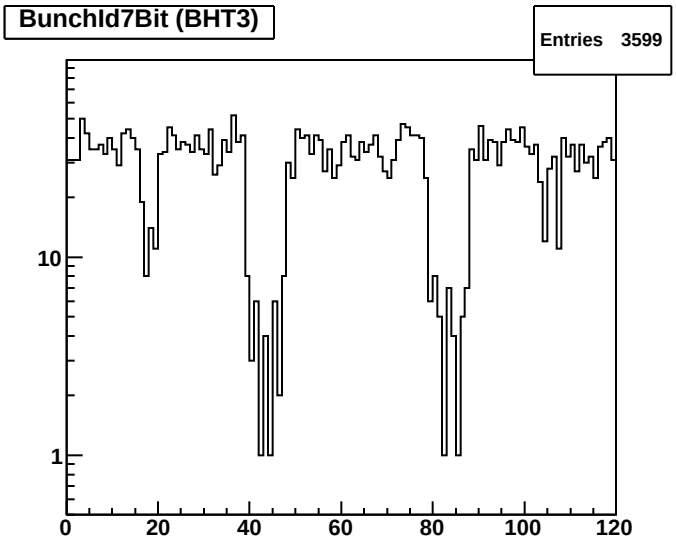

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

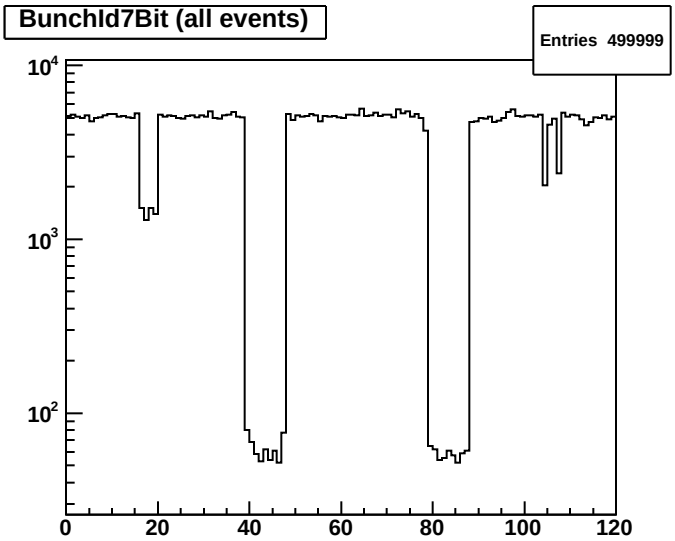

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3999992
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

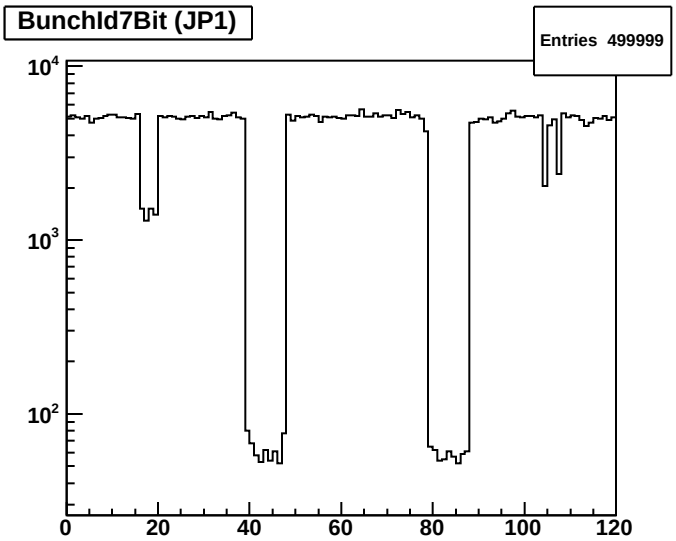

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

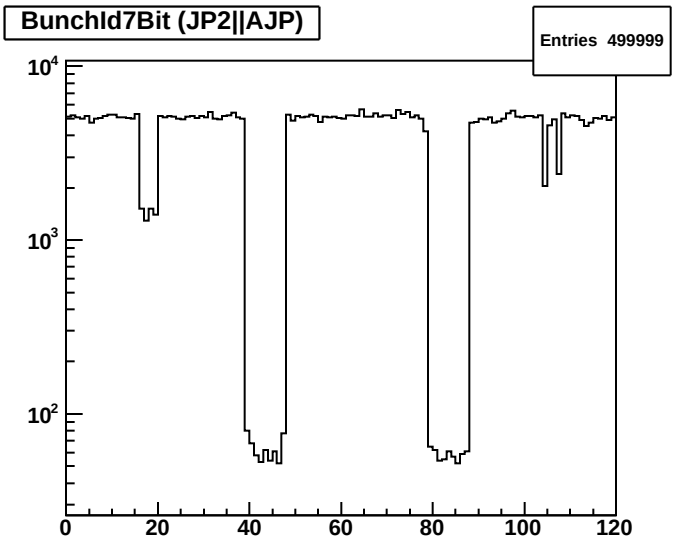

EMC L2 Input - HT23 bits
Entries 3999992
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

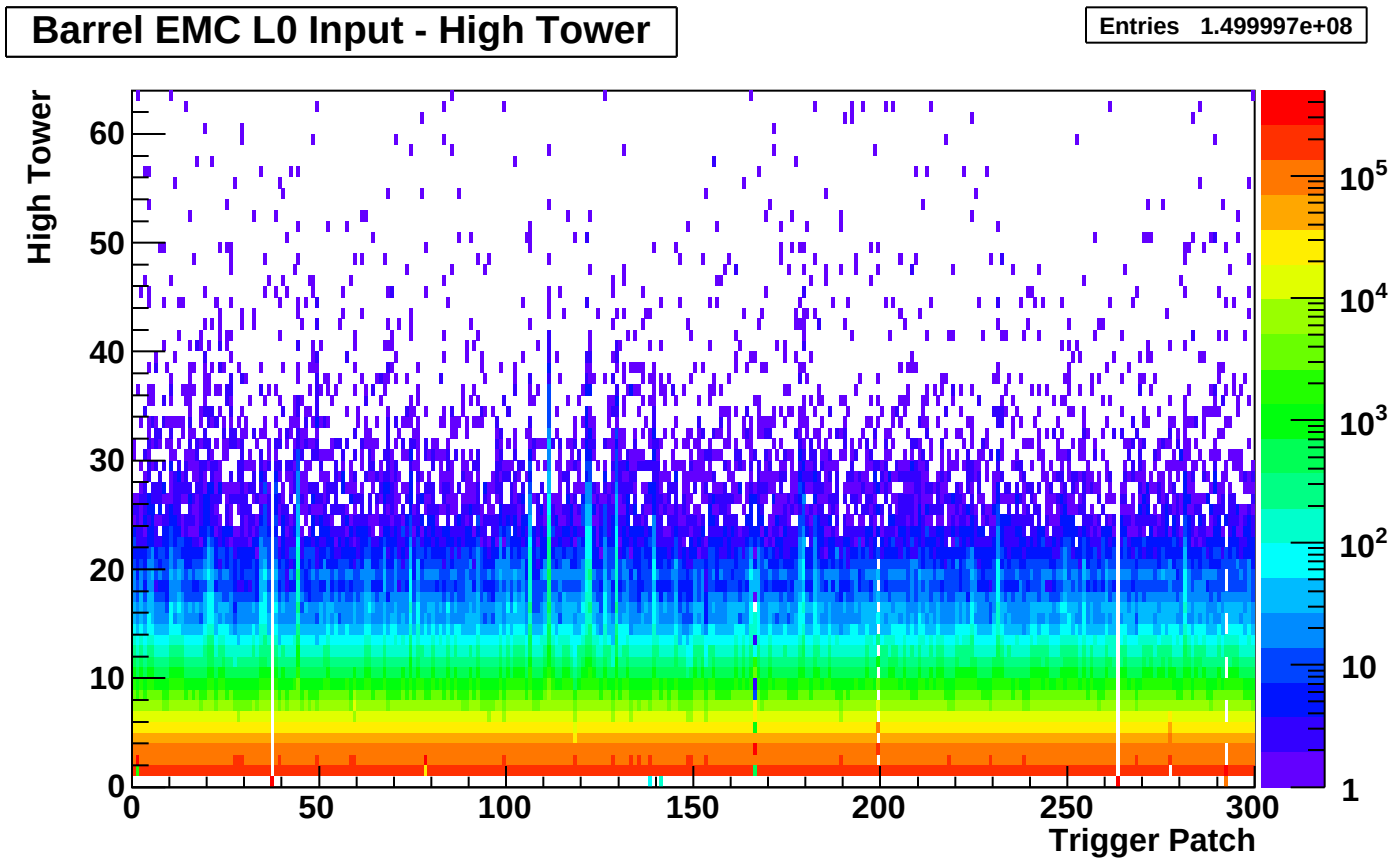

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

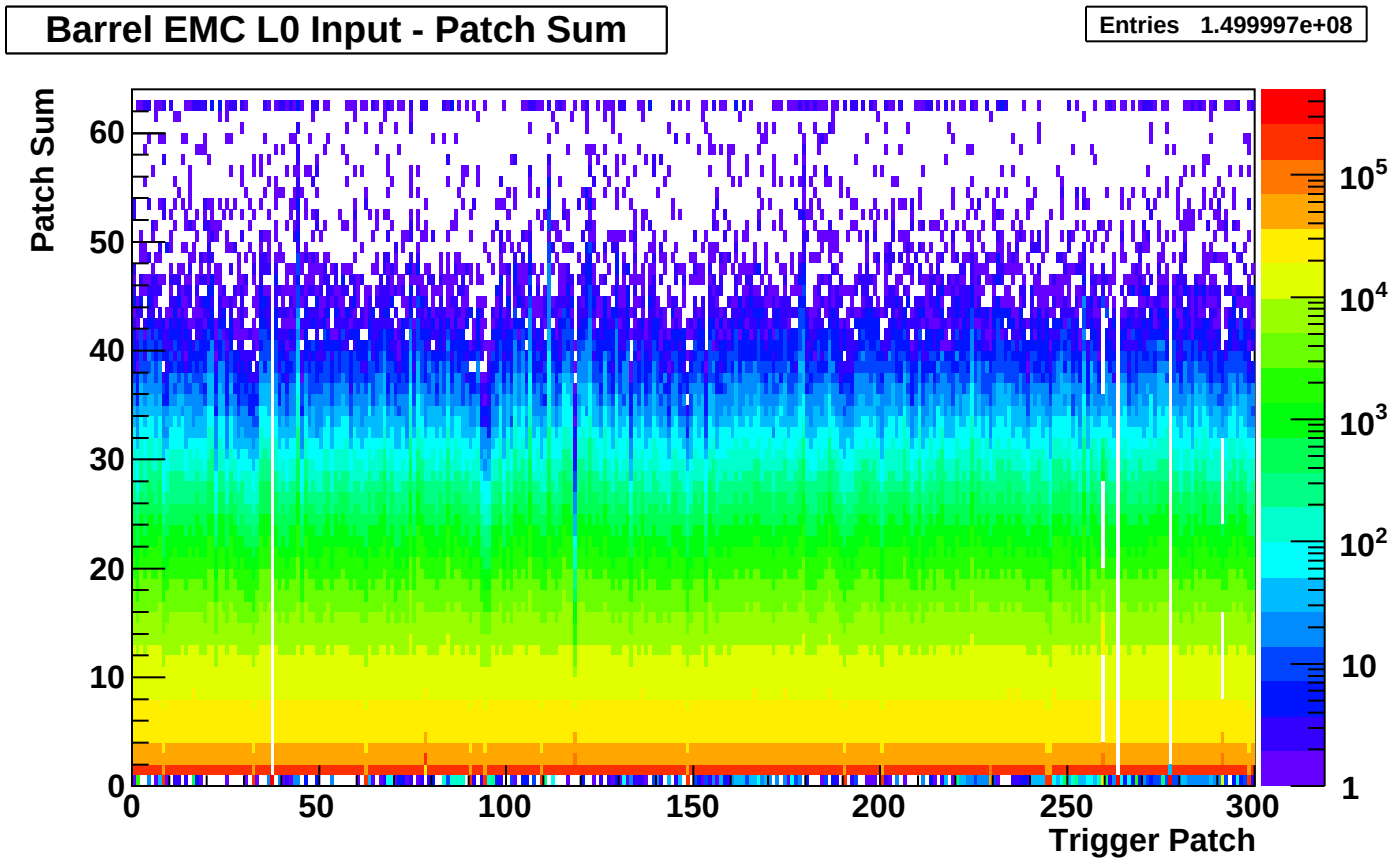

BunchId7Bit (BHT3)
Entries 3599
10
1
0
20
40
60
80
100
120

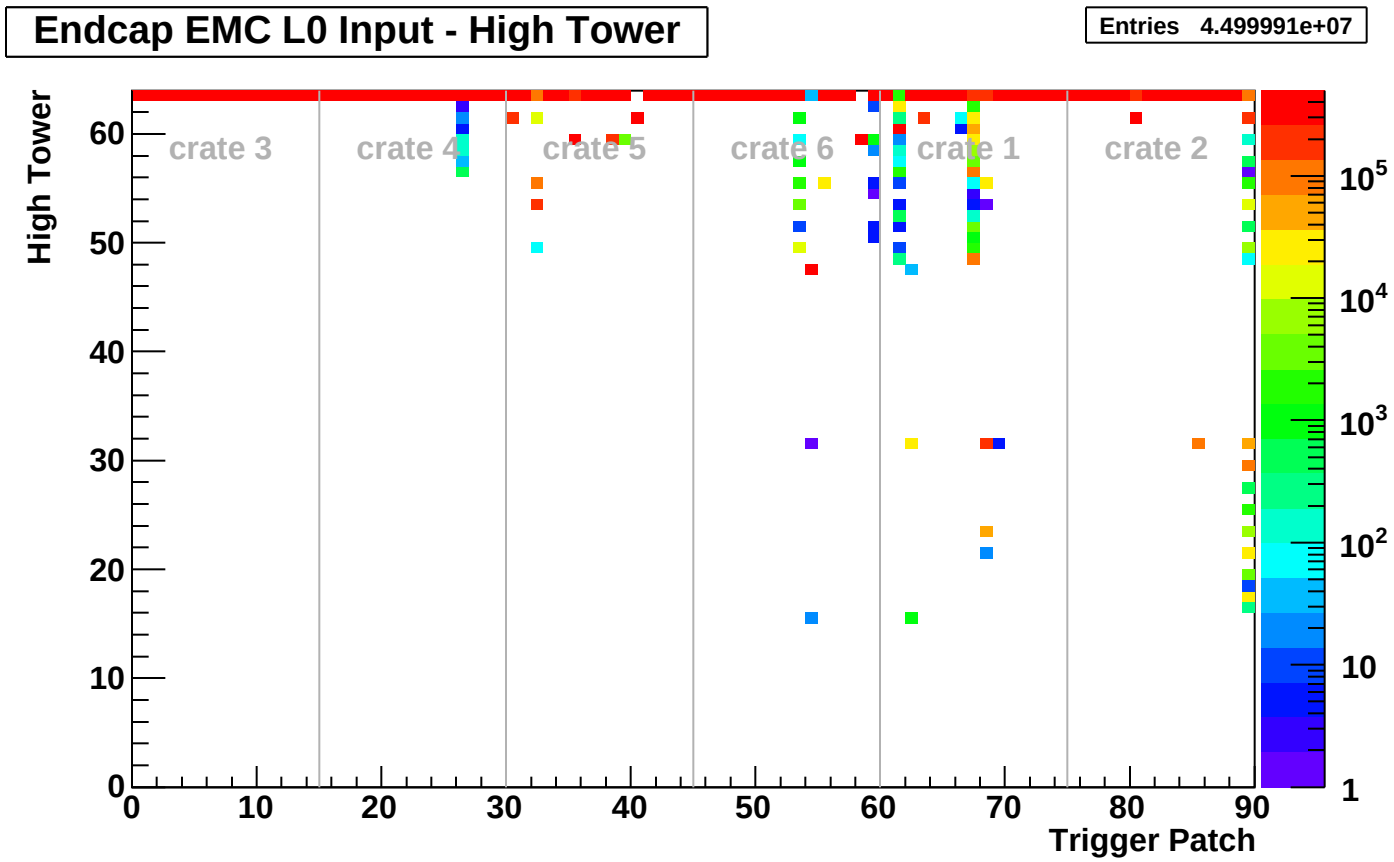

BunchId7Bit (all events)
Entries 499999
10^4
10^3
10^2
0
20
40
60
80
100
120

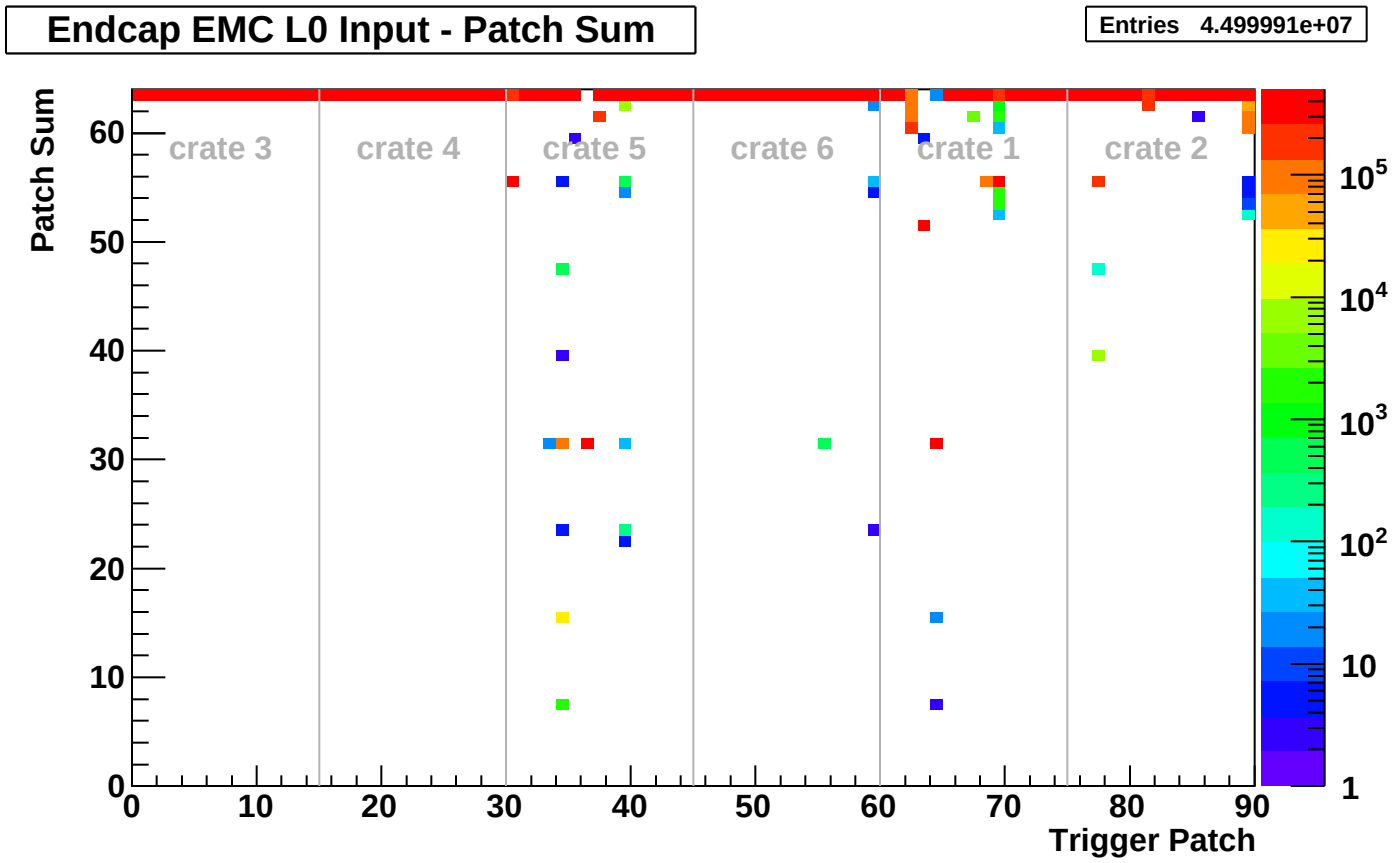

BunchId7Bit (JP1)
Entries 499999
10^4
10^3
10^2
0
20
40
60
80
100
120

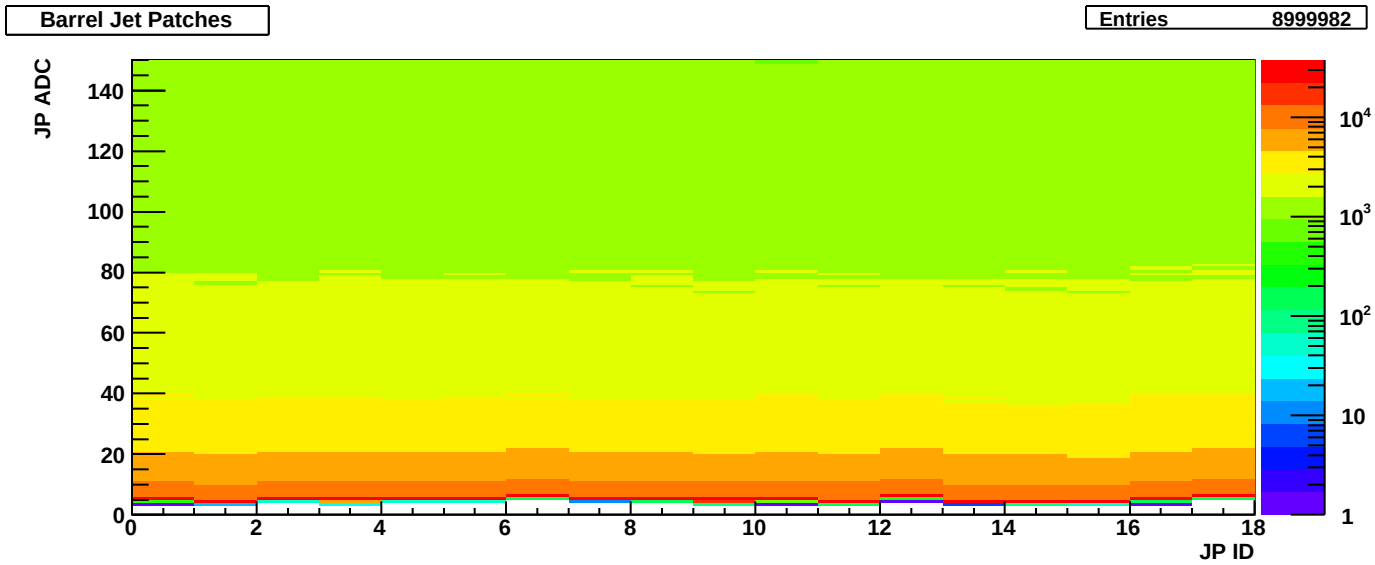

BunchId7Bit (JP2||AJP)
Entries 499999
10^4
10^3
10^2
0
20
40
60
80
100
120

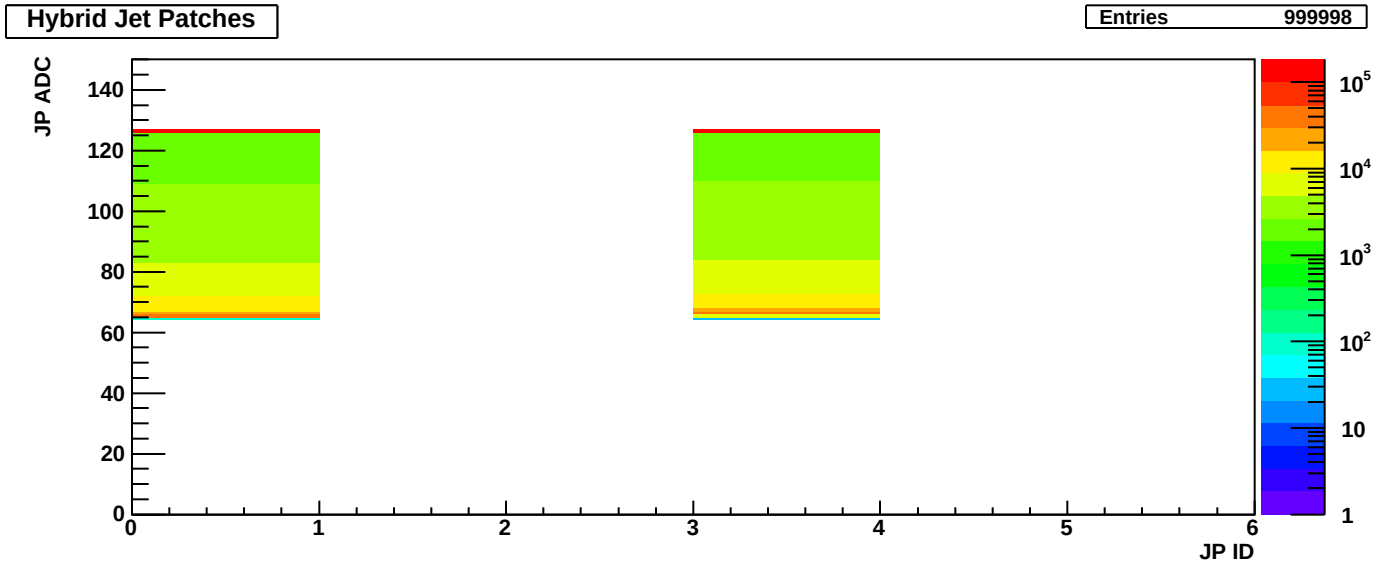

Barrel EMC L0 Input - High Tower
Entries 1.499997e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

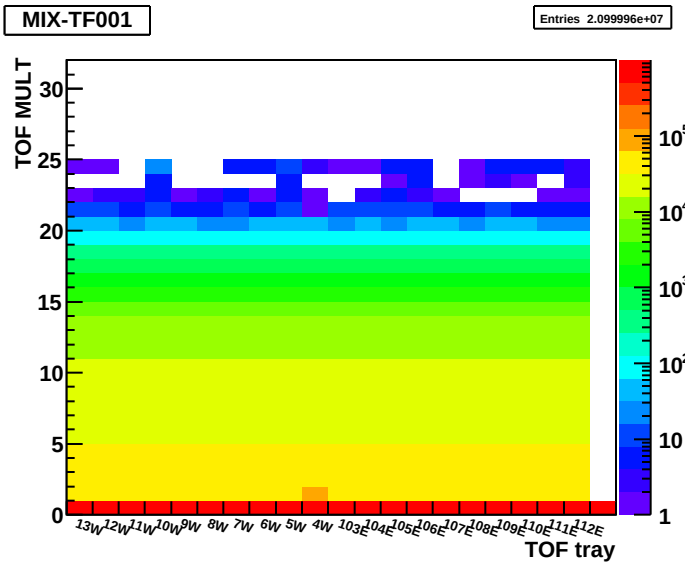

Barrel EMC L0 Input - Patch Sum
Entries 1.499997e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

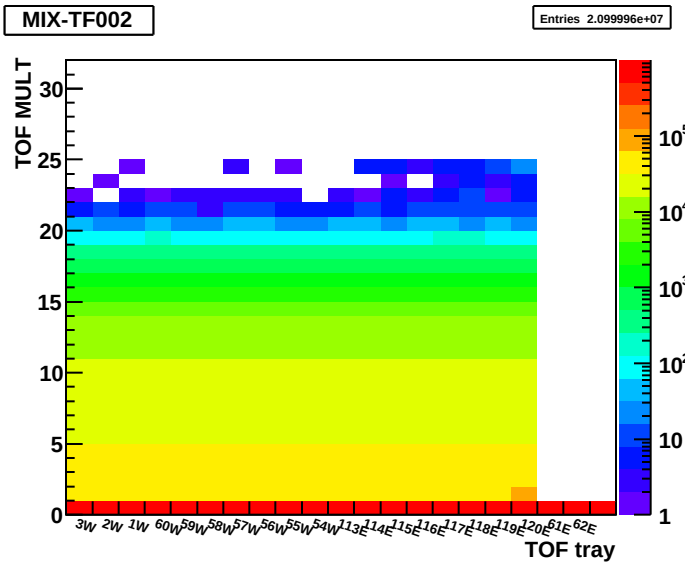

Endcap EMC L0 Input - High Tower
Entries 4.499991e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

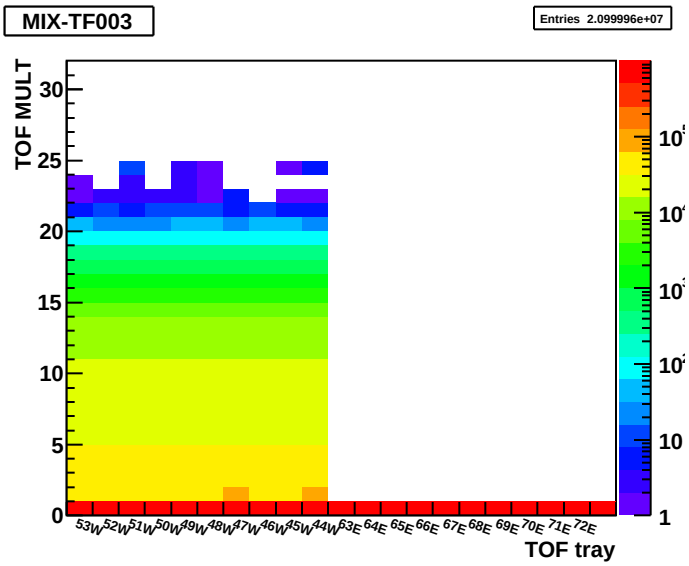

Endcap EMC L0 Input - Patch Sum
Entries 4.499991e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

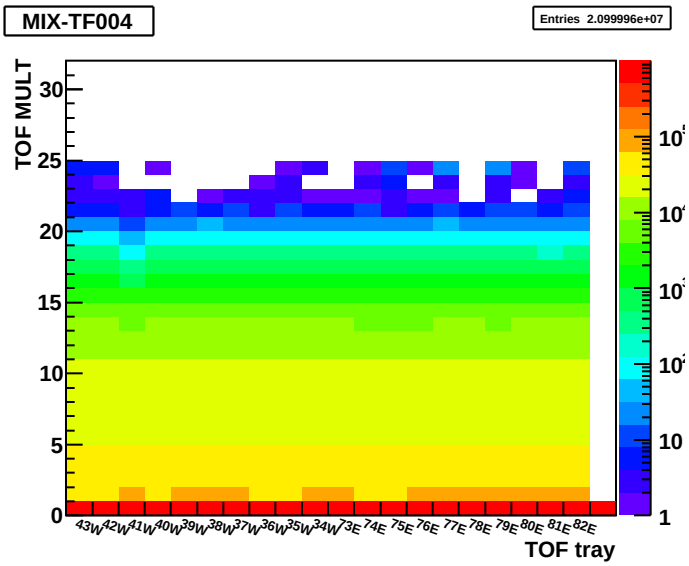

Barrel Jet Patches
Entries 8999982
JP ADC
JP ID

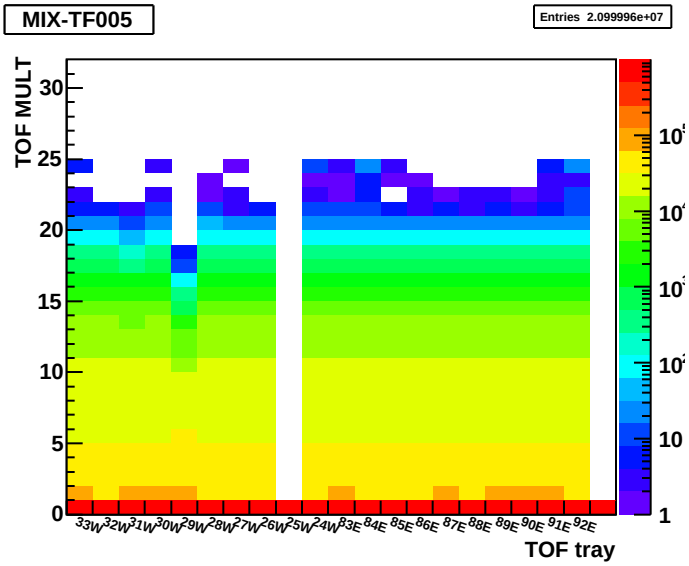

Hybrid Jet Patches
Entries 999998
JP ADC
JP ID

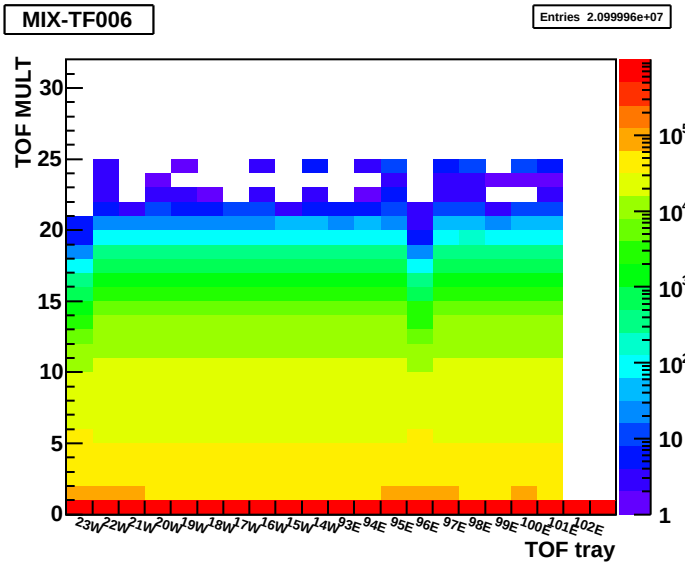

MIX-TF001
Entries 2.099996e+07
TOF MULT
TOF tray

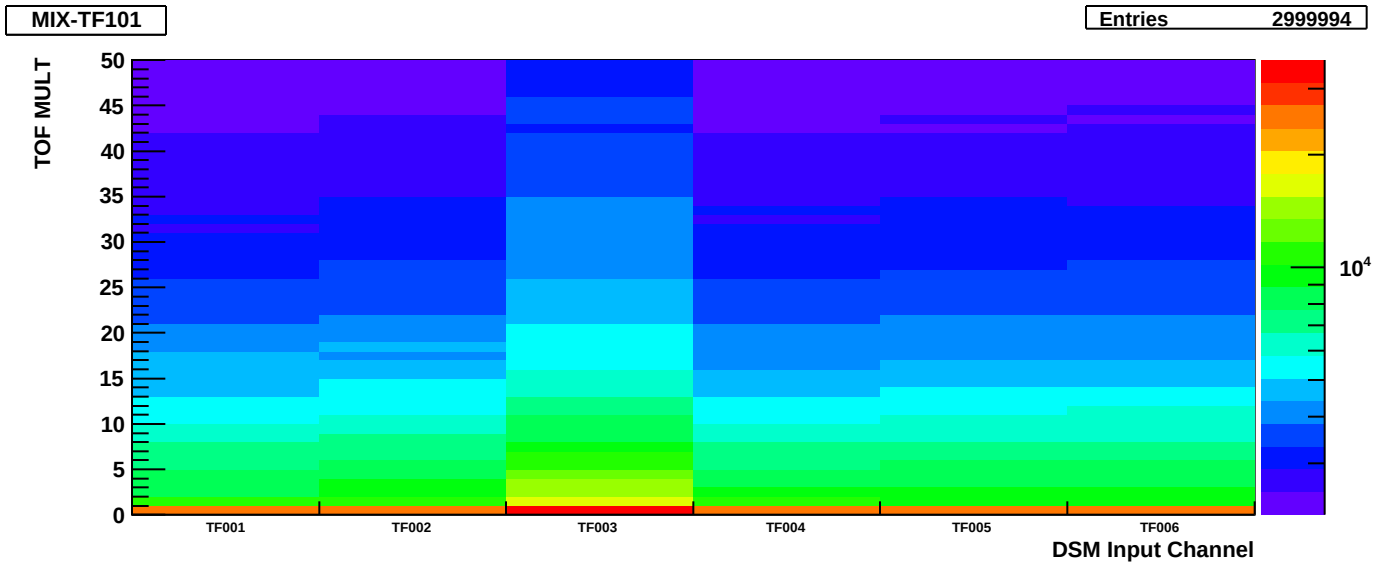

MIX-TF002
Entries 2.099996e+07
TOF MULT
TOF tray

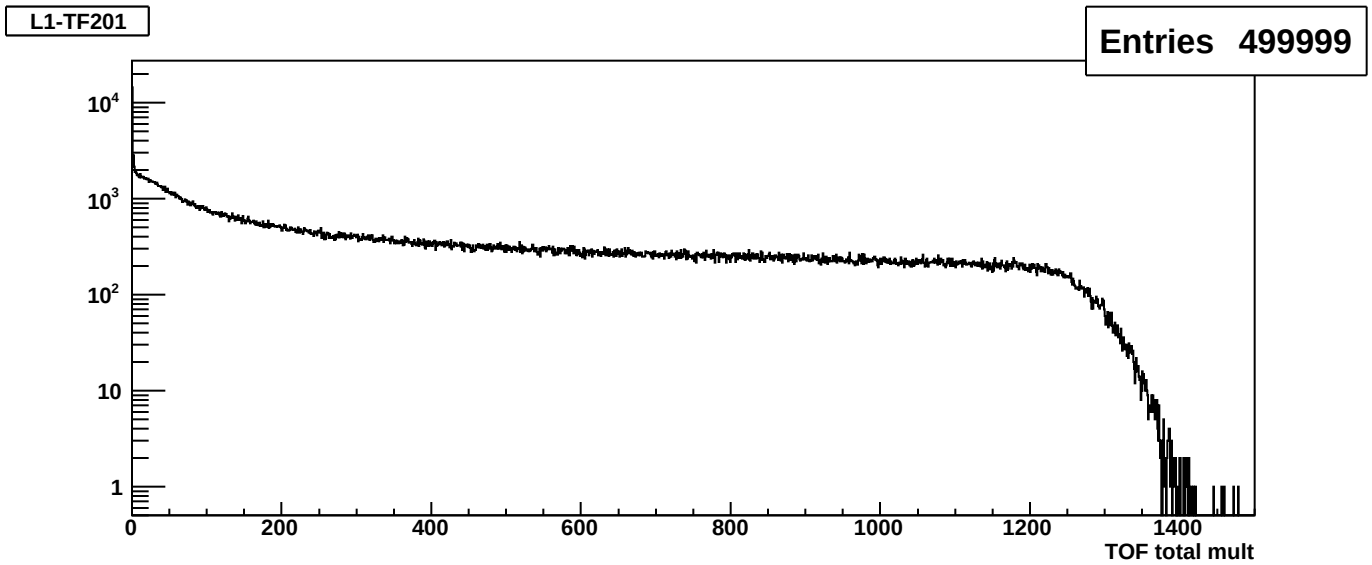

MIX-TF003
Entries 2.099996e+07
TOF MULT
TOF tray

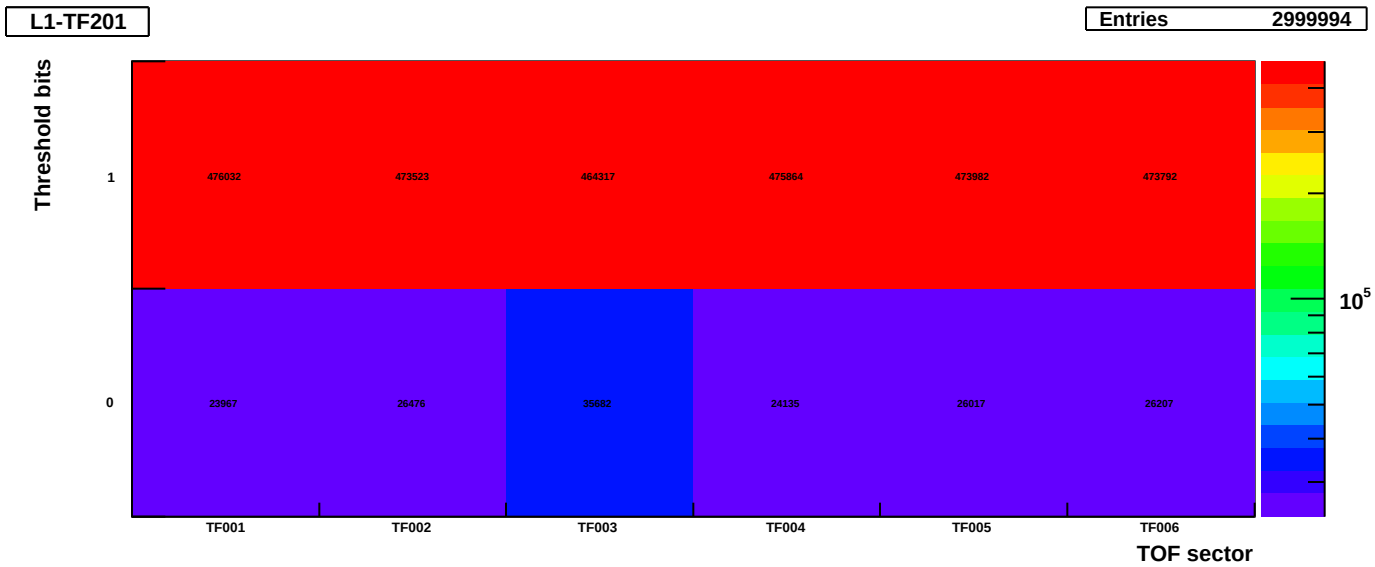

MIX-TF004
Entries 2.099996e+07
TOF MULT
TOF tray

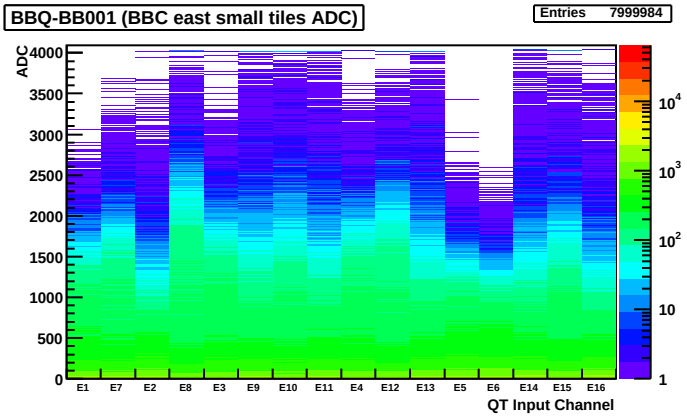

MIX-TF005
Entries 2.099996e+07
TOF MULT
TOF tray

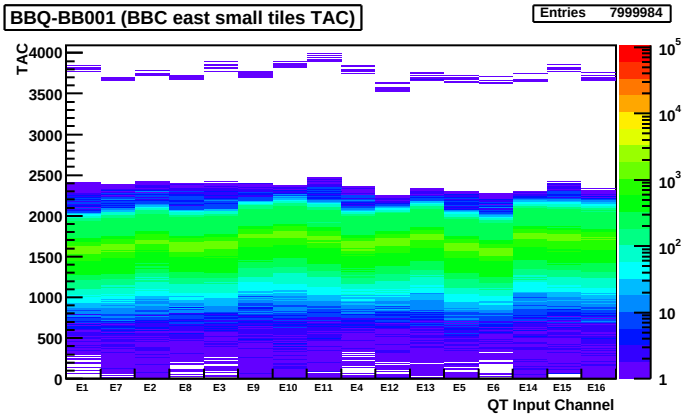

MIX-TF006
Entries 2.099996e+07
TOF MULT
TOF tray

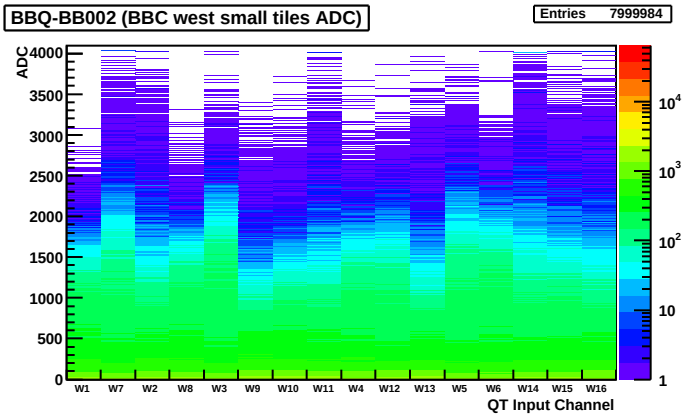

MIX-TF101
Entries 2999994
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

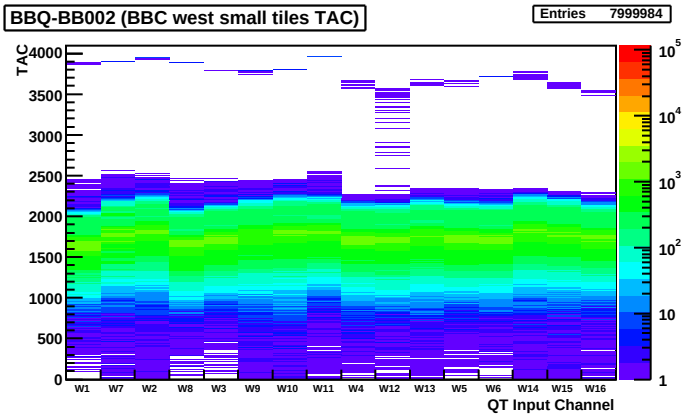

L1-TF201
Entries 499999
10^4
10^3
10^2
10
1
0
200
400
600
800
1000
1200
1400
TOF total mult

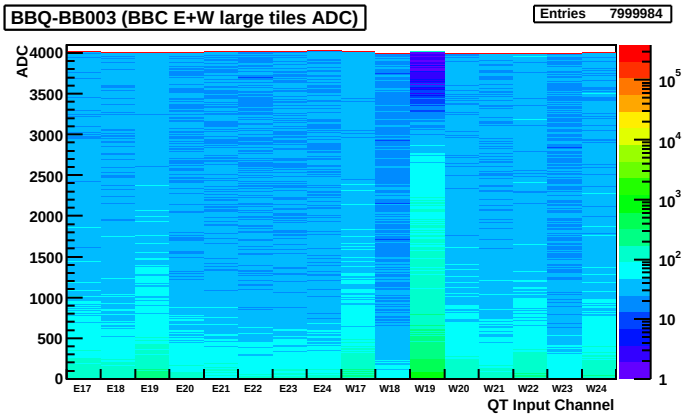

L1-TF201
Entries 2999994
Threshold bits
1
0
476032
473523
464317
475864
473982
473792
23967
26476
35682
24135
26017
26207
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

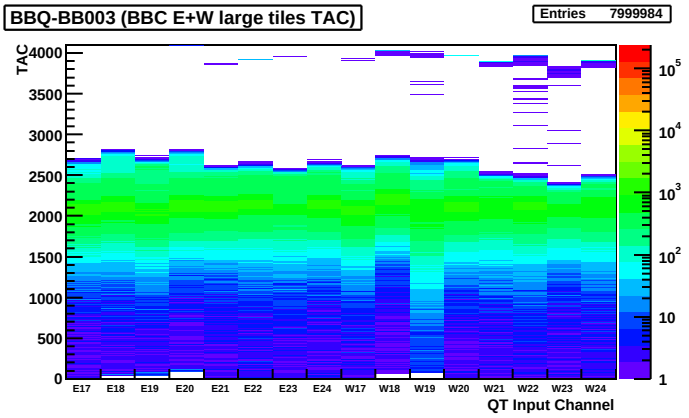

BBQ-BB001 (BBC east small tiles ADC)
Entries 7999984
ADC
QT Input Channel

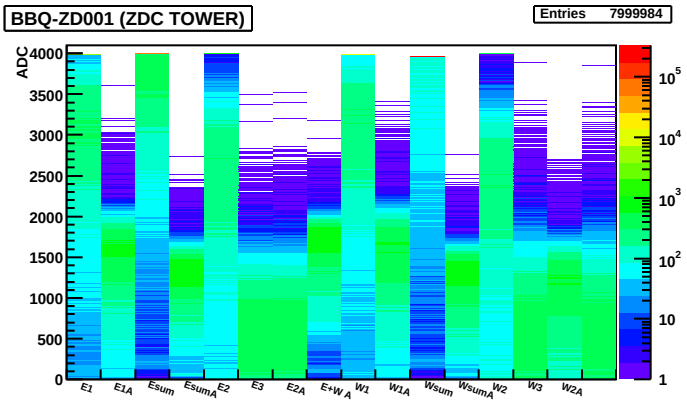

BBQ-BB001 (BBC east small tiles TAC)
Entries 7999984
TAC
QT Input Channel

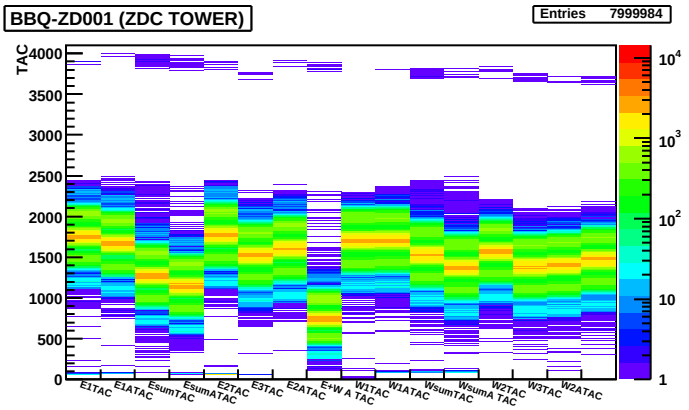

BBQ-BB002 (BBC west small tiles ADC)
Entries 7999984
ADC
QT Input Channel

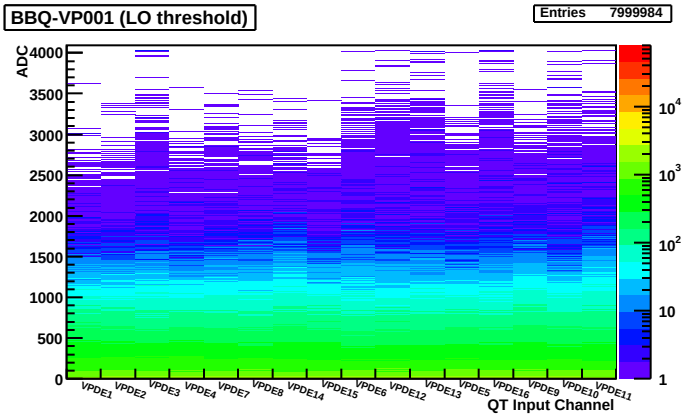

BBQ-BB002 (BBC west small tiles TAC)
Entries 7999984
TAC
QT Input Channel

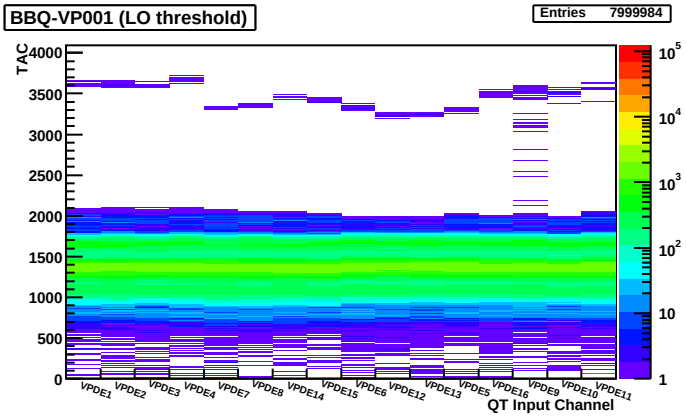

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 7999984
ADC
QT Input Channel

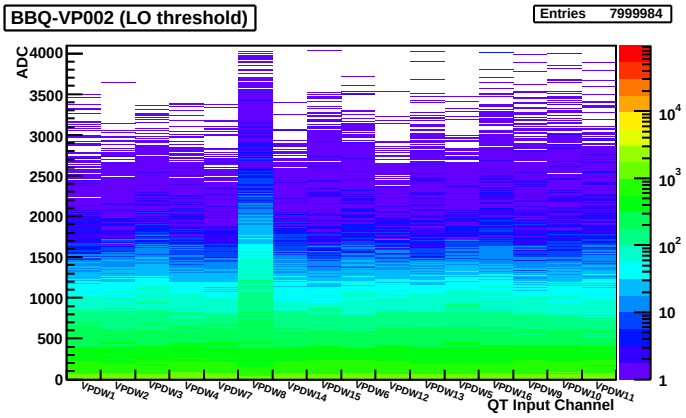

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 7999984
TAC
QT Input Channel

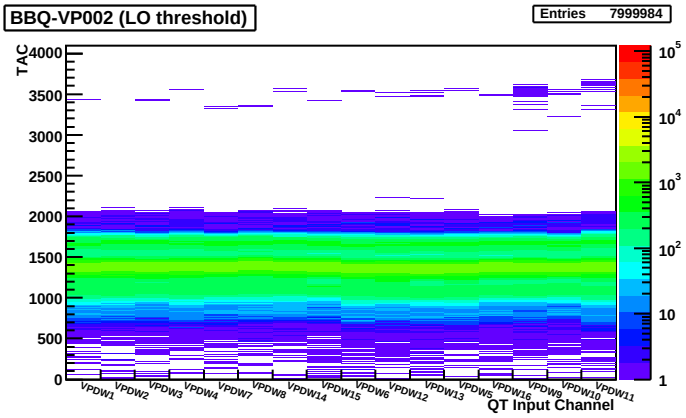

BBQ-ZD001 (ZDC TOWER)
Entries 7999984
ADC

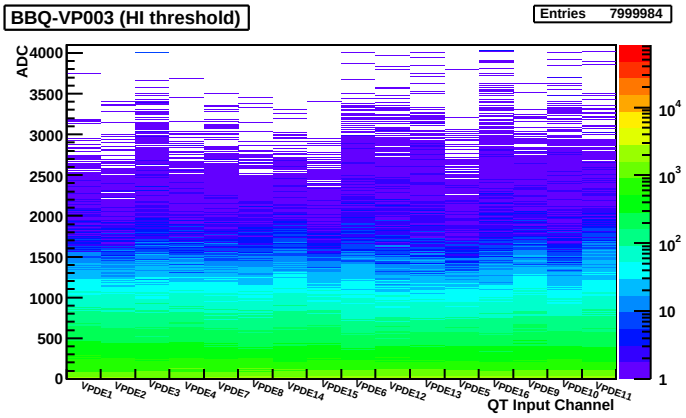

BBQ-ZD001 (ZDC TOWER)
Entries 7999984
TAC

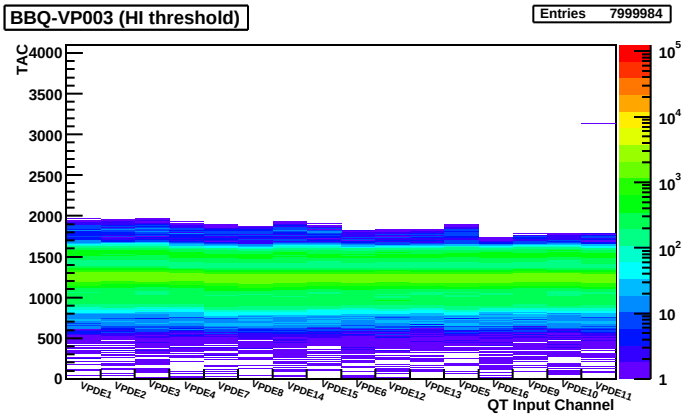

BBQ-VP001 (LO threshold)
Entries 7999984
ADC
QT Input Channel

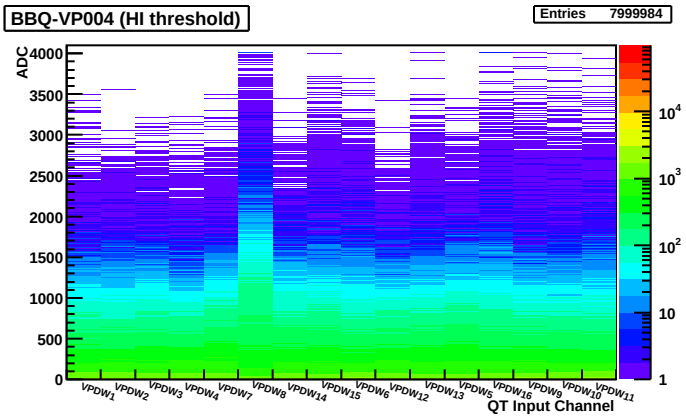

BBQ-VP001 (LO threshold)
Entries 7999984
TAC
QT Input Channel

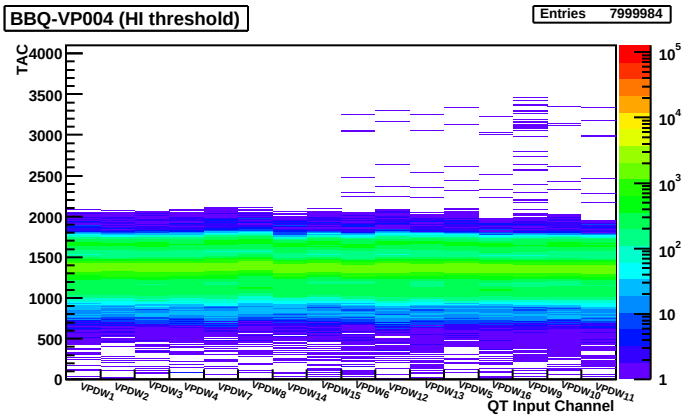

BBQ-VP002 (LO threshold)
Entries 7999984
ADC
QT Input Channel

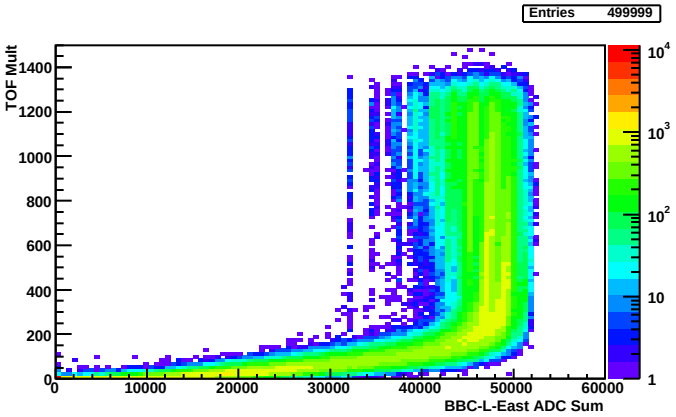

BBQ-VP002 (LO threshold)
Entries 7999984
TAC
QT Input Channel

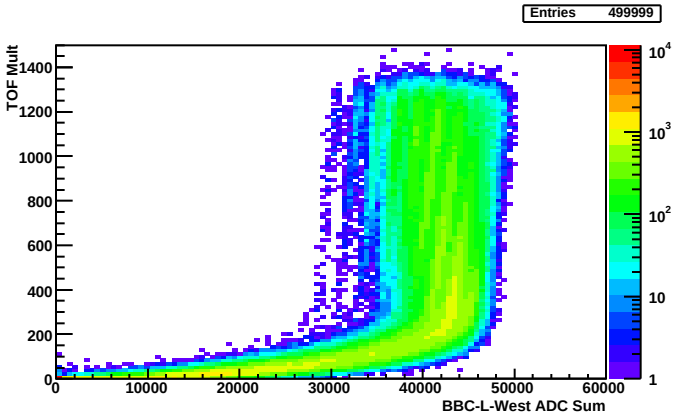

BBQ-VP003 (HI threshold)
Entries 7999984
ADC
QT Input Channel

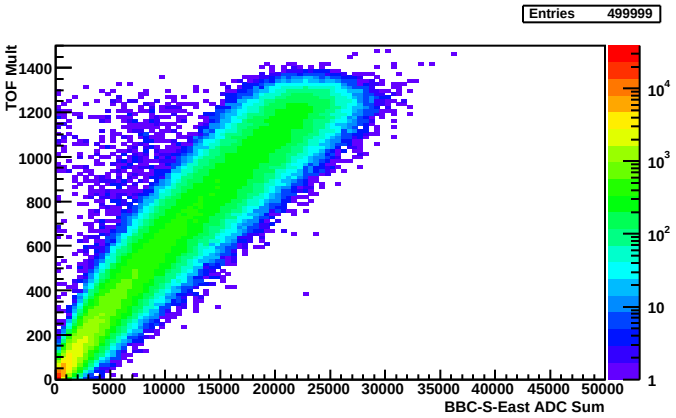

BBQ-VP003 (HI threshold)
Entries 7999984
TAC
QT Input Channel

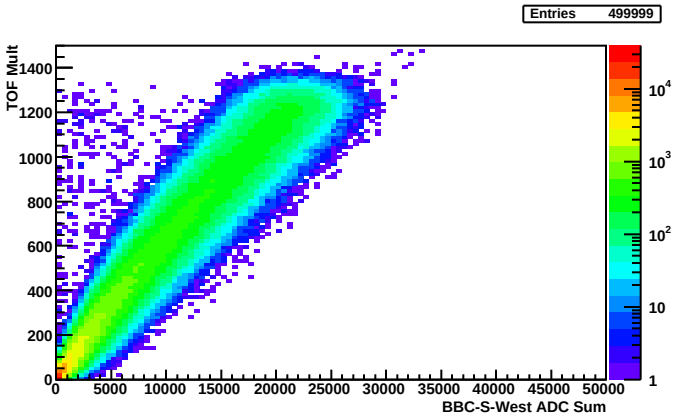

BBQ-VP004 (HI threshold)
Entries 7999984
ADC
QT Input Channel

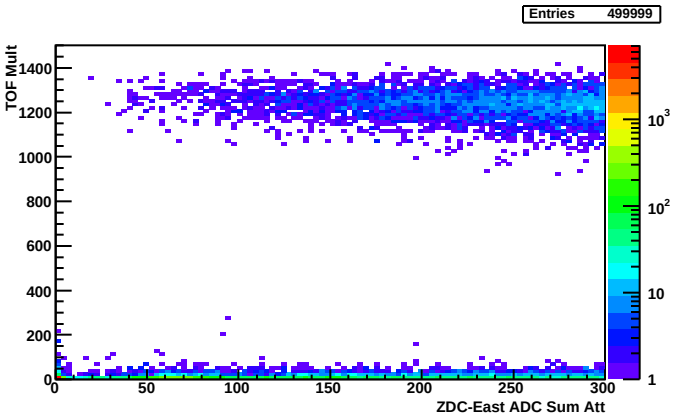

BBQ-VP004 (HI threshold)
Entries 7999984
TAC
QT Input Channel

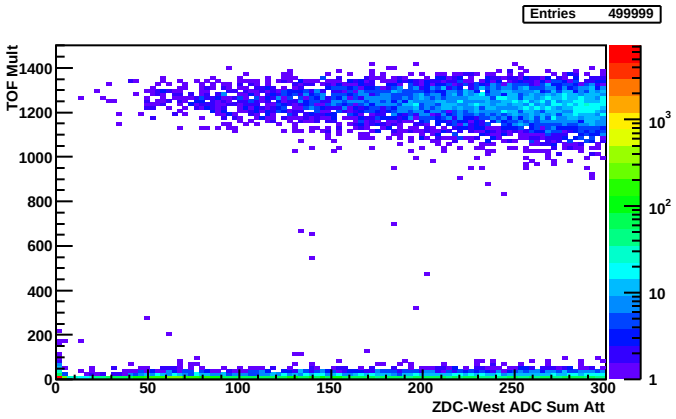

Entries 499999
TOF Mult
BBC-L-East ADC Sum

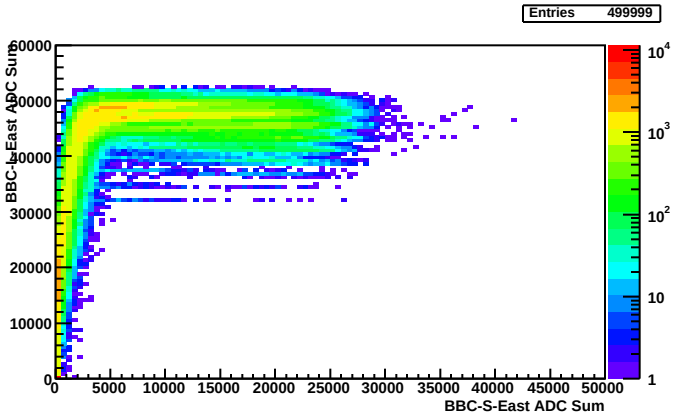

Entries 499999
TOF Mult
BBC-L-West ADC Sum

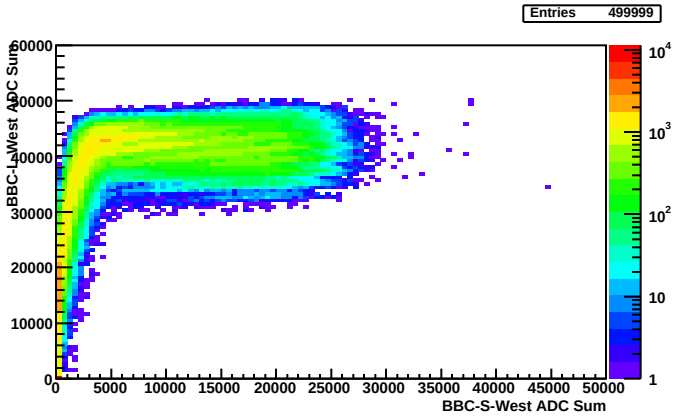

Entries 499999
TOF Mult
BBC-S-East ADC Sum

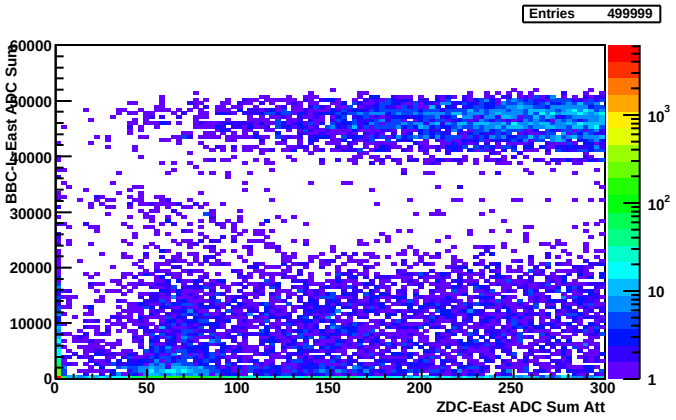

Entries 499999
TOF Mult
BBC-S-West ADC Sum

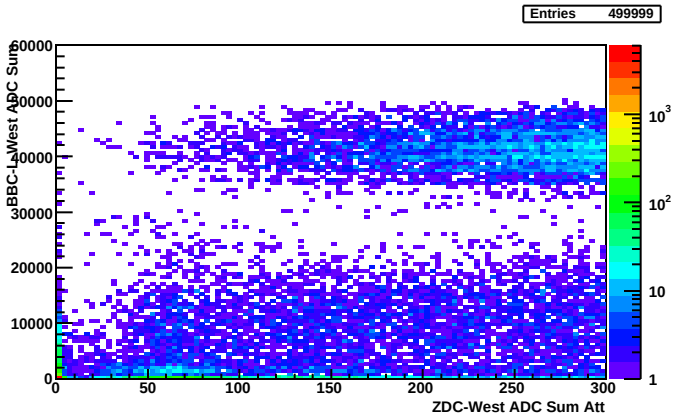

Entries 499999
TOF Mult
ZDC-East ADC Sum Att


Entries 499999
TOF Mult
ZDC-West ADC Sum Att


Entries 499999
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 499999
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 499999
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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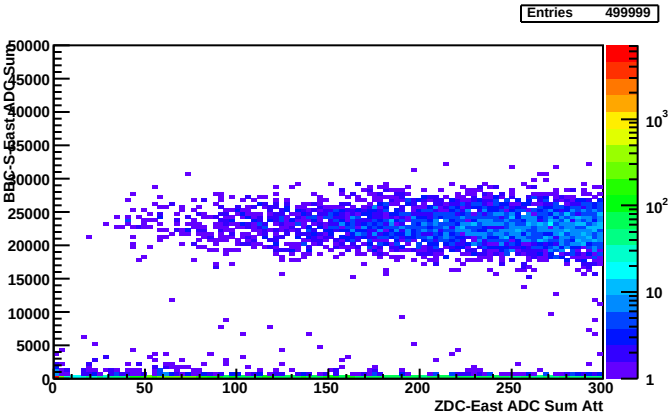
BBC-L-West ADC Sum
ZDC-West ADC Sum Att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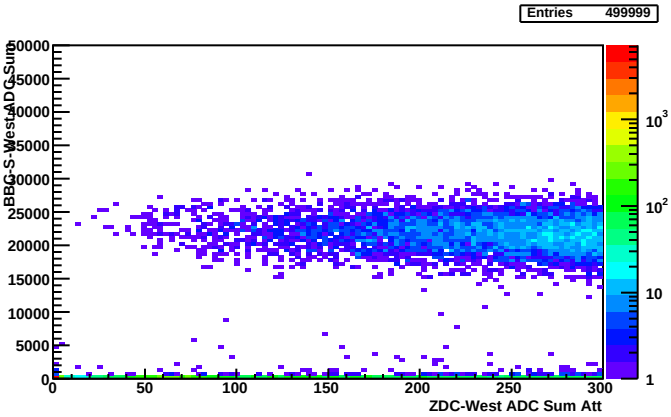
BBC-S-East ADC Sum
ZDC-East ADC Sum Att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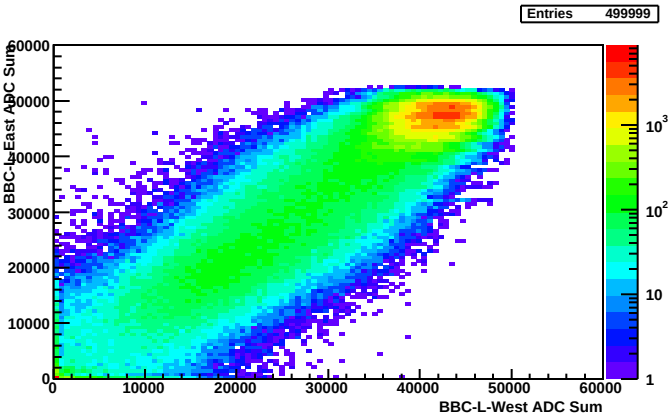
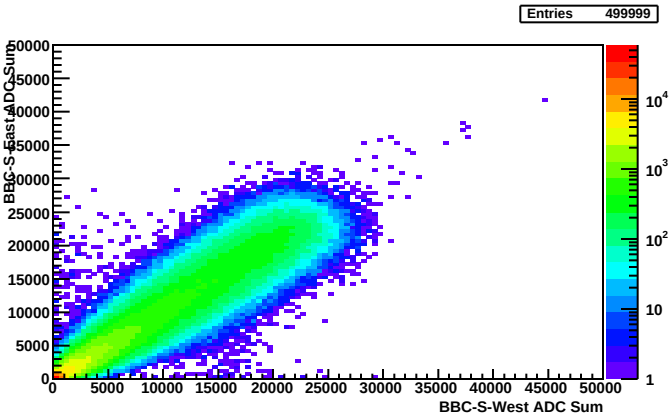
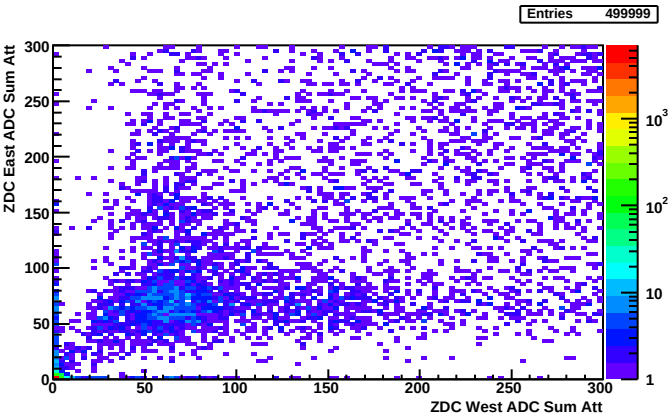
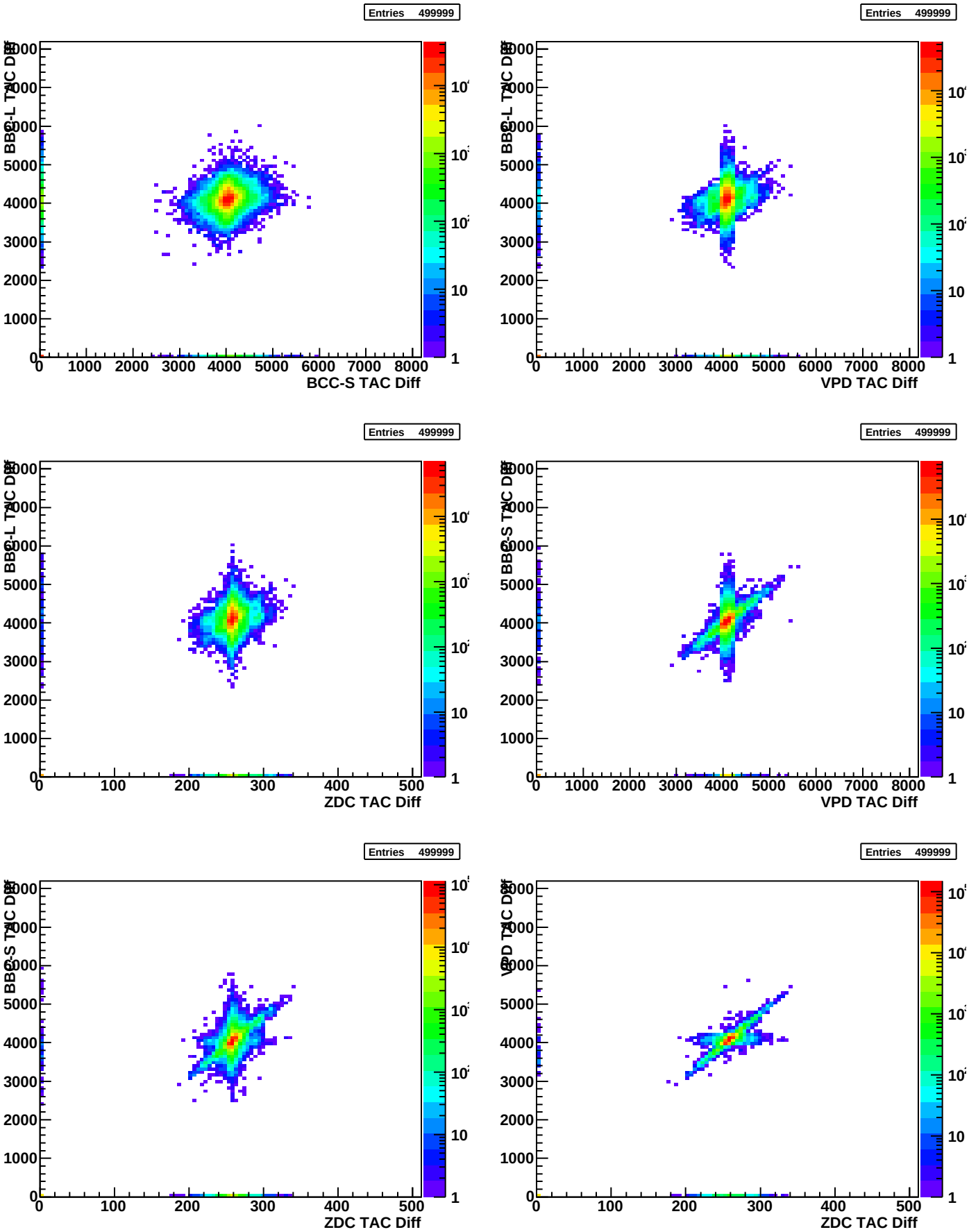
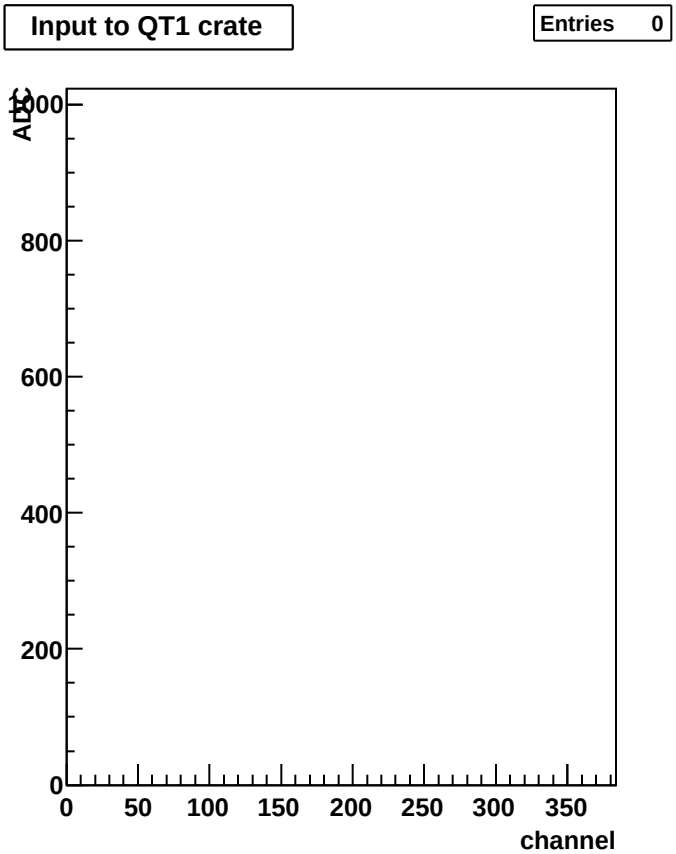
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

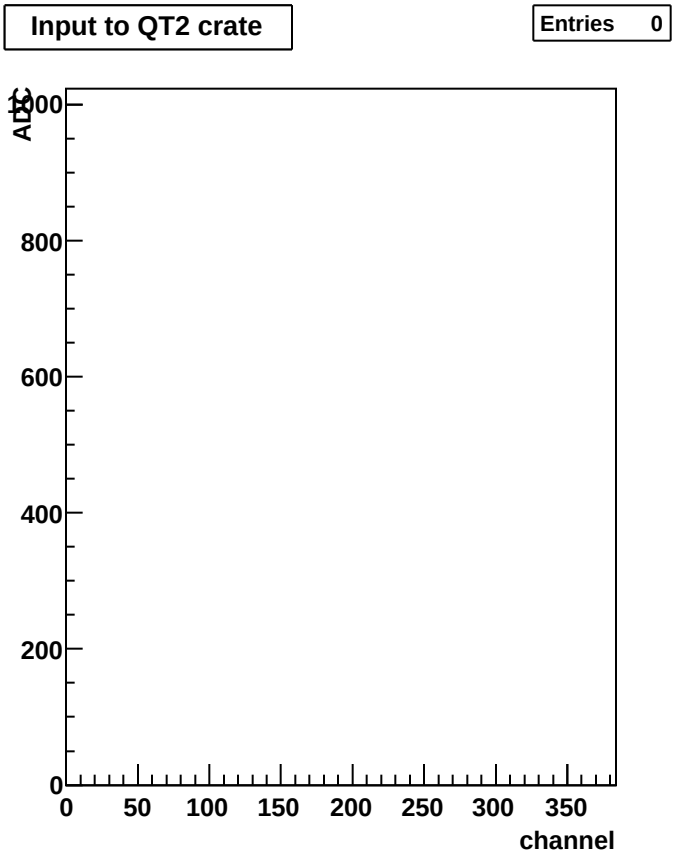

Entries 499999
BBC-L-East ADC Sum
BBC-L-West ADC Sum

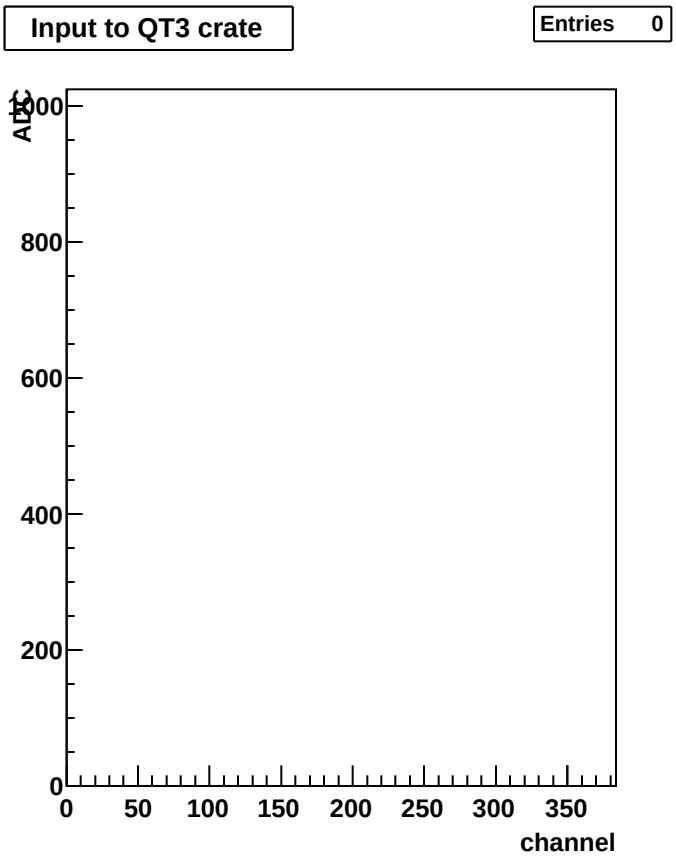

Entries 499999
BBC-S-East ADC Sum
BBC-S-West ADC Sum

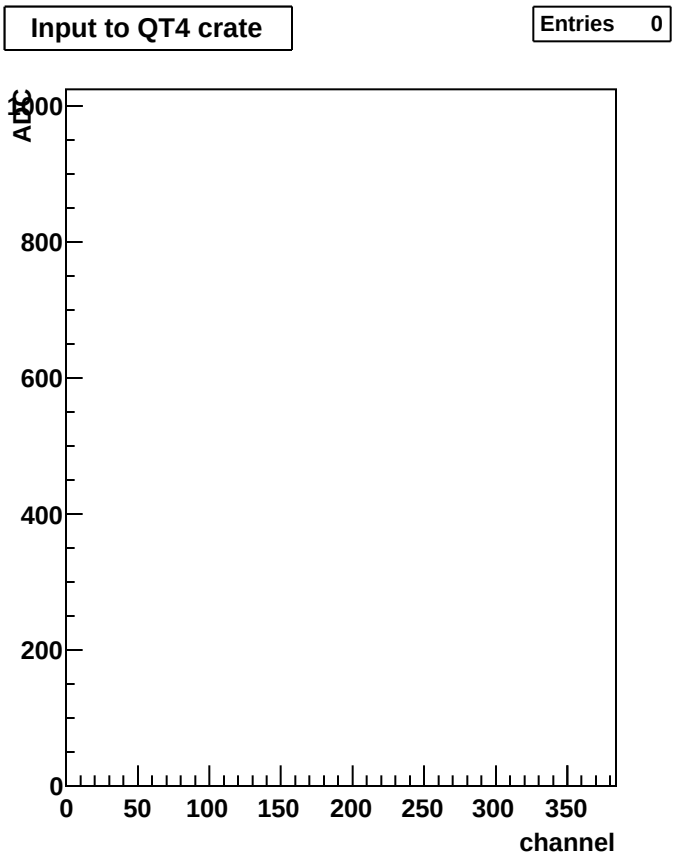

Entries 499999
ZDC East ADC Sum Att
ZDC West ADC Sum Att

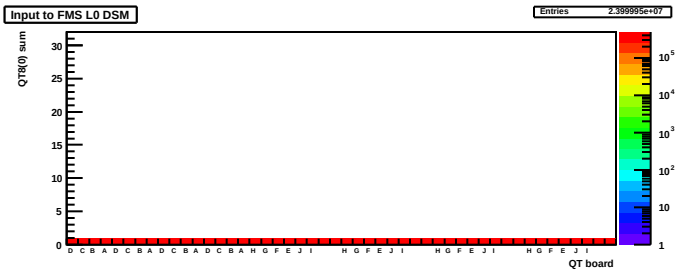

Entries 499999
BBC-L TAC Diff
BCC-S TAC Diff
Entries 499999
BBC-L TAC Diff
VPD TAC Diff
Entries 499999
BBC-L TAC Diff
ZDC TAC Diff
Entries 499999
BBC-S TAC Diff
VPD TAC Diff
Entries 499999
BBC-S TAC Diff
ZDC TAC Diff
Entries 499999
VPD TAC Diff
ZDC TAC Diff

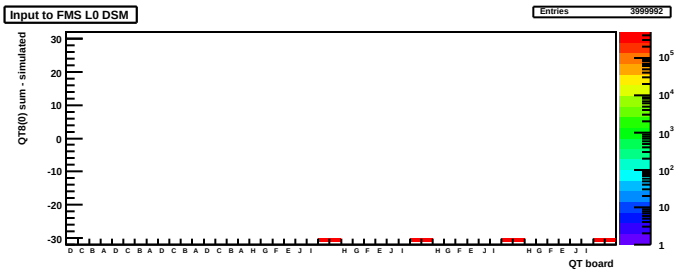

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

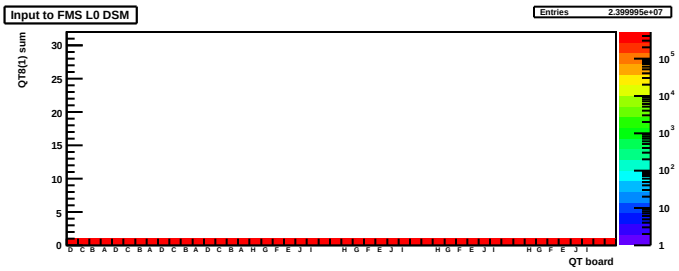

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

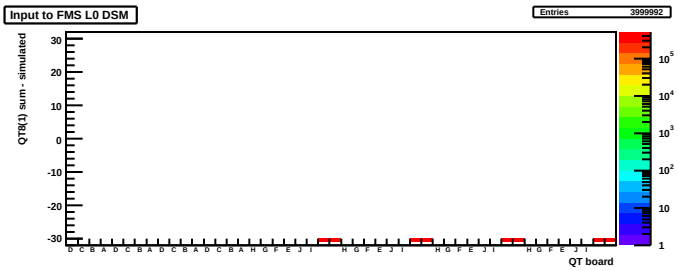

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

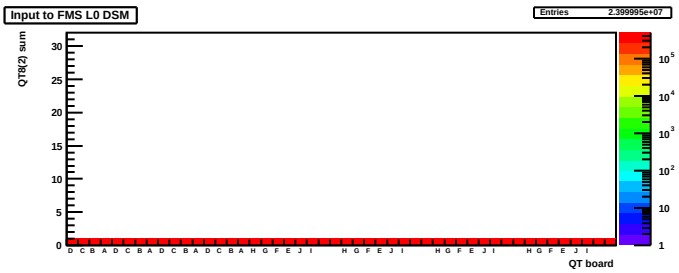

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

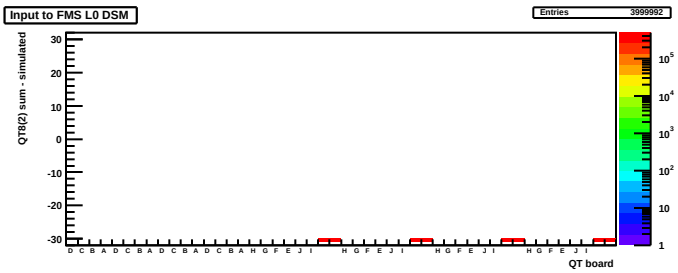

Input to FMS L0 DSM
Entries 2.399995e+07
QT8(0) sum
QT board

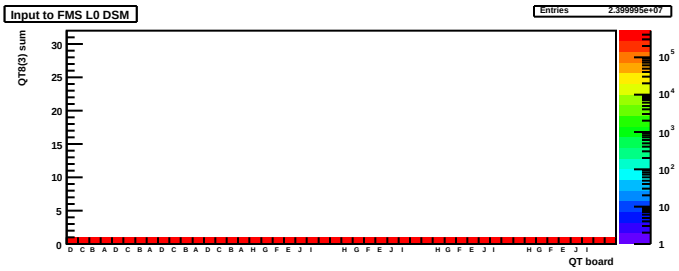

Input to FMS L0 DSM
Entries 3999992
QT8(0) sum - simulated
QT board

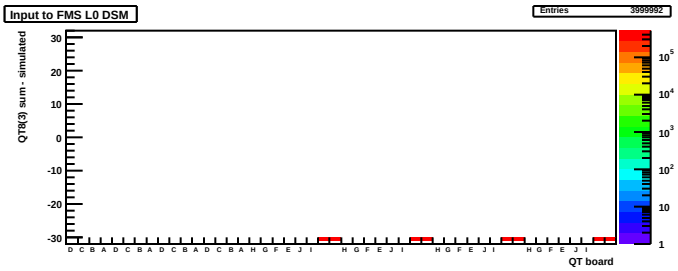

Input to FMS L0 DSM
Entries 2.399995e+07
QT8(1) sum
QT board

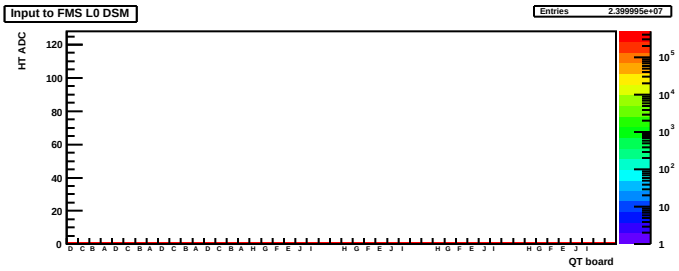

Input to FMS L0 DSM
Entries 3999992
QT8(1) sum - simulated
QT board

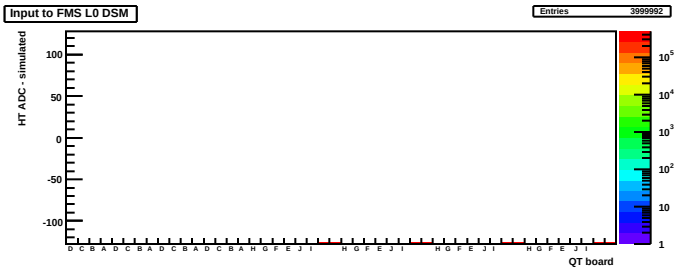

Input to FMS L0 DSM
Entries 2.399995e+07
QT8(2) sum
QT board

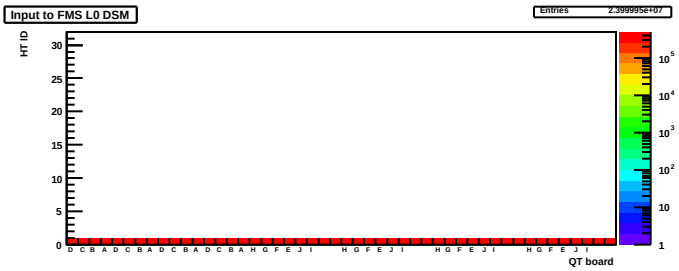

Input to FMS L0 DSM
Entries 3999992
QT8(2) sum - simulated
QT board

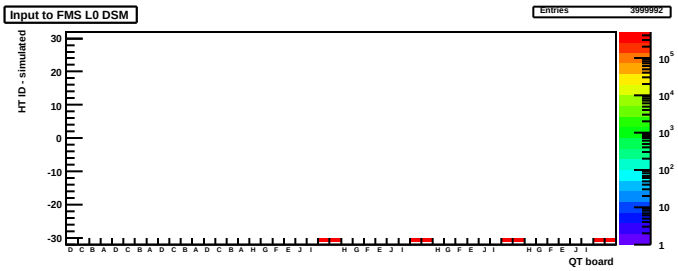

Input to FMS L0 DSM
Entries 2.399995e+07
QT8(3) sum
QT board

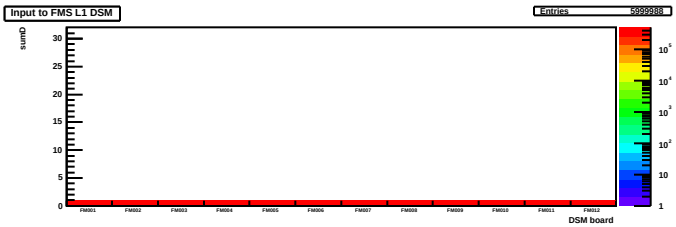

Input to FMS L0 DSM
Entries 3999992
QT8(3) sum - simulated
QT board

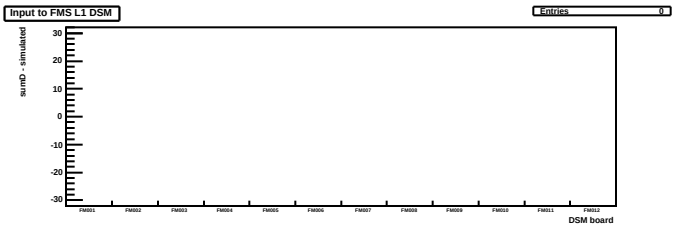

Input to FMS L0 DSM
Entries 2.399995e+07
HT ADC
QT board

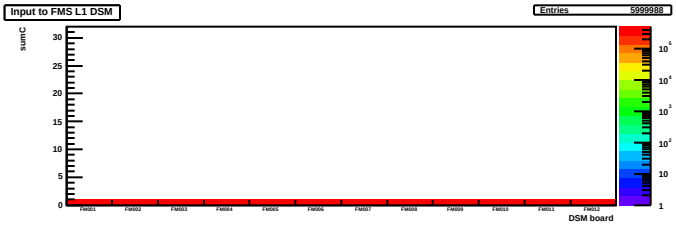

Input to FMS L0 DSM
Entries 3999992
HT ADC - simulated
QT board

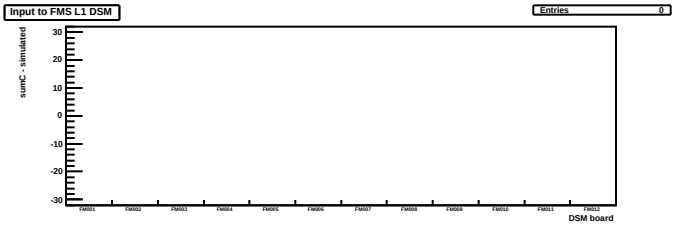

Input to FMS L0 DSM
Entries 2.399995e+07
HT ID
QT board

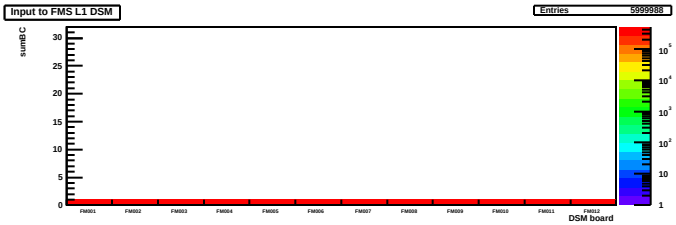

Input to FMS L0 DSM
Entries 3999992
HT ID - simulated
QT board

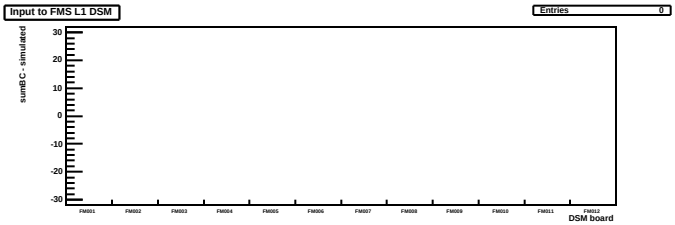

Input to FMS L1 DSM
Entries 5999988
sumD
DSM board

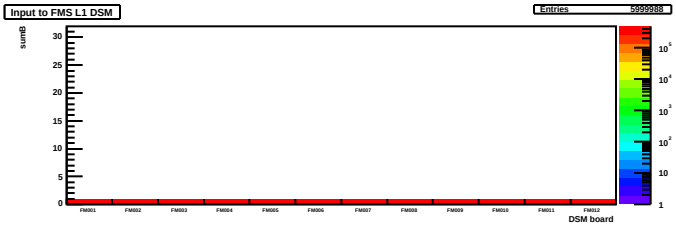

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

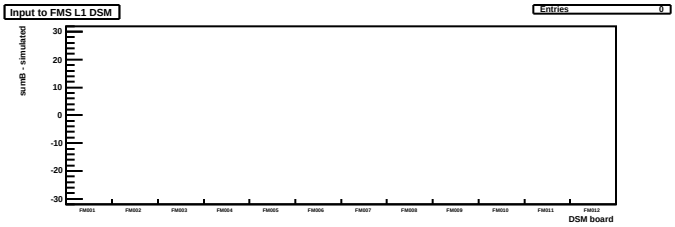

Input to FMS L1 DSM
Entries 5999988
sumC
DSM board

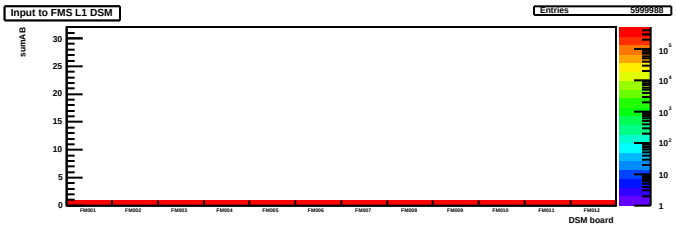

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

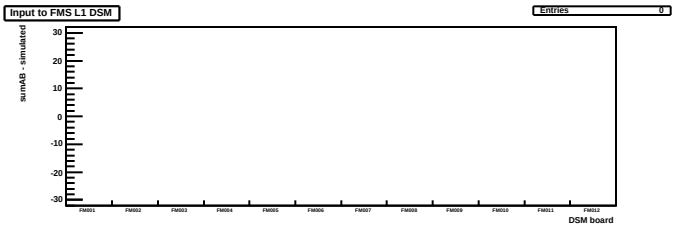

Input to FMS L1 DSM
Entries 5999988
sumBC
DSM board

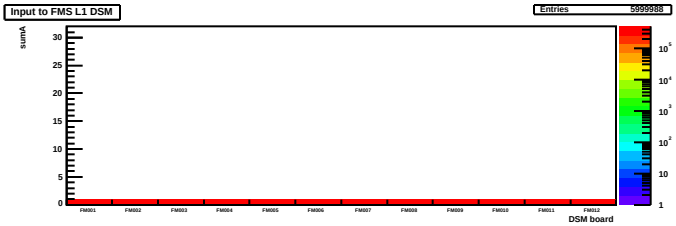

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

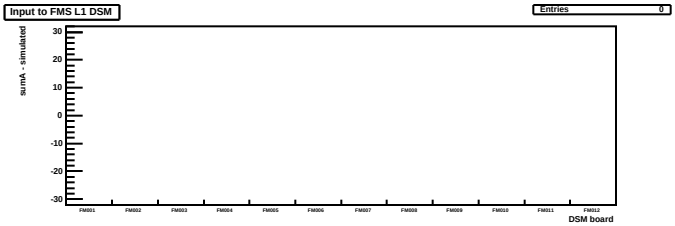

Input to FMS L1 DSM
Entries 5999988
sumB
DSM board

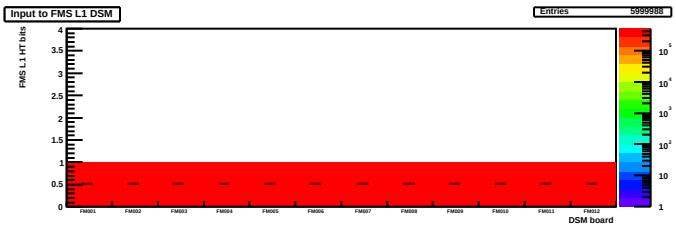

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

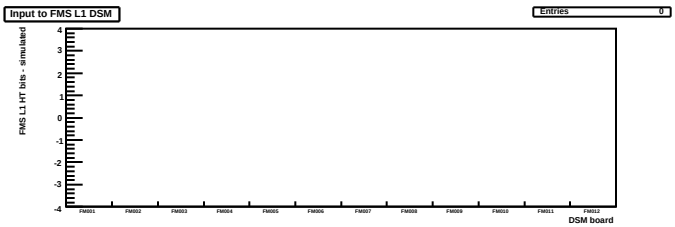

Input to FMS L1 DSM
Entries 5999988
sumAB
DSM board

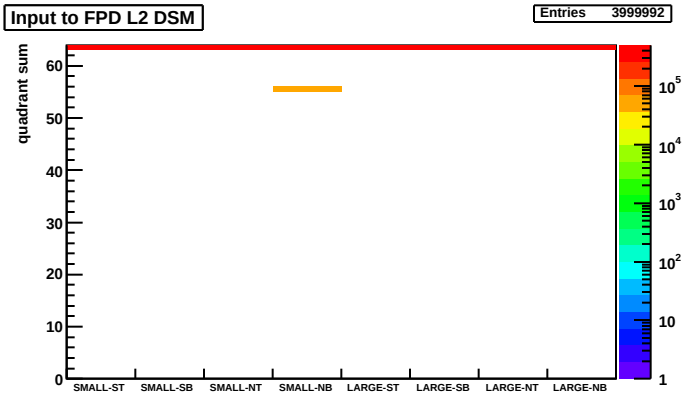

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

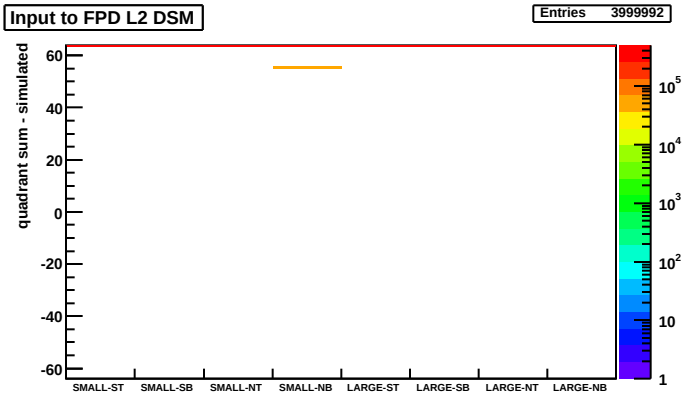

Input to FMS L1 DSM
Entries 5999988
sumA
DSM board

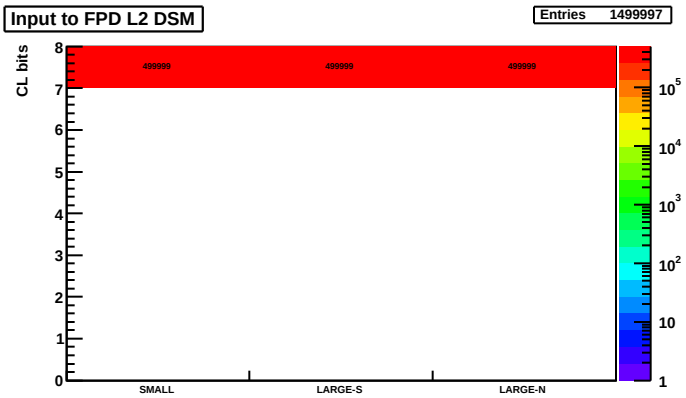

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

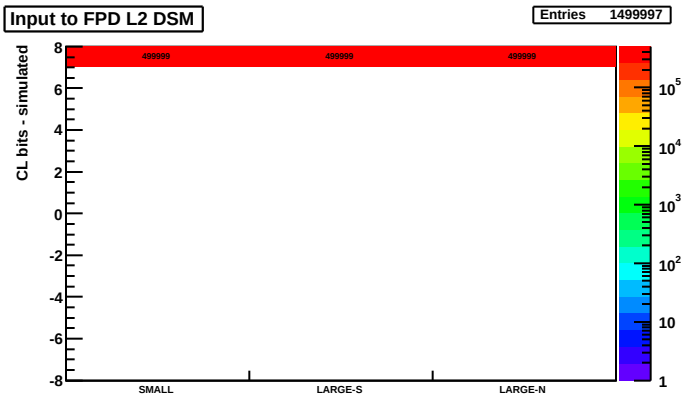

Input to FMS L1 DSM
Entries 5999988
FMS L1 HT bits
DSM board

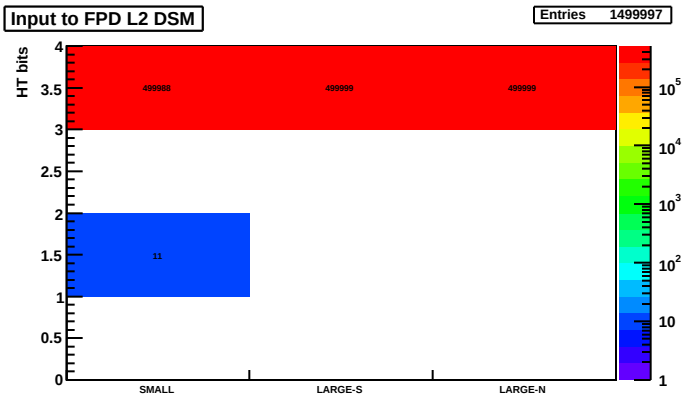

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

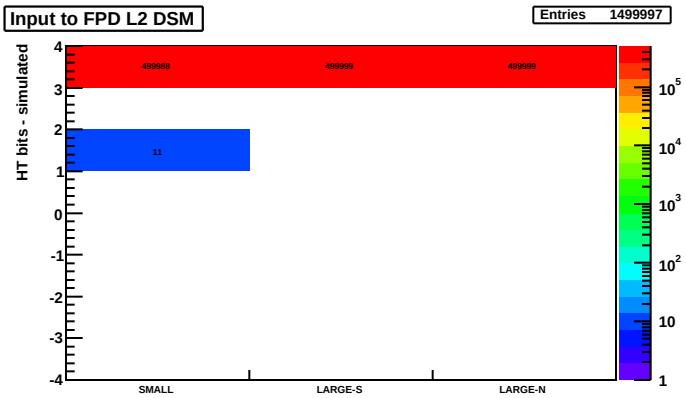

Input to FPD L2 DSM
Entries 3999992
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

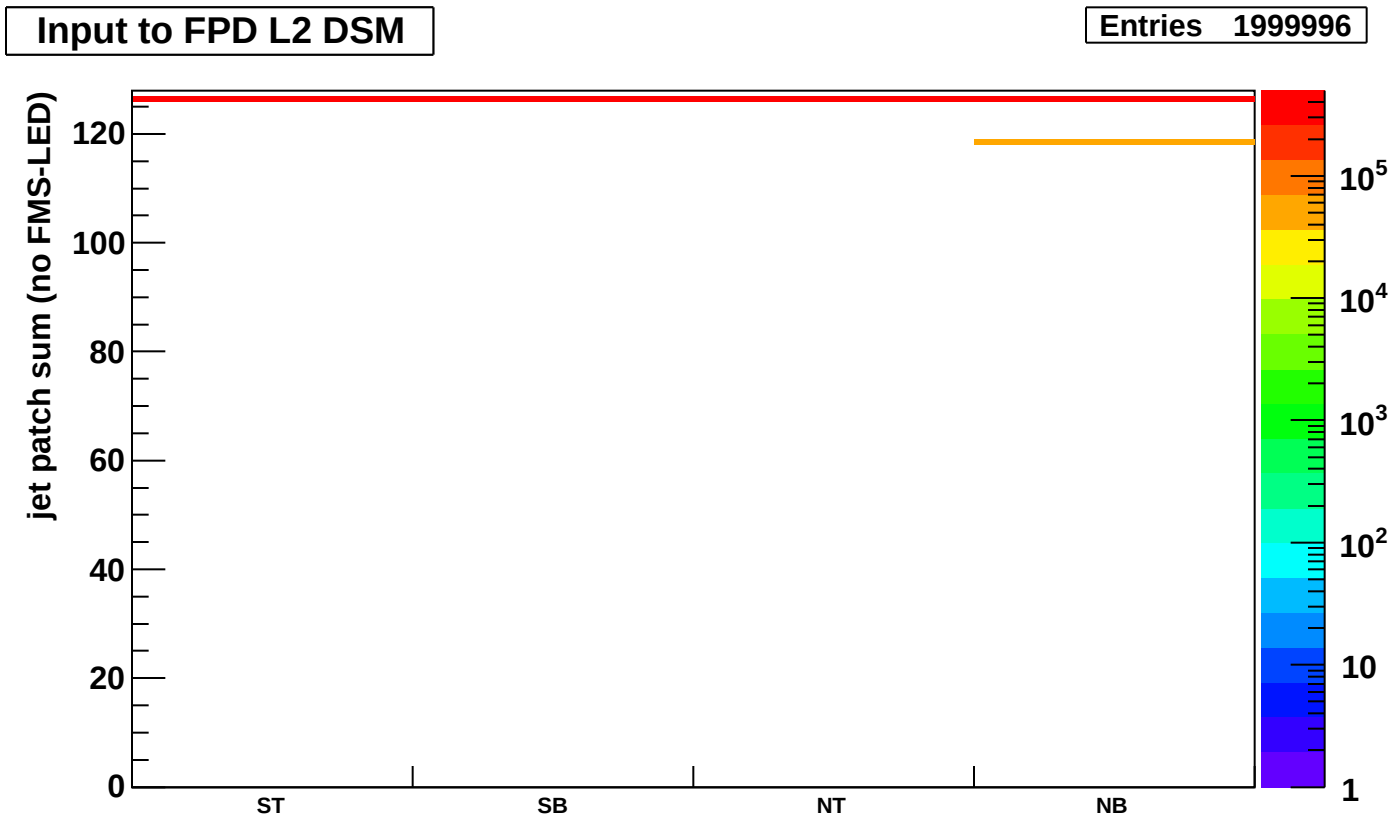

Input to FPD L2 DSM
Entries 3999992
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

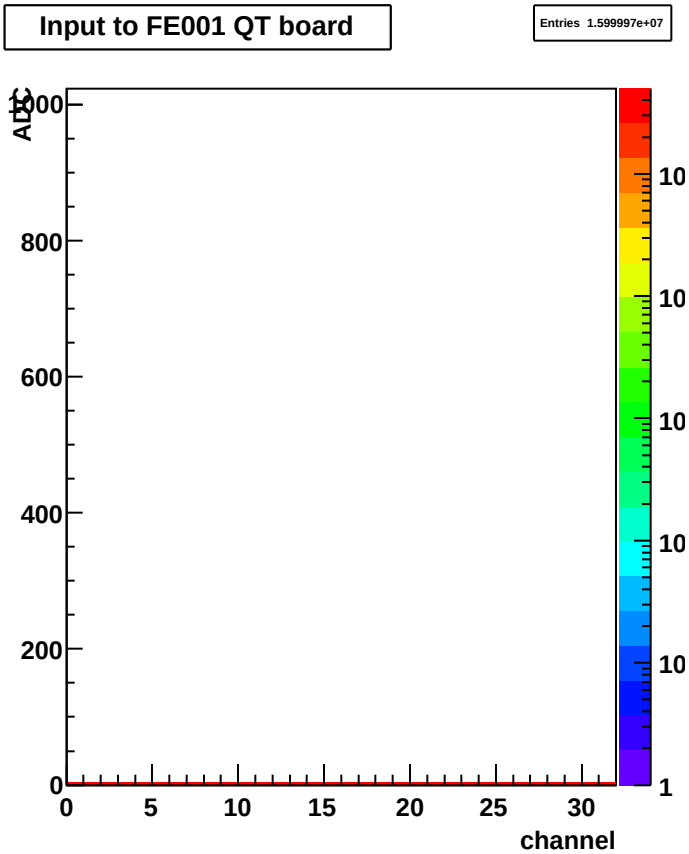

Input to FPD L2 DSM
Entries 1499997
CL bits
499999
499999
499999
SMALL
LARGE-S
LARGE-N

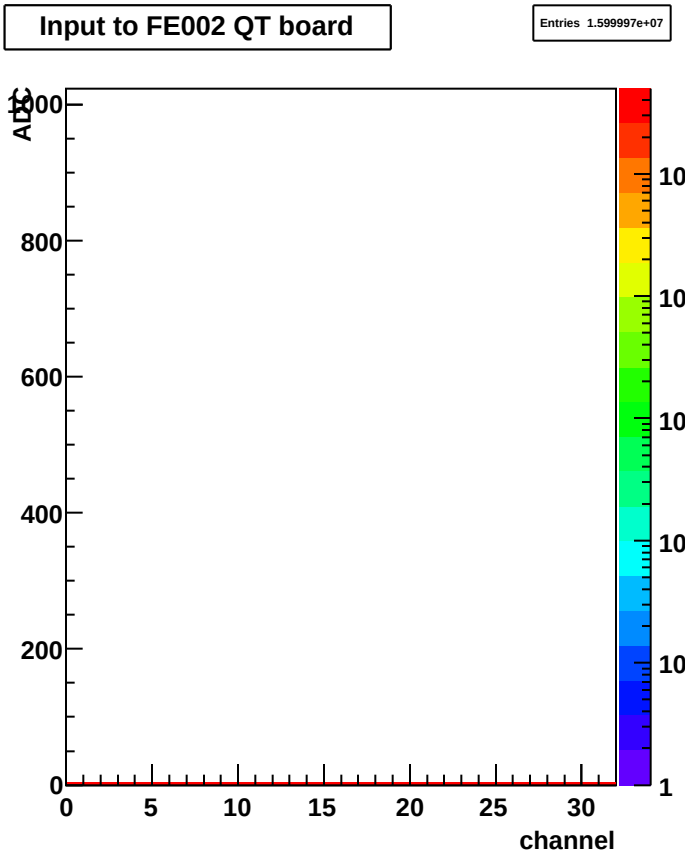

Input to FPD L2 DSM
Entries 1499997
CL bits - simulated
499999
499999
499999
SMALL
LARGE-S
LARGE-N

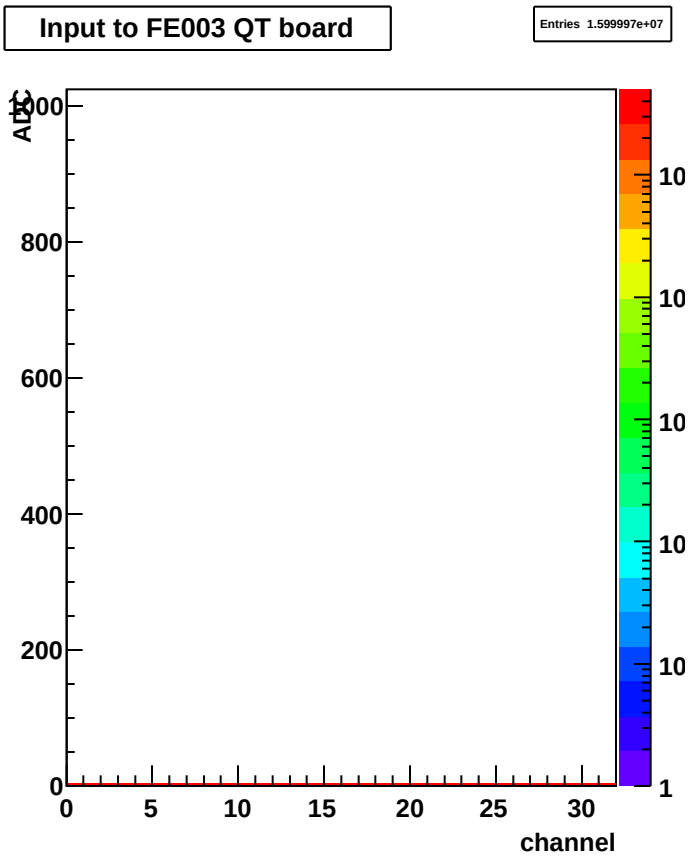

Input to FPD L2 DSM
Entries 1499997
HT bits
499988
499999
499999
11
SMALL
LARGE-S
LARGE-N

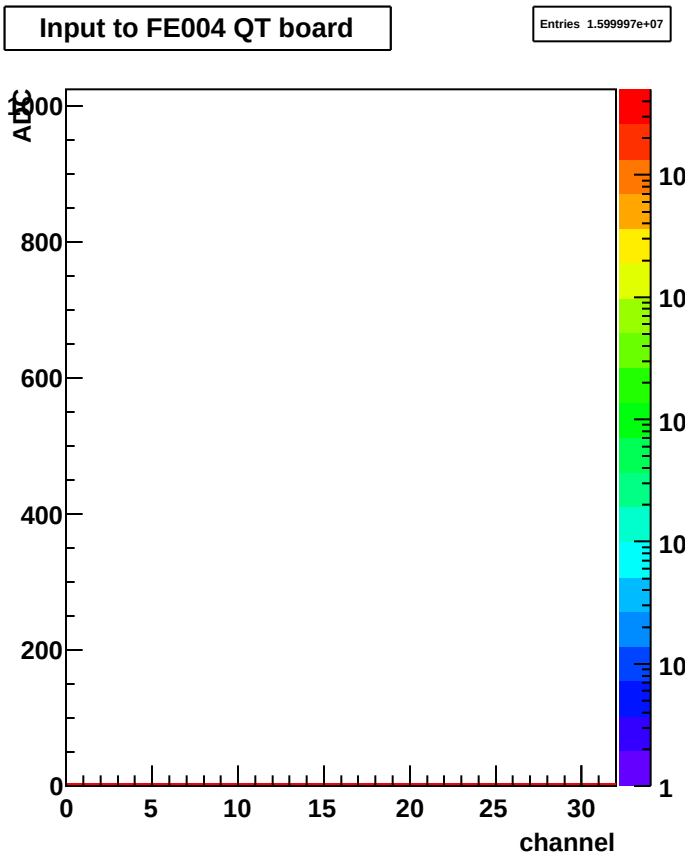

Input to FPD L2 DSM
Entries 1499997
HT bits - simulated
499988
499999
499999
11
SMALL
LARGE-S
LARGE-N


Input to FPD L2 DSM
Entries 1999996
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

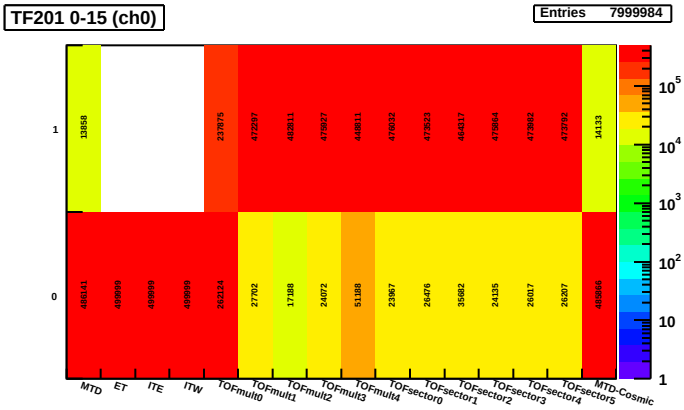

Input to FE001 QT board
Entries 1.599997e+07
ADC
channel


Input to FE002 QT board
Entries 1.599997e+07
ADC
channel

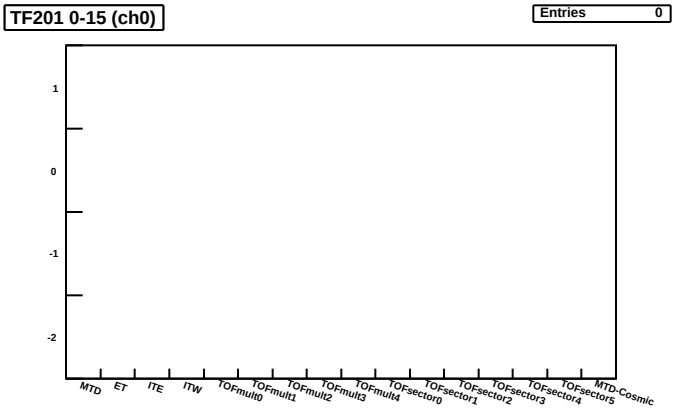

Input to FE003 QT board
Entries 1.599997e+07
ADC
channel


Input to FE004 QT board
Entries 1.599997e+07
ADC
channel

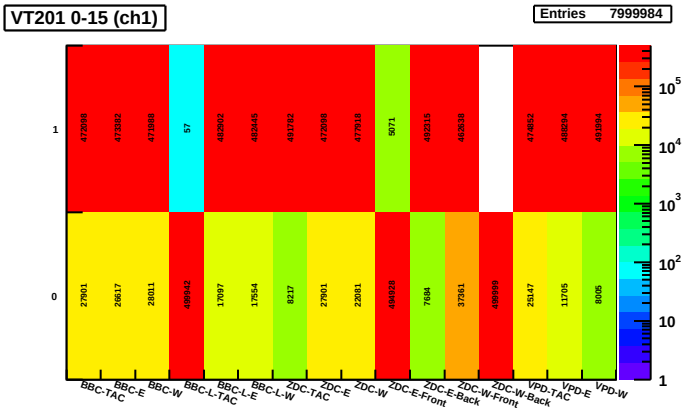
TF201 0-15 (ch0)



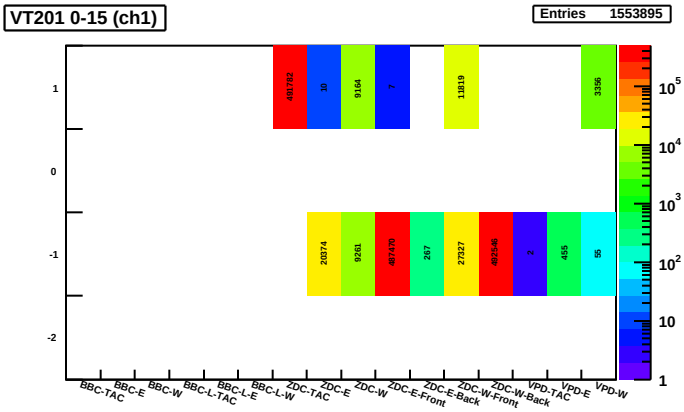
TF201 0-15 (ch0)



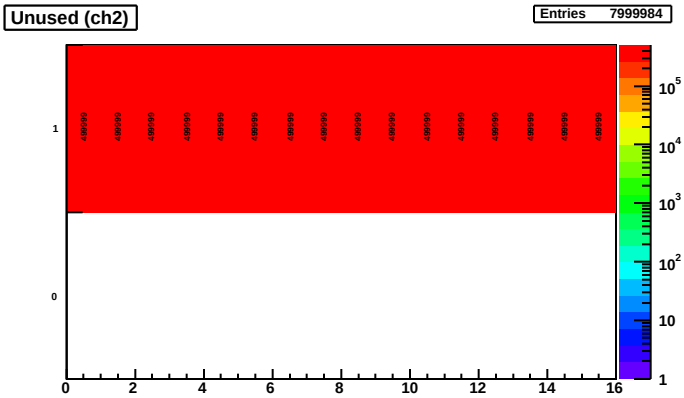
VT201 0-15 (ch1)

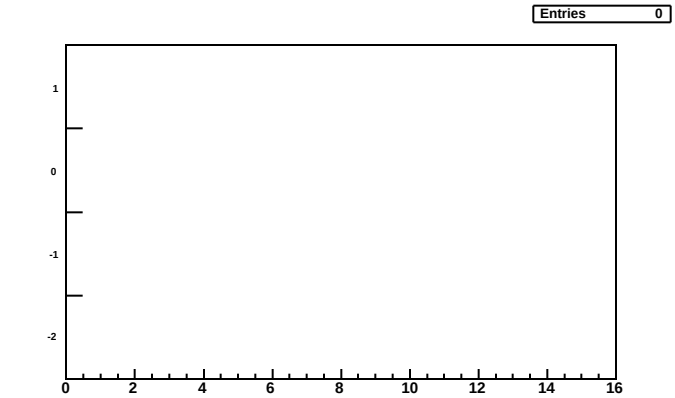


VT201 0-15 (ch1)

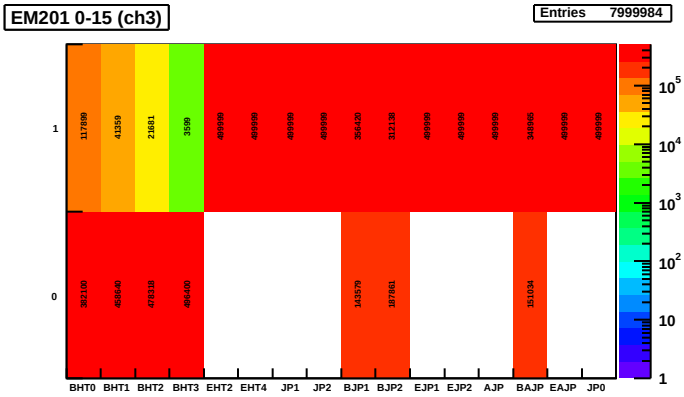


Unused (ch2)

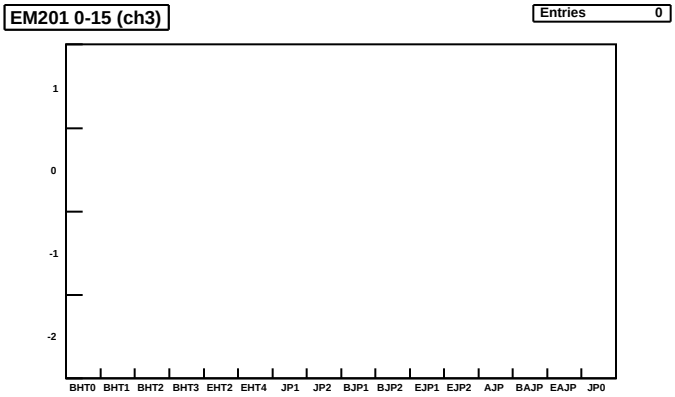




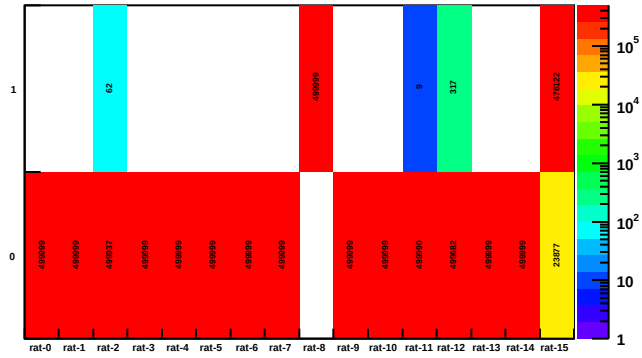
EM201 0-15 (ch3)

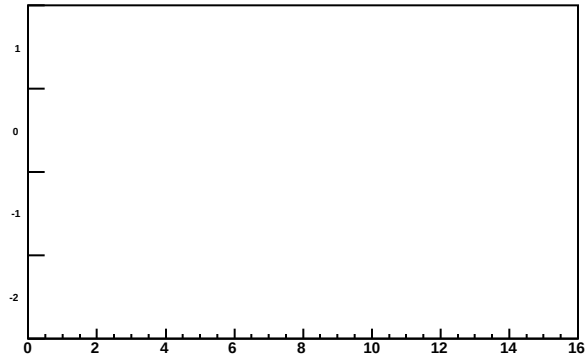


EM201 0-15 (ch3)

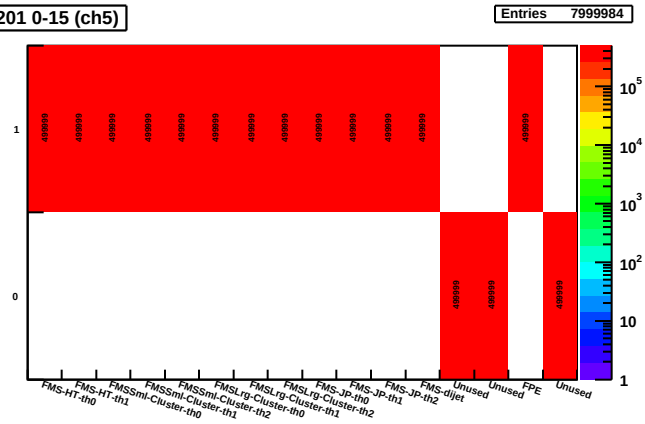


RAT board (ch4)

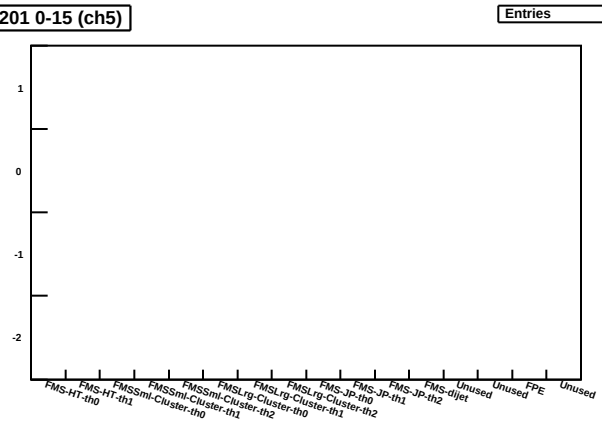




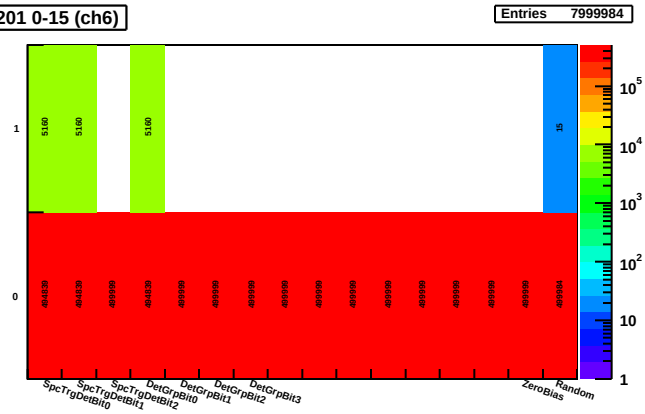
FP201 0-15 (ch5)

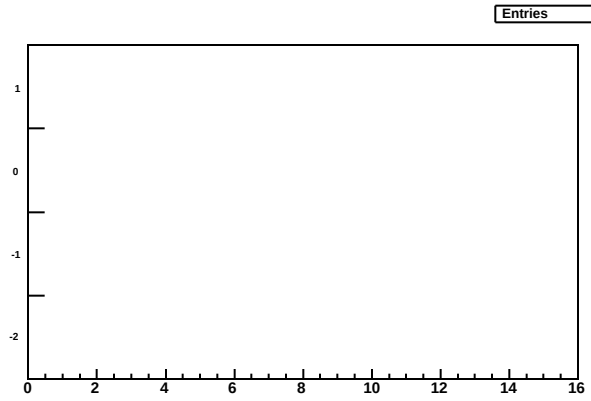
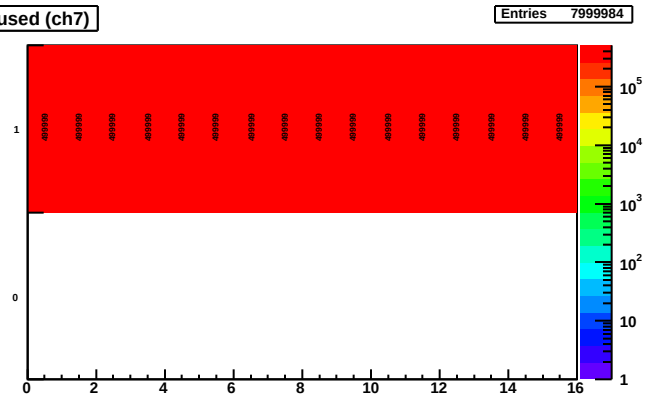
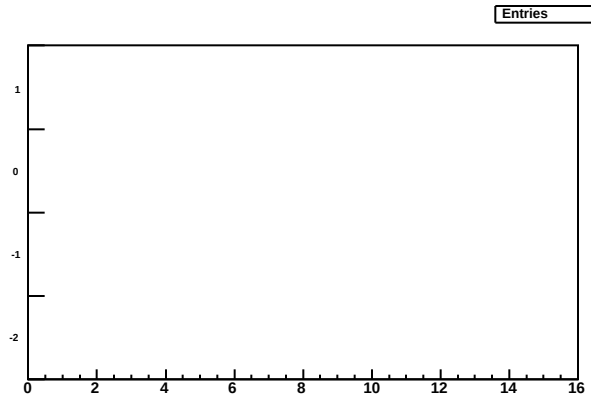


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)