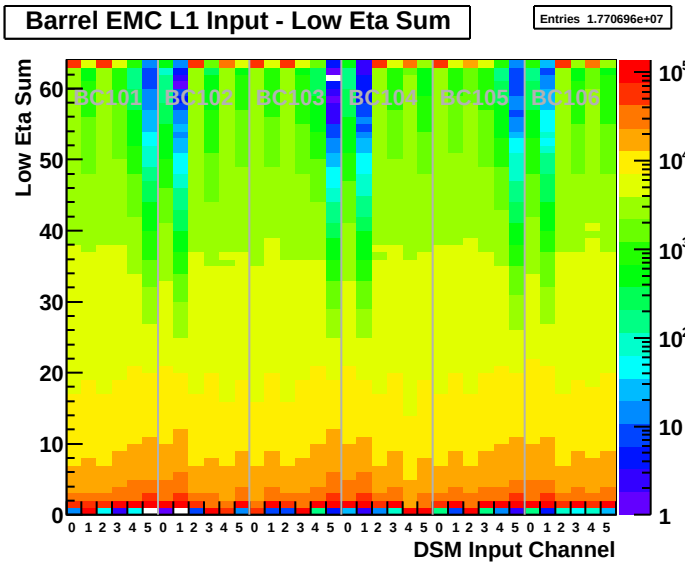
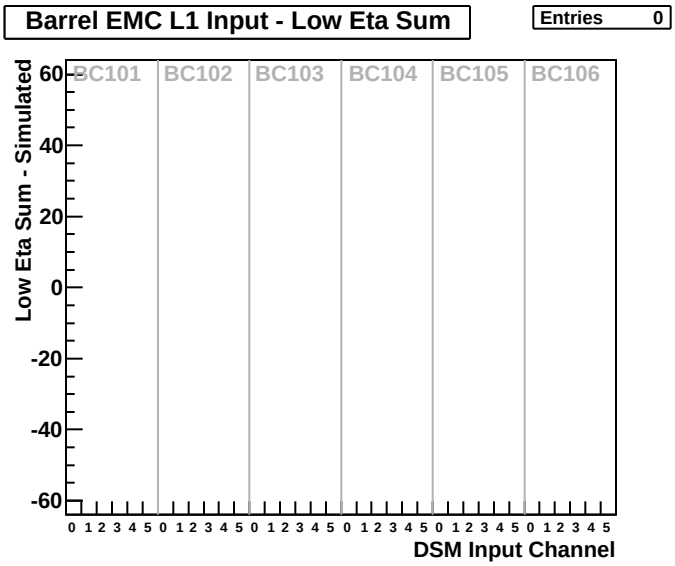
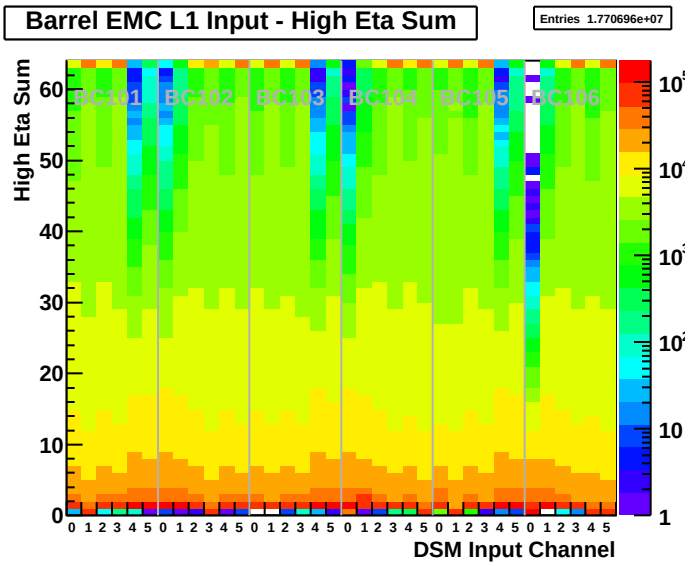
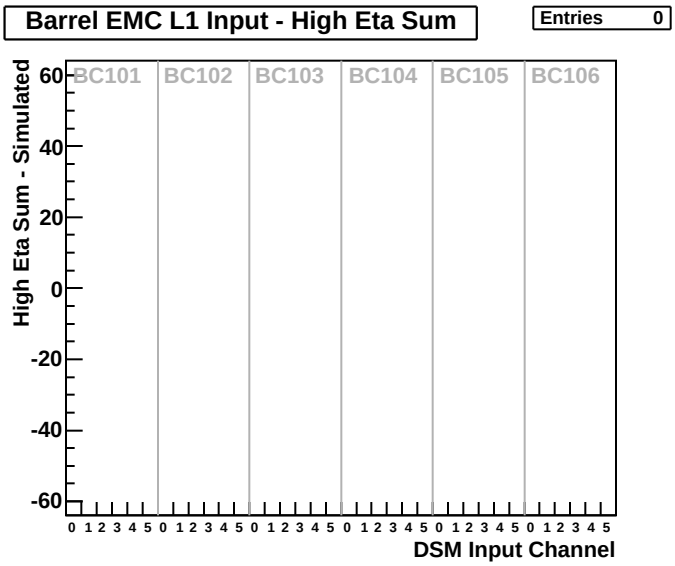
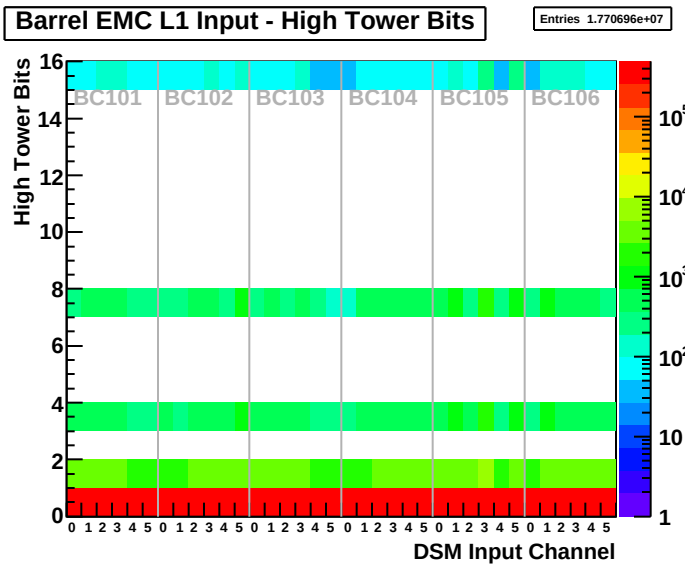
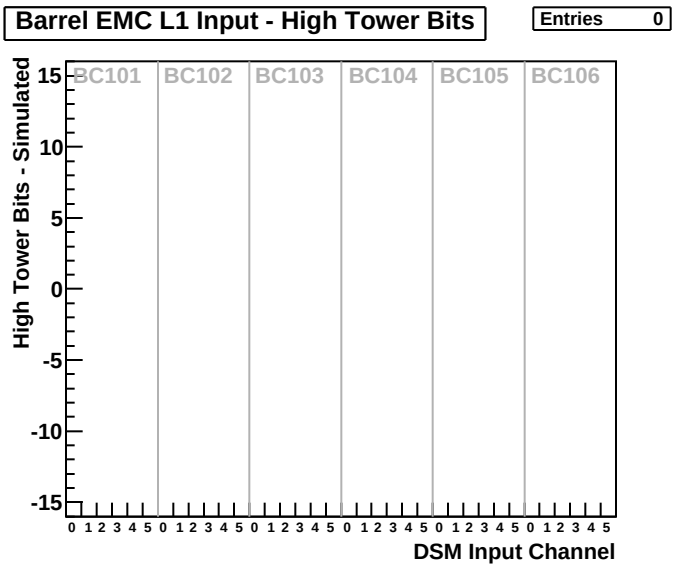

Barrel EMC L1 Input - Low Eta Sum
Entries 1.770696e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

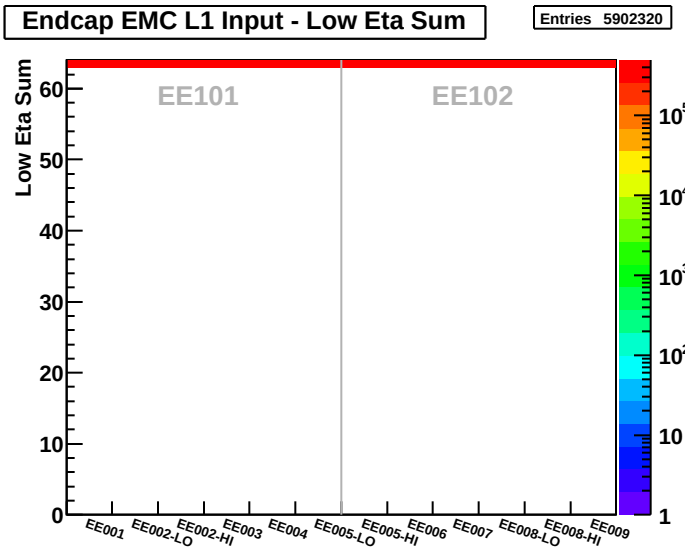

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

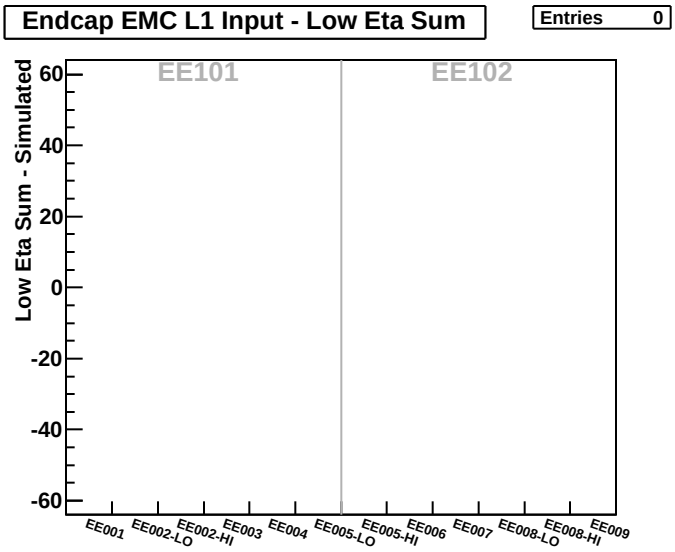

Barrel EMC L1 Input - High Eta Sum
Entries 1.770696e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

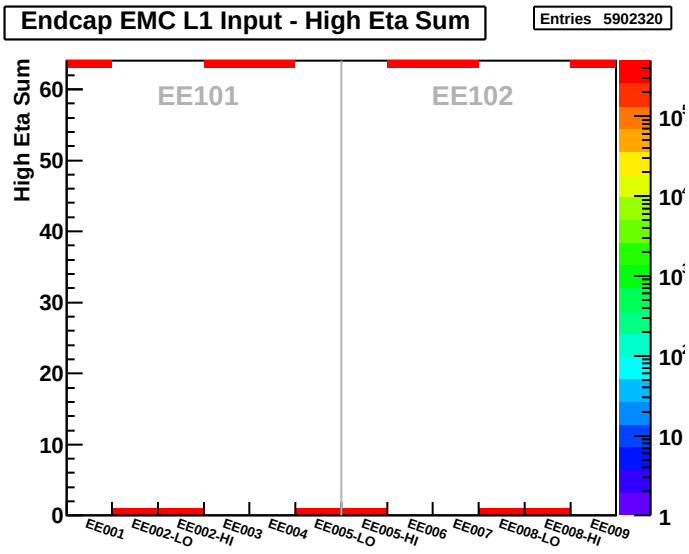

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

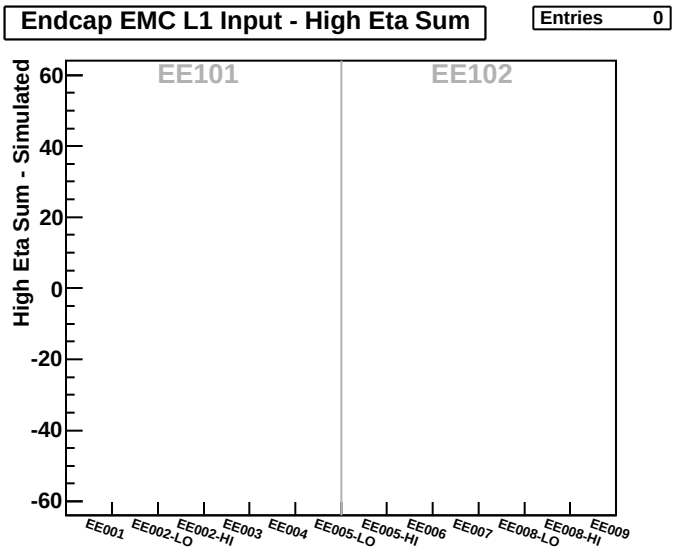

Barrel EMC L1 Input - High Tower Bits
Entries 1.770696e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

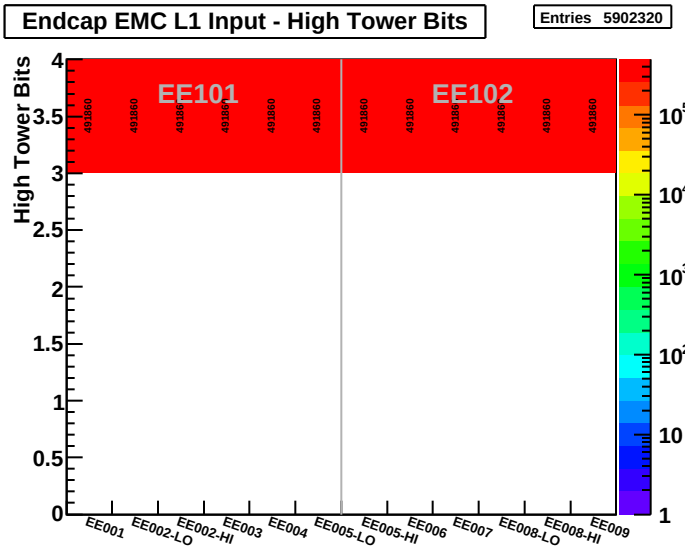

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

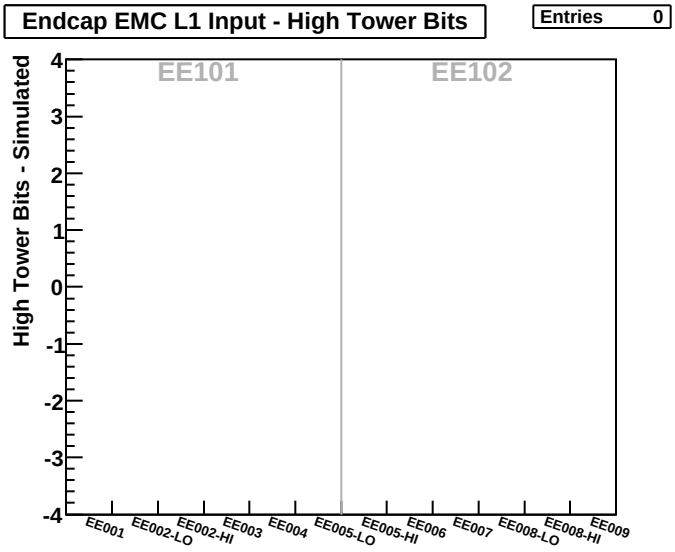

Endcap EMC L1 Input - Low Eta Sum
Entries 5902320
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

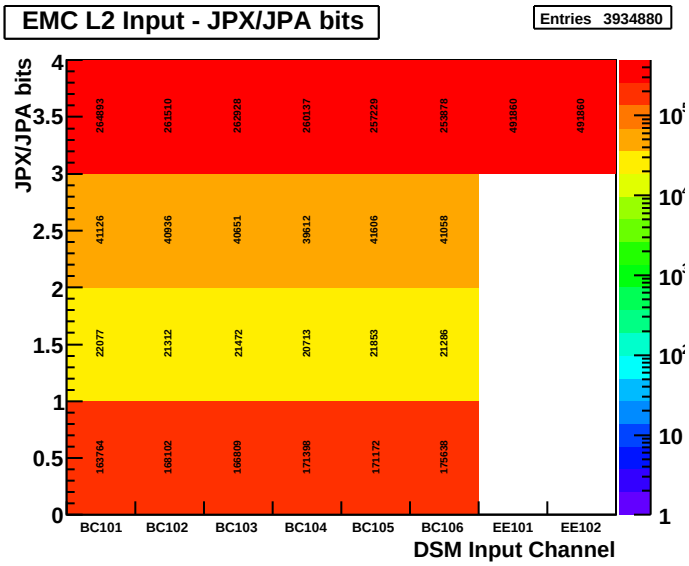

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

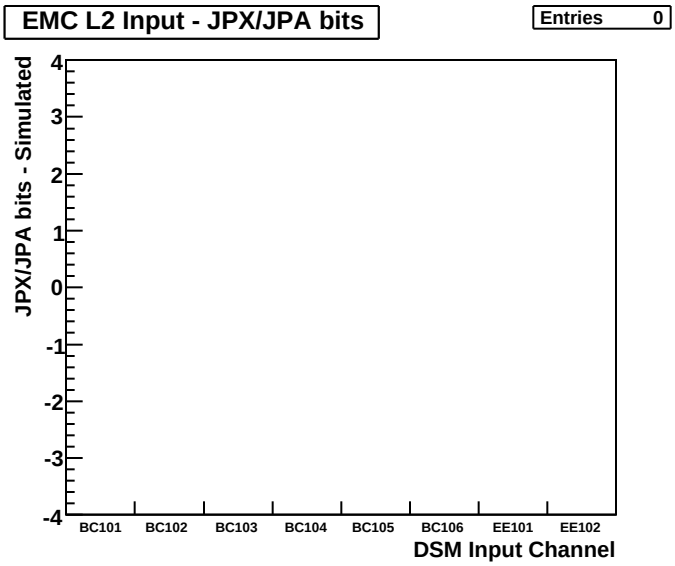

Endcap EMC L1 Input - High Eta Sum
Entries 5902320
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

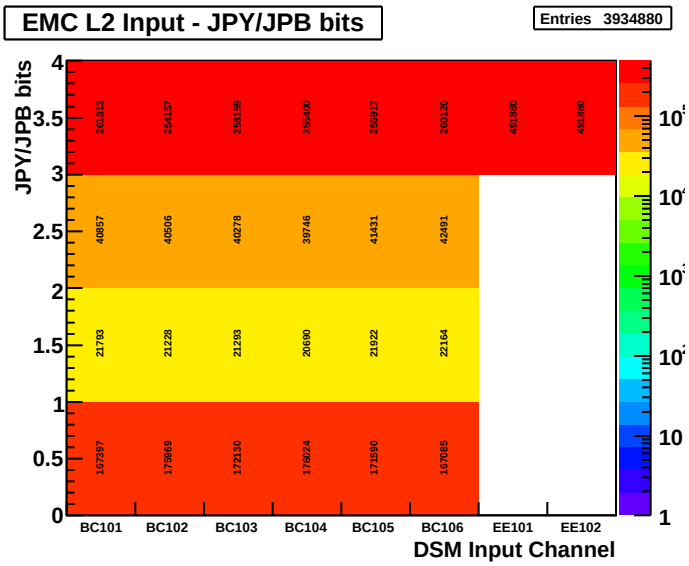

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

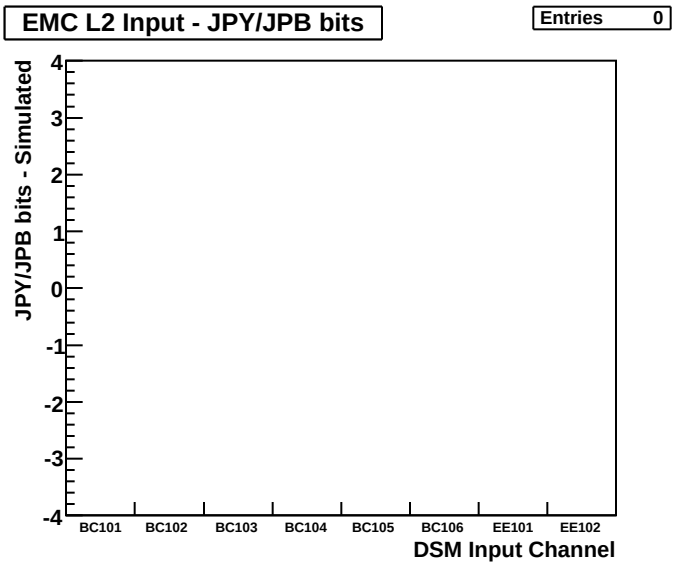

Endcap EMC L1 Input - High Tower Bits
Entries 5902320
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

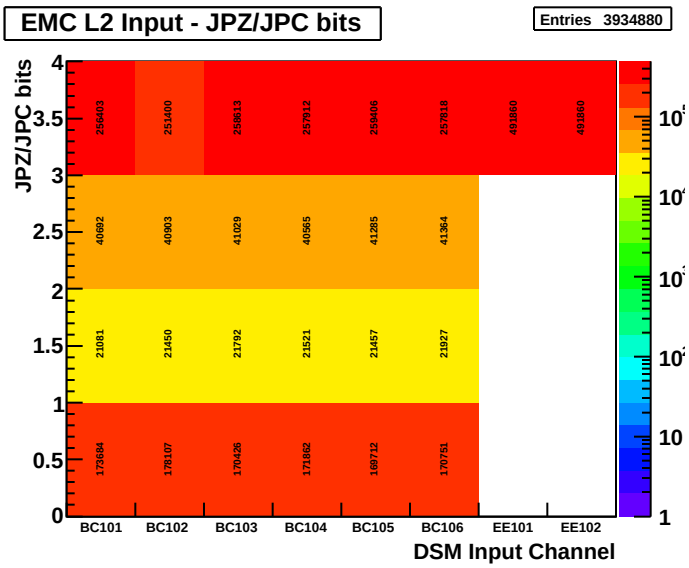

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

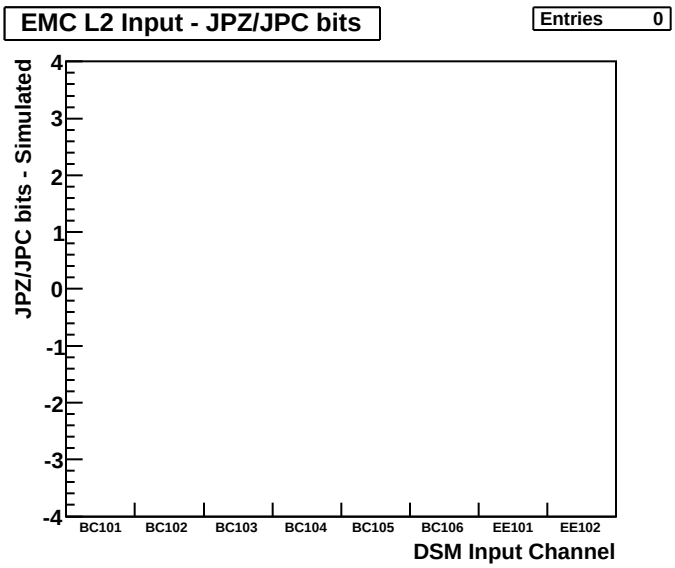

EMC L2 Input - JPX/JPA bits
Entries 3934880
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
254693 261510 263828 260137 257229 253878 491860 491860
41126 40936 40651 39612 41606 41058
22077 21312 21472 20713 21853 21286
163764 168102 166809 171398 171172 175638

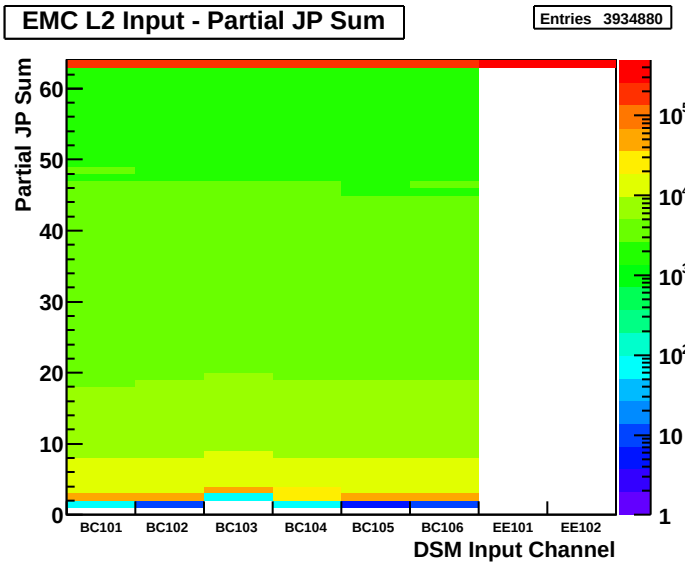

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

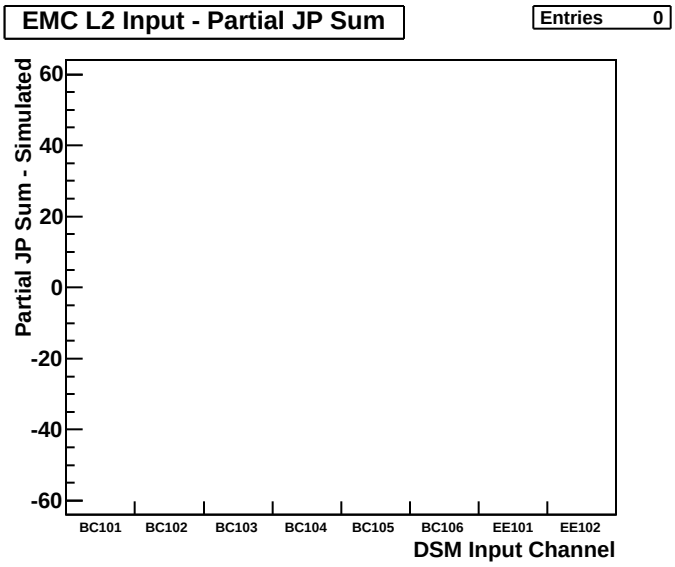

EMC L2 Input - JPY/JPB bits
Entries 3934880
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
261813 254157 258159 255400 256917 260120 491860 491860
40857 40506 40278 39746 41431 42491
21793 21228 21293 20690 21922 22164
167397 175969 172130 176024 171590 167085

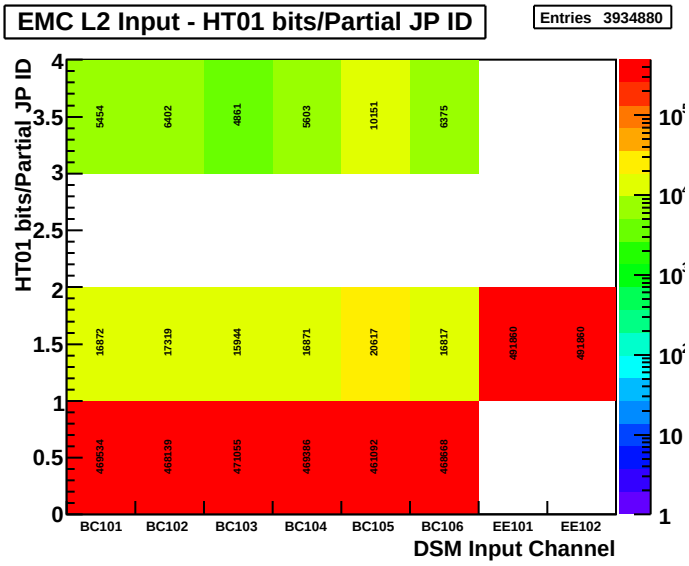

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

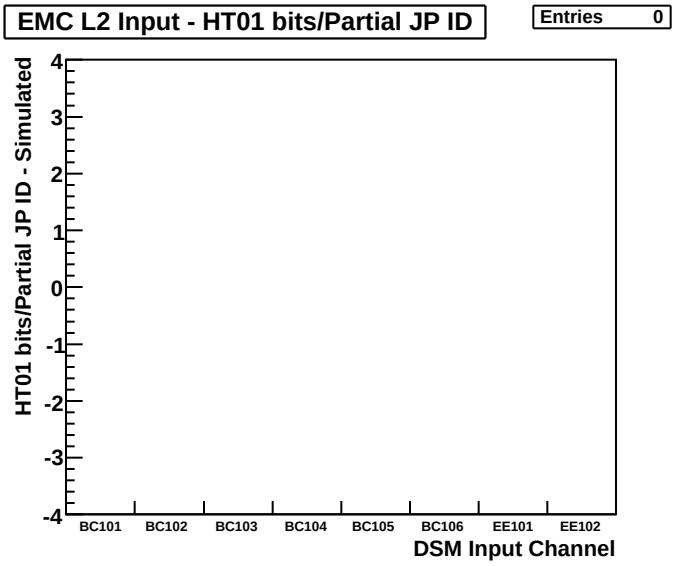

EMC L2 Input - JPZ/JPC bits
Entries 3934880
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
255403 251400 258613 257912 259406 257818 491860 491860
40692 40903 41029 40565 41285 41364
21081 21450 21792 21521 21457 21927
173584 178107 170426 171862 169712 170751

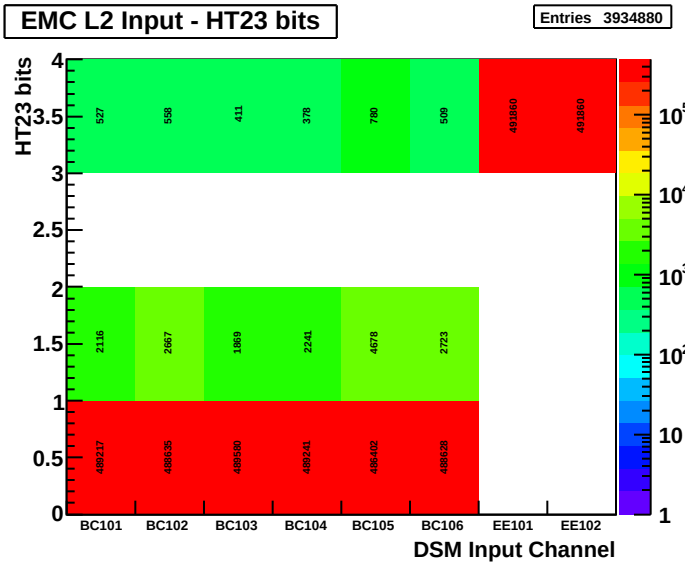

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

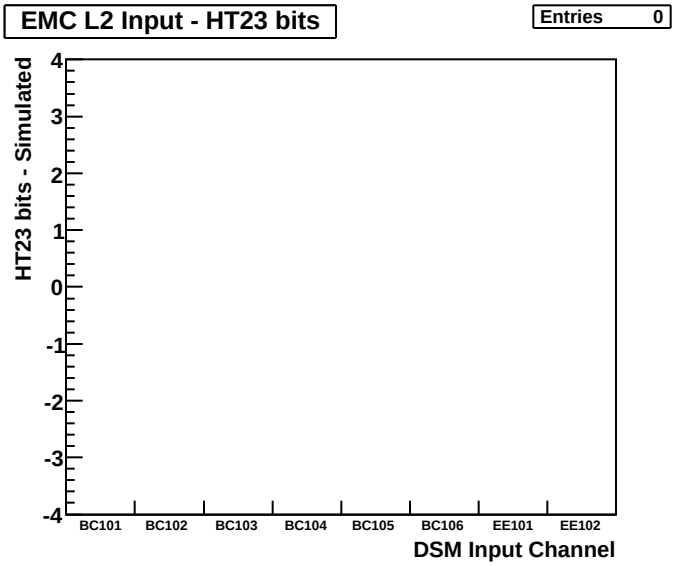

EMC L2 Input - Partial JP Sum
Entries 3934880
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

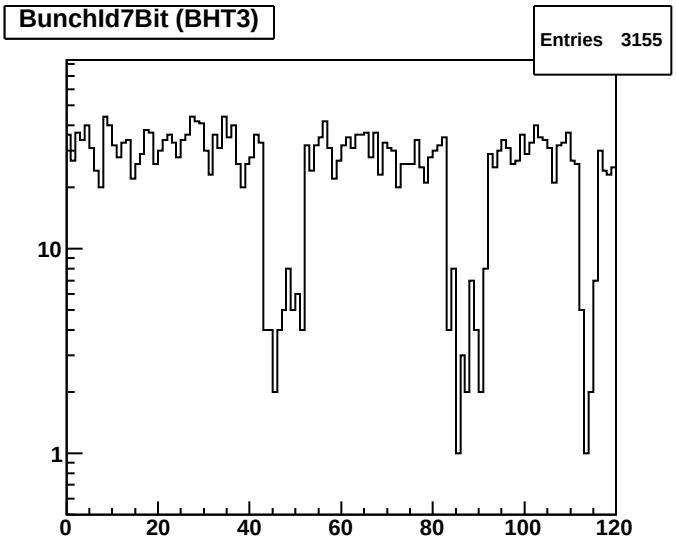

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

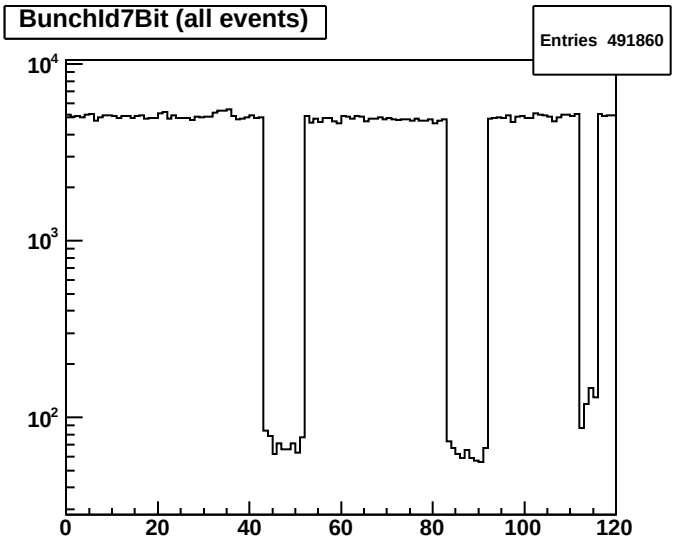

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3934880
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

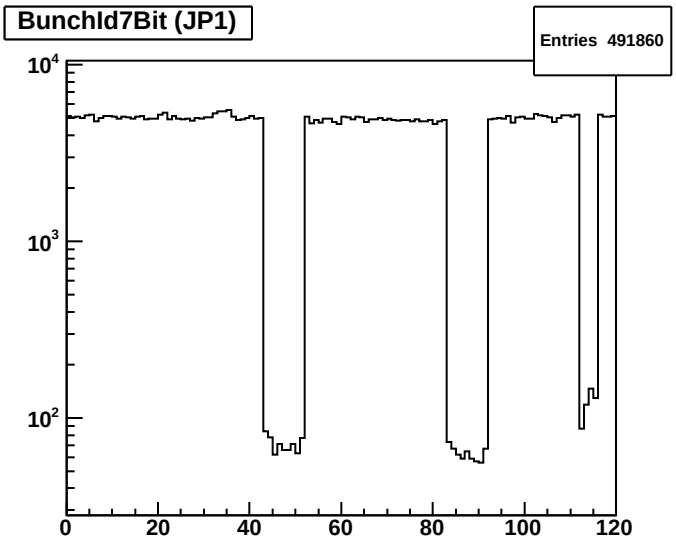

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

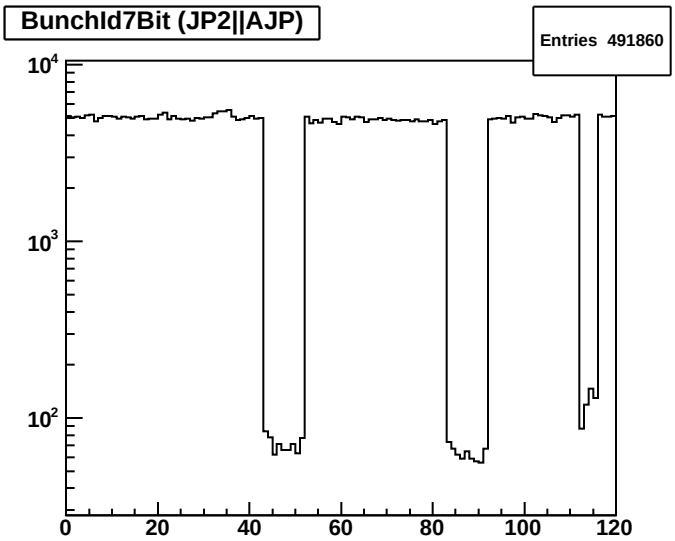

EMC L2 Input - HT23 bits
Entries 3934880
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

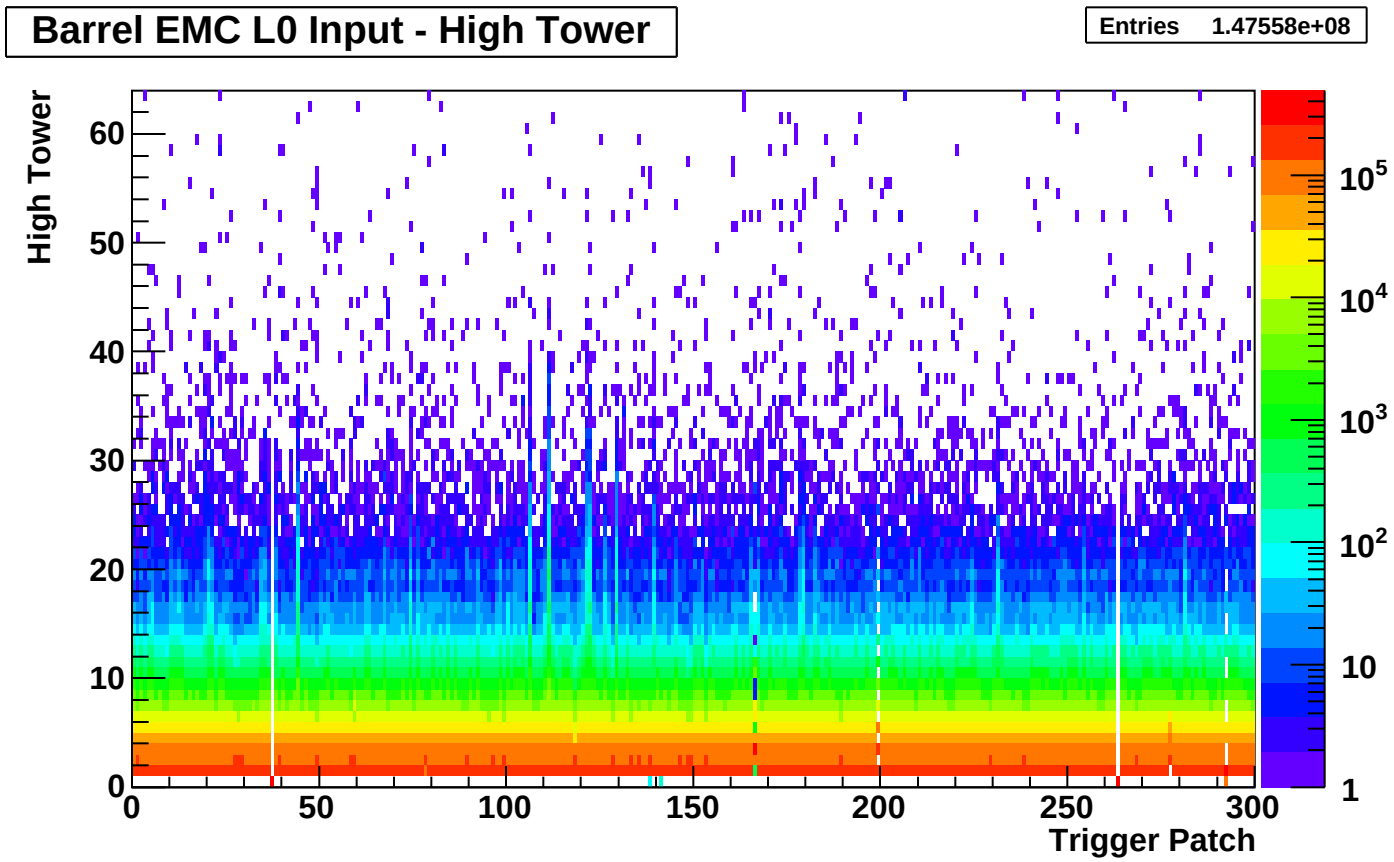

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

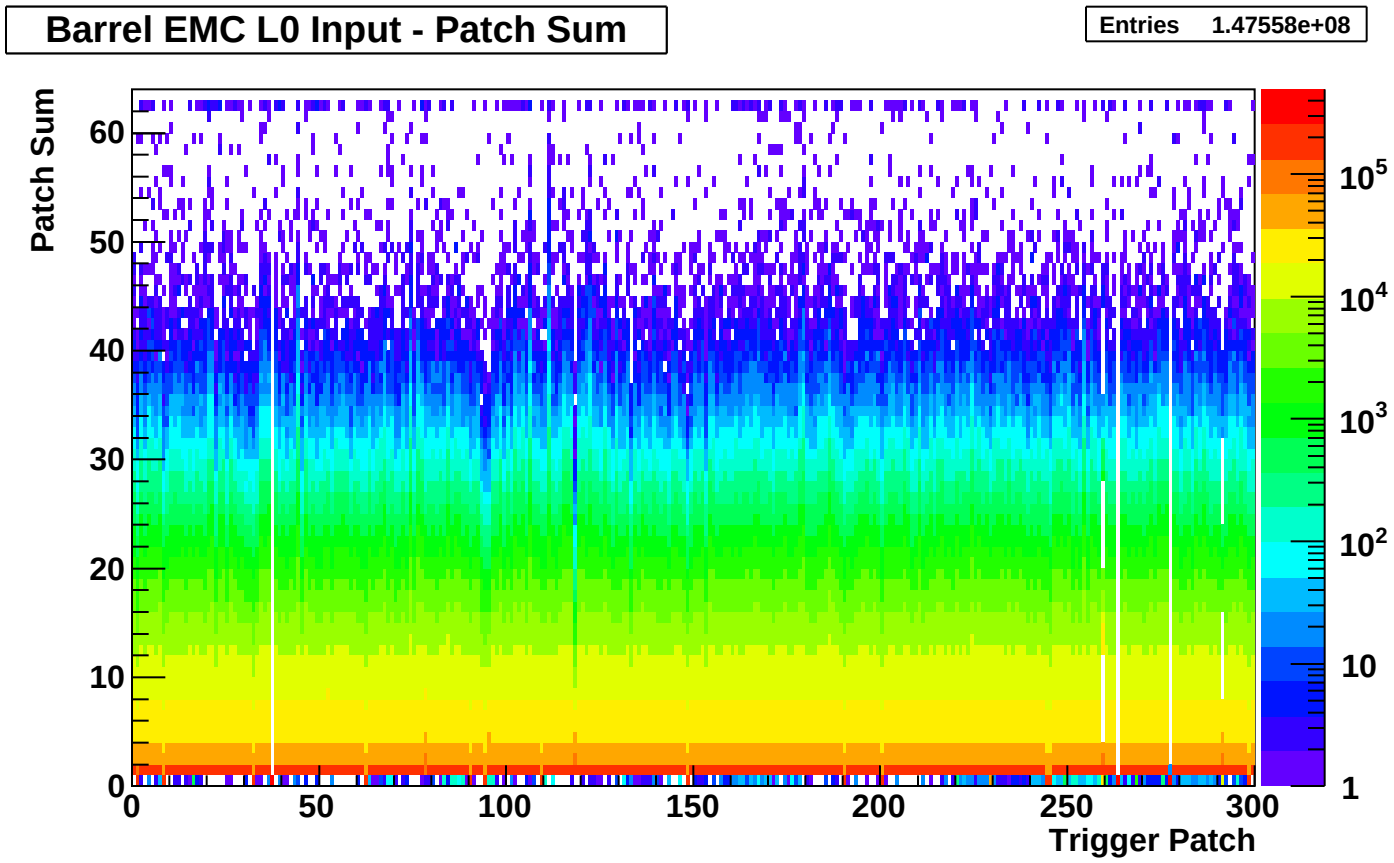

BunchId7Bit (BHT3)
Entries 3155
10
1
0
20
40
60
80
100
120

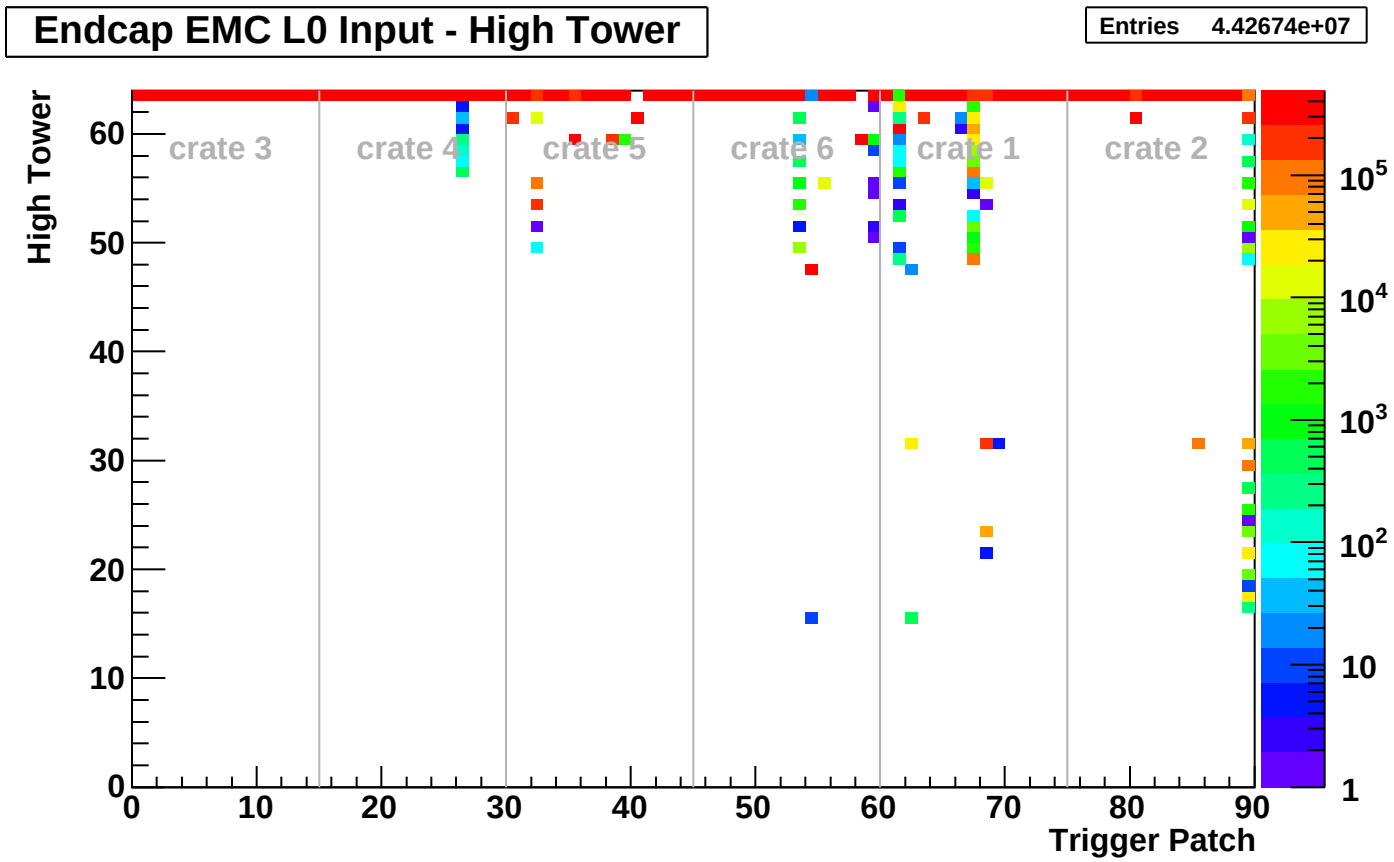

BunchId7Bit (all events)
Entries 491860
10^4
10^3
10^2
0
20
40
60
80
100
120

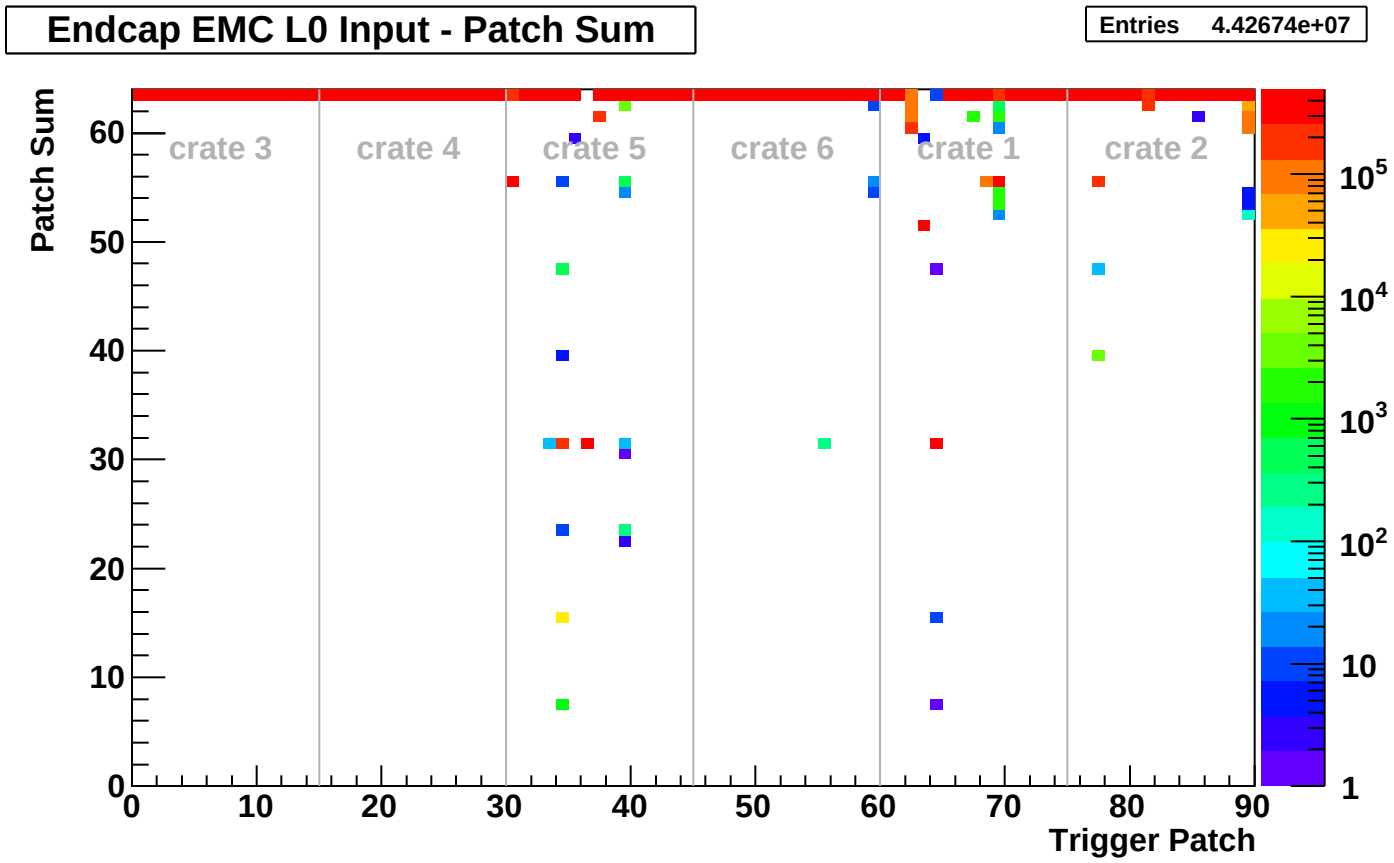

BunchId7Bit (JP1)
Entries 491860
10^4
10^3
10^2
0
20
40
60
80
100
120

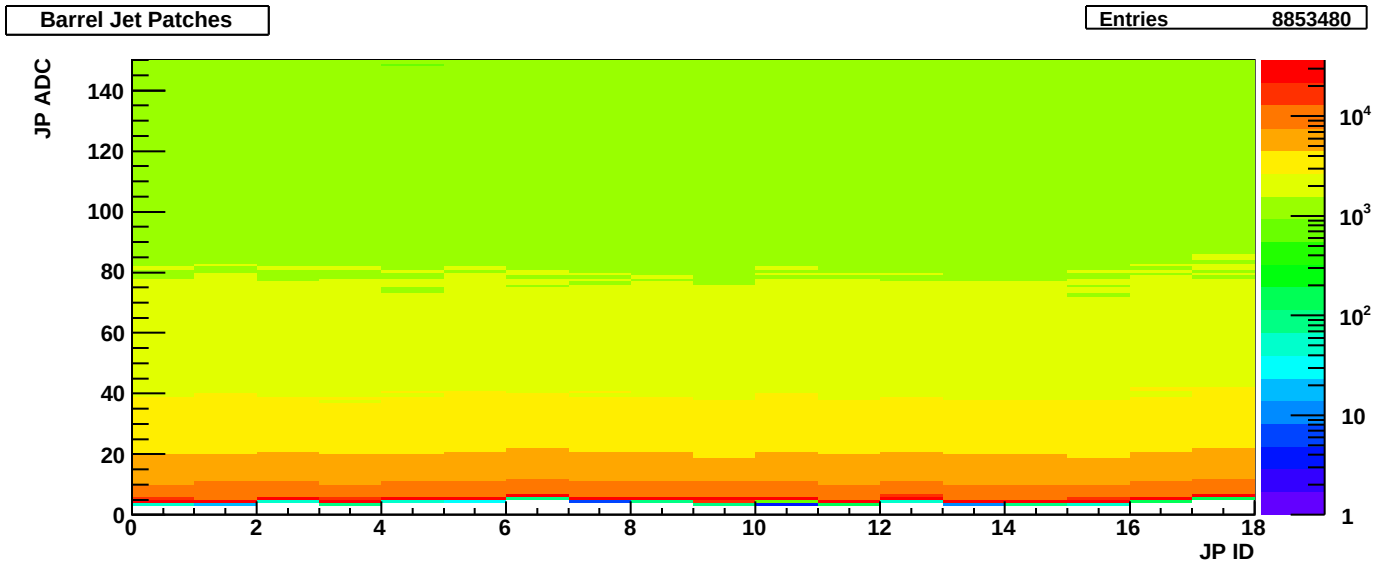

BunchId7Bit (JP2||AJP)
Entries 491860
10^4
10^3
10^2
0
20
40
60
80
100
120

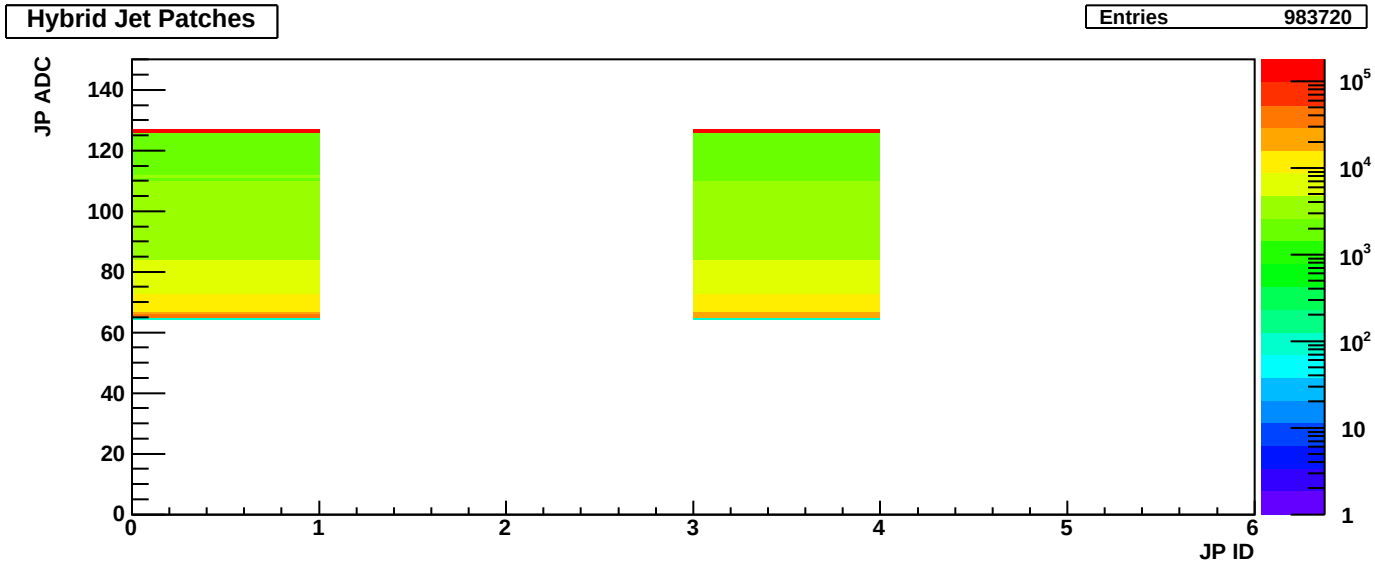

Barrel EMC L0 Input - High Tower
Entries 1.47558e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

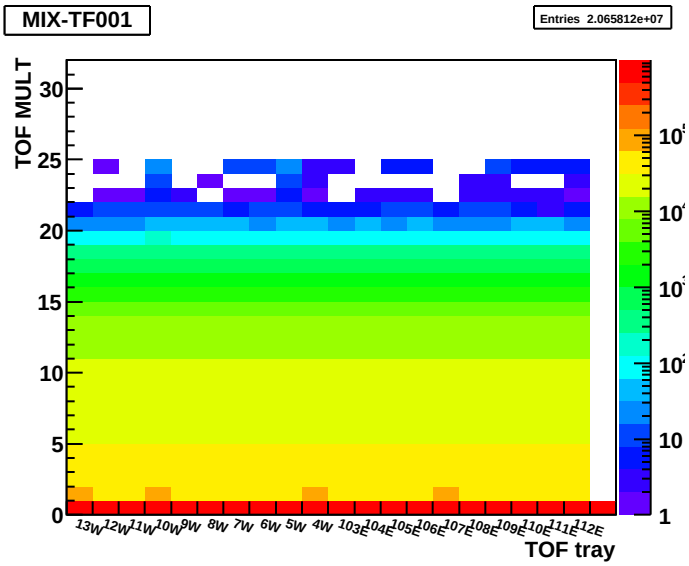

Barrel EMC L0 Input - Patch Sum
Entries 1.47558e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

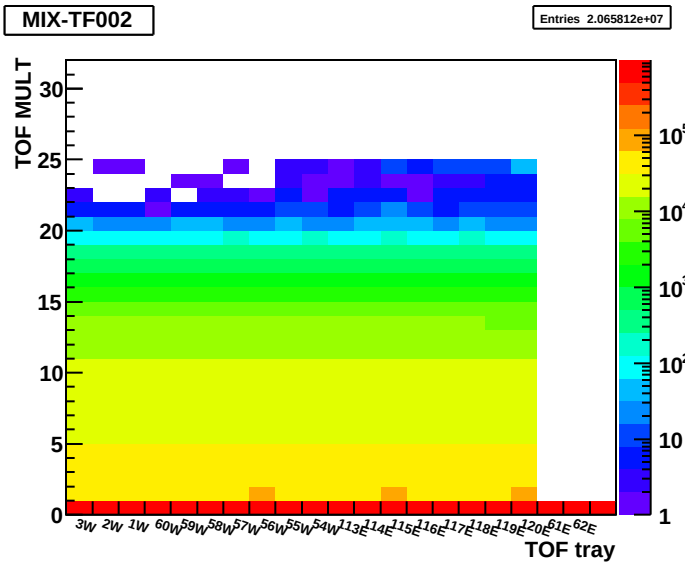

Endcap EMC L0 Input - High Tower
Entries 4.42674e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

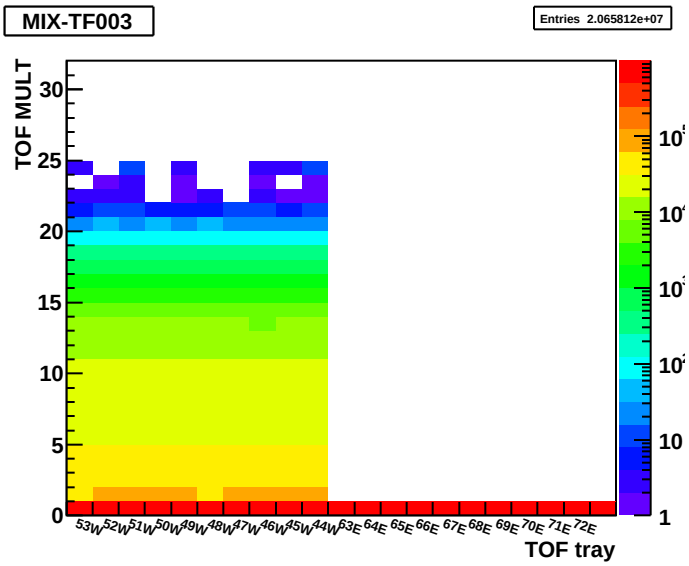

Endcap EMC L0 Input - Patch Sum
Entries 4.42674e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

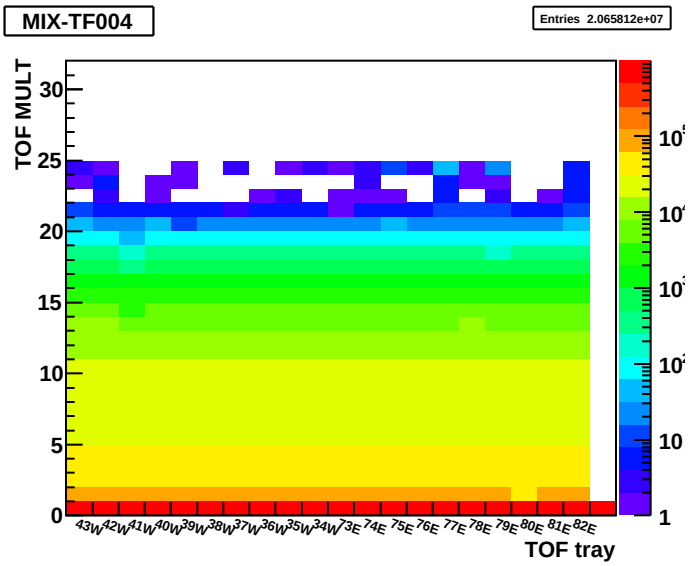

Barrel Jet Patches
Entries 8853480
JP ADC
JP ID

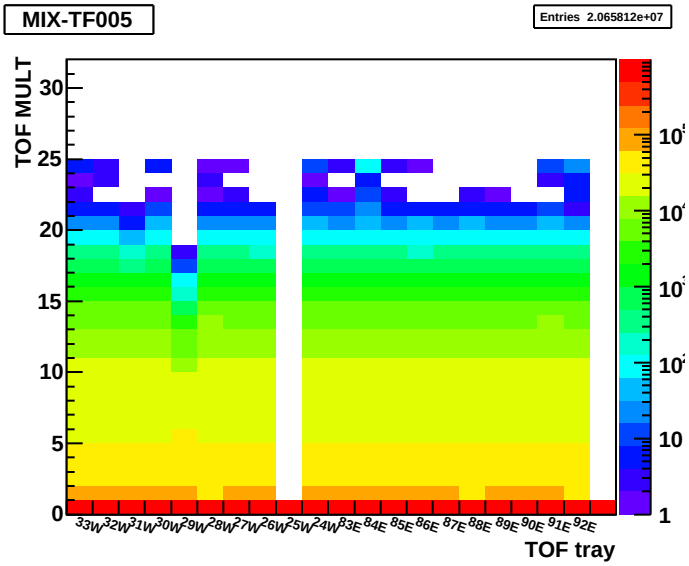

Hybrid Jet Patches
Entries 983720
JP ADC
JP ID

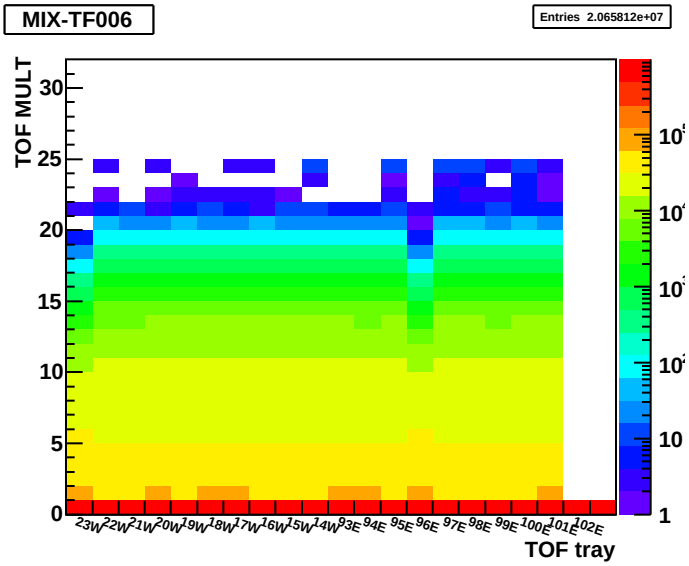

MIX-TF001
Entries 2.065812e+07
TOF MULT
TOF tray

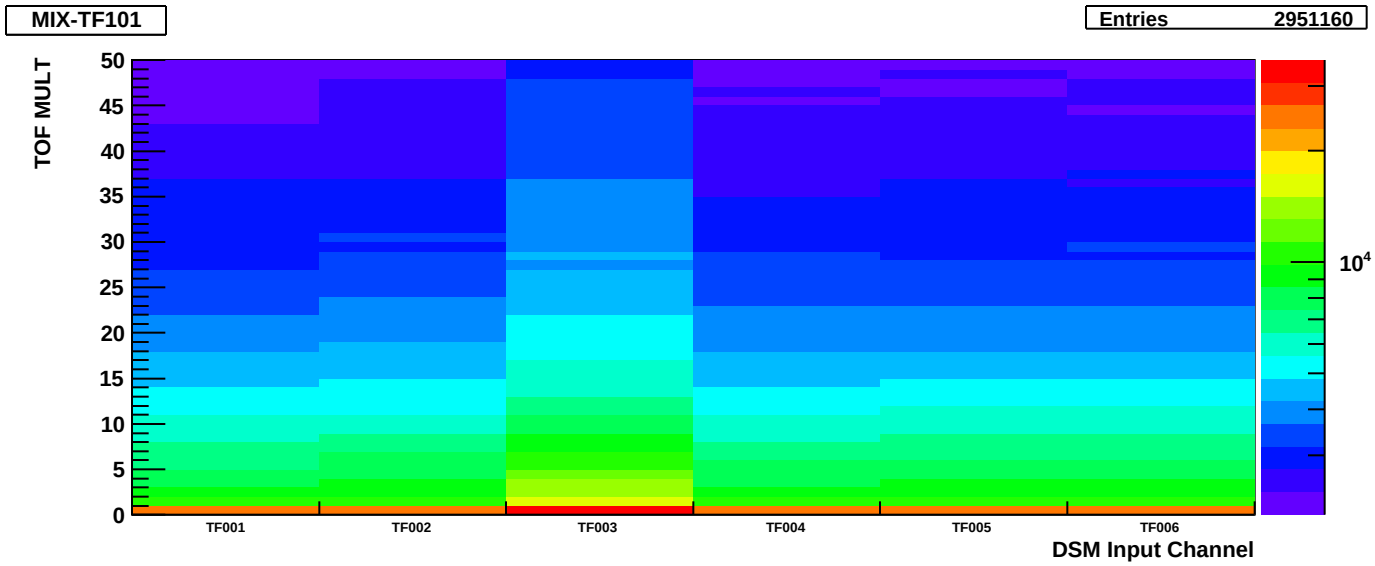

MIX-TF002
Entries 2.065812e+07
TOF MULT
TOF tray

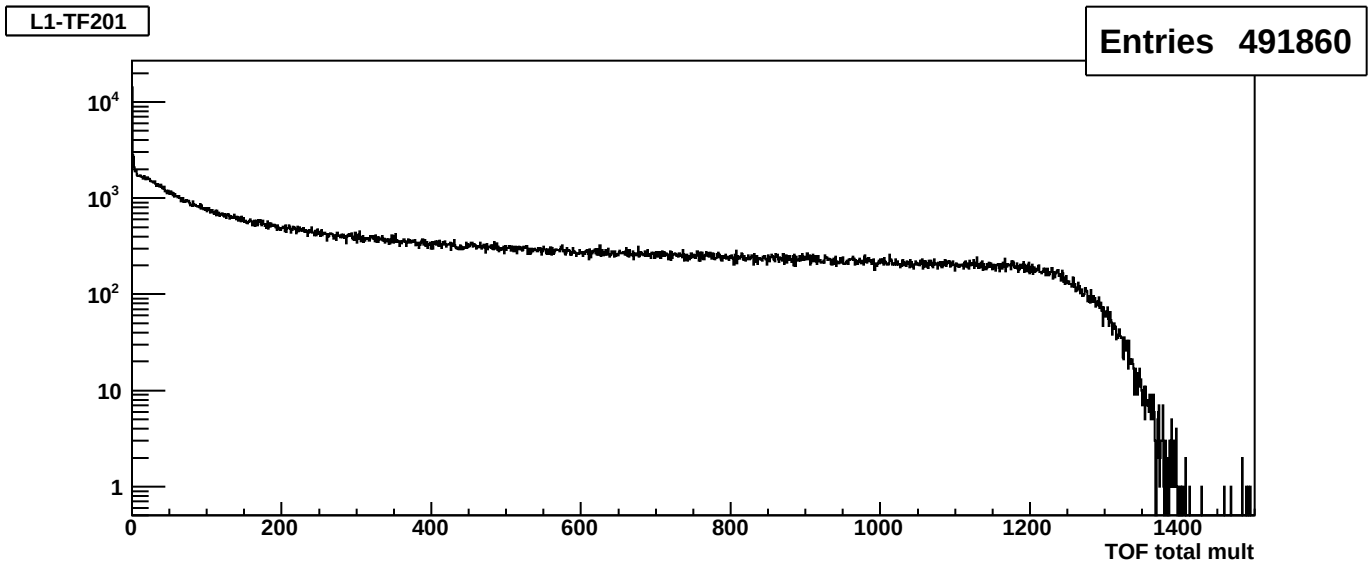

MIX-TF003
Entries 2.065812e+07
TOF MULT
TOF tray

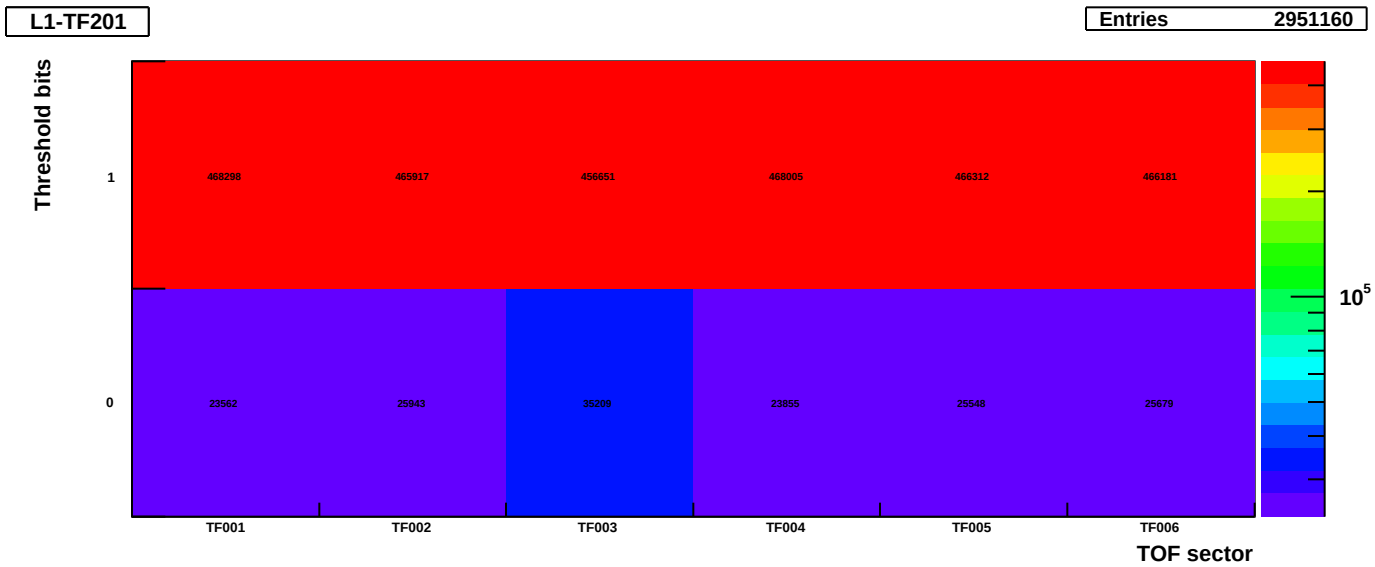

MIX-TF004
Entries 2.065812e+07
TOF MULT
TOF tray

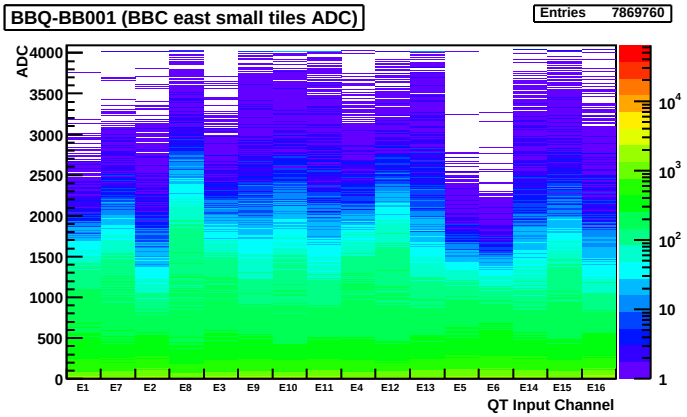

MIX-TF005
Entries 2.065812e+07
TOF MULT
TOF tray

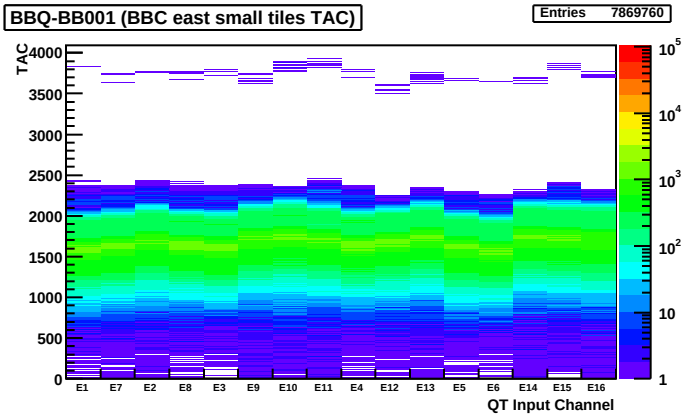

MIX-TF006
Entries 2.065812e+07
TOF MULT
TOF tray

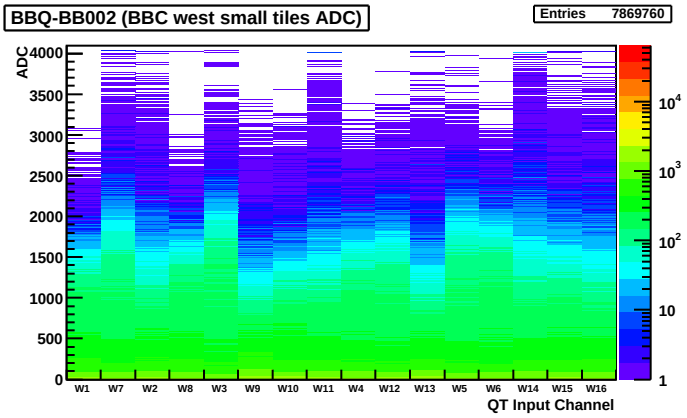

MIX-TF101
Entries 2951160
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

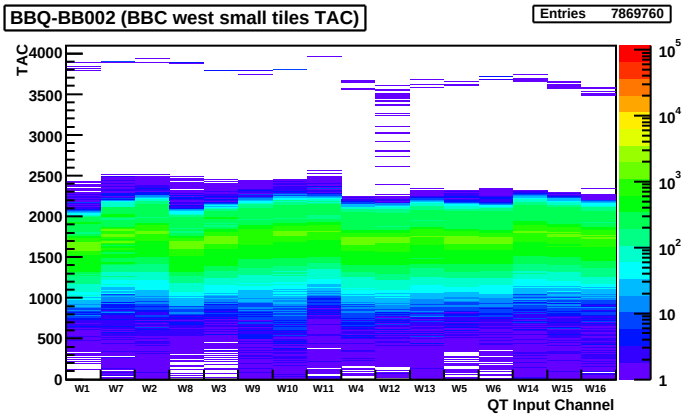

L1-TF201
Entries 491860
10^4
10^3
10^2
10
1
0
200
400
600
800
1000
1200
1400
TOF total mult

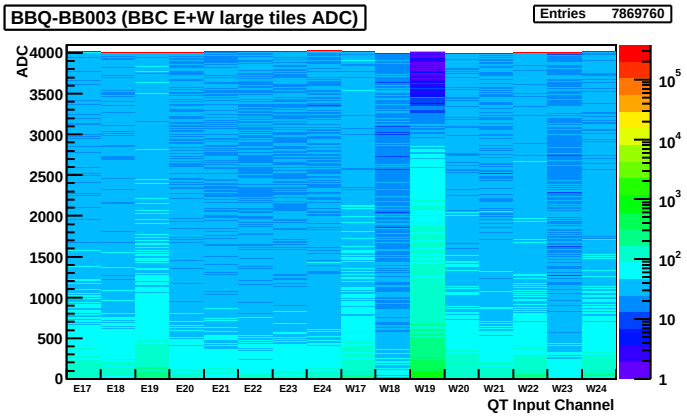

L1-TF201
Entries 2951160
Threshold bits
1
0
468298
465917
456651
468005
466312
466181
23562
25943
35209
23855
25548
25679
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

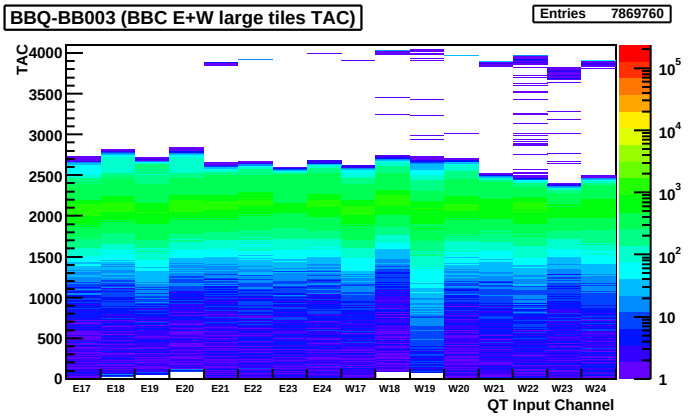

BBQ-BB001 (BBC east small tiles ADC)
Entries 7869760
ADC
QT Input Channel

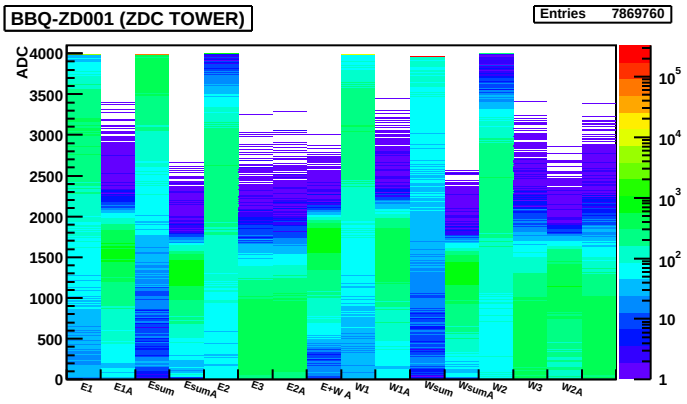

BBQ-BB001 (BBC east small tiles TAC)
Entries 7869760
TAC
QT Input Channel

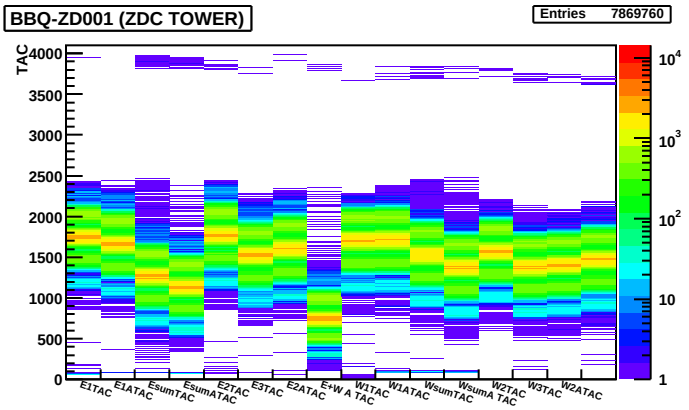

BBQ-BB002 (BBC west small tiles ADC)
Entries 7869760
ADC
QT Input Channel

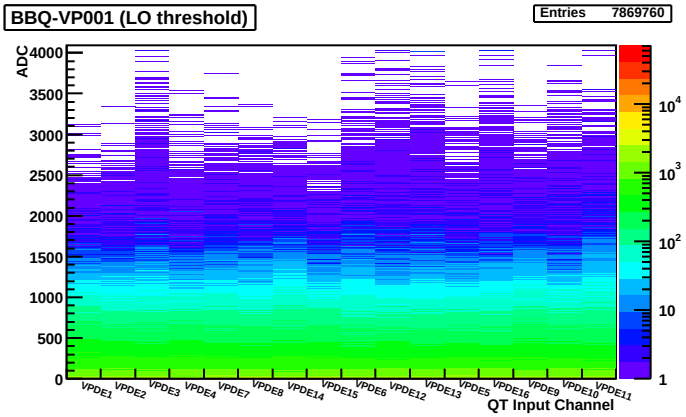

BBQ-BB002 (BBC west small tiles TAC)
Entries 7869760
TAC
QT Input Channel

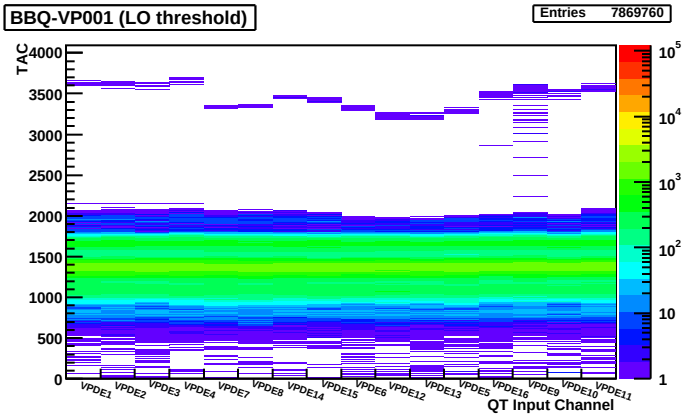

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 7869760
ADC
QT Input Channel

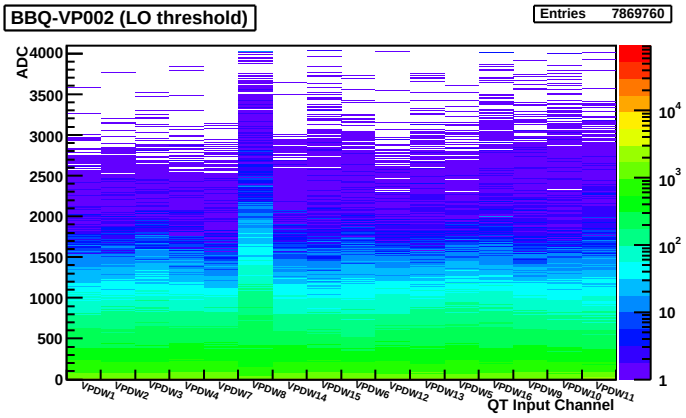

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 7869760
TAC
QT Input Channel

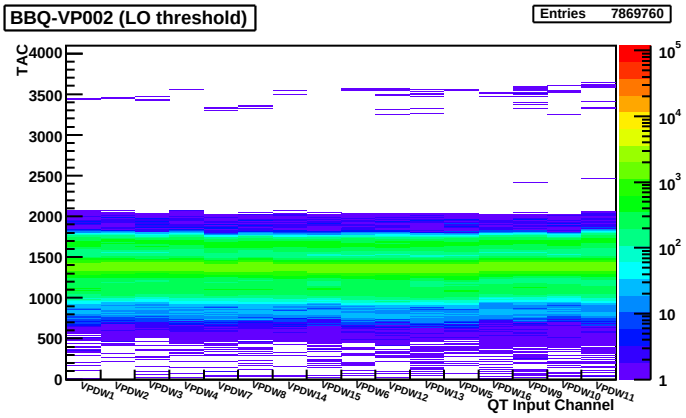

BBQ-ZD001 (ZDC TOWER)
Entries 7869760
ADC

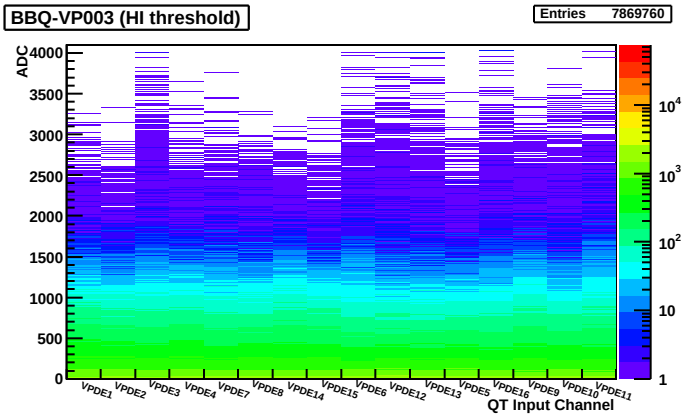

BBQ-ZD001 (ZDC TOWER)
Entries 7869760
TAC

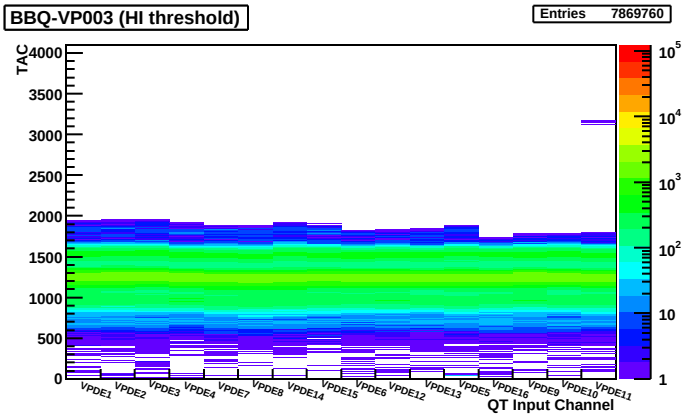

BBQ-VP001 (LO threshold)
Entries 7869760
ADC
QT Input Channel

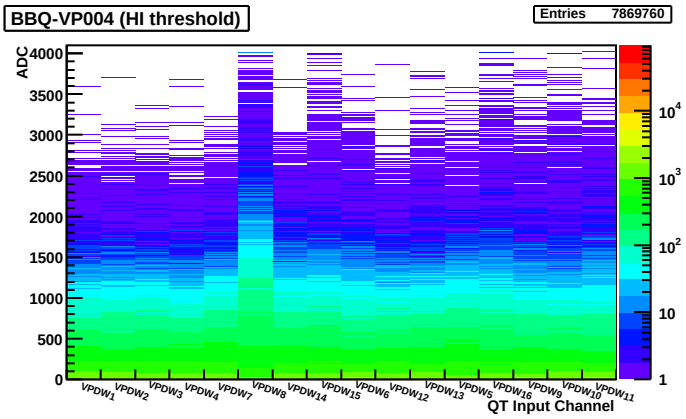

BBQ-VP001 (LO threshold)
Entries 7869760
TAC
QT Input Channel

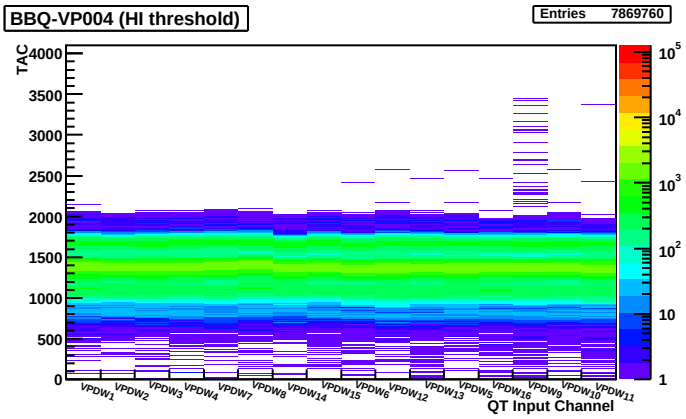

BBQ-VP002 (LO threshold)
Entries 7869760
ADC
QT Input Channel

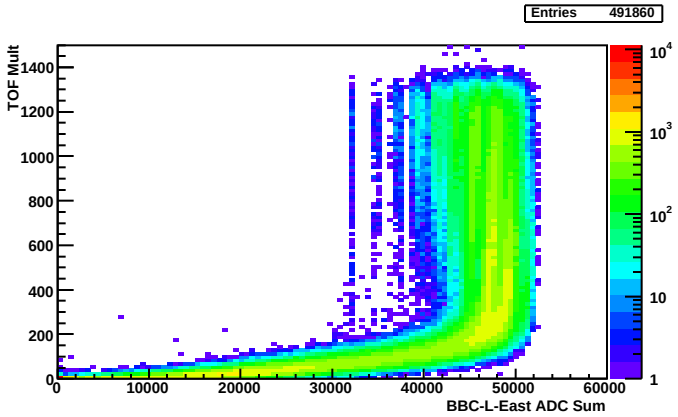

BBQ-VP002 (LO threshold)
Entries 7869760
TAC
QT Input Channel

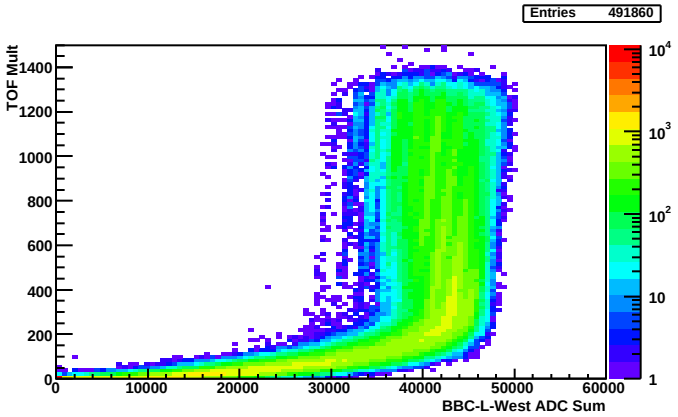

BBQ-VP003 (HI threshold)
Entries 7869760
ADC
QT Input Channel

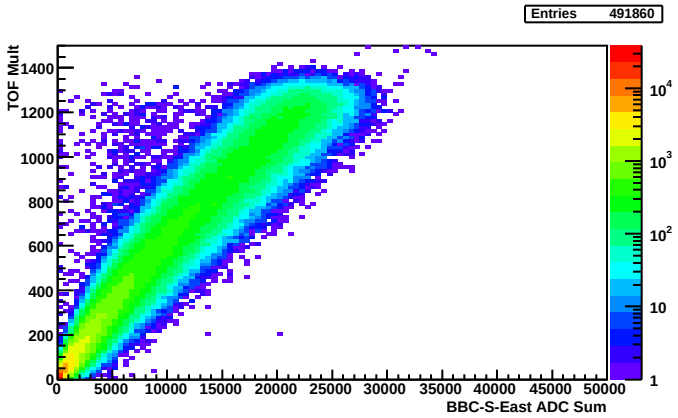

BBQ-VP003 (HI threshold)
Entries 7869760
TAC
QT Input Channel

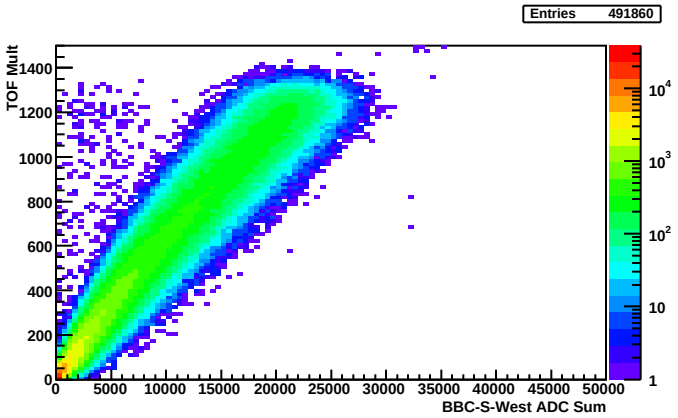

BBQ-VP004 (HI threshold)
Entries 7869760
ADC
QT Input Channel

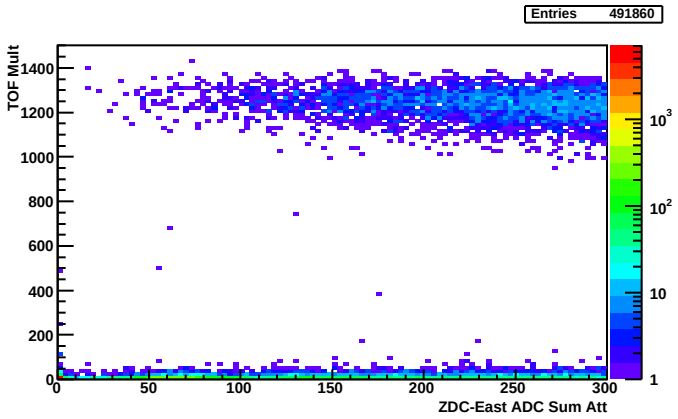

BBQ-VP004 (HI threshold)
Entries 7869760
TAC
QT Input Channel

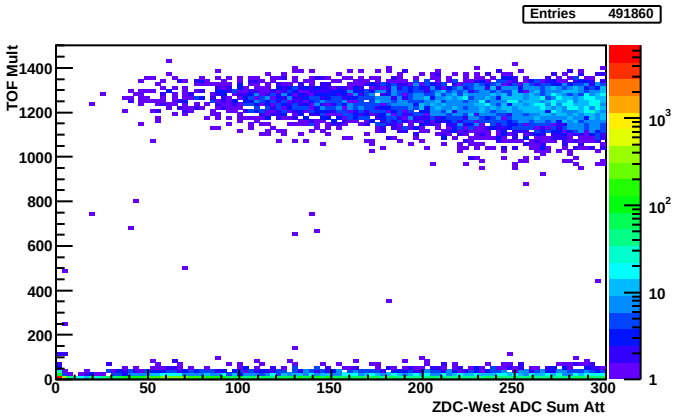

Entries 491860
TOF Mult
BBC-L-East ADC Sum

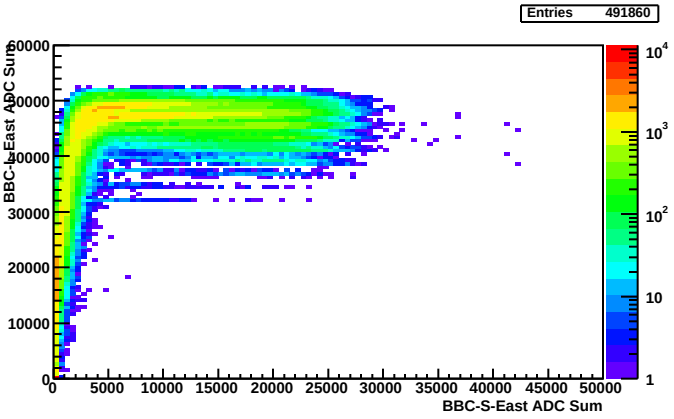

Entries 491860
TOF Mult
BBC-L-West ADC Sum

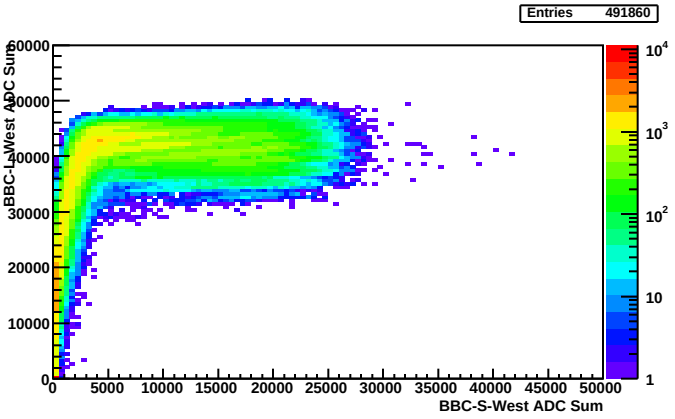

Entries 491860
TOF Mult
BBC-S-East ADC Sum

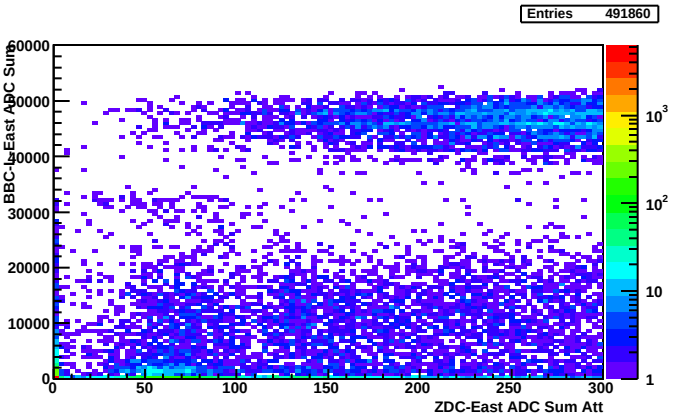

Entries 491860
TOF Mult
BBC-S-West ADC Sum

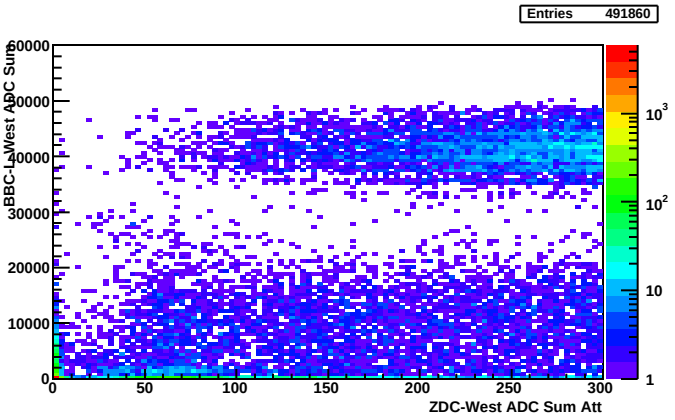

Entries 491860
TOF Mult
ZDC-East ADC Sum Att


Entries 491860
TOF Mult
ZDC-West ADC Sum Att


Entries 491860
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 491860
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 491860
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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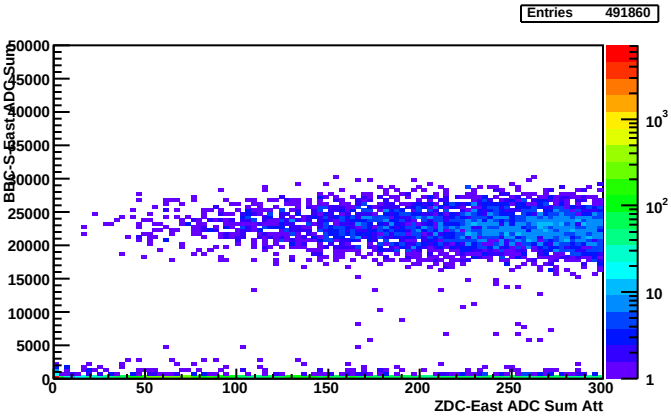
BBC-L-West ADC Sum
ZDC-West ADC Sum Att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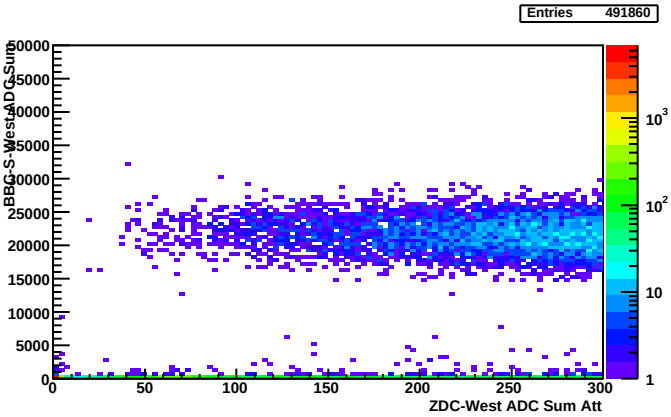
BBC-S-East ADC Sum
ZDC-East ADC Sum Att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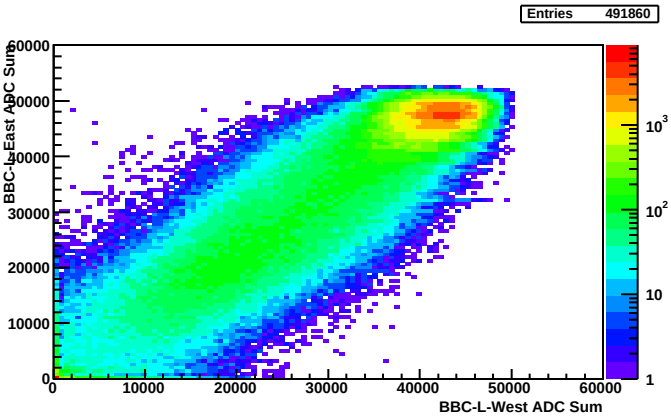
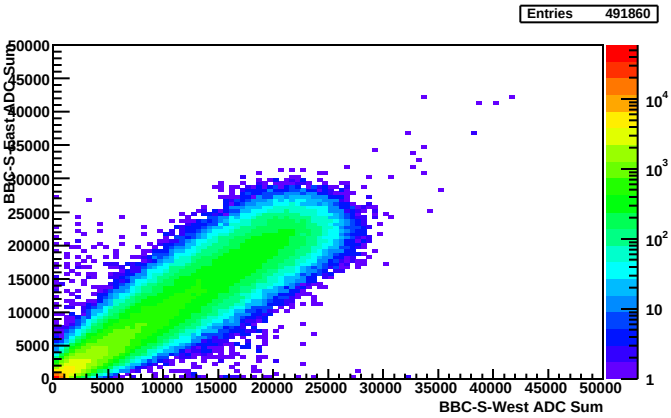
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 491860
BBC-L-East ADC Sum
BBC-L-West ADC Sum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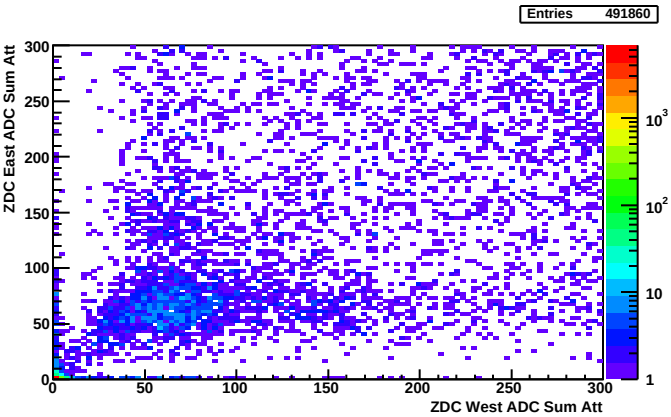
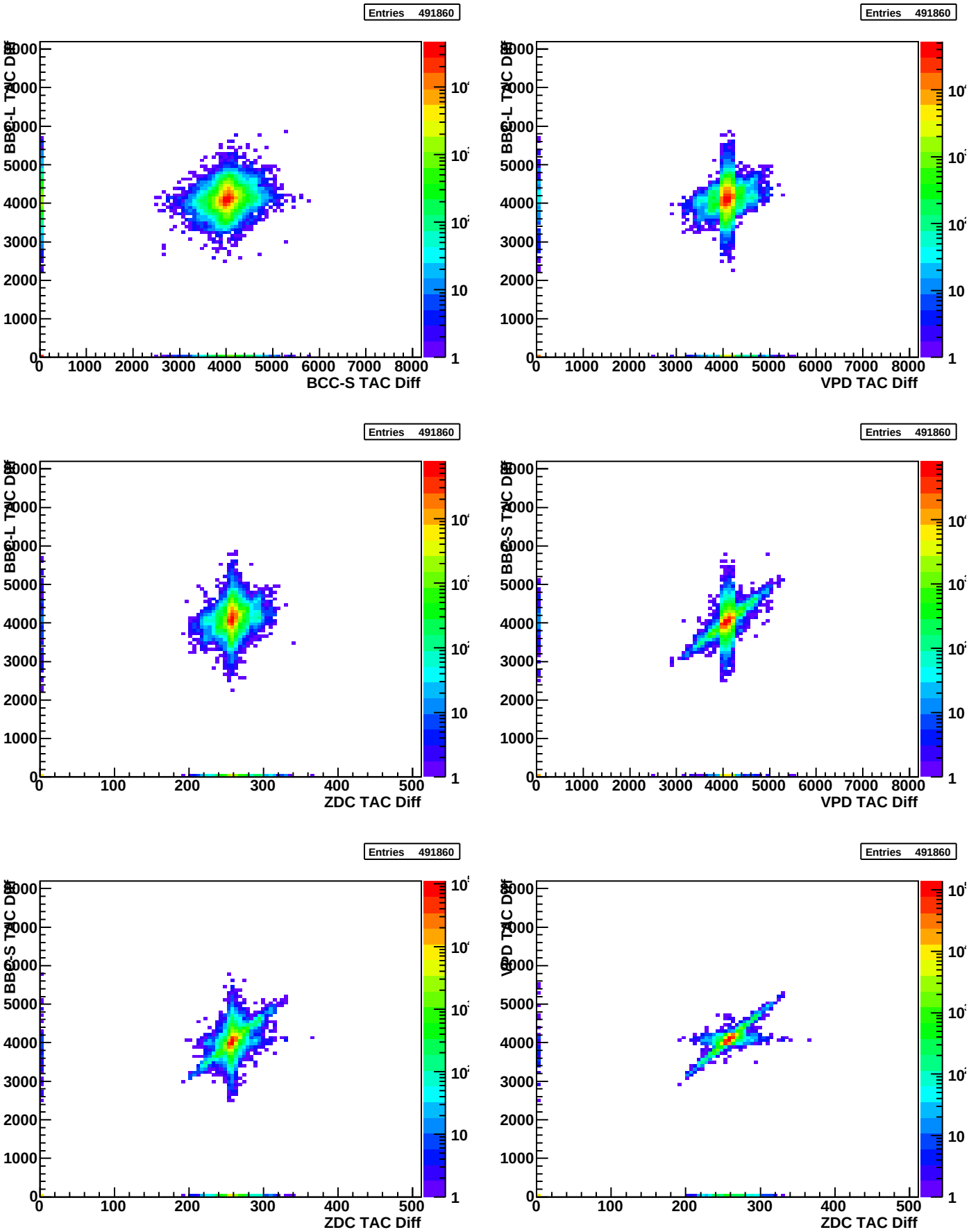
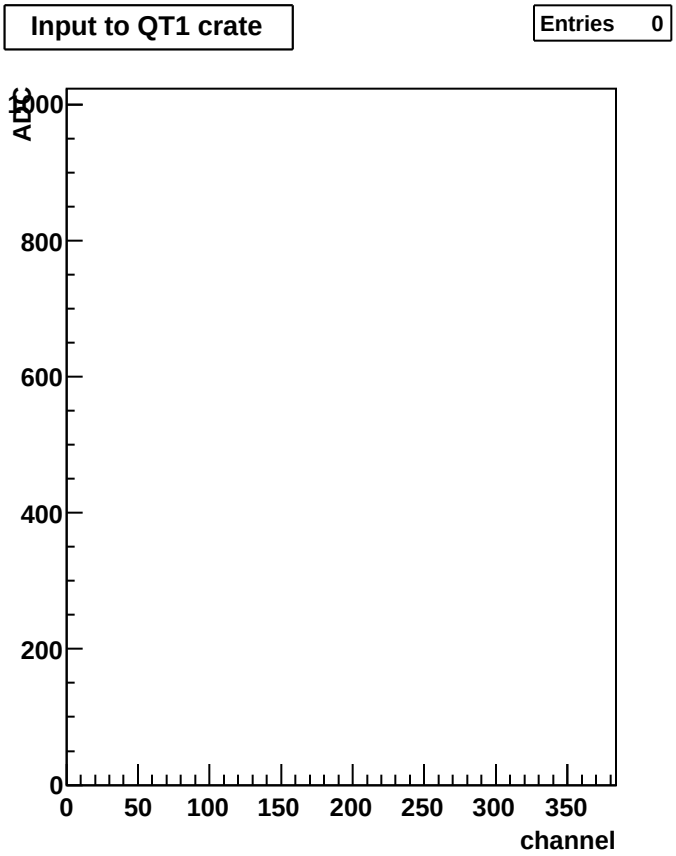
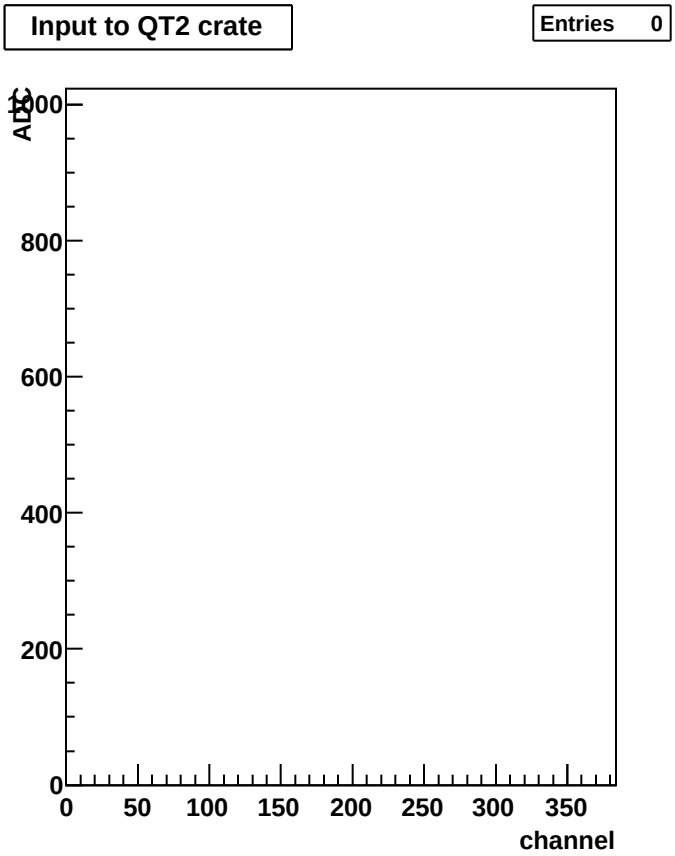
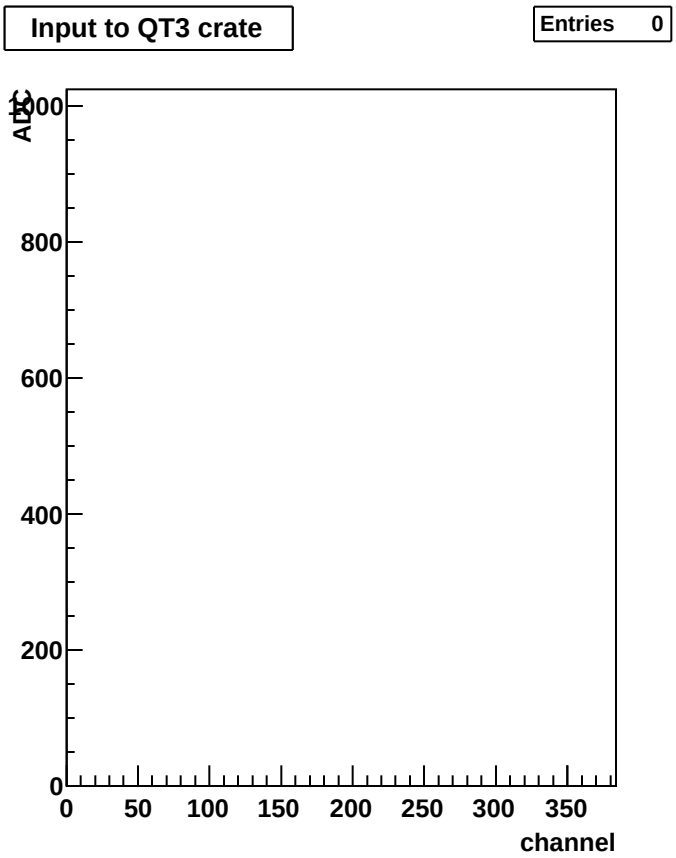
BBC-S-East ADC Sum
BBC-S-West ADC Sum

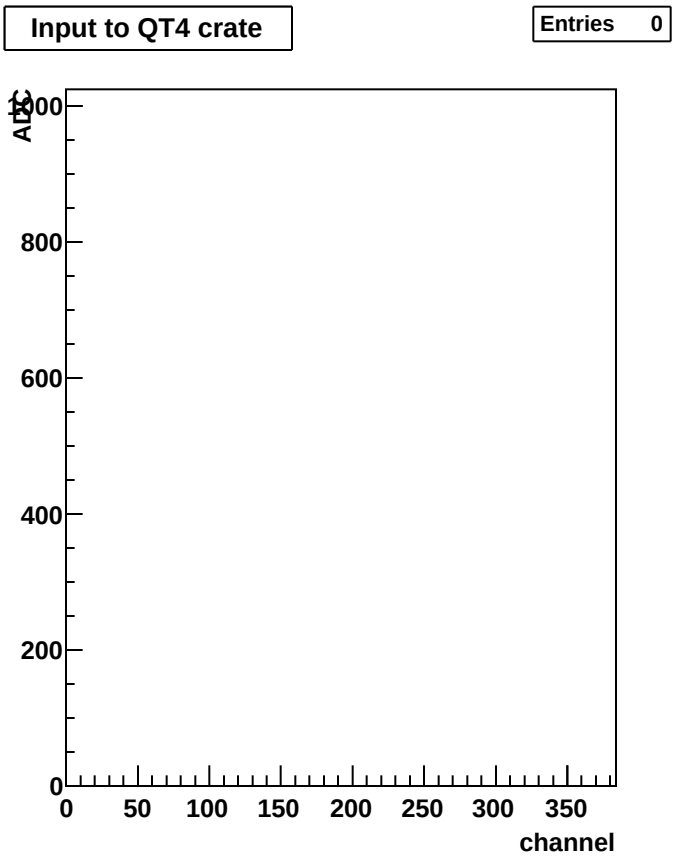

Entries 491860
ZDC East ADC Sum Att
ZDC West ADC Sum Att

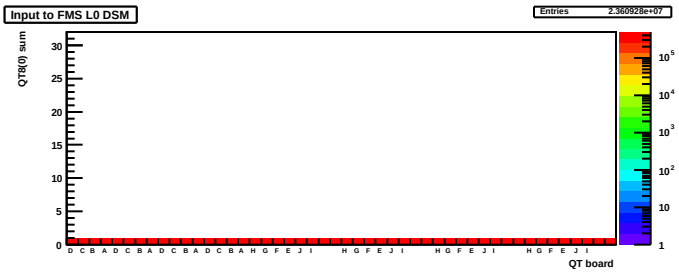

Entries 491860
BBC-L TAC Diff
BCC-S TAC Diff
Entries 491860
BBC-L TAC Diff
VPD TAC Diff
Entries 491860
BBC-L TAC Diff
ZDC TAC Diff
Entries 491860
BBC-S TAC Diff
VPD TAC Diff
Entries 491860
BBC-S TAC Diff
ZDC TAC Diff
Entries 491860
VPD TAC Diff
ZDC TAC Diff

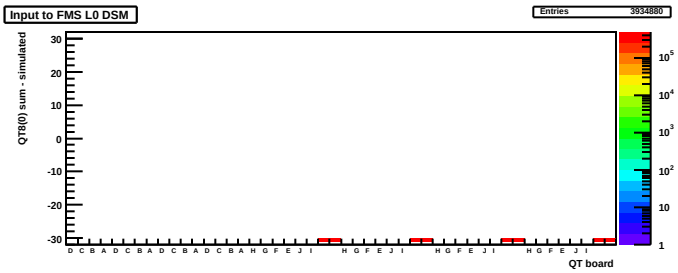

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

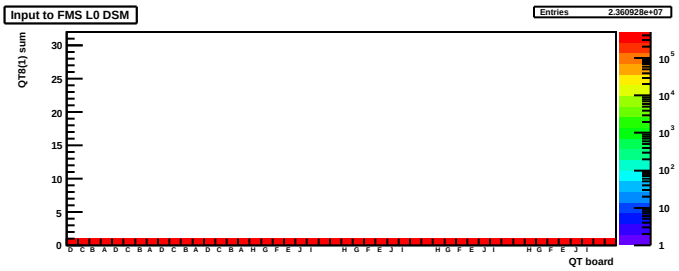

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

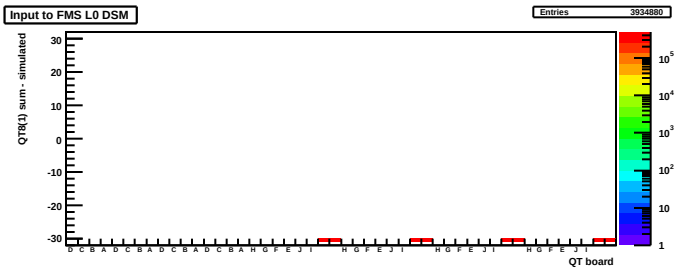

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

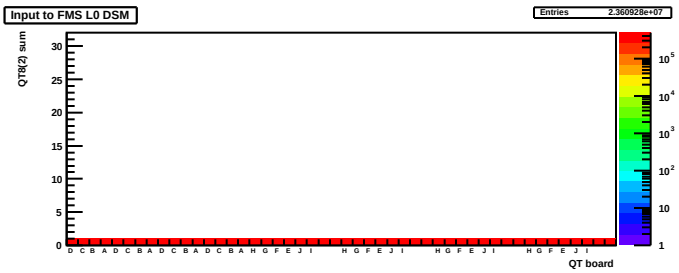

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

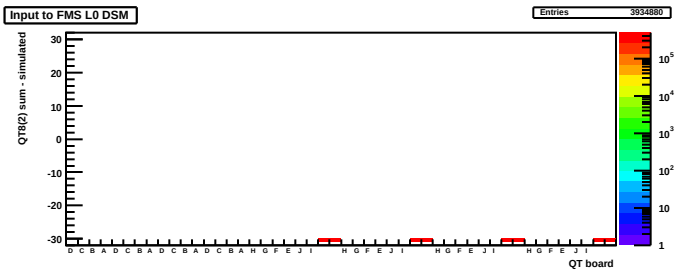

Input to FMS L0 DSM
Entries 2.360928e+07
QT8(0) sum
QT board

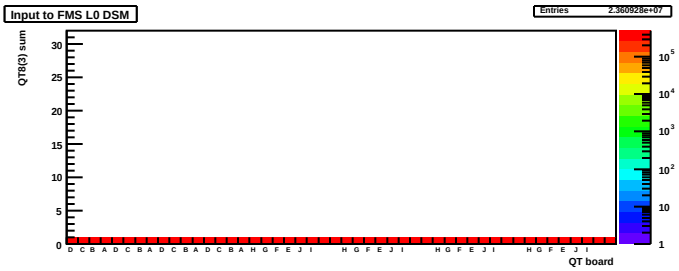

Input to FMS L0 DSM
Entries 3934880
QT8(0) sum - simulated
QT board

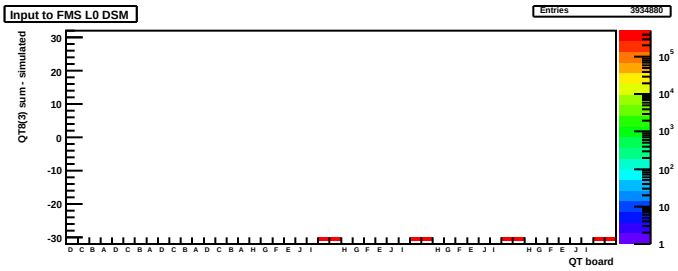

Input to FMS L0 DSM
Entries 2.360928e+07
QT8(1) sum
QT board

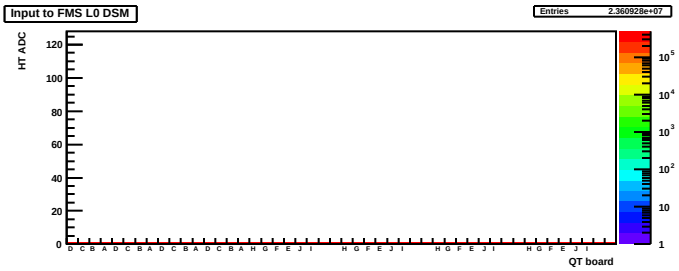

Input to FMS L0 DSM
Entries 3934880
QT8(1) sum - simulated
QT board

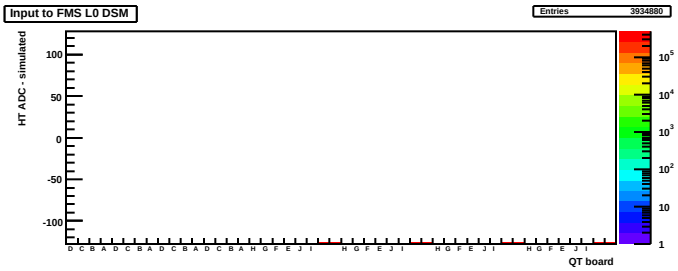

Input to FMS L0 DSM
Entries 2.360928e+07
QT8(2) sum
QT board

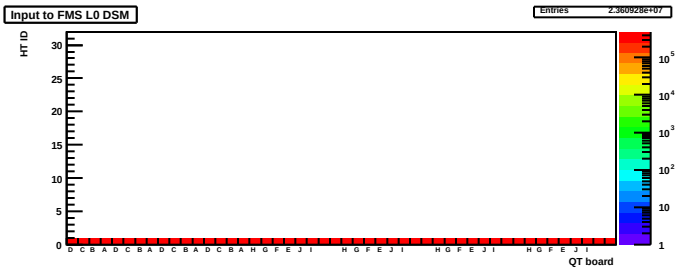

Input to FMS L0 DSM
Entries 3934880
QT8(2) sum - simulated
QT board

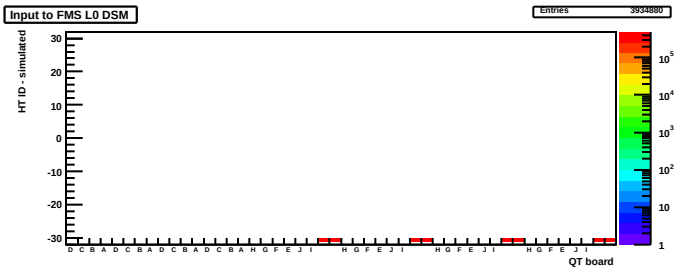

Input to FMS L0 DSM
Entries 2.360928e+07
QT8(3) sum
QT board

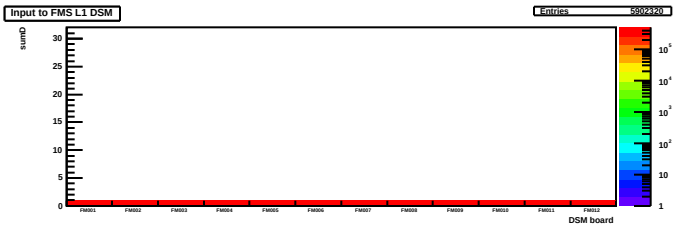

Input to FMS L0 DSM
Entries 3934880
QT8(3) sum - simulated
QT board

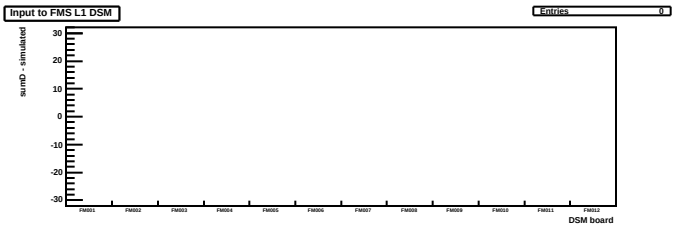

Input to FMS L0 DSM
Entries 2.360928e+07
HT ADC
QT board

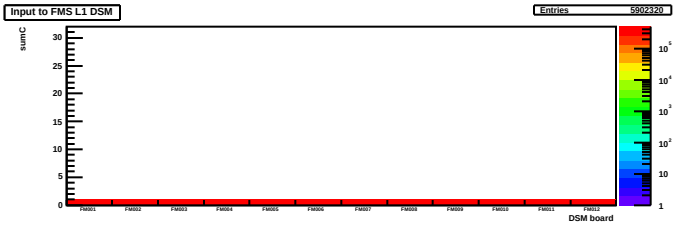

Input to FMS L0 DSM
Entries 3934880
HT ADC - simulated
QT board

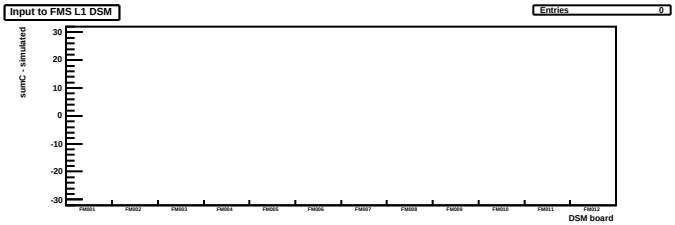

Input to FMS L0 DSM
Entries 2.360928e+07
HT ID
QT board

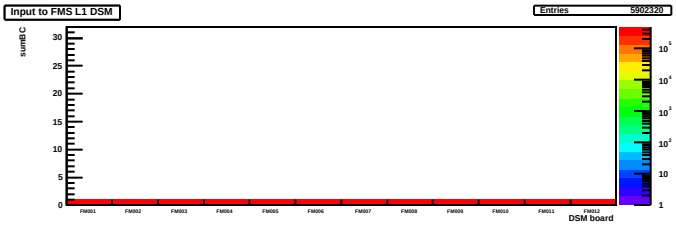

Input to FMS L0 DSM
Entries 3934880
HT ID - simulated
QT board

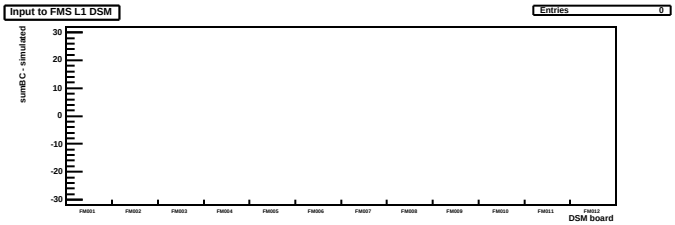

Input to FMS L1 DSM
Entries 5902320
sumD
DSM board

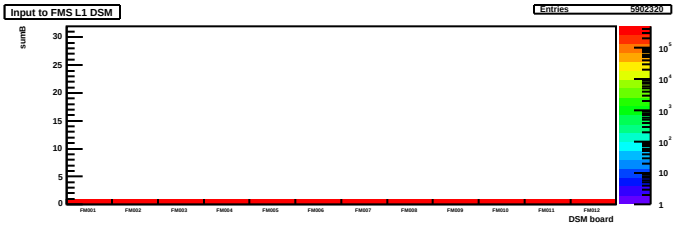

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

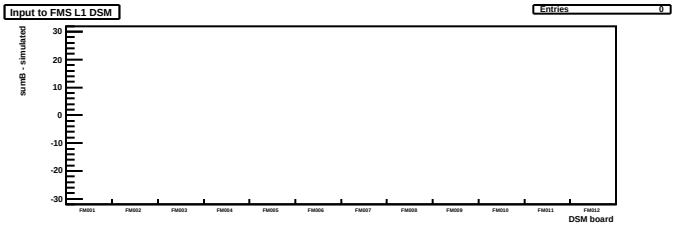

Input to FMS L1 DSM
Entries 5902320
sumC
DSM board

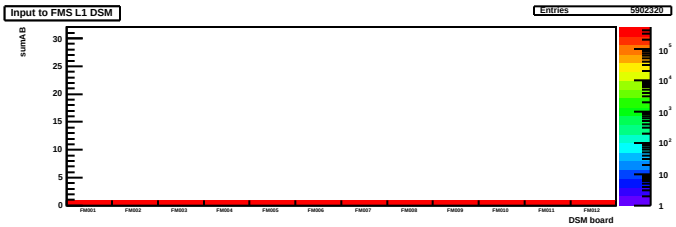

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

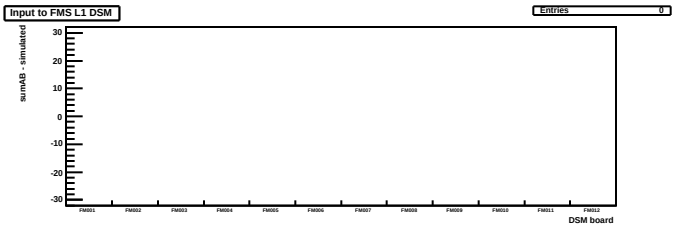

Input to FMS L1 DSM
Entries 5902320
sumBC
DSM board

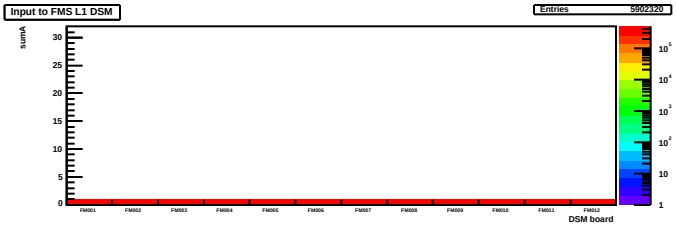

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

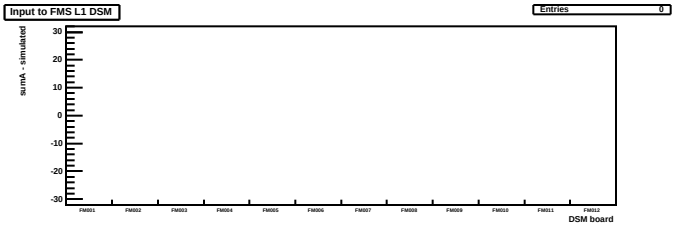

Input to FMS L1 DSM
Entries 5902320
sumB
DSM board

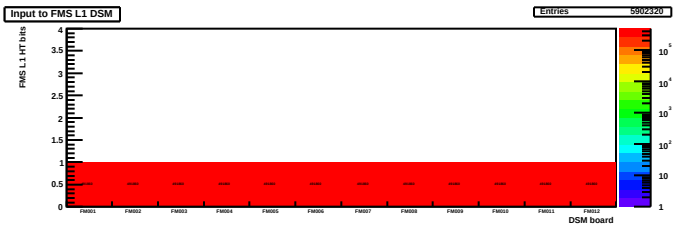

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

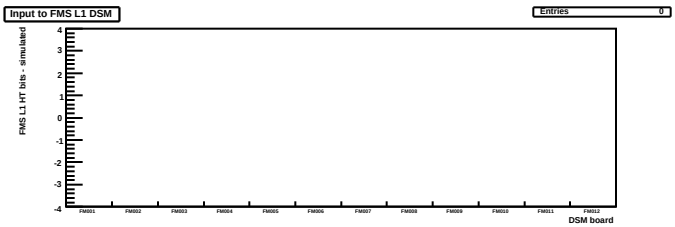

Input to FMS L1 DSM
Entries 5902320
sumAB
DSM board

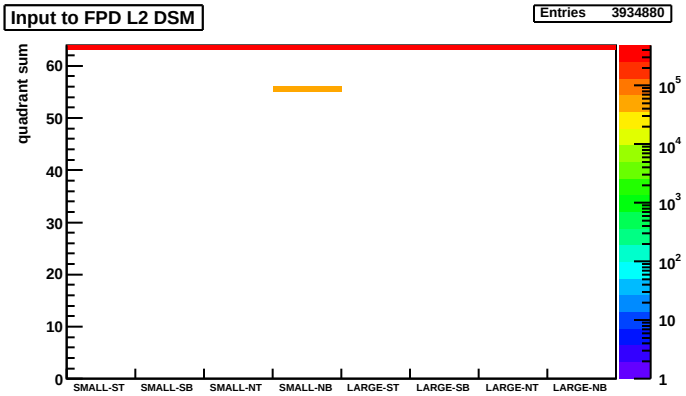

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

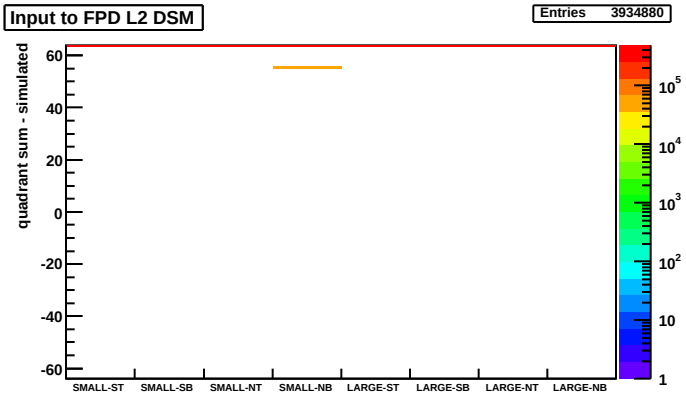

Input to FMS L1 DSM
Entries 5902320
sumA
DSM board

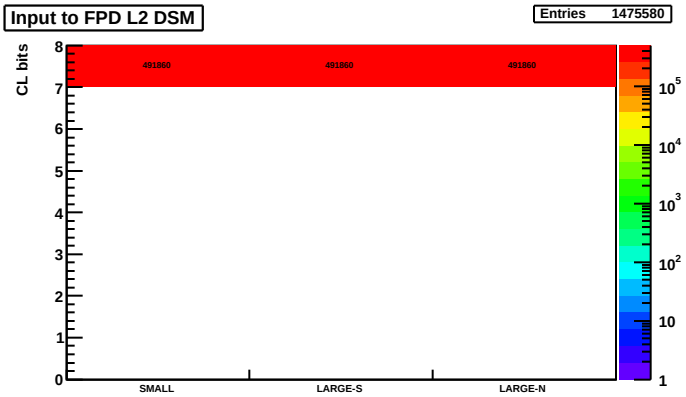

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

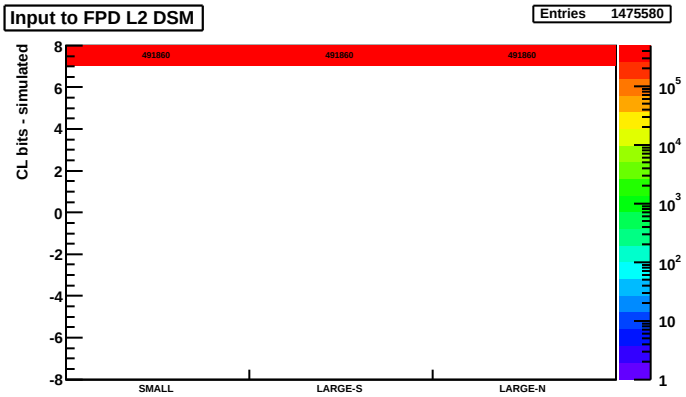

Input to FMS L1 DSM
Entries 5902320
FMS L1 HT bits
DSM board

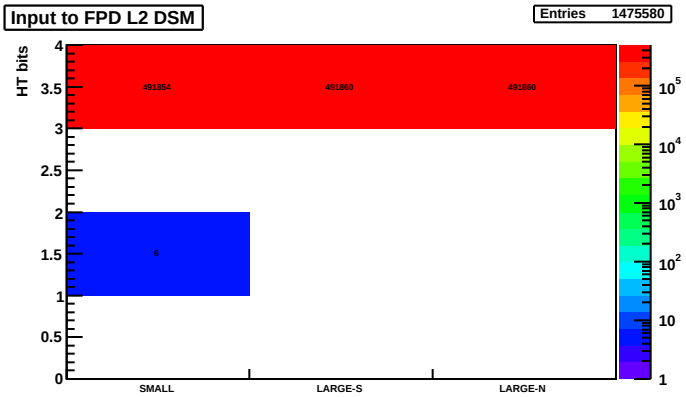

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

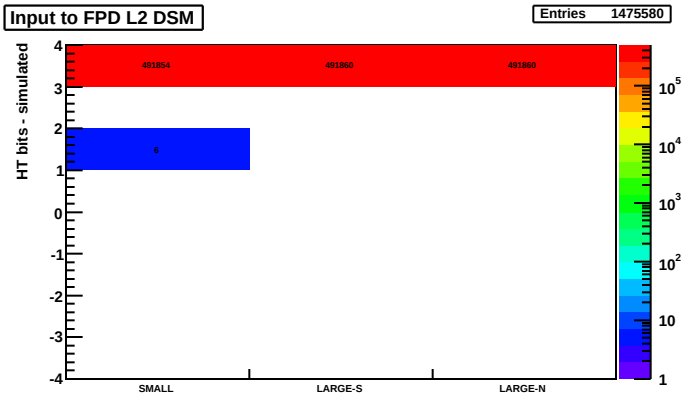

Input to FPD L2 DSM
Entries 3934880
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

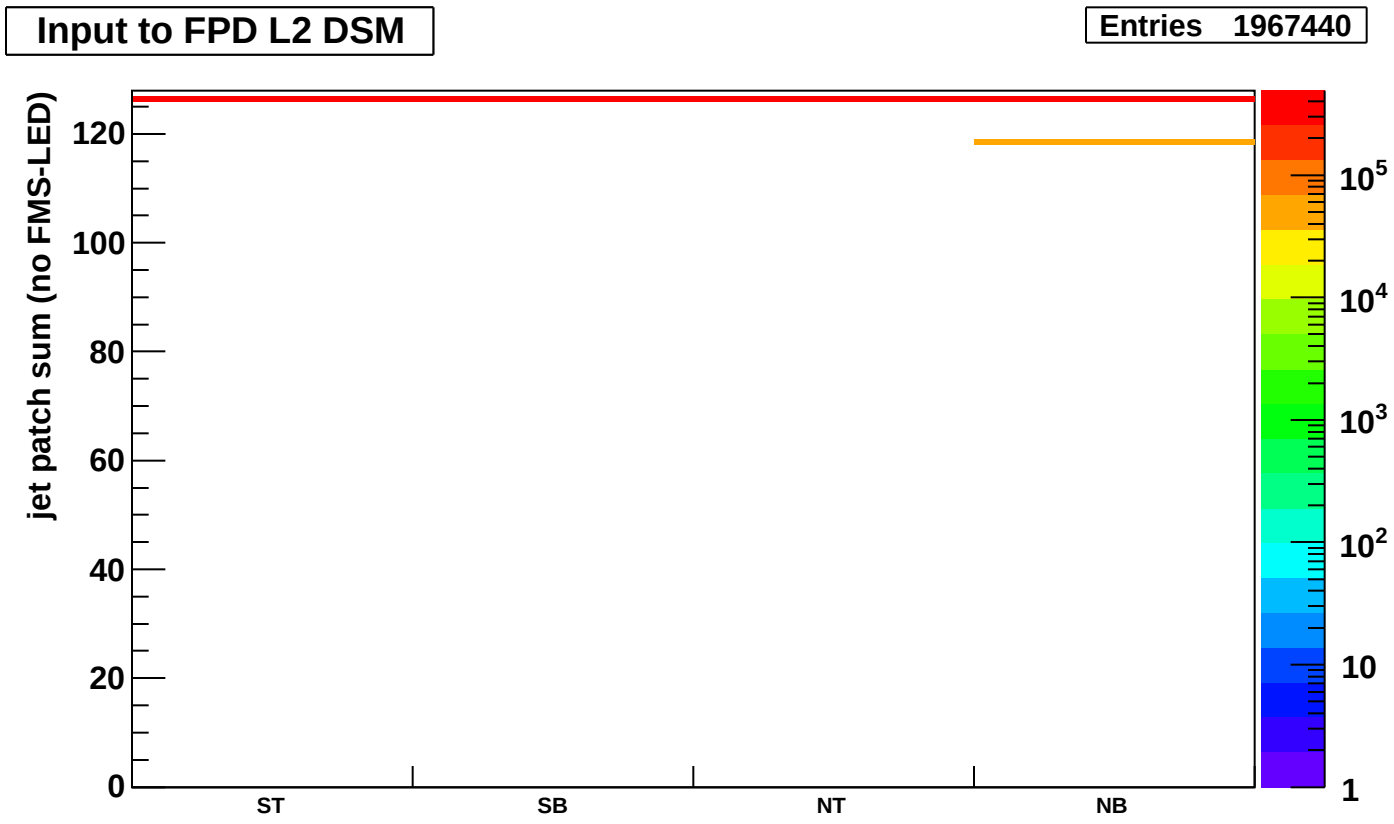

Input to FPD L2 DSM
Entries 3934880
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

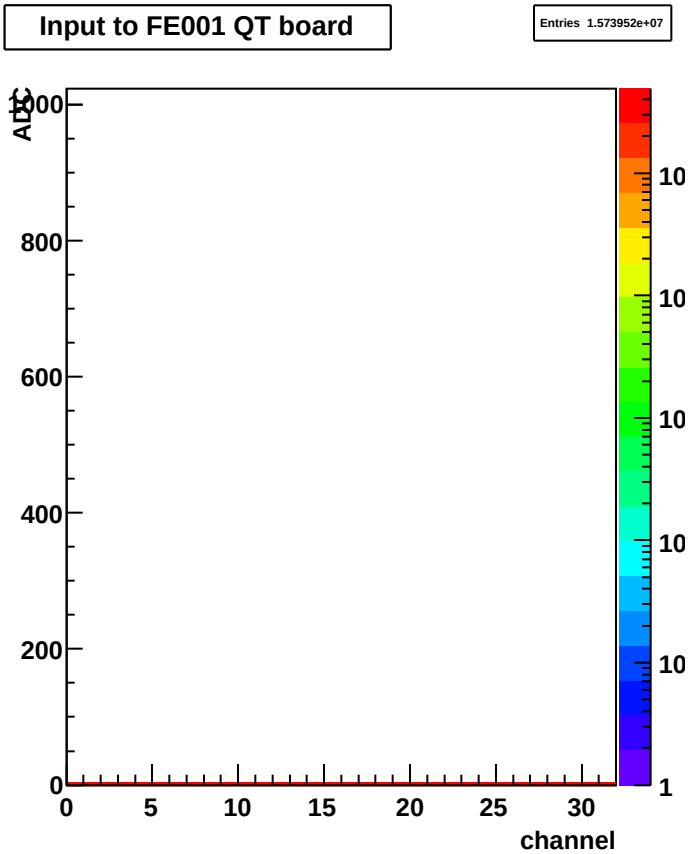

Input to FPD L2 DSM
Entries 1475580
CL bits
491860
491860
491860
SMALL
LARGE-S
LARGE-N

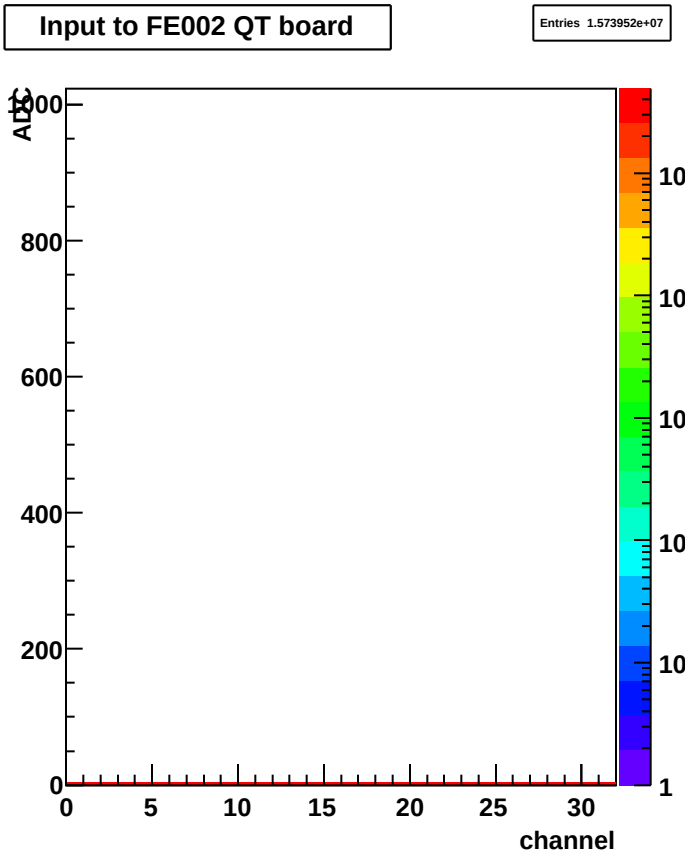

Input to FPD L2 DSM
Entries 1475580
CL bits - simulated
491860
491860
491860
SMALL
LARGE-S
LARGE-N

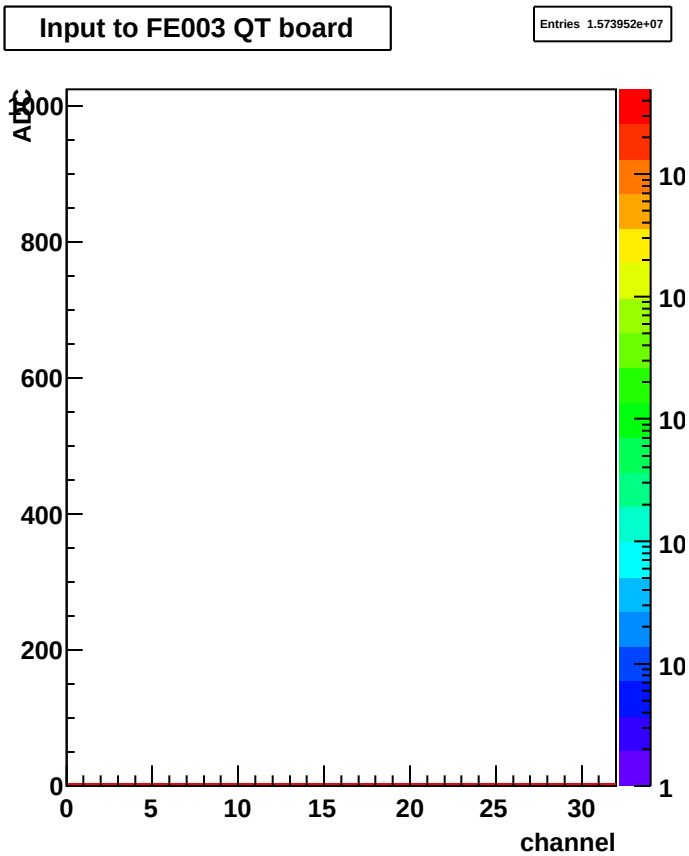

Input to FPD L2 DSM
Entries 1475580
HT bits
491854
491860
491860
6
SMALL
LARGE-S
LARGE-N

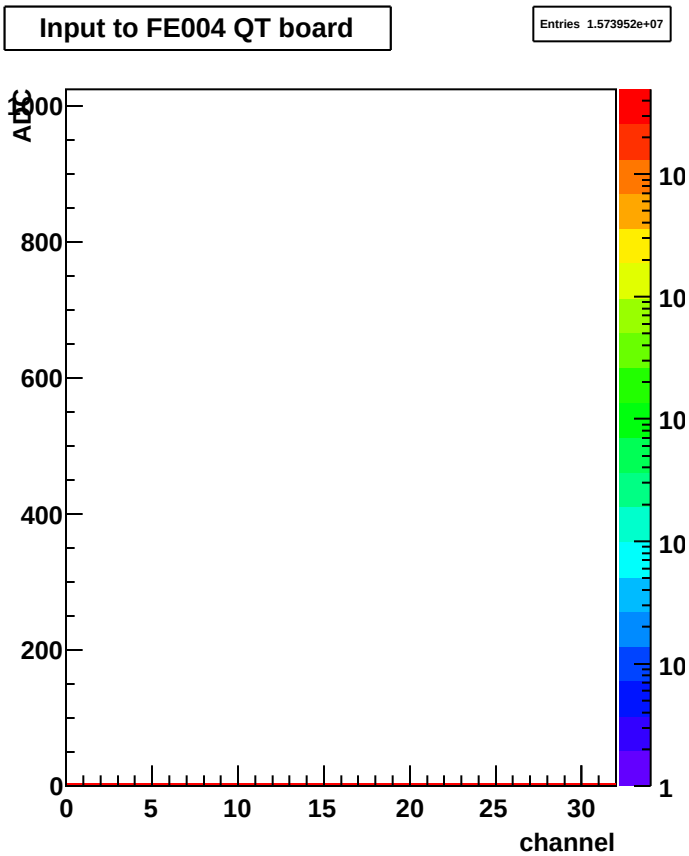

Input to FPD L2 DSM
Entries 1475580
HT bits - simulated
491854
491860
491860
6
SMALL
LARGE-S
LARGE-N


Input to FPD L2 DSM
Entries 1967440
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

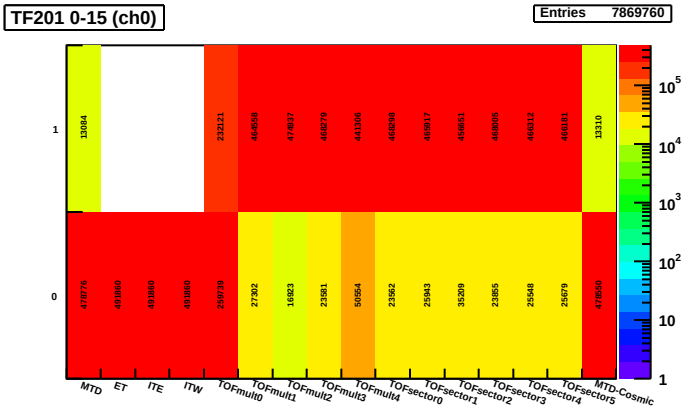

Input to FE001 QT board
Entries 1.573952e+07
ADC
channel


Input to FE002 QT board
Entries 1.573952e+07
ADC
channel

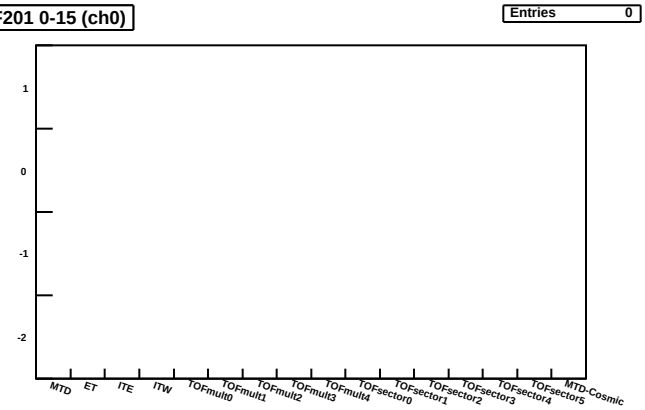

Input to FE003 QT board
Entries 1.573952e+07
ADC
channel


Input to FE004 QT board
Entries 1.573952e+07
ADC
channel

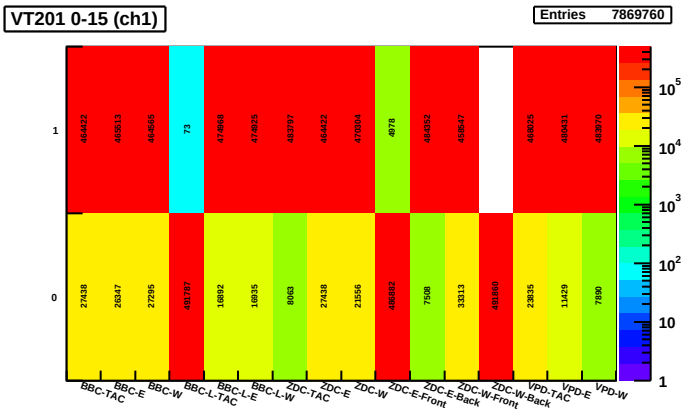
TF201 0-15 (ch0)



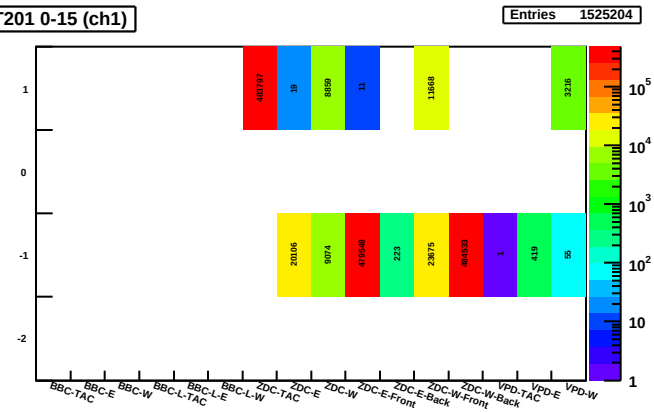
TF201 0-15 (ch0)



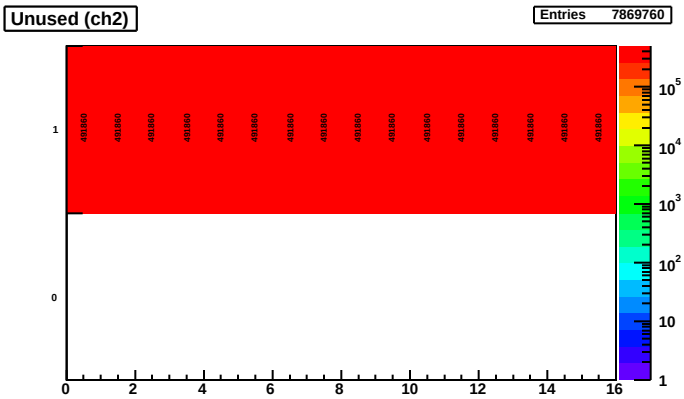
VT201 0-15 (ch1)

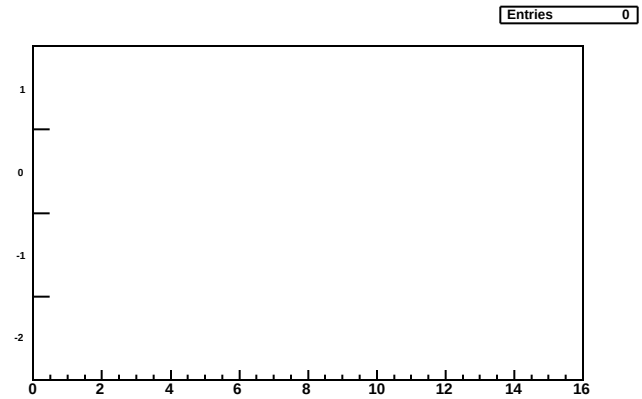


VT201 0-15 (ch1)

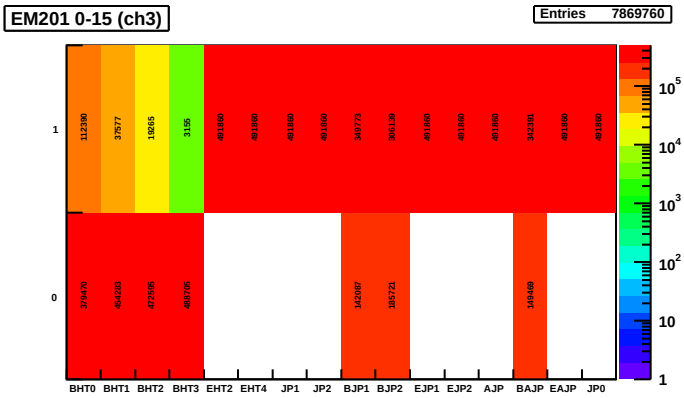


Unused (ch2)

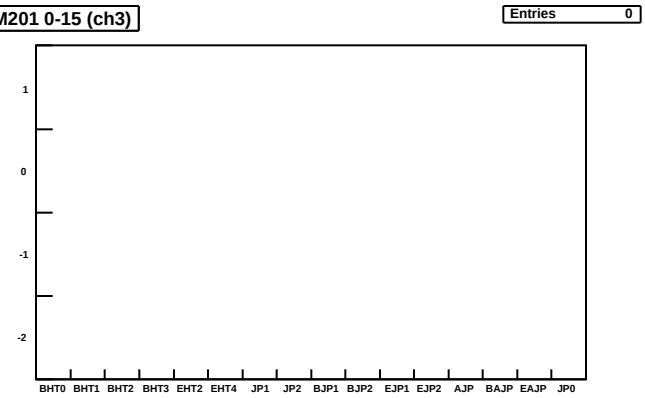




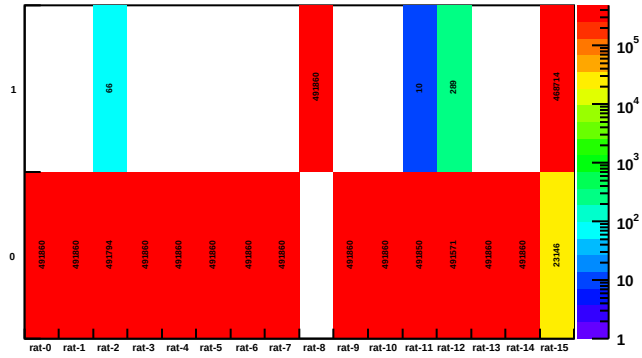
EM201 0-15 (ch3)

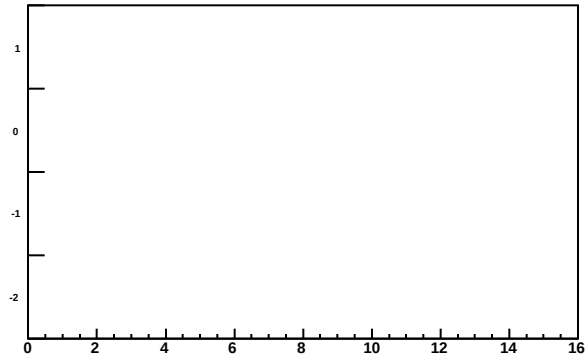


EM201 0-15 (ch3)

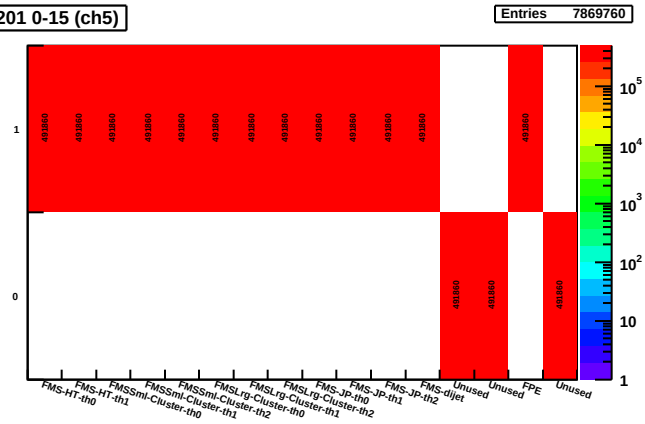


RAT board (ch4)

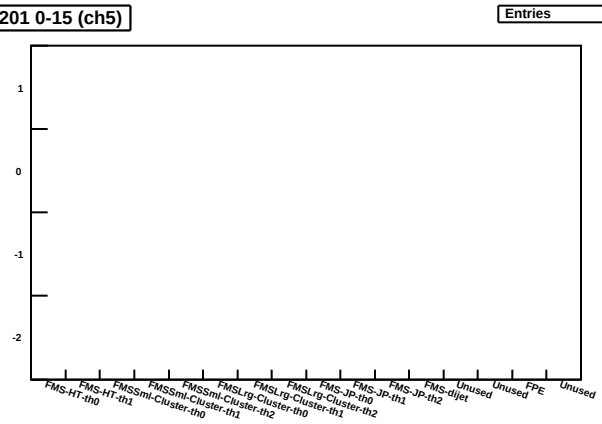




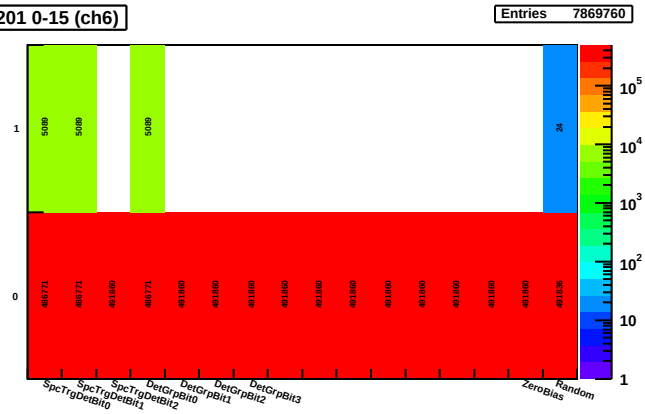
FP201 0-15 (ch5)

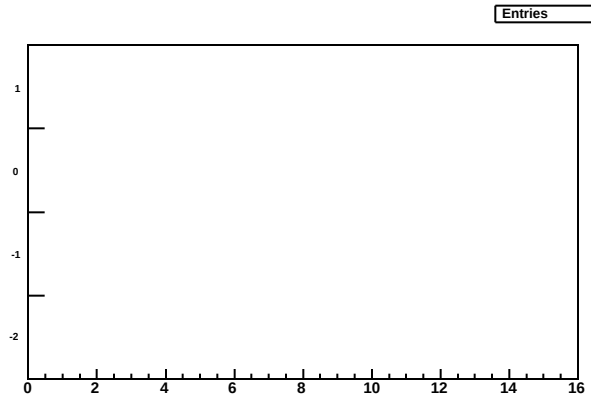
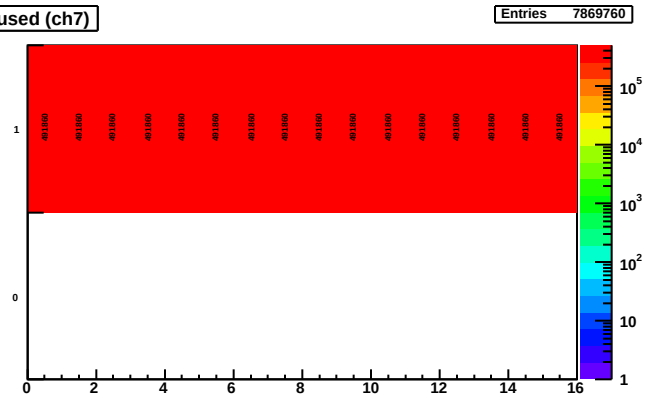
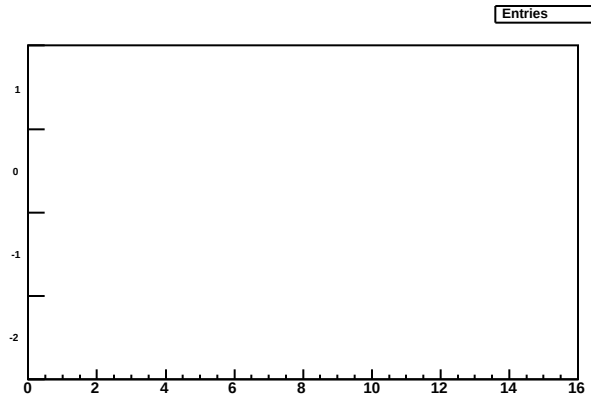


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)