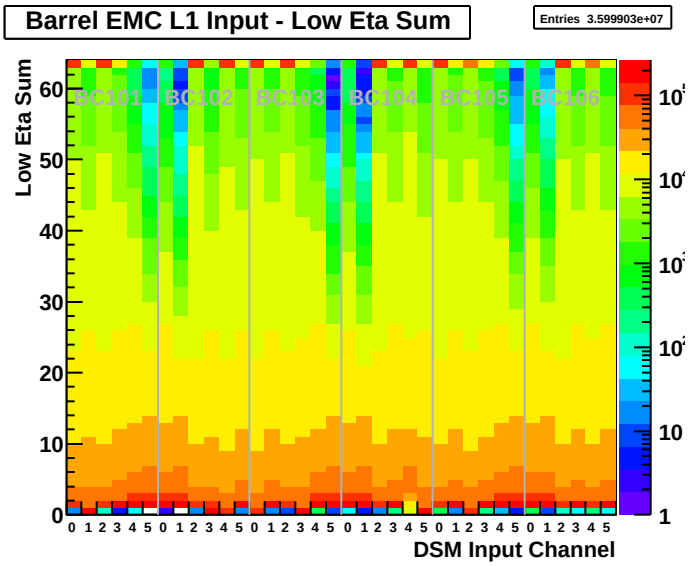
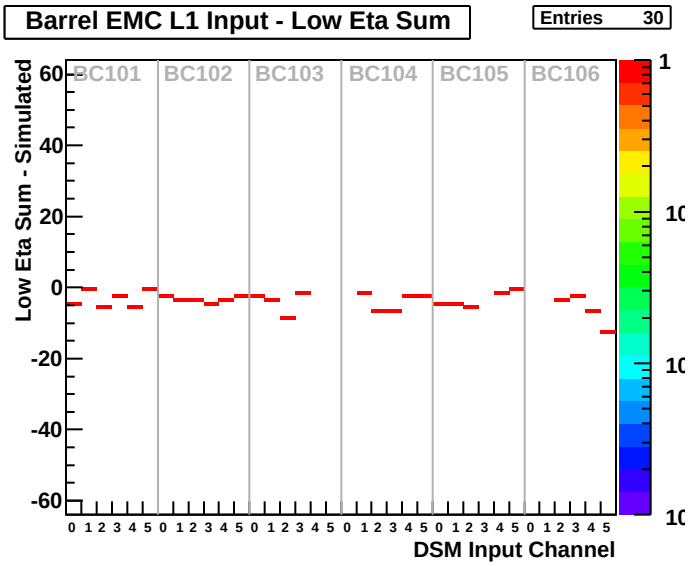
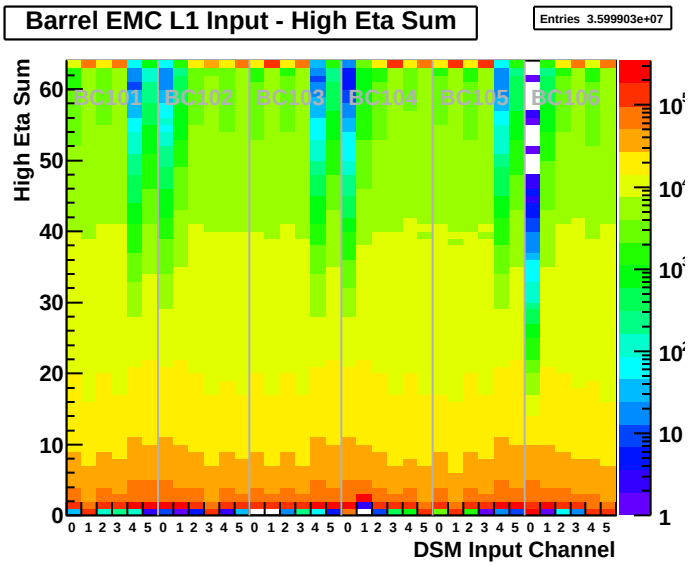
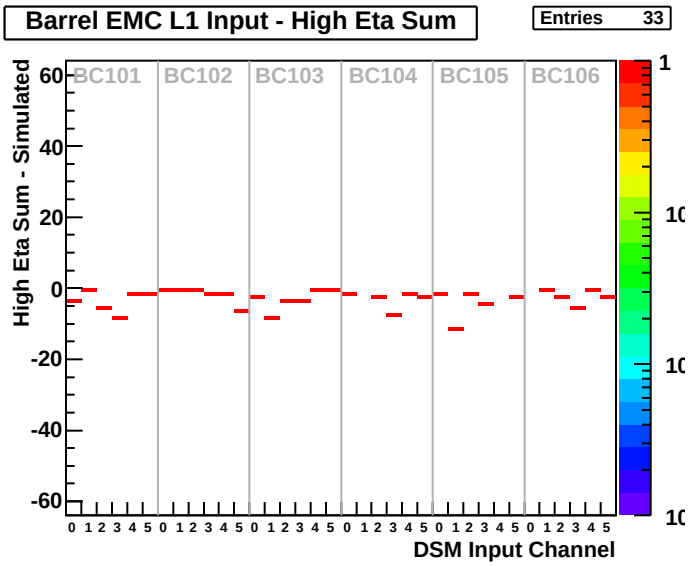
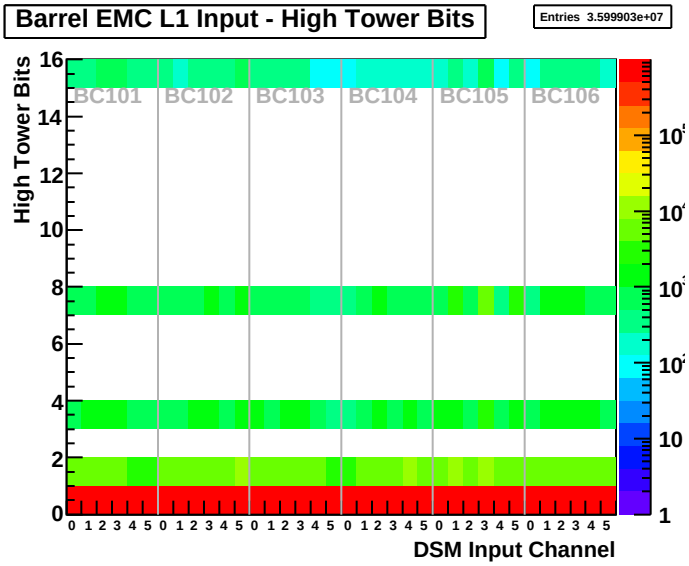
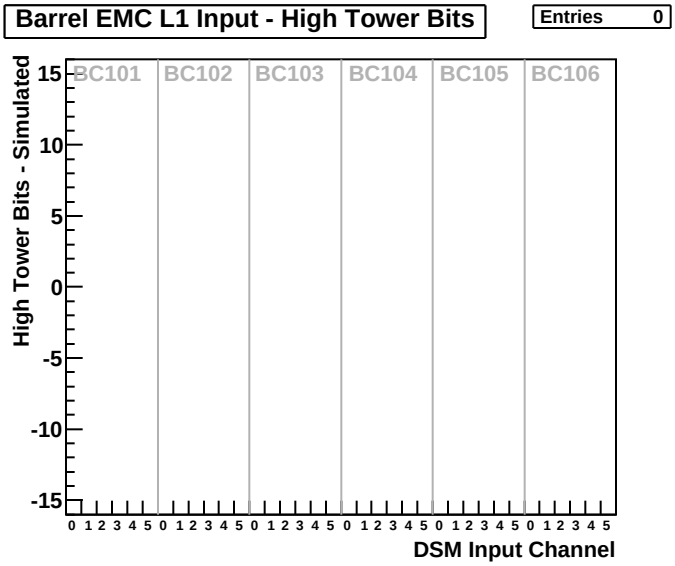

Barrel EMC L1 Input - Low Eta Sum
Entries 3.599903e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

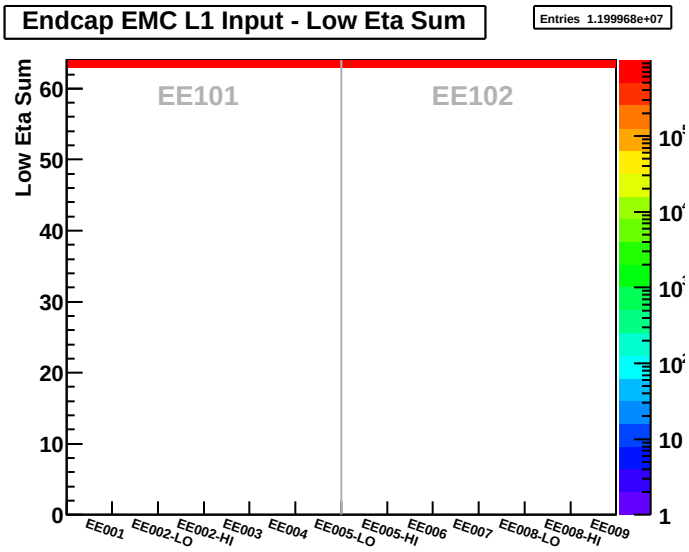

Barrel EMC L1 Input - Low Eta Sum
Entries 30
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

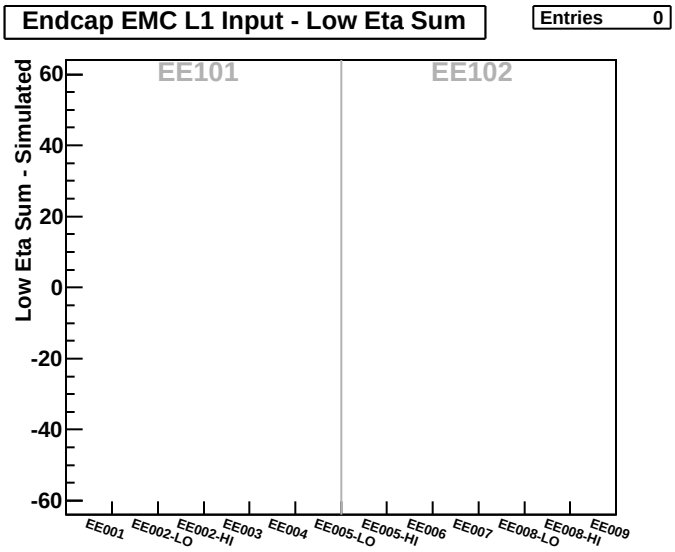

Barrel EMC L1 Input - High Eta Sum
Entries 3.599903e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

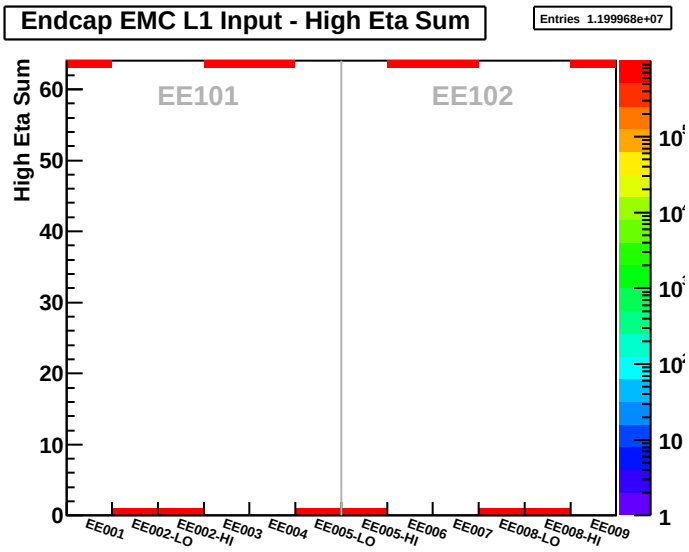

Barrel EMC L1 Input - High Eta Sum
Entries 33
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

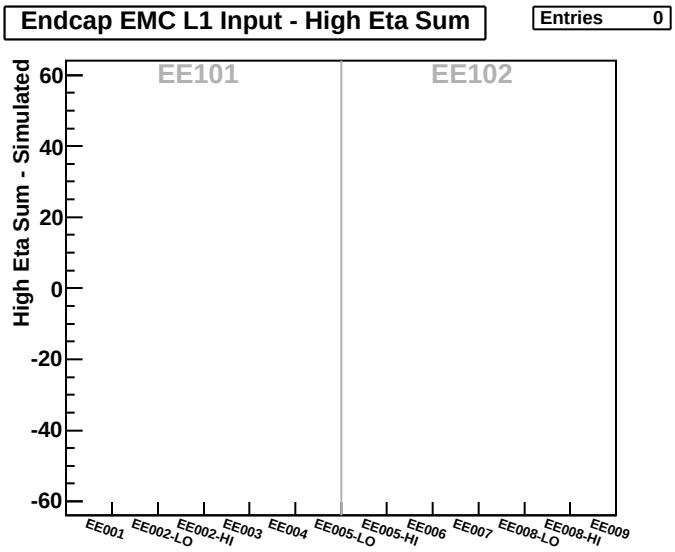

Barrel EMC L1 Input - High Tower Bits
Entries 3.599903e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

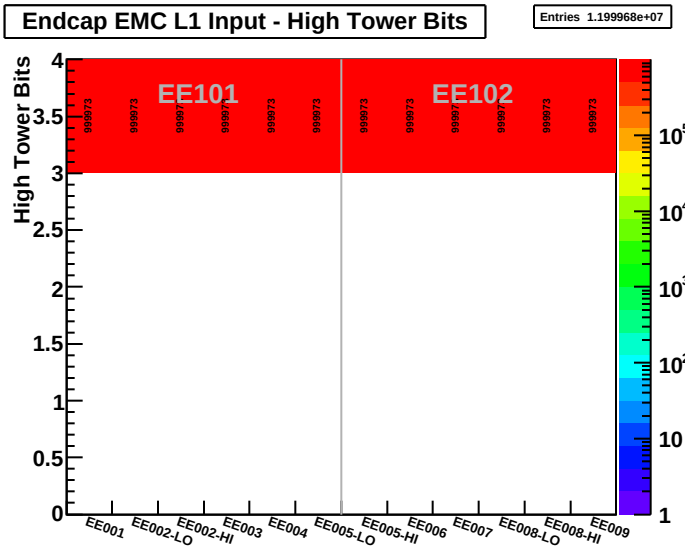

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

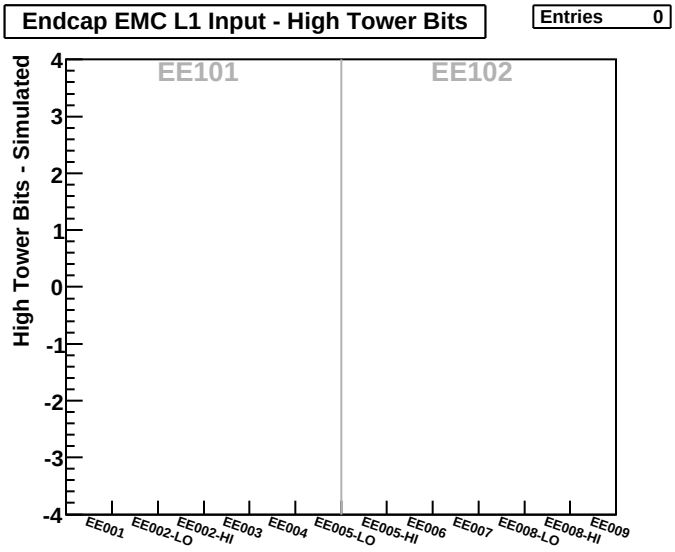

Endcap EMC L1 Input - Low Eta Sum
Entries 1.199968e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

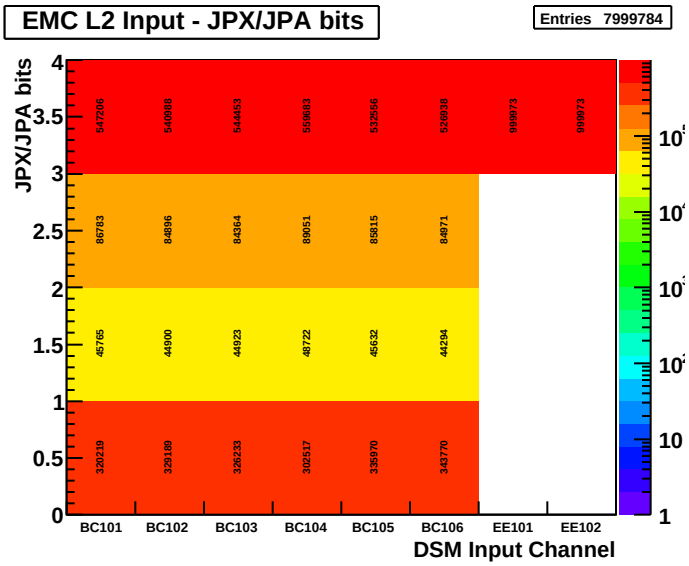

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

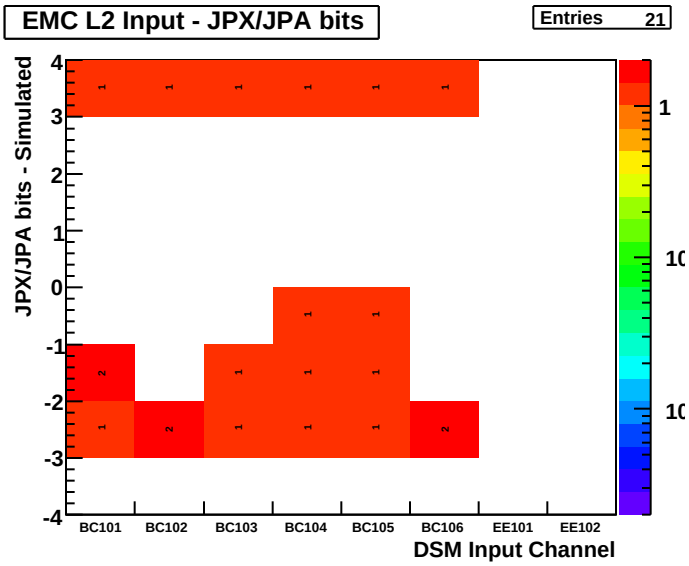

Endcap EMC L1 Input - High Eta Sum
Entries 1.199968e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

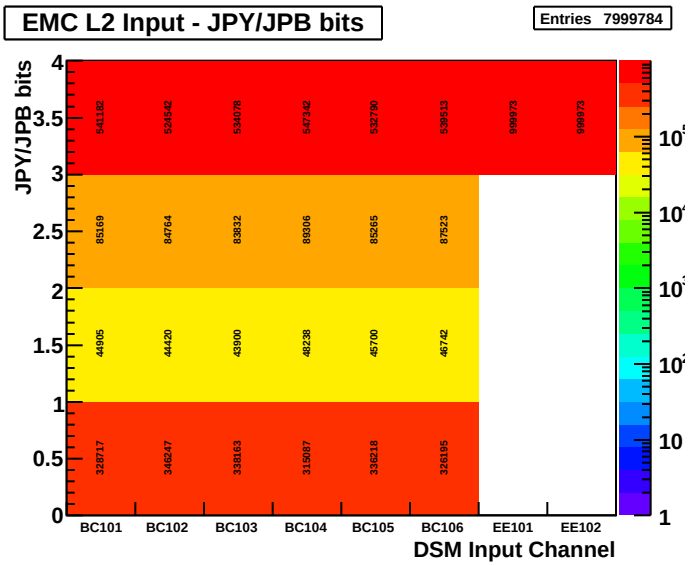

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

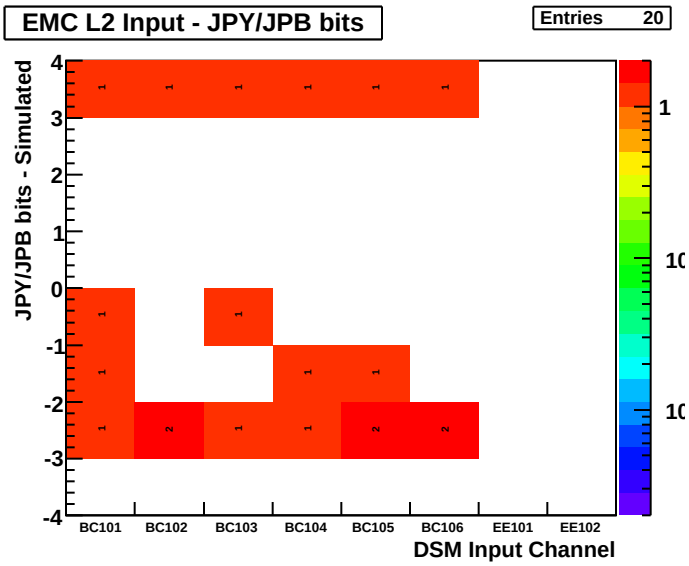

Endcap EMC L1 Input - High Tower Bits
Entries 1.199968e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

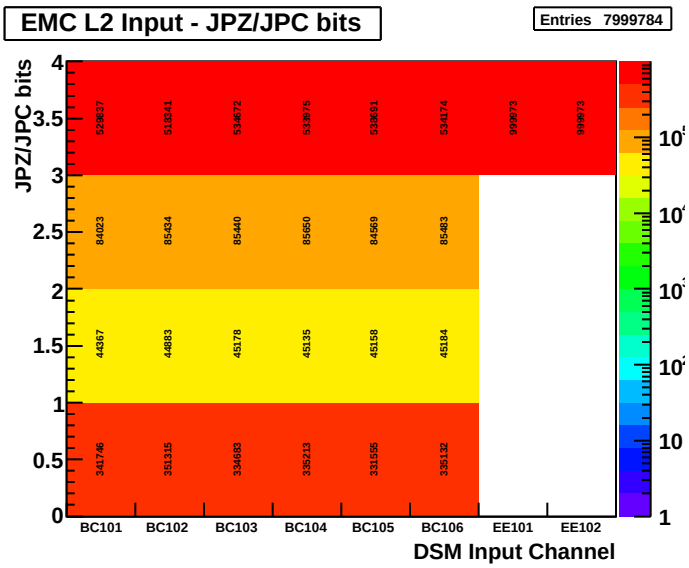

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

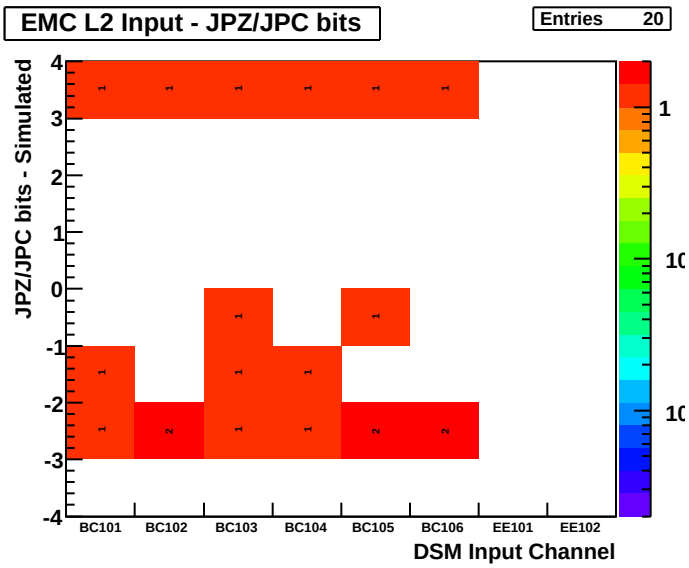

EMC L2 Input - JPX/JPA bits
Entries 7999784
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
547206 540988 544453 559683 532556 526938 999973 999973
86783 84896 84364 89051 85815 84971
45765 44900 44923 48722 45632 44294
320219 329189 326233 302517 335970 343770

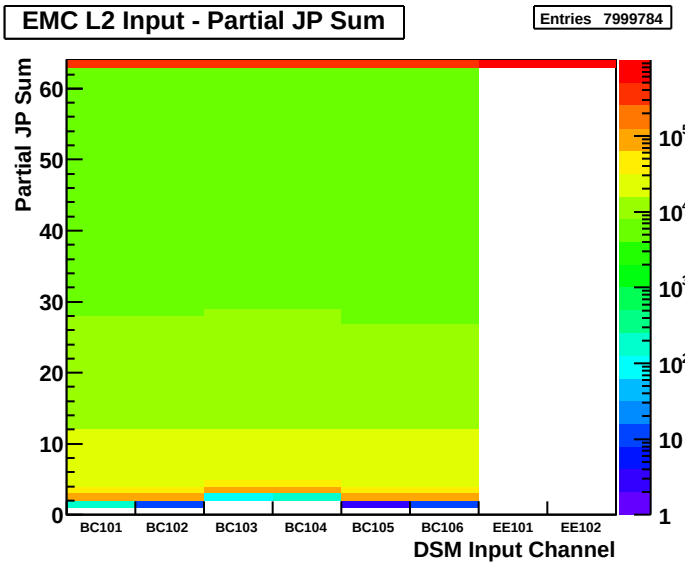

EMC L2 Input - JPX/JPA bits
Entries 21
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

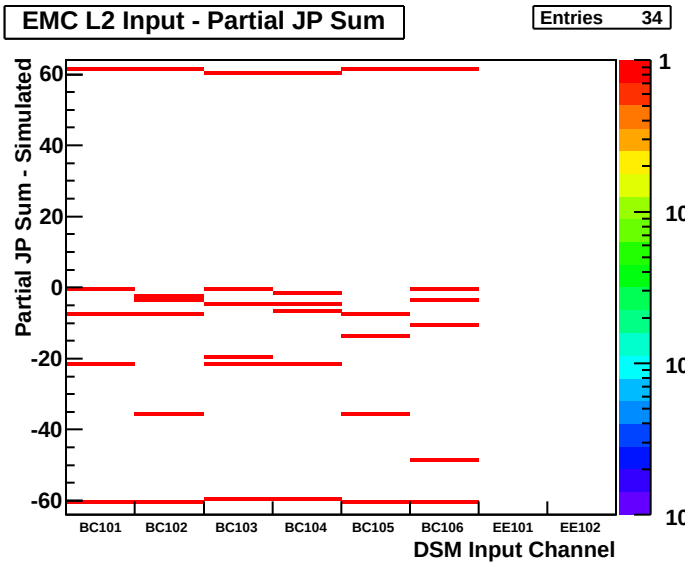

EMC L2 Input - JPY/JPB bits
Entries 7999784
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
541182 524542 534078 547342 532790 539513 999973 999973
85169 84764 83832 89306 85265 87523
44905 44420 43900 48238 45700 46742
328717 346247 338163 315087 338218 326195

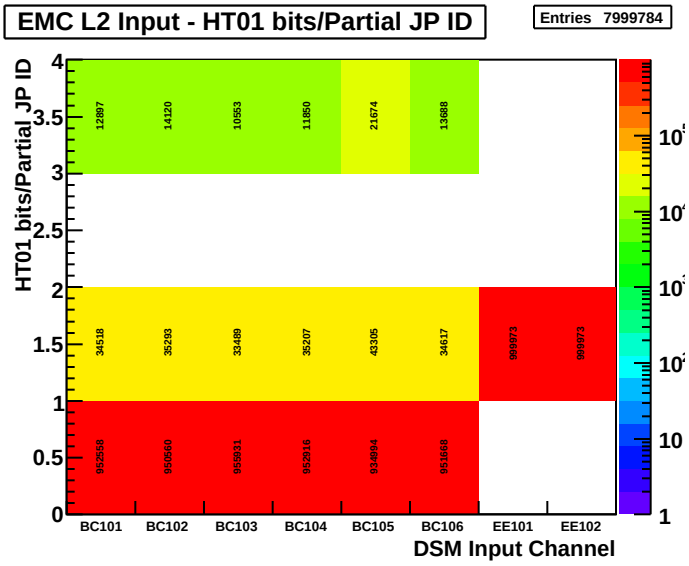

EMC L2 Input - JPY/JPB bits
Entries 20
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

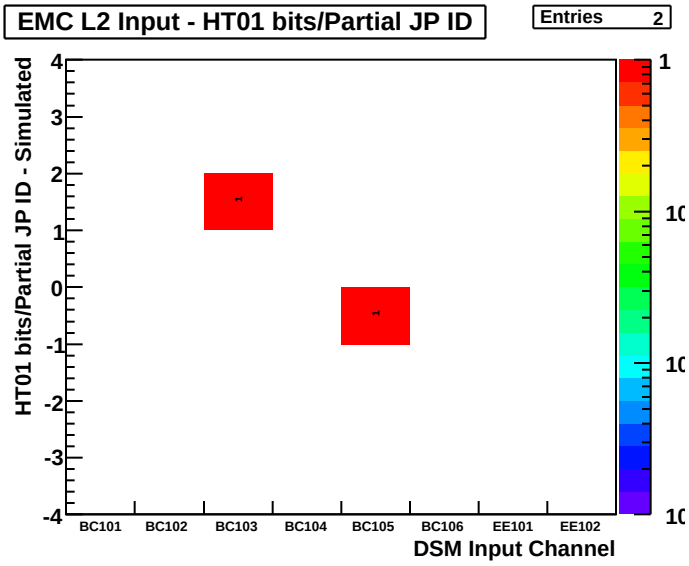

EMC L2 Input - JPZ/JPC bits
Entries 7999784
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
528937 518341 534672 533975 539891 534174 999973 999973
84023 85434 85440 85650 84569 85483
44667 44883 45178 45135 45158 45184
341746 351315 334683 335213 331556 335132

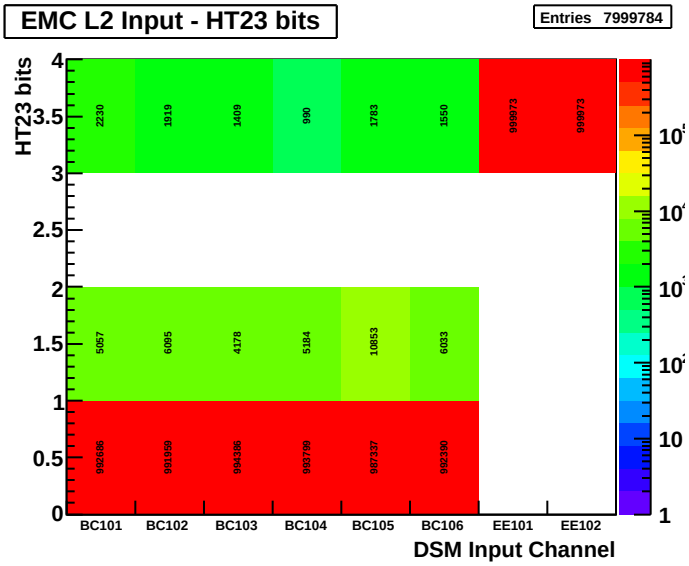

EMC L2 Input - JPZ/JPC bits
Entries 20
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

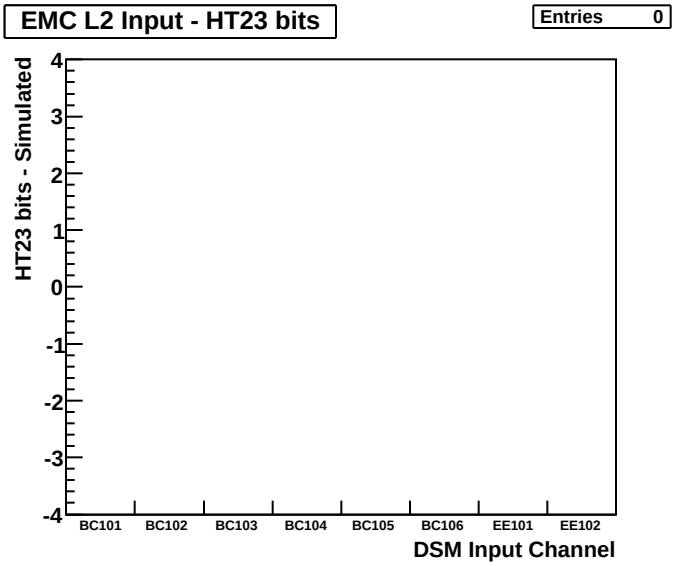

EMC L2 Input - Partial JP Sum
Entries 7999784
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

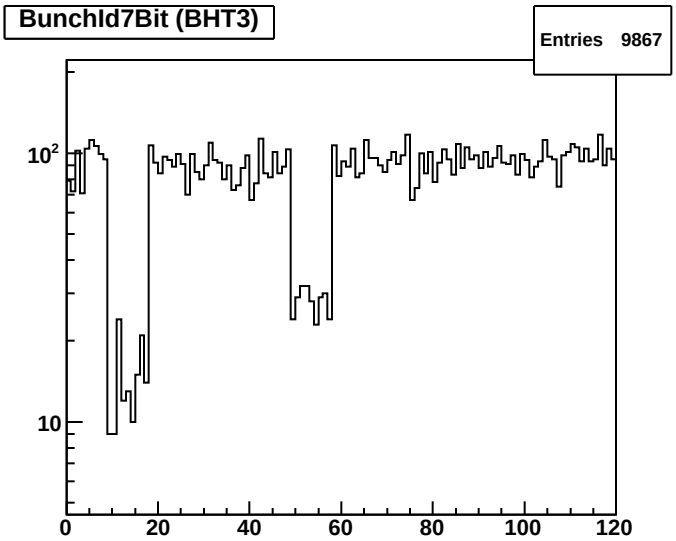

EMC L2 Input - Partial JP Sum
Entries 34
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

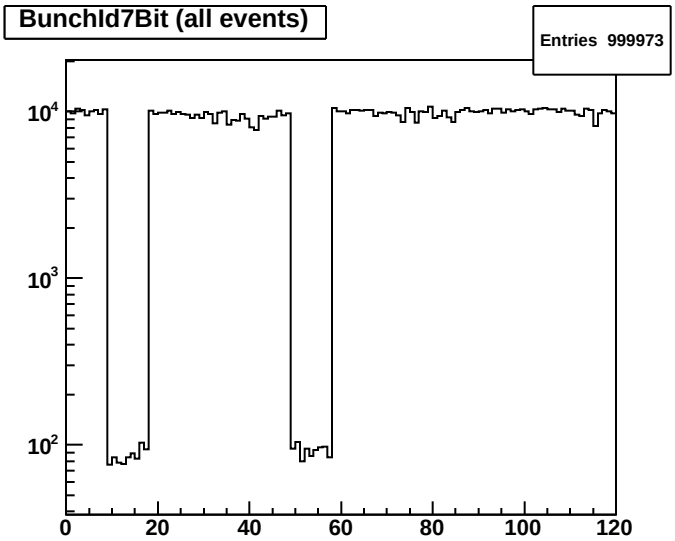

EMC L2 Input - HT01 bits/Partial JP ID
Entries 7999784
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

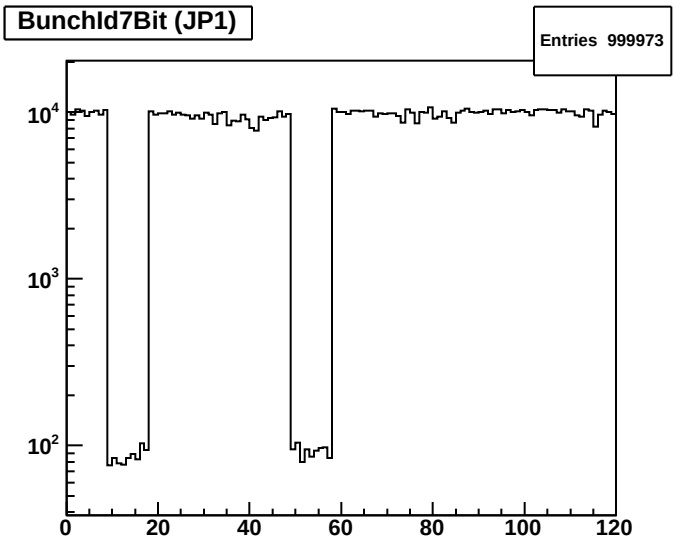

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

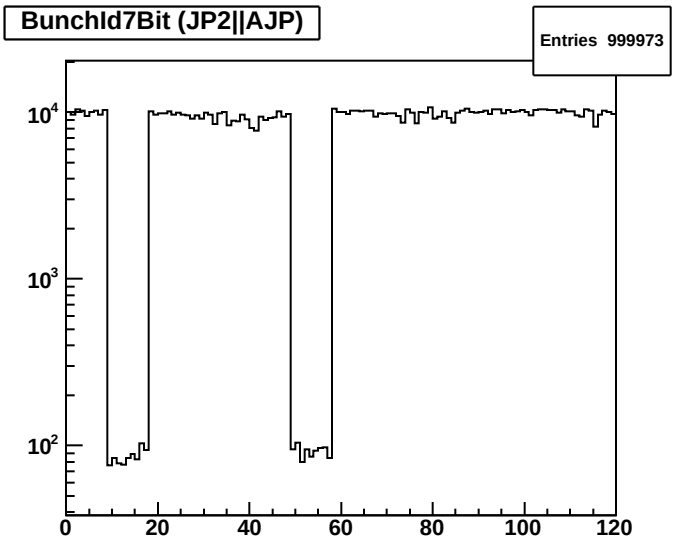

EMC L2 Input - HT23 bits
Entries 7999784
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

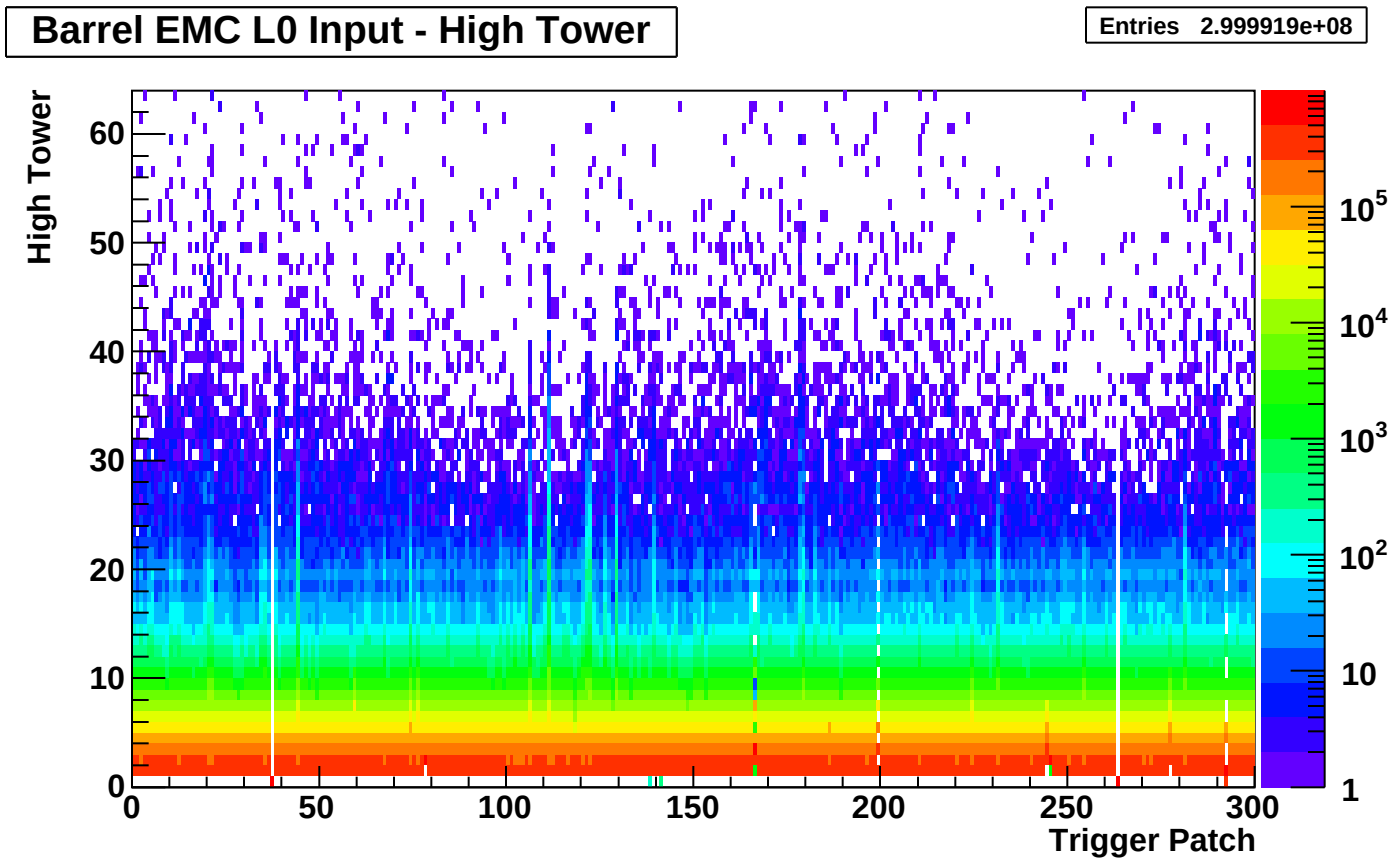

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

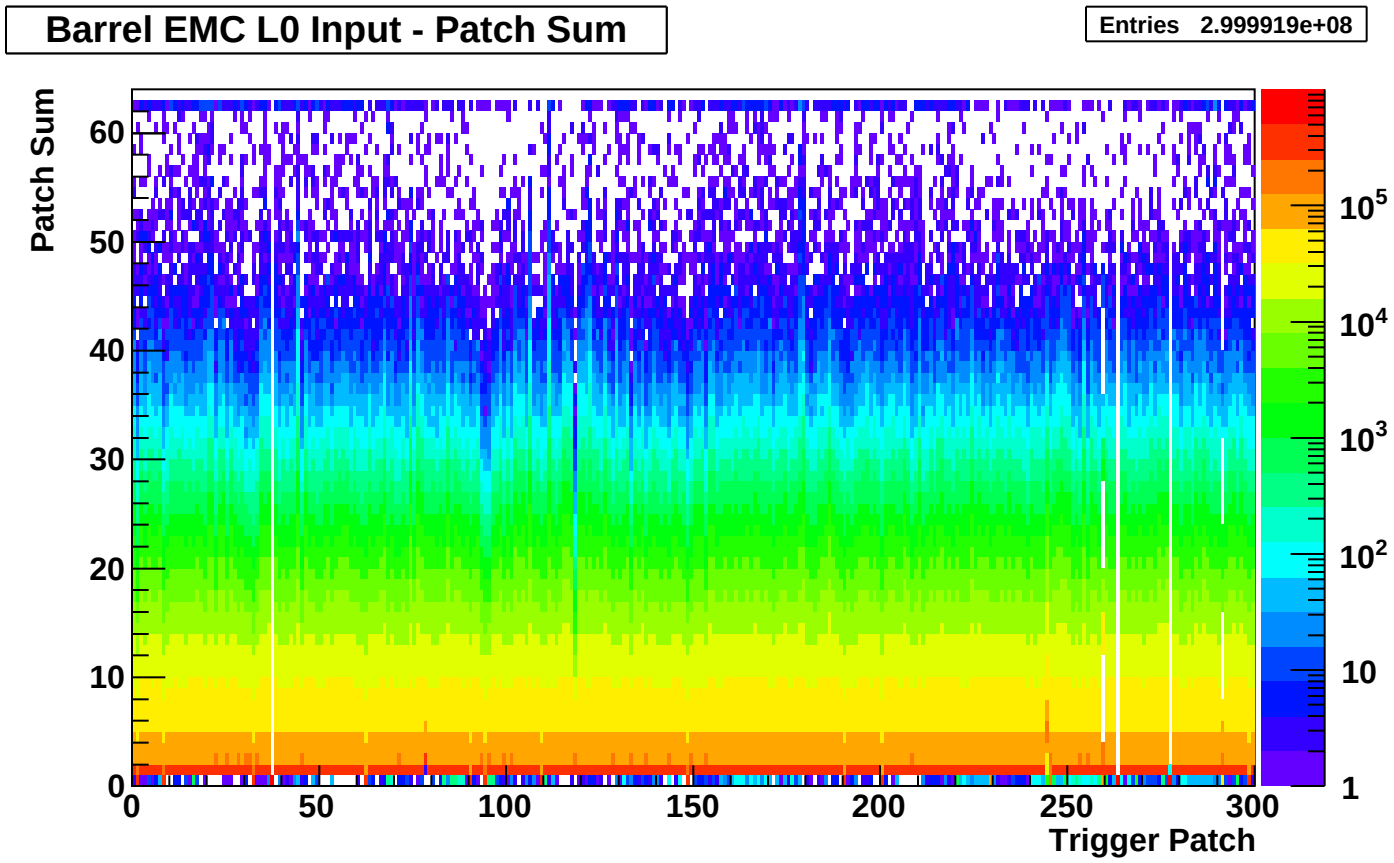

BunchId7Bit (BHT3)
Entries 9867
10^2
10
0
20
40
60
80
100
120

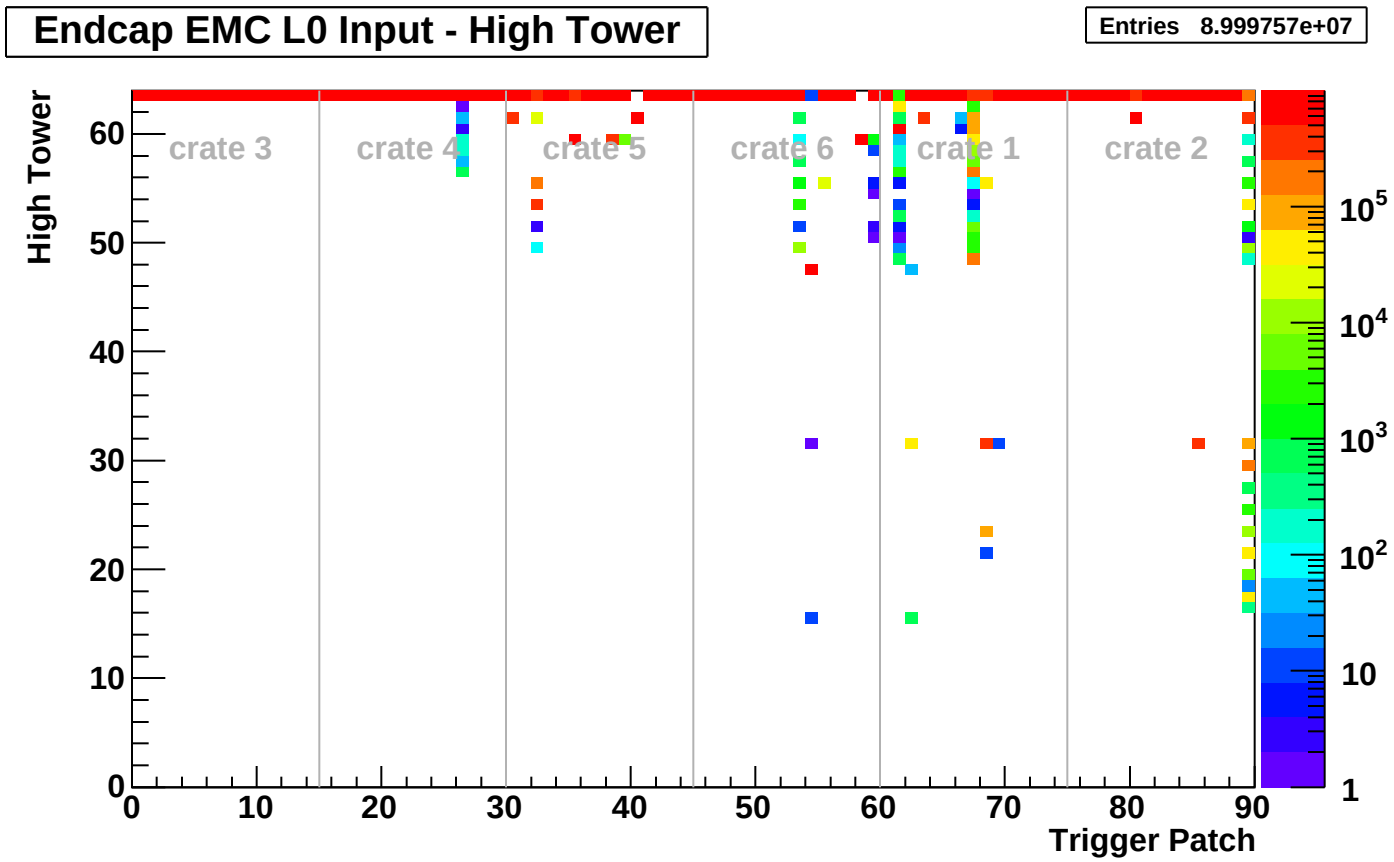

BunchId7Bit (all events)
Entries 999973
10^4
10^3
10^2
0
20
40
60
80
100
120

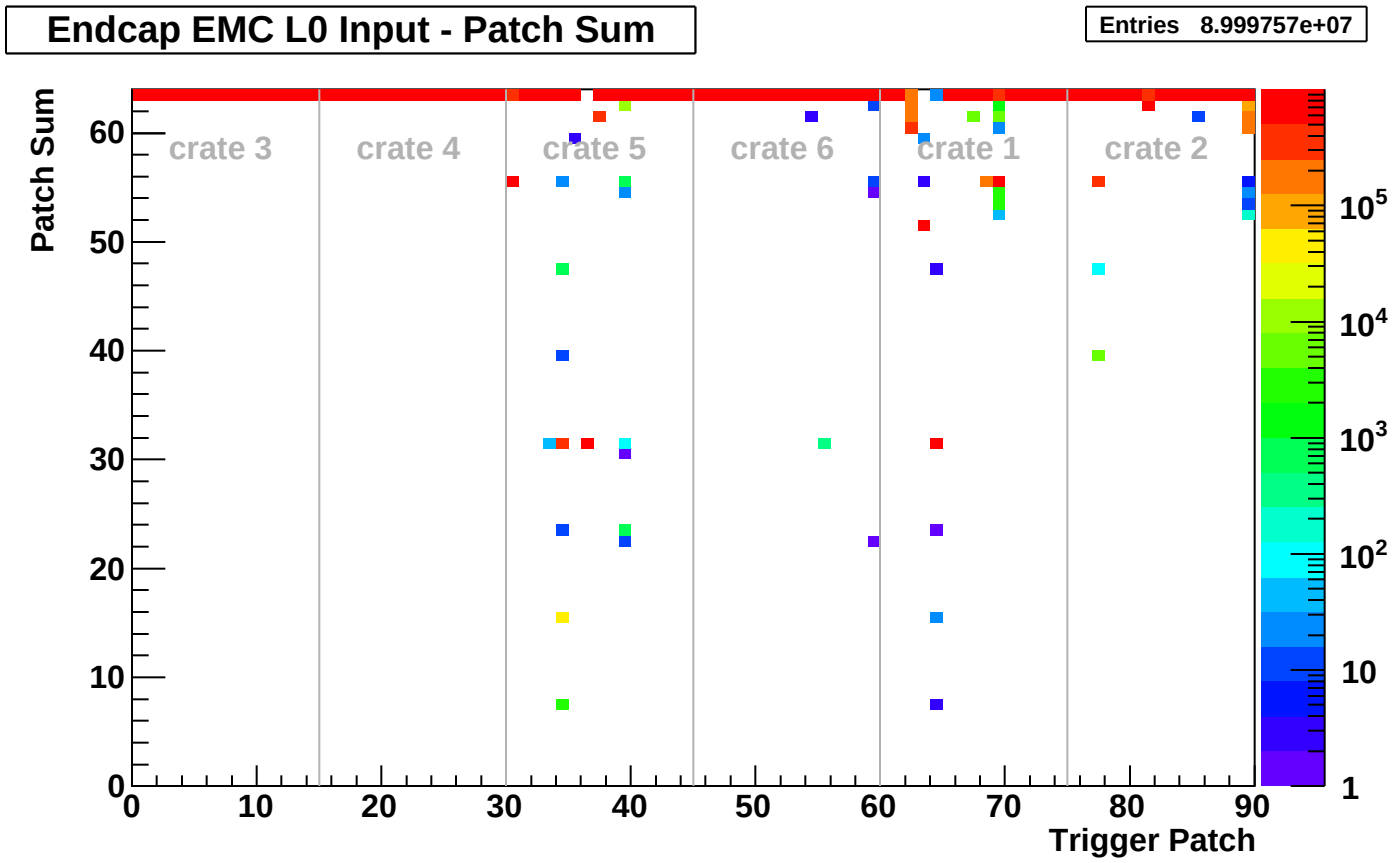

BunchId7Bit (JP1)
Entries 999973
10^4
10^3
10^2
0
20
40
60
80
100
120

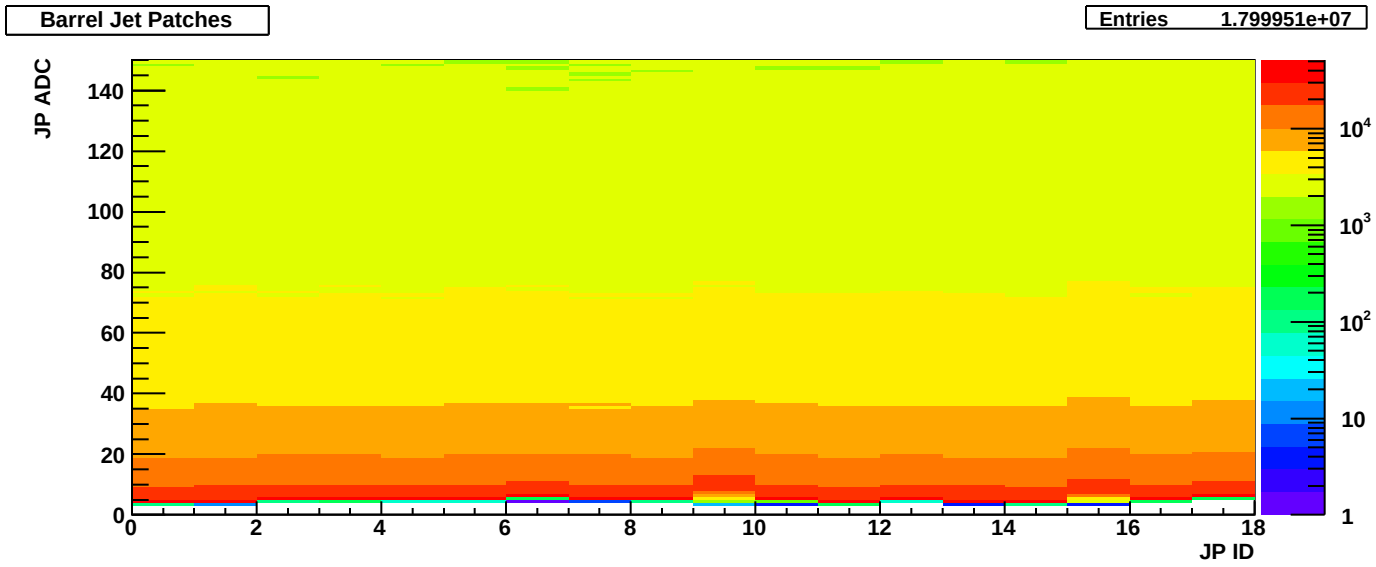

BunchId7Bit (JP2||AJP)
Entries 999973
10^4
10^3
10^2
0
20
40
60
80
100
120

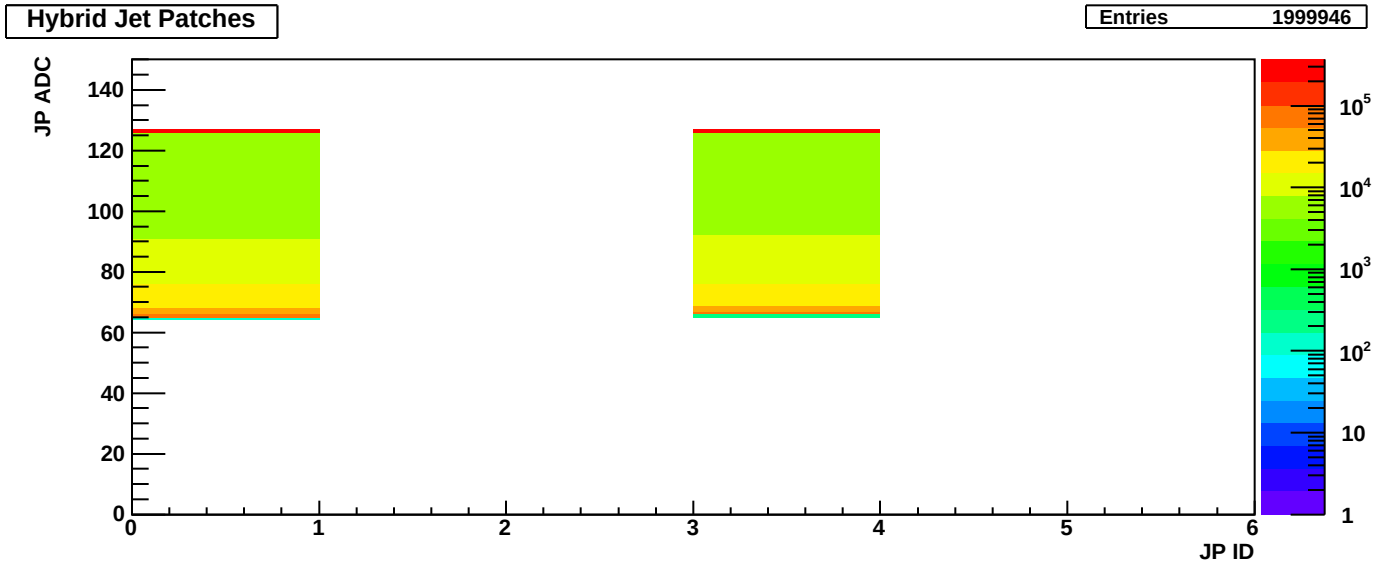

Barrel EMC L0 Input - High Tower
Entries 2.999919e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

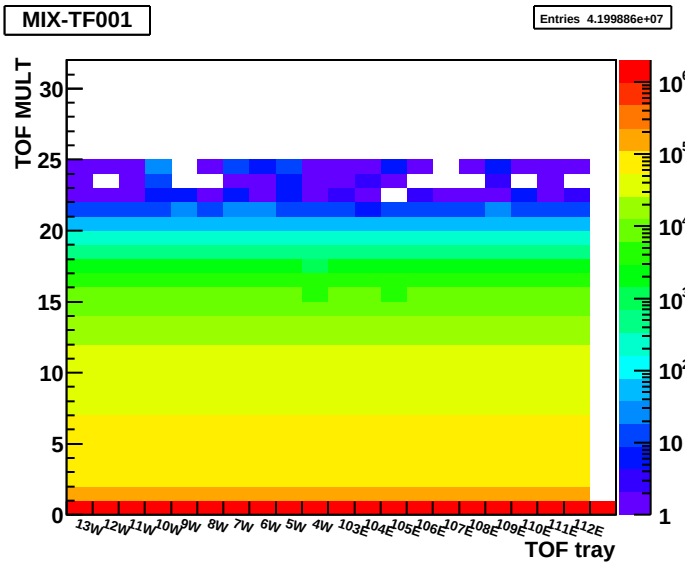

Barrel EMC L0 Input - Patch Sum
Entries 2.999919e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

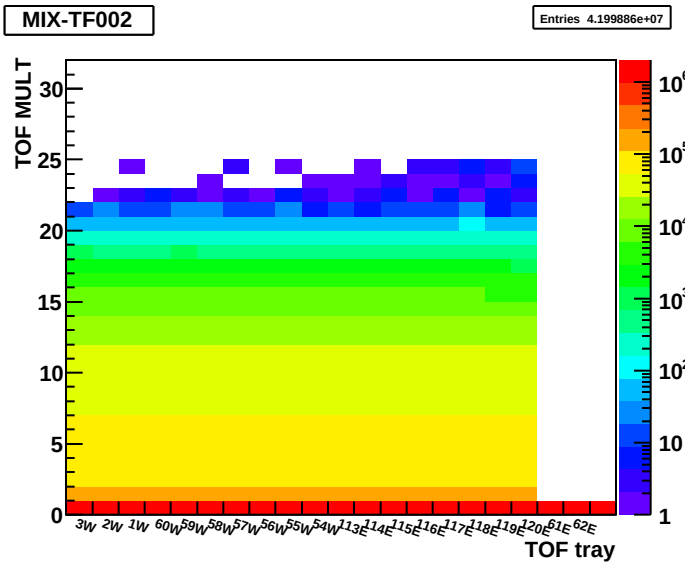

Endcap EMC L0 Input - High Tower
Entries 8.999757e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

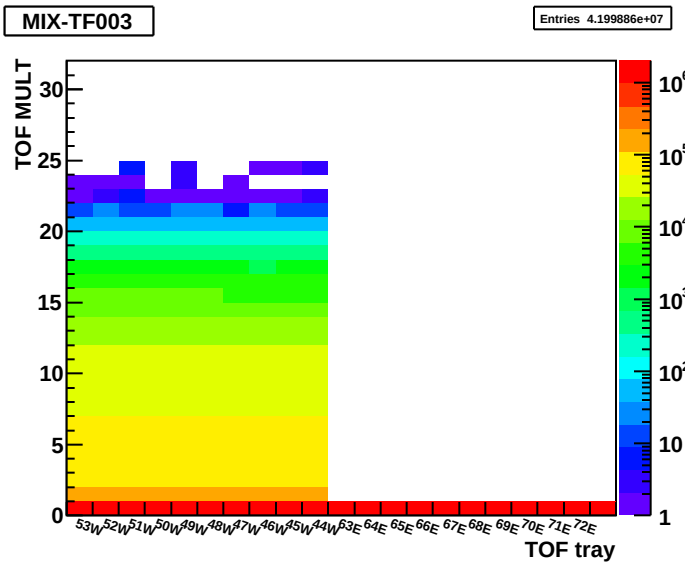

Endcap EMC L0 Input - Patch Sum
Entries 8.999757e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

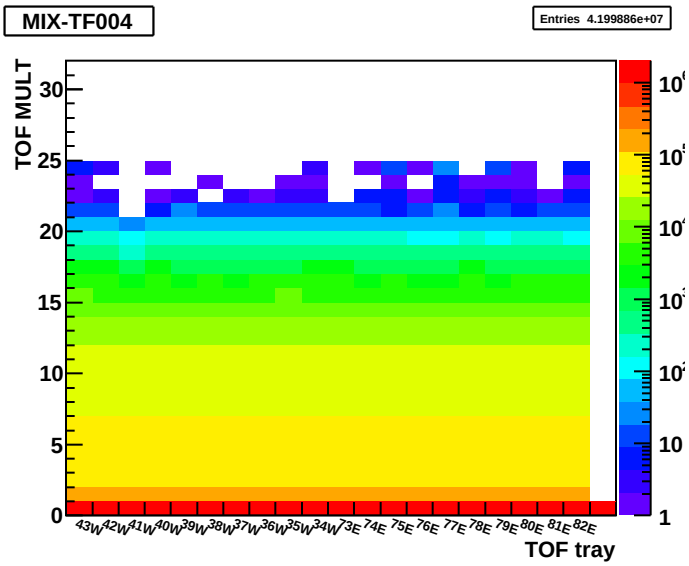

Barrel Jet Patches
Entries 1.799951e+07
JP ADC
JP ID

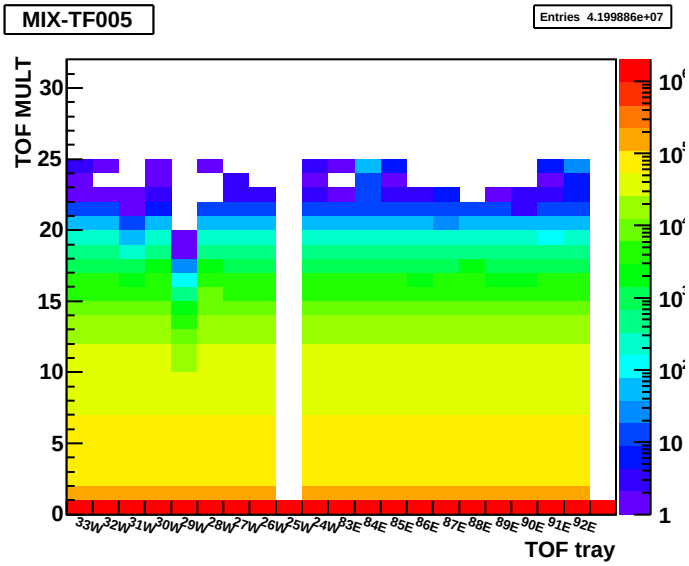

Hybrid Jet Patches
Entries 1999946
JP ADC
JP ID

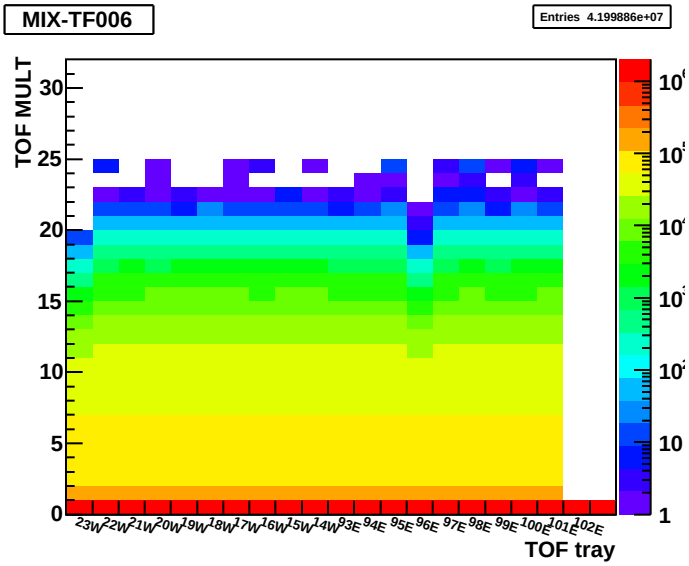

MIX-TF001
Entries 4.199886e+07
TOF MULT
TOF tray

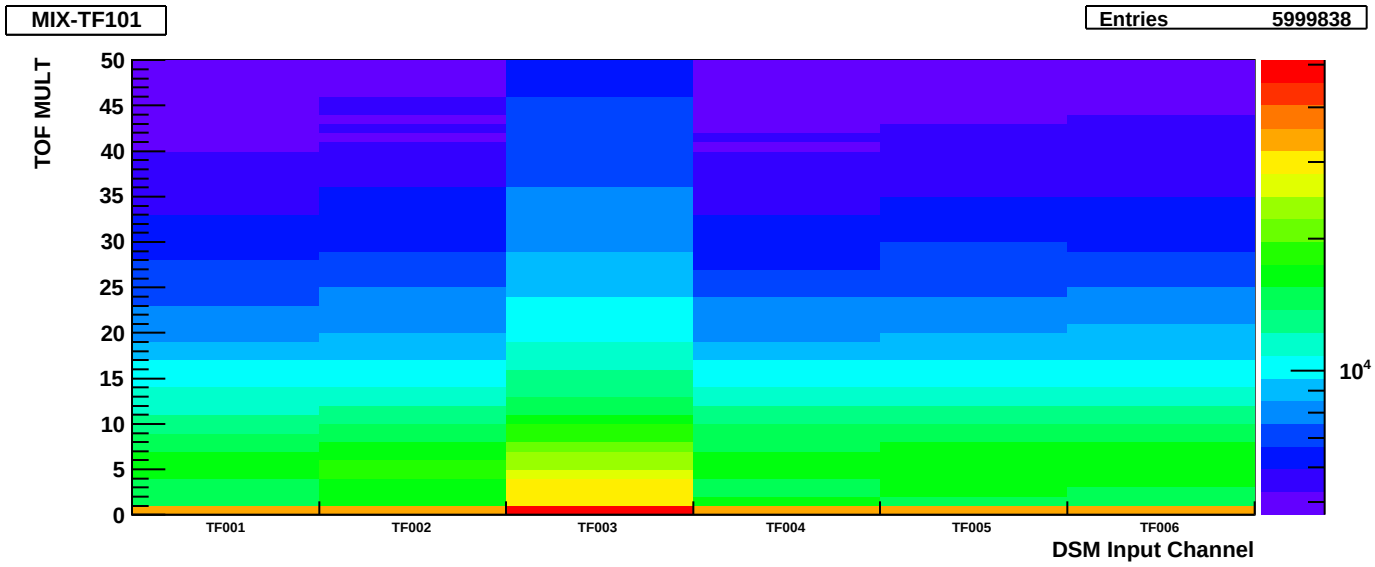

MIX-TF002
Entries 4.199886e+07
TOF MULT
TOF tray

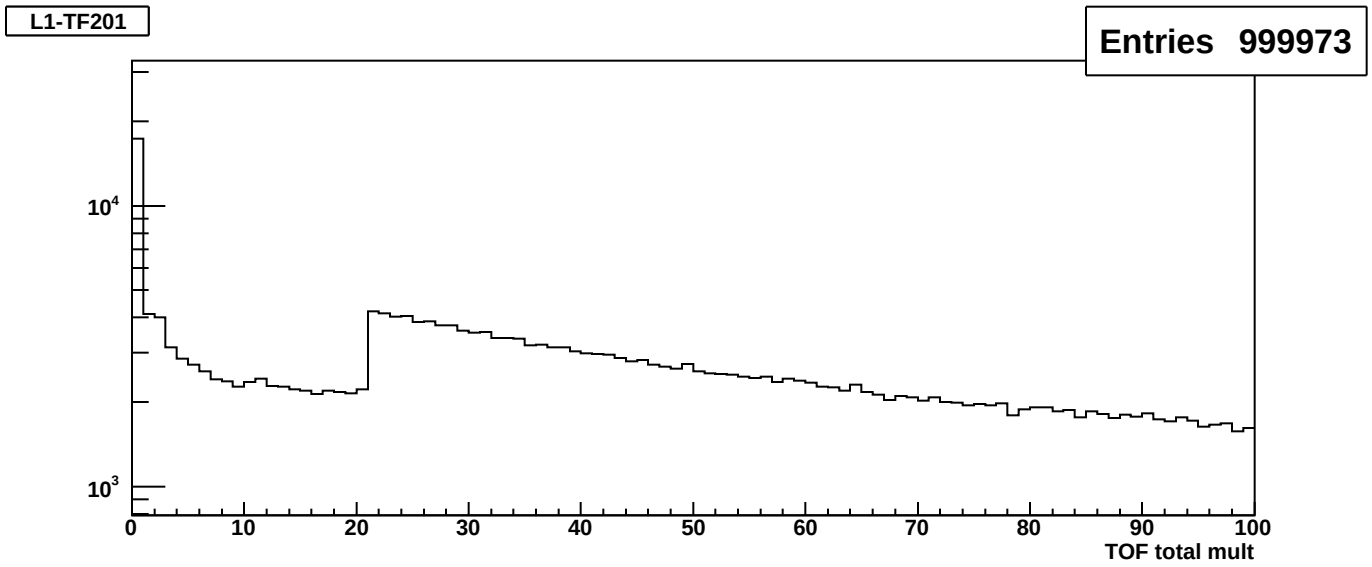

MIX-TF003
Entries 4.199886e+07
TOF MULT
TOF tray

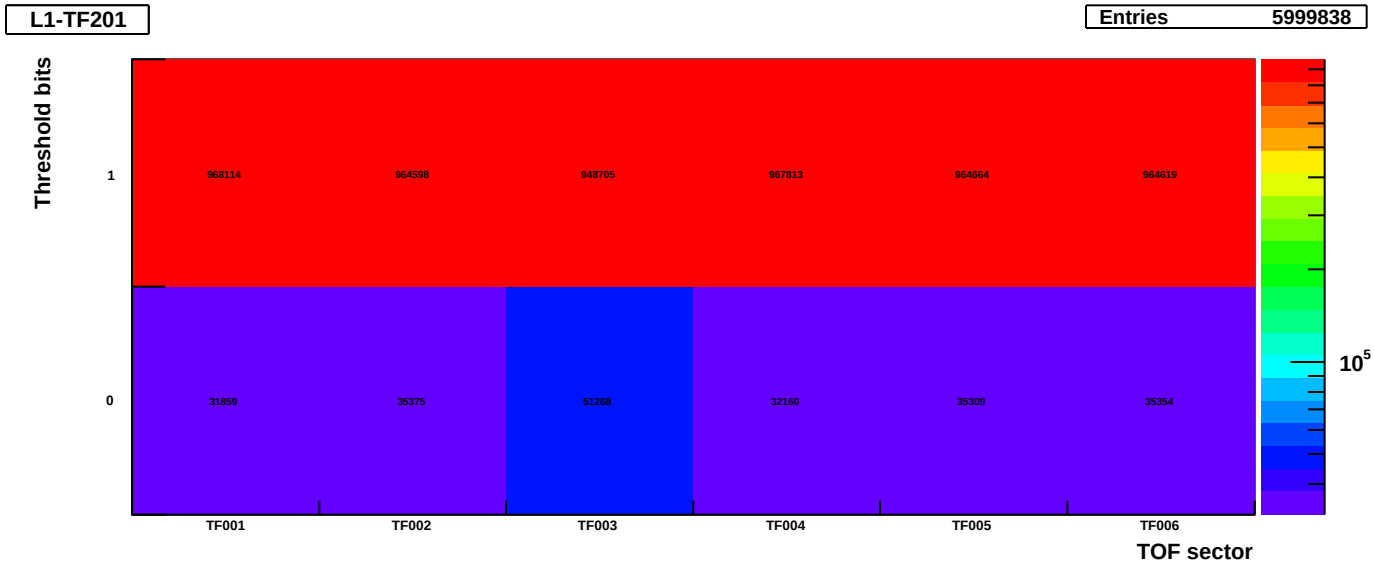

MIX-TF004
Entries 4.199886e+07
TOF MULT
TOF tray

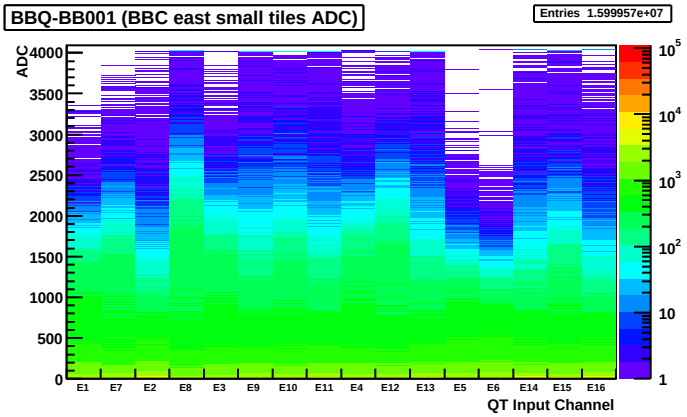

MIX-TF005
Entries 4.199886e+07
TOF MULT
TOF tray

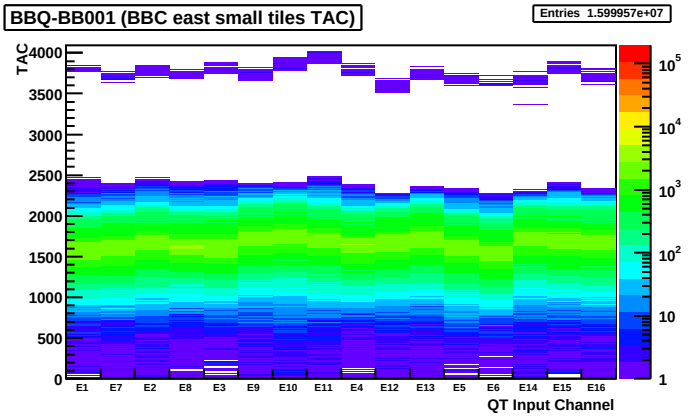

MIX-TF006
Entries 4.199886e+07
TOF MULT
TOF tray

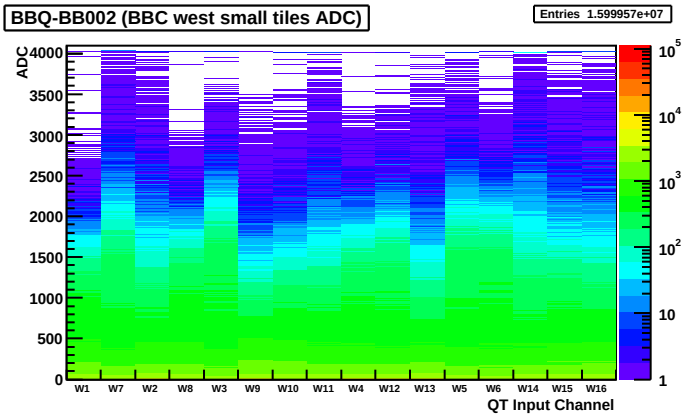

MIX-TF101
Entries 5999838
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4

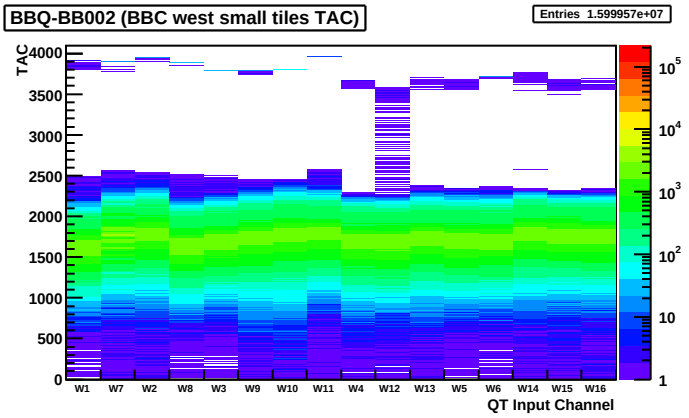

L1-TF201
Entries 999973
10^4
10^3
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

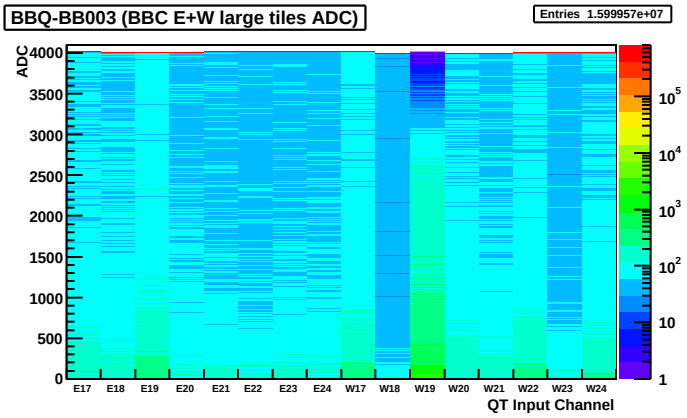

L1-TF201
Entries 5999838
Threshold bits
1
0
968114
964598
948705
967813
964664
964619
31859
35375
51268
32160
35309
35354
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

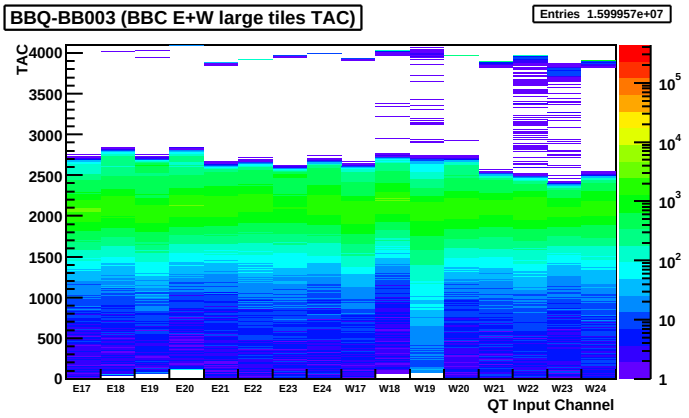

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.599957e+07
ADC
QT Input Channel

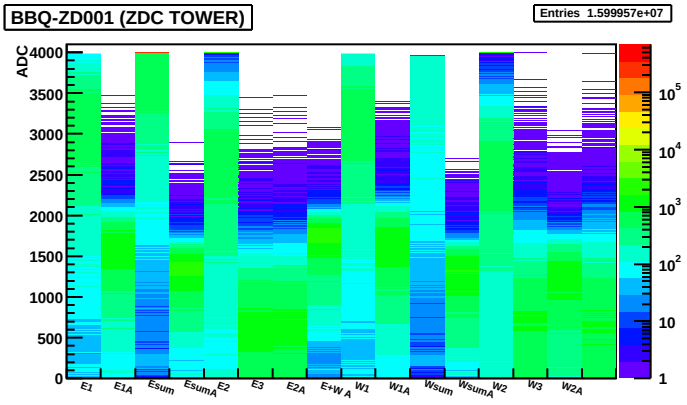

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.599957e+07
TAC
QT Input Channel

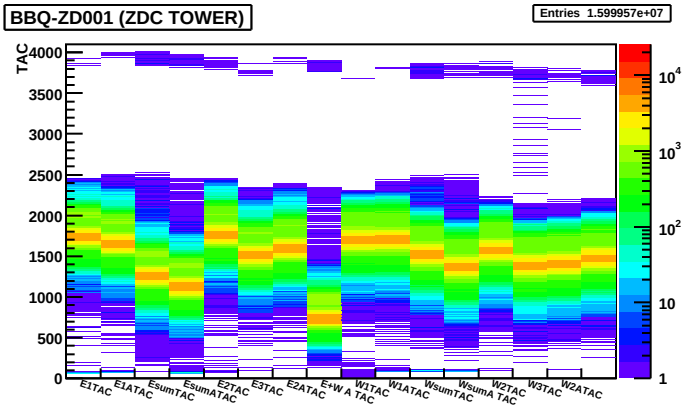

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.599957e+07
ADC
QT Input Channel

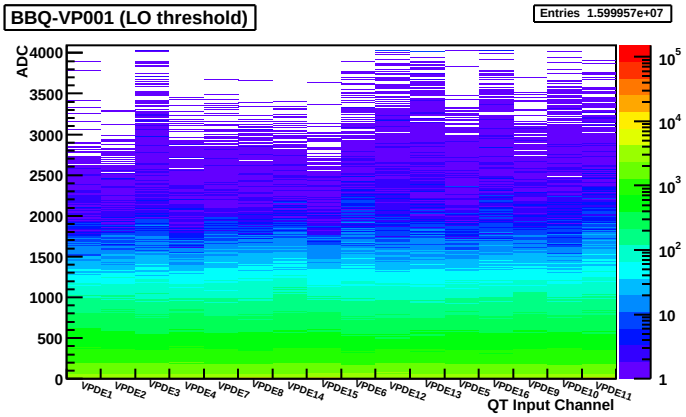

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.599957e+07
TAC
QT Input Channel

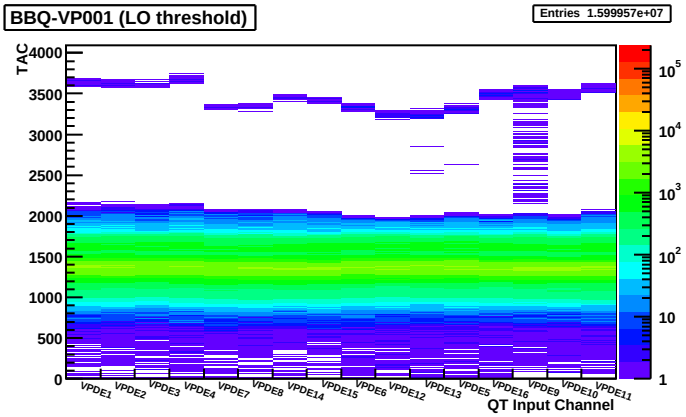

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.599957e+07
ADC
QT Input Channel

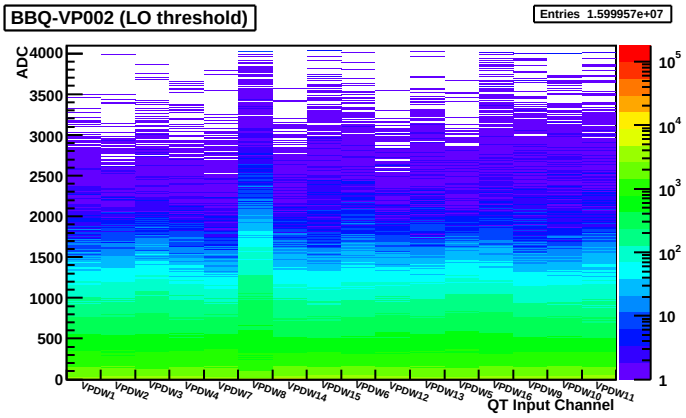

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.599957e+07
TAC
QT Input Channel

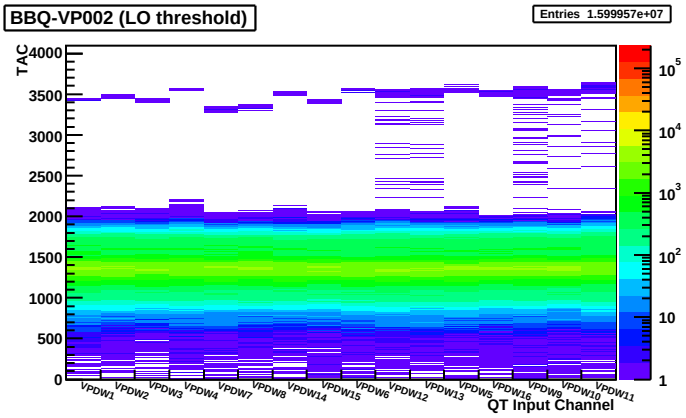

BBQ-ZD001 (ZDC TOWER)
Entries 1.599957e+07
ADC

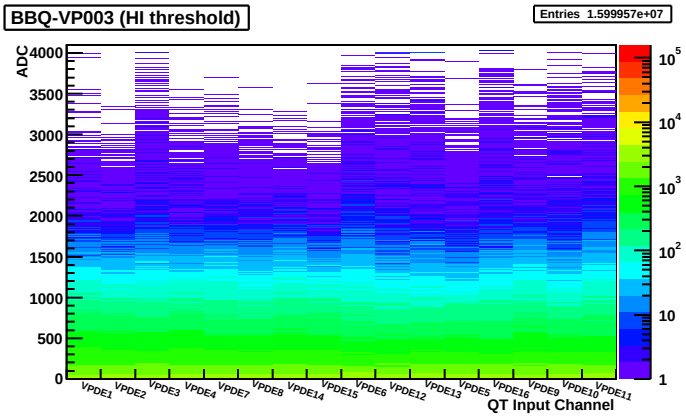

BBQ-ZD001 (ZDC TOWER)
Entries 1.599957e+07
TAC

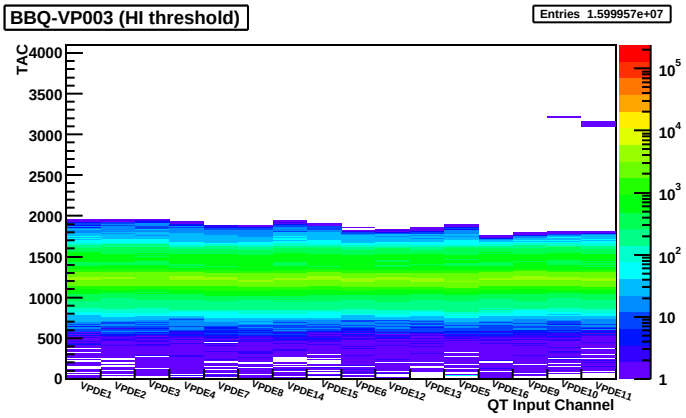

BBQ-VP001 (LO threshold)
Entries 1.599957e+07
ADC
QT Input Channel

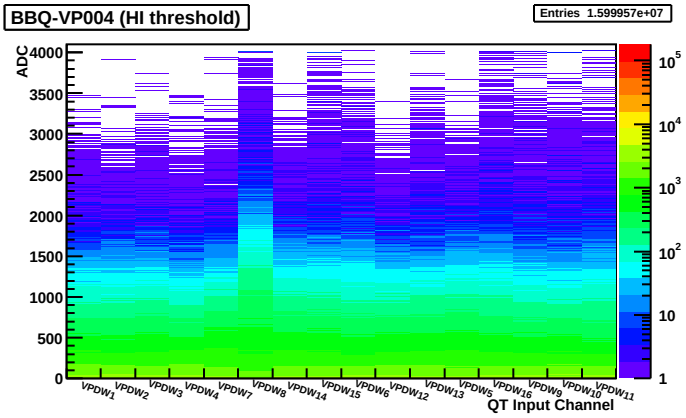

BBQ-VP001 (LO threshold)
Entries 1.599957e+07
TAC
QT Input Channel

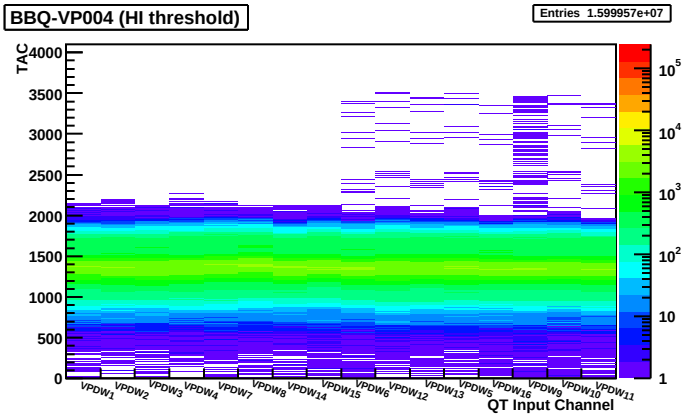

BBQ-VP002 (LO threshold)
Entries 1.599957e+07
ADC
QT Input Channel

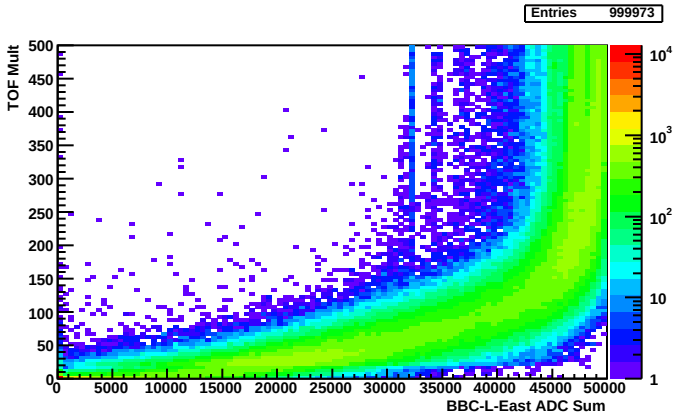

BBQ-VP002 (LO threshold)
Entries 1.599957e+07
TAC
QT Input Channel

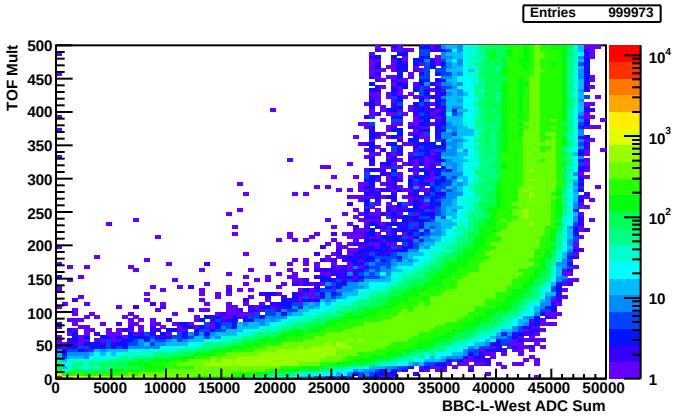

BBQ-VP003 (HI threshold)
Entries 1.599957e+07
ADC
QT Input Channel

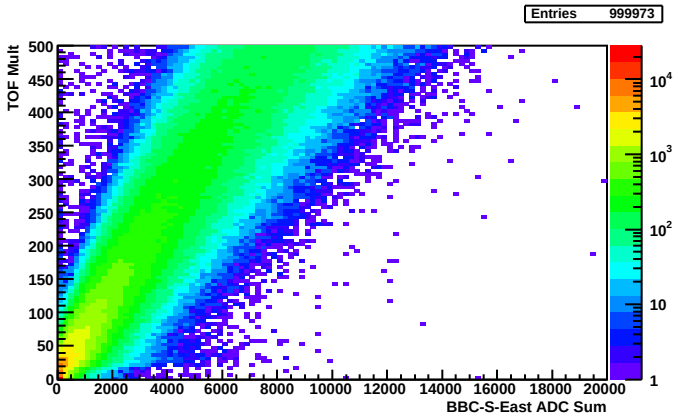

BBQ-VP003 (HI threshold)
Entries 1.599957e+07
TAC
QT Input Channel

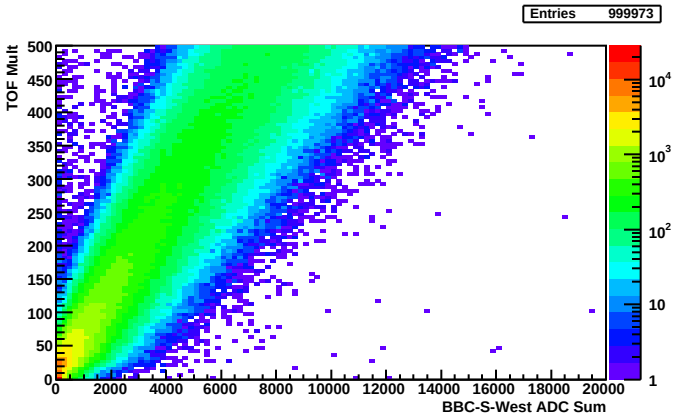

BBQ-VP004 (HI threshold)
Entries 1.599957e+07
ADC
QT Input Channel

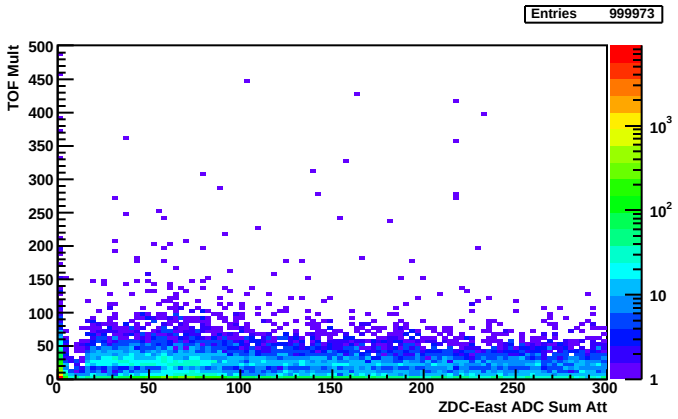

BBQ-VP004 (HI threshold)
Entries 1.599957e+07
TAC
QT Input Channel

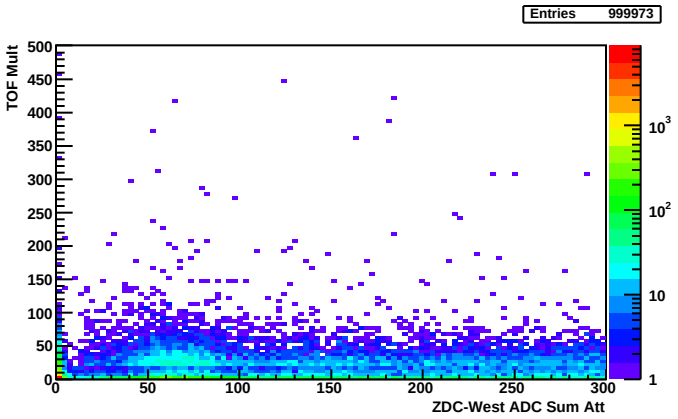

Entries 999973
TOF Mult
BBC-L-East ADC Sum

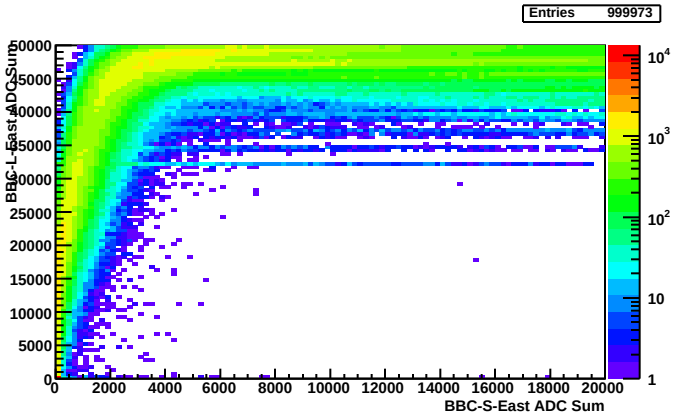

Entries 999973
TOF Mult
BBC-L-West ADC Sum

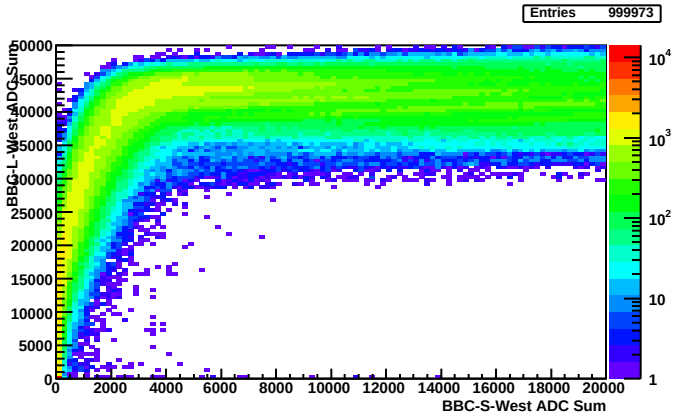

Entries 999973
TOF Mult
BBC-S-East ADC Sum

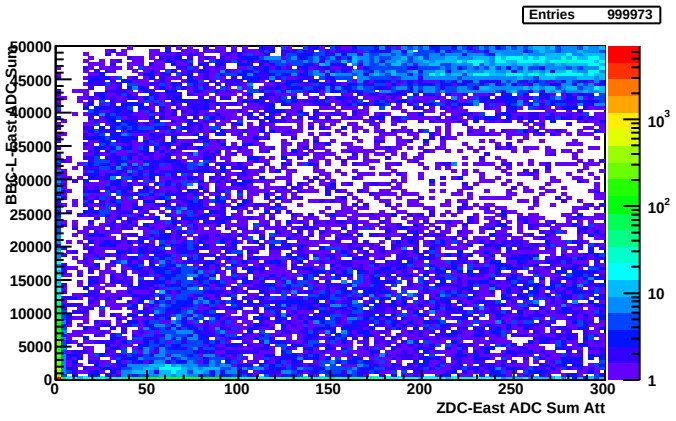

Entries 999973
TOF Mult
BBC-S-West ADC Sum

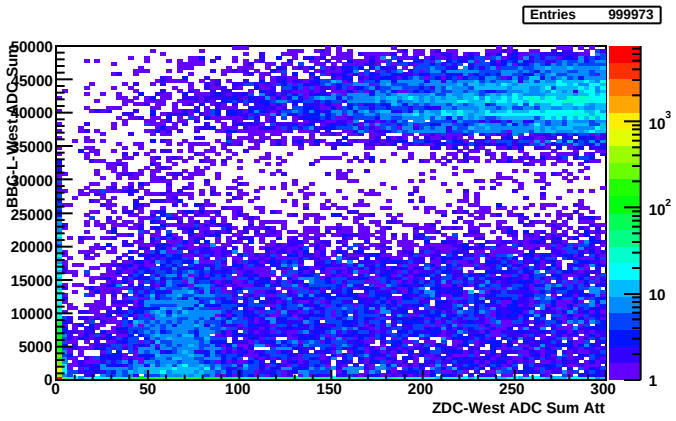

Entries 999973
TOF Mult
ZDC-East ADC Sum Att


Entries 999973
TOF Mult
ZDC-West ADC Sum Att


Entries 999973
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 999973
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 999973
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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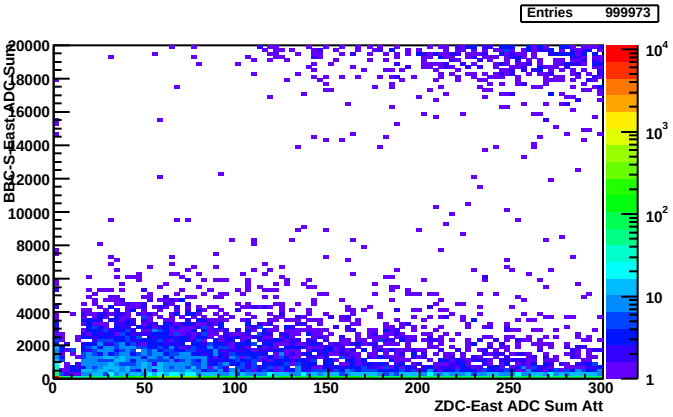
BBC-L-West ADC Sum
ZDC-West ADC Sum Att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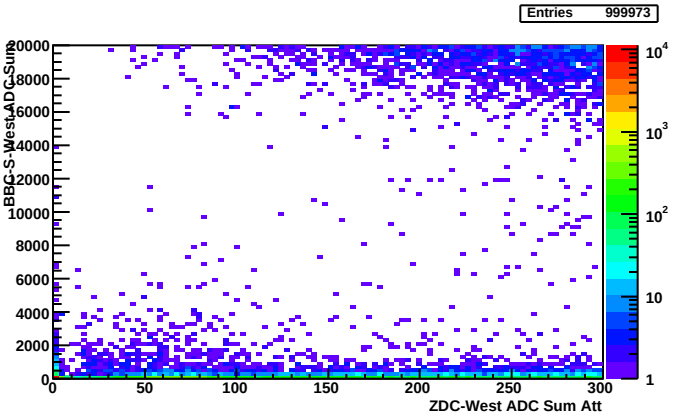
BBC-S-East ADC Sum
ZDC-East ADC Sum Att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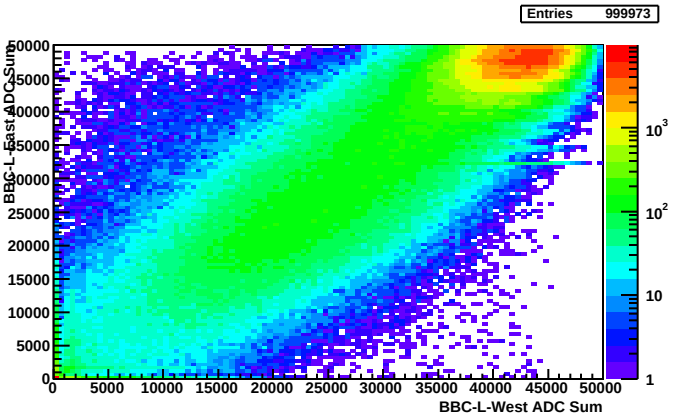
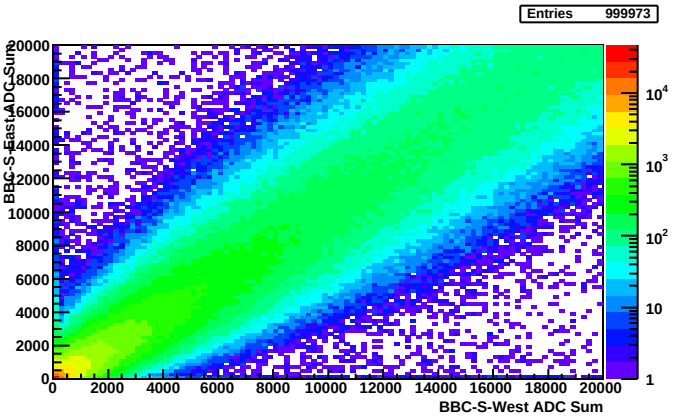
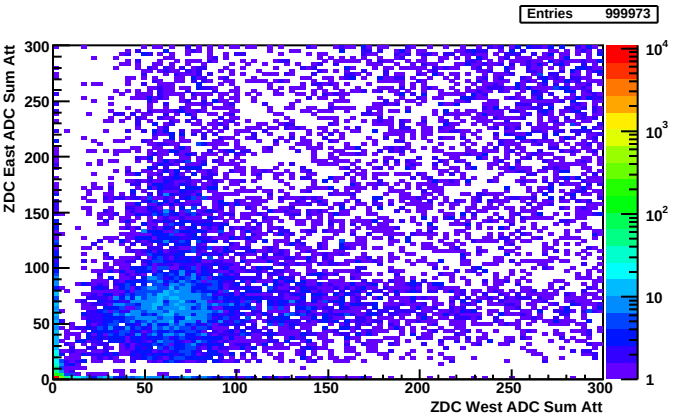
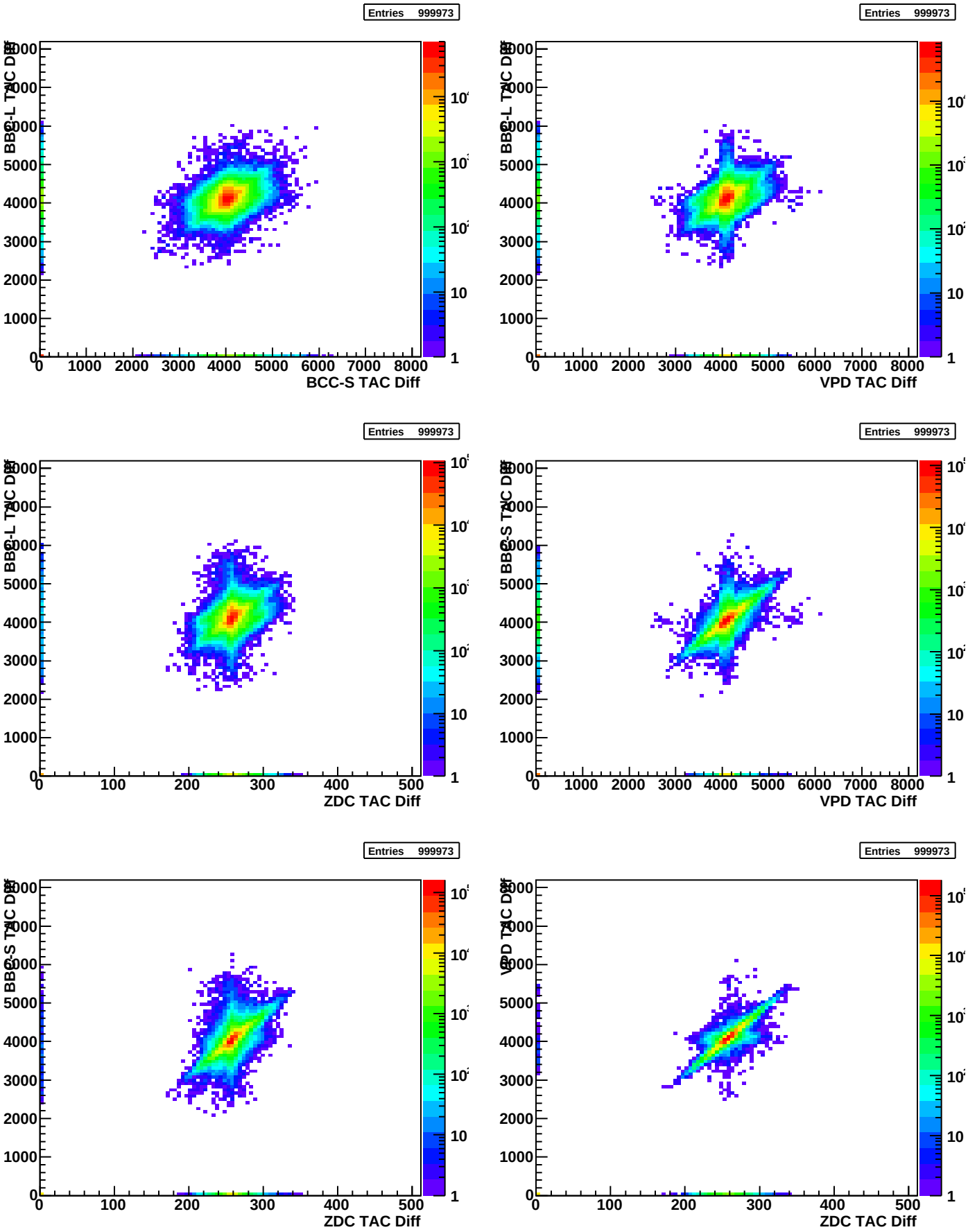
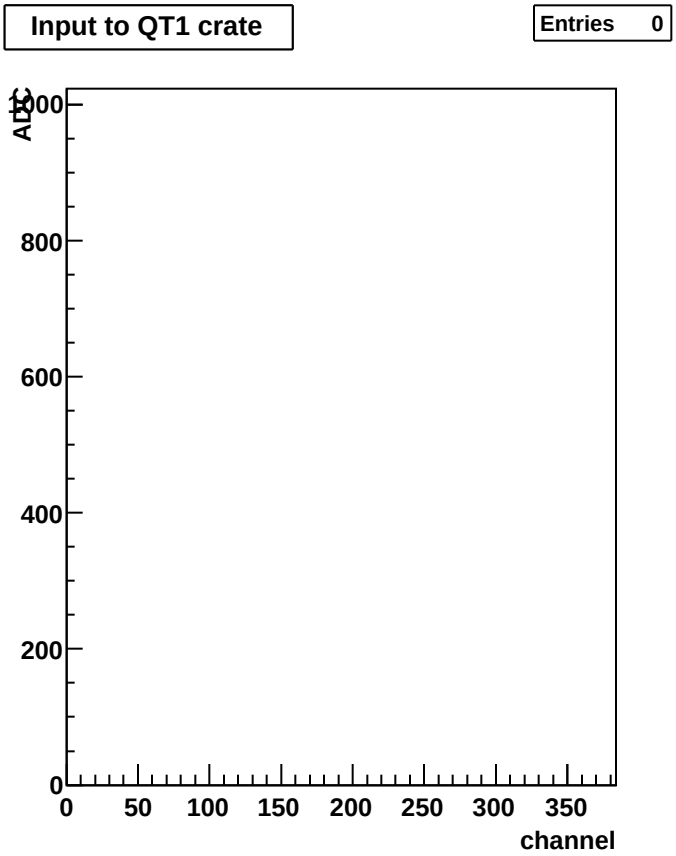
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

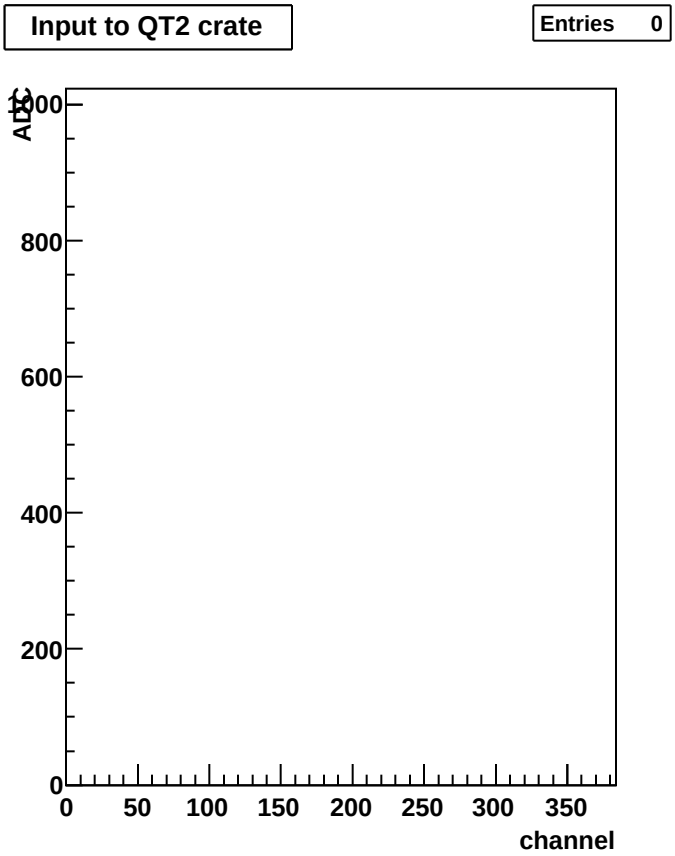

Entries 999973
BBC-L-East ADC Sum
BBC-L-West ADC Sum

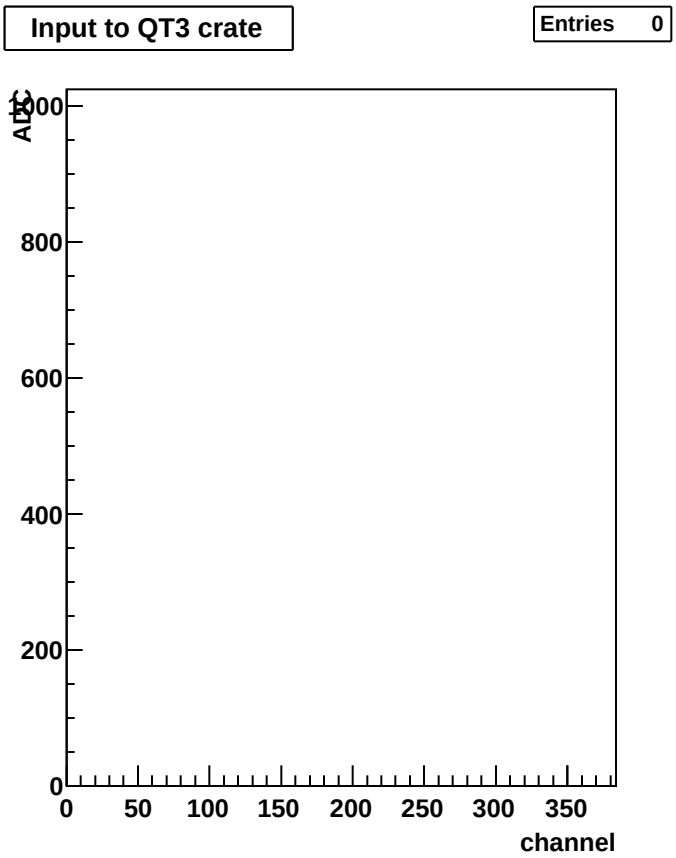

Entries 999973
BBC-S-East ADC Sum
BBC-S-West ADC Sum

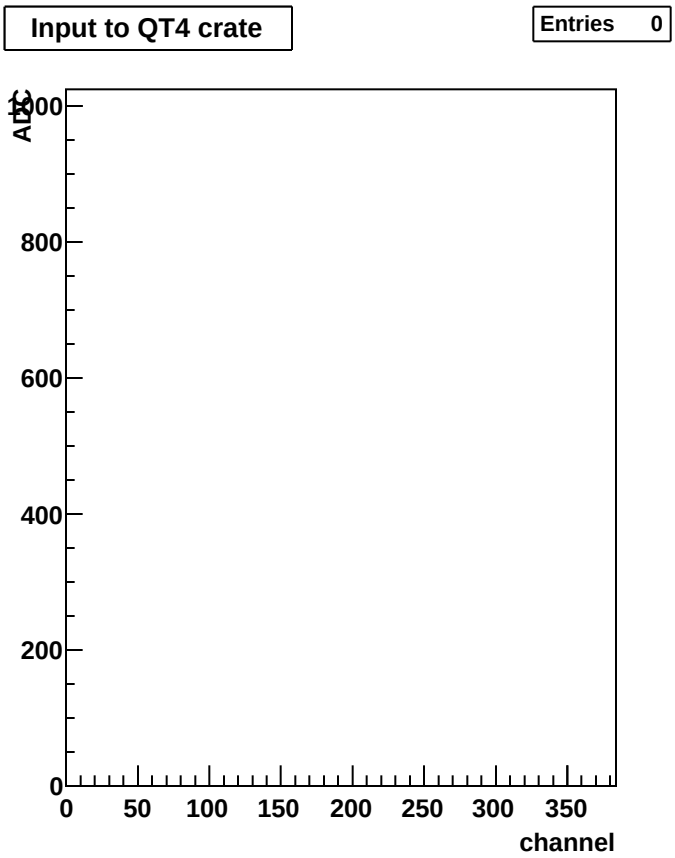

Entries 999973
ZDC East ADC Sum Att
ZDC West ADC Sum Att

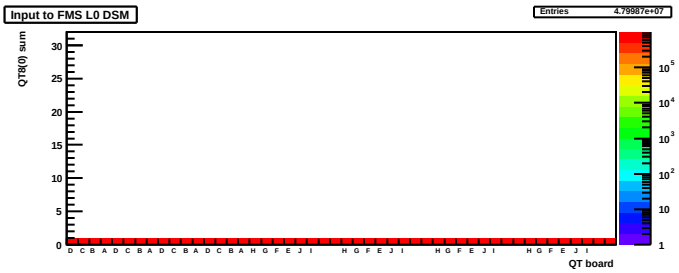

Entries 999973
BBC-L TAC Diff
BCC-S TAC Diff
Entries 999973
BBC-L TAC Diff
VPD TAC Diff
Entries 999973
BBC-L TAC Diff
ZDC TAC Diff
Entries 999973
BBC-S TAC Diff
VPD TAC Diff
Entries 999973
BBC-S TAC Diff
ZDC TAC Diff
Entries 999973
VPD TAC Diff
ZDC TAC Diff

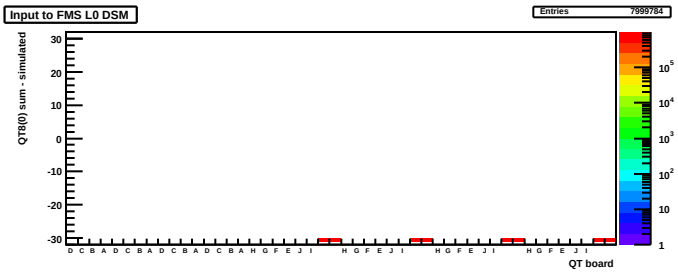

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

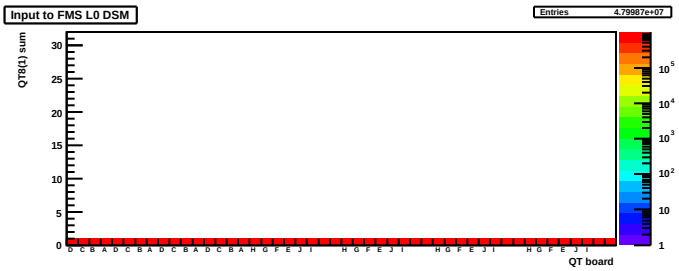

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

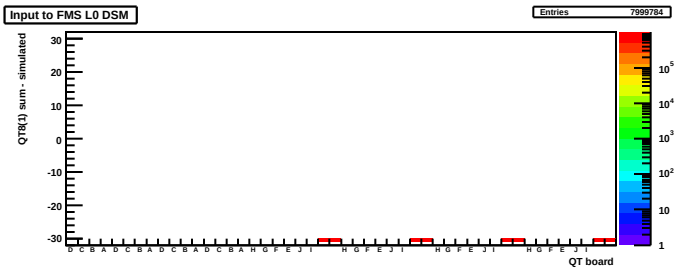

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

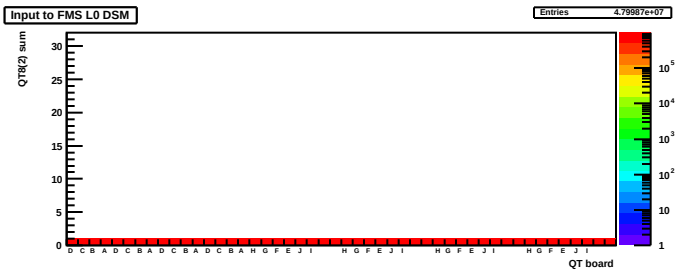

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

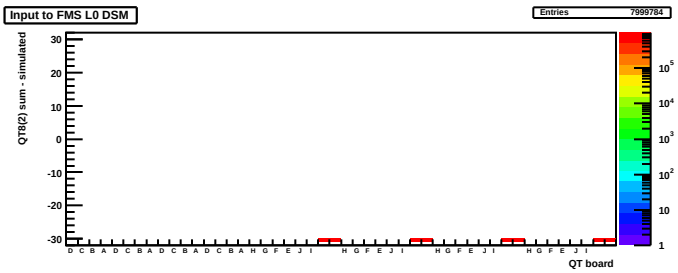

Input to FMS L0 DSM
Entries 4.79987e+07
QT8(0) sum
QT board

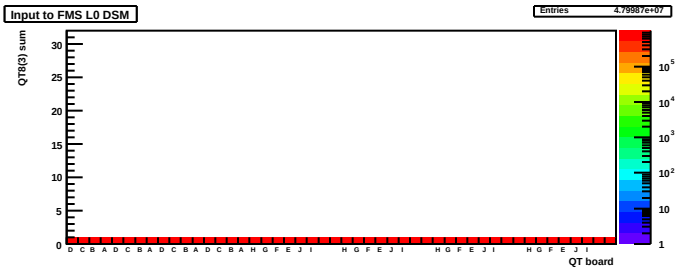

Input to FMS L0 DSM
Entries 7999784
QT8(0) sum - simulated
QT board

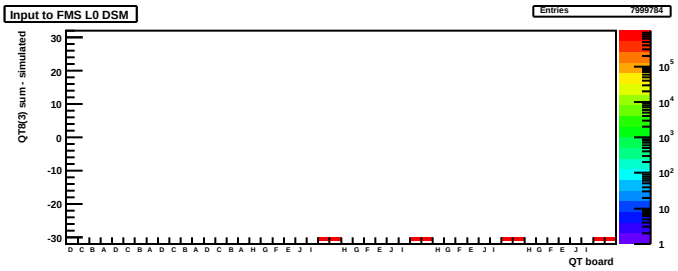

Input to FMS L0 DSM
Entries 4.79987e+07
QT8(1) sum
QT board

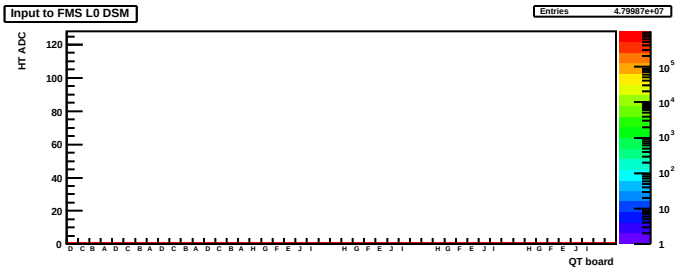

Input to FMS L0 DSM
Entries 7999784
QT8(1) sum - simulated
QT board

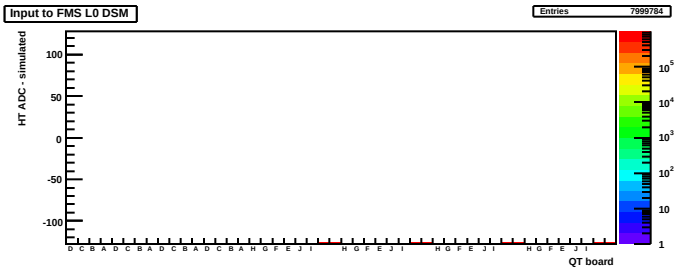

Input to FMS L0 DSM
Entries 4.79987e+07
QT8(2) sum
QT board

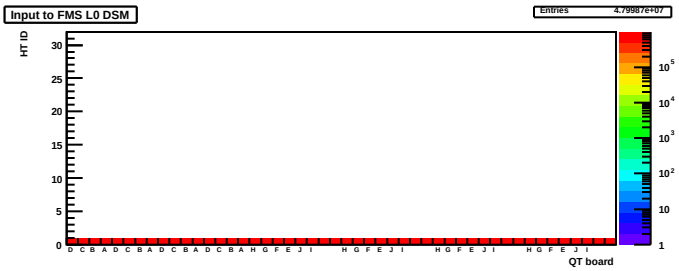

Input to FMS L0 DSM
Entries 7999784
QT8(2) sum - simulated
QT board

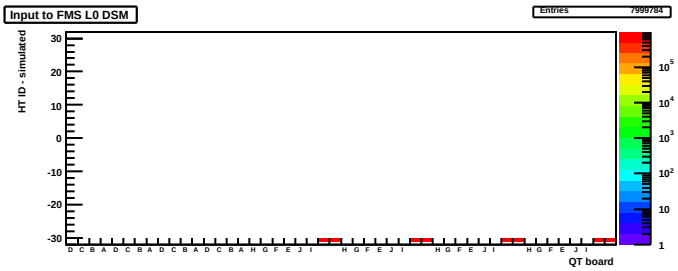

Input to FMS L0 DSM
Entries 4.79987e+07
QT8(3) sum
QT board

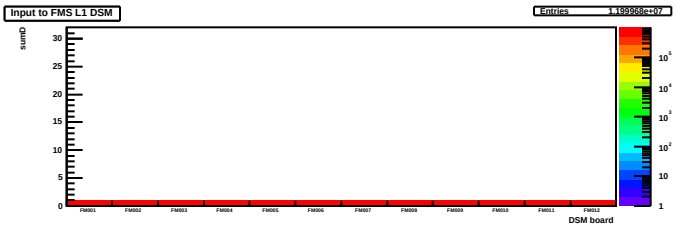

Input to FMS L0 DSM
Entries 7999784
QT8(3) sum - simulated
QT board

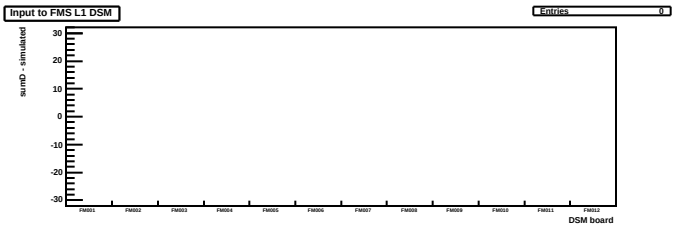

Input to FMS L0 DSM
Entries 4.79987e+07
HT ADC
QT board

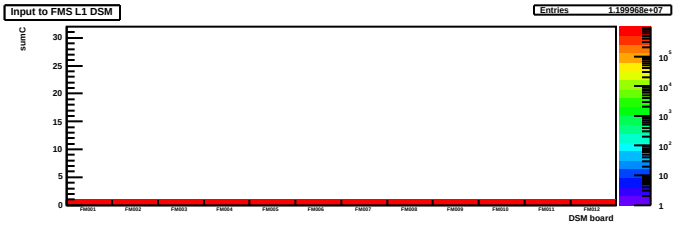

Input to FMS L0 DSM
Entries 7999784
HT ADC - simulated
QT board

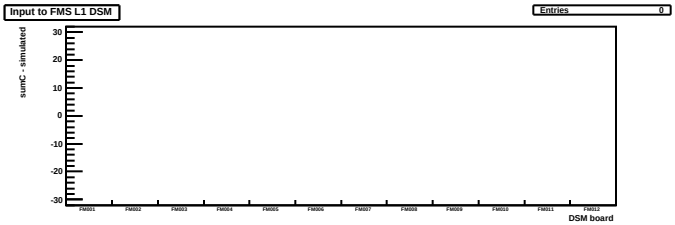

Input to FMS L0 DSM
Entries 4.79987e+07
HT ID
QT board

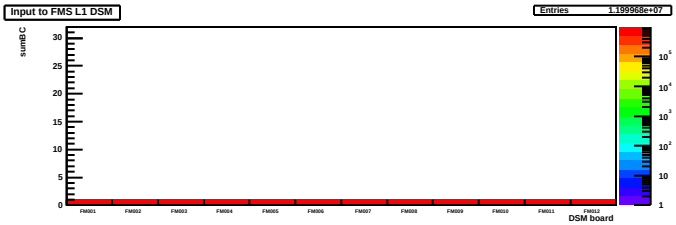

Input to FMS L0 DSM
Entries 7999784
HT ID - simulated
QT board

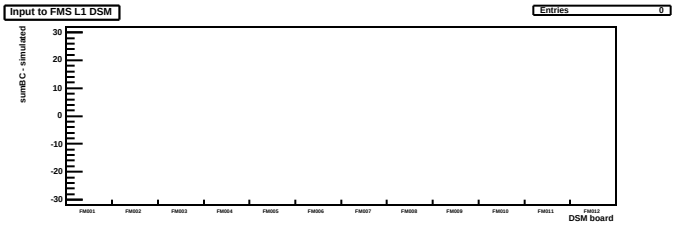

Input to FMS L1 DSM
Entries 1.199968e+07
sumD
DSM board

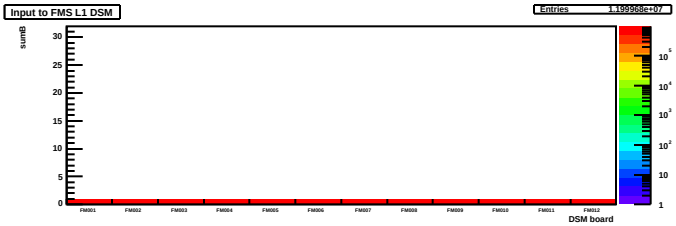

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

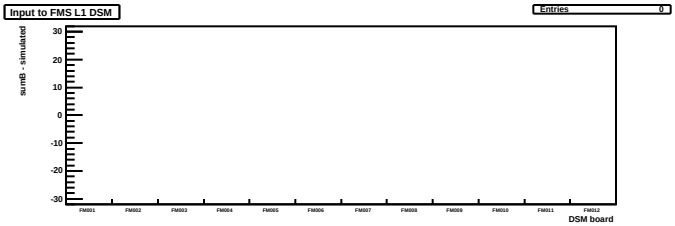

Input to FMS L1 DSM
Entries 1.199968e+07
sumC
DSM board

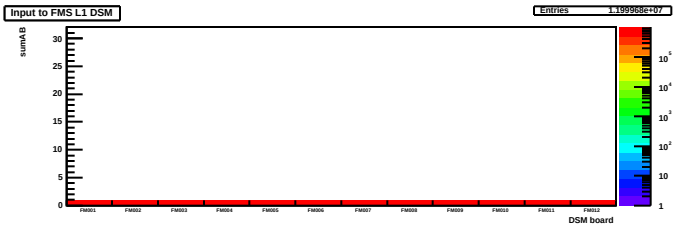

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

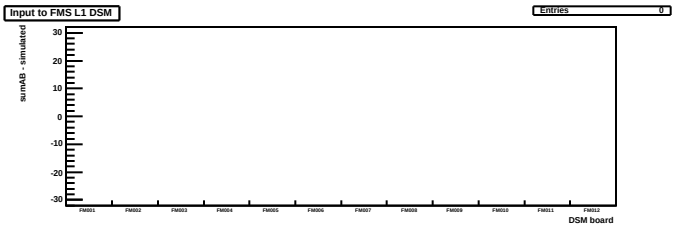

Input to FMS L1 DSM
Entries 1.199968e+07
sumBC
DSM board

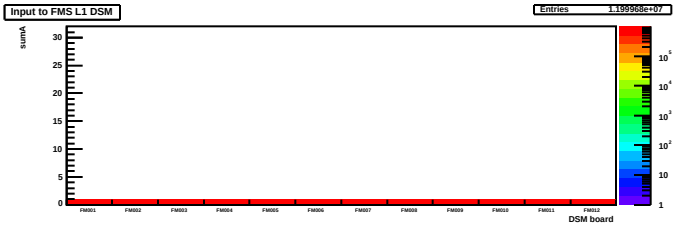

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

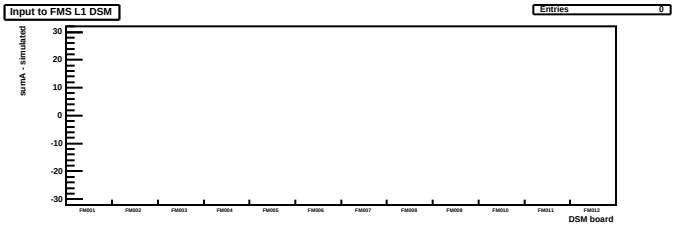

Input to FMS L1 DSM
Entries 1.199968e+07
sumB
DSM board

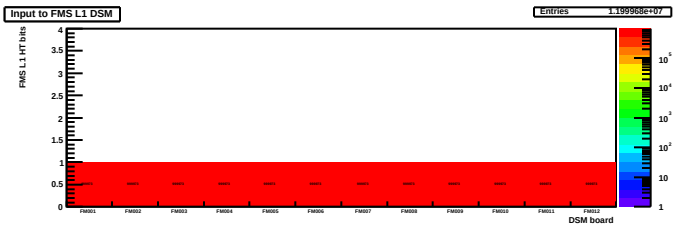

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

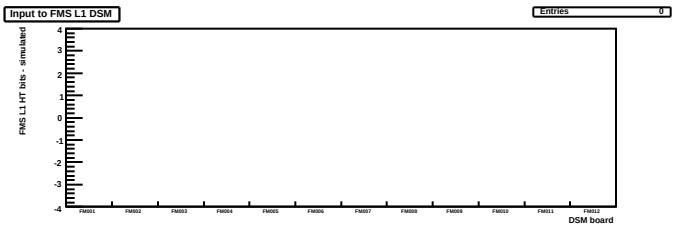

Input to FMS L1 DSM
Entries 1.199968e+07
sumAB
DSM board

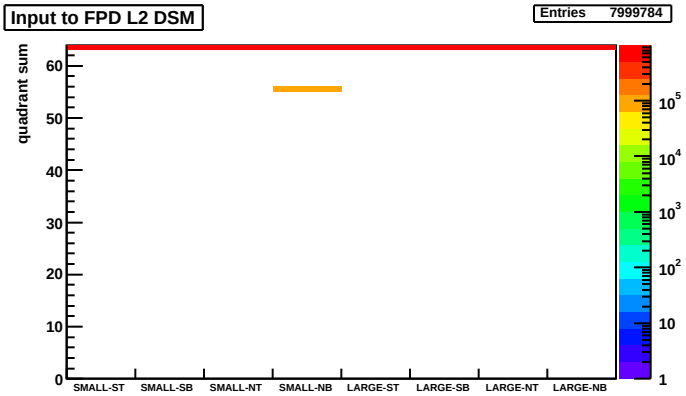

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

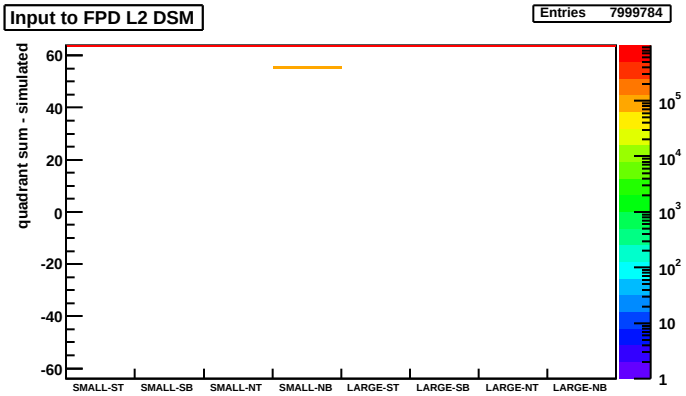

Input to FMS L1 DSM
Entries 1.199968e+07
sumA
DSM board

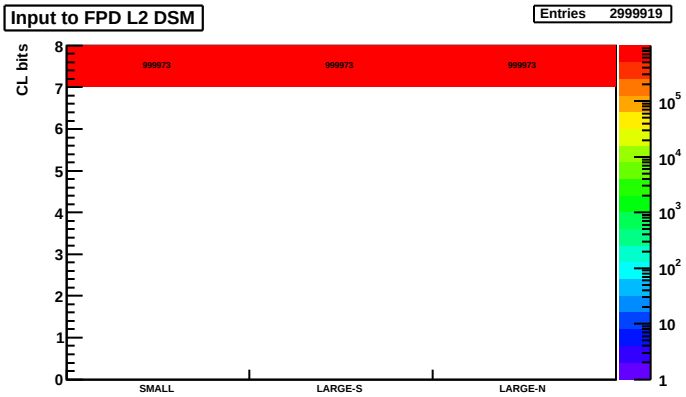

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

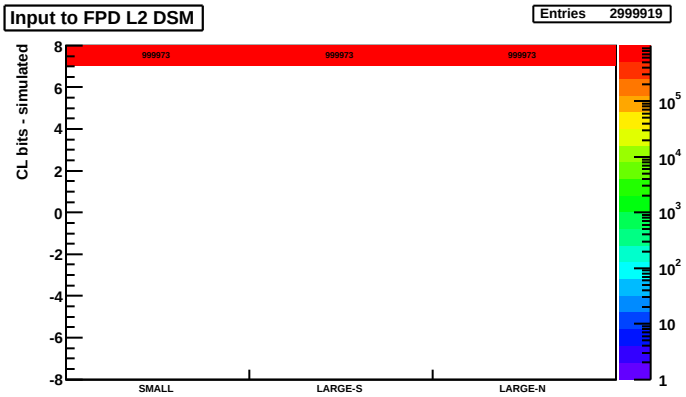

Input to FMS L1 DSM
Entries 1.199968e+07
FMS L1 HT bits
DSM board

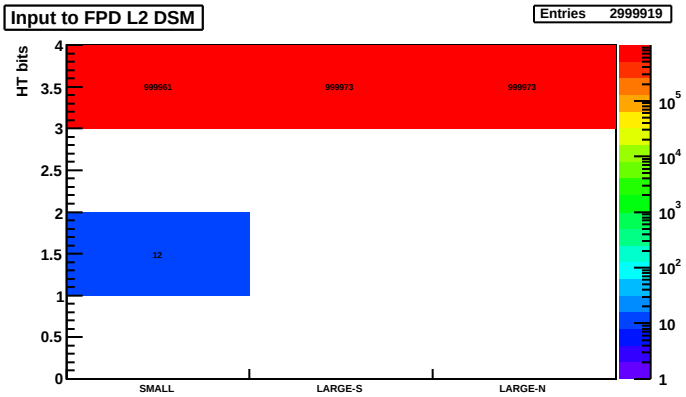

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

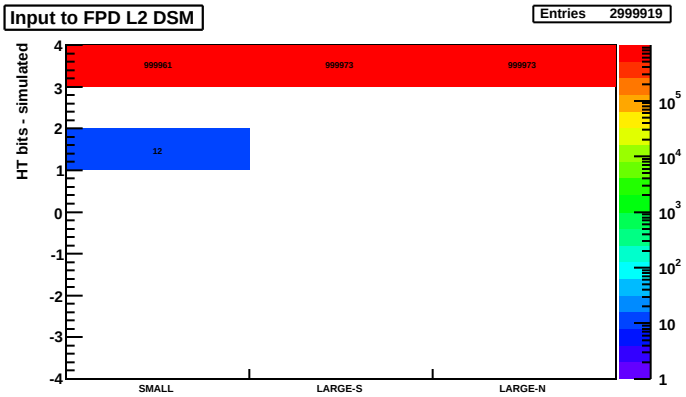

Input to FPD L2 DSM
Entries 7999784
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

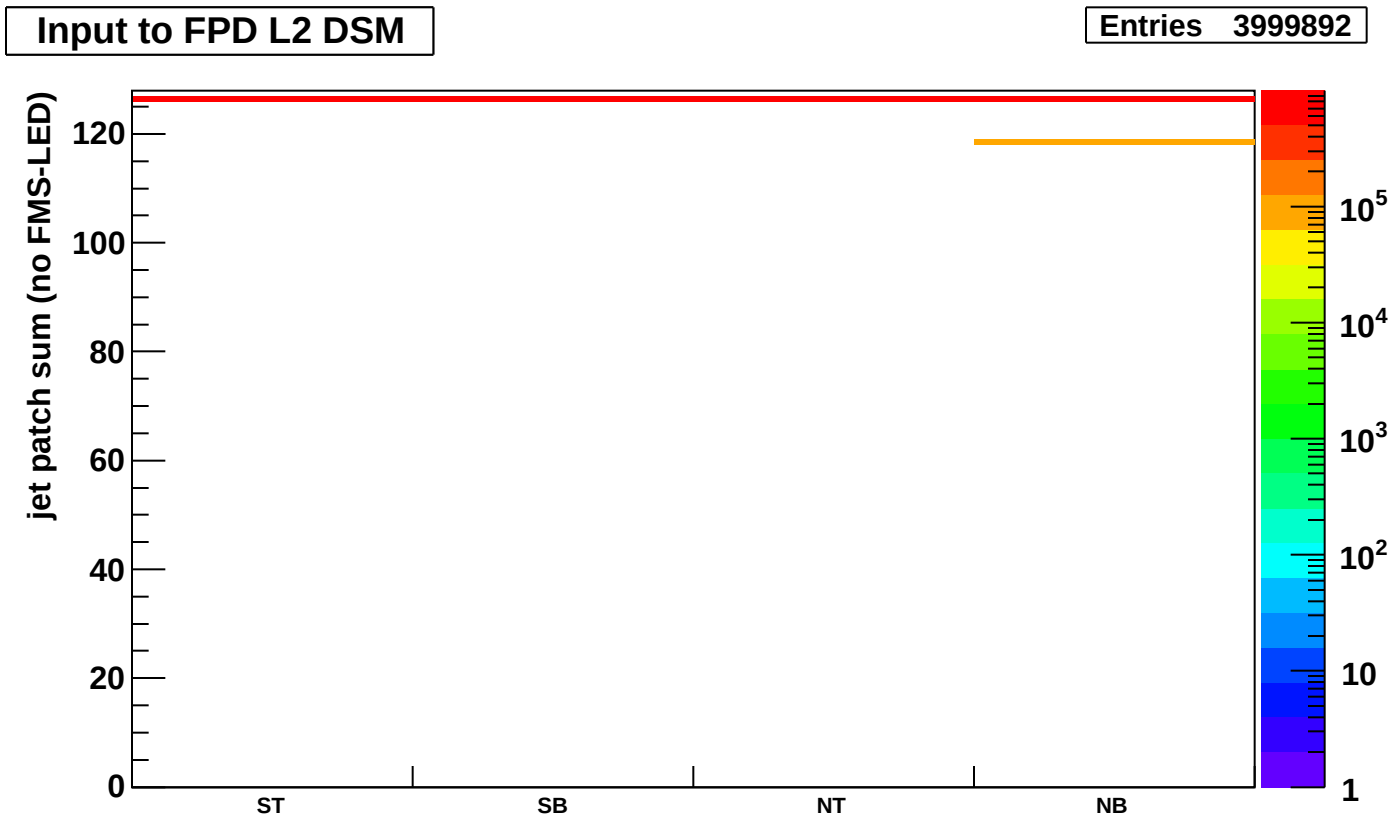

Input to FPD L2 DSM
Entries 7999784
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

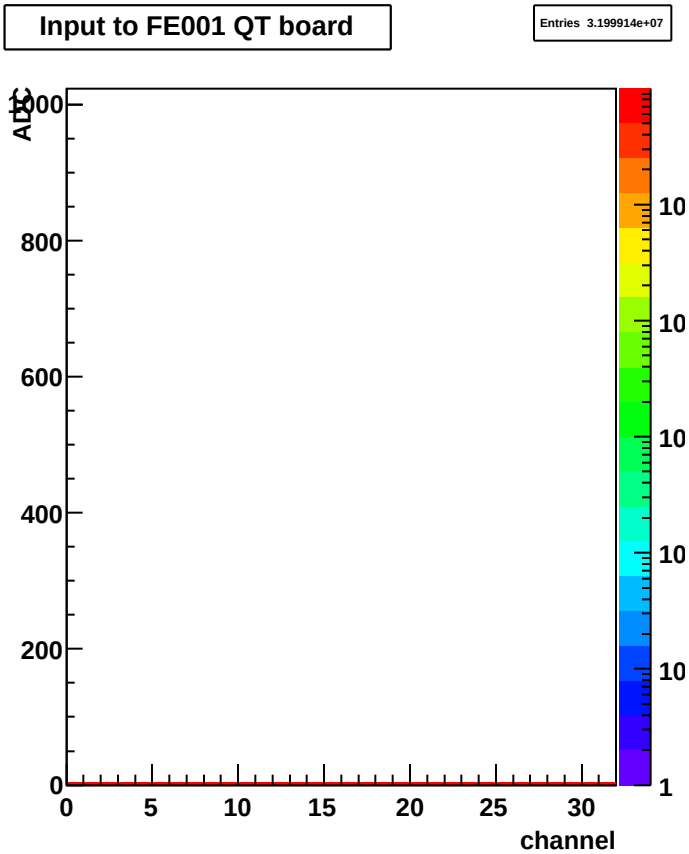

Input to FPD L2 DSM
Entries 2999919
CL bits
999973
999973
999973
SMALL
LARGE-S
LARGE-N

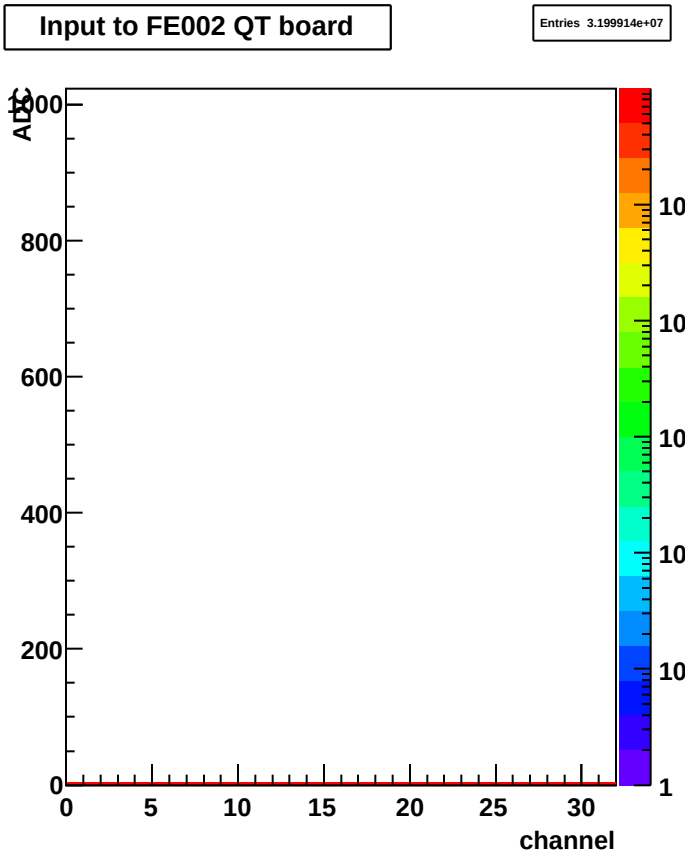

Input to FPD L2 DSM
Entries 2999919
CL bits - simulated
999973
999973
999973
SMALL
LARGE-S
LARGE-N

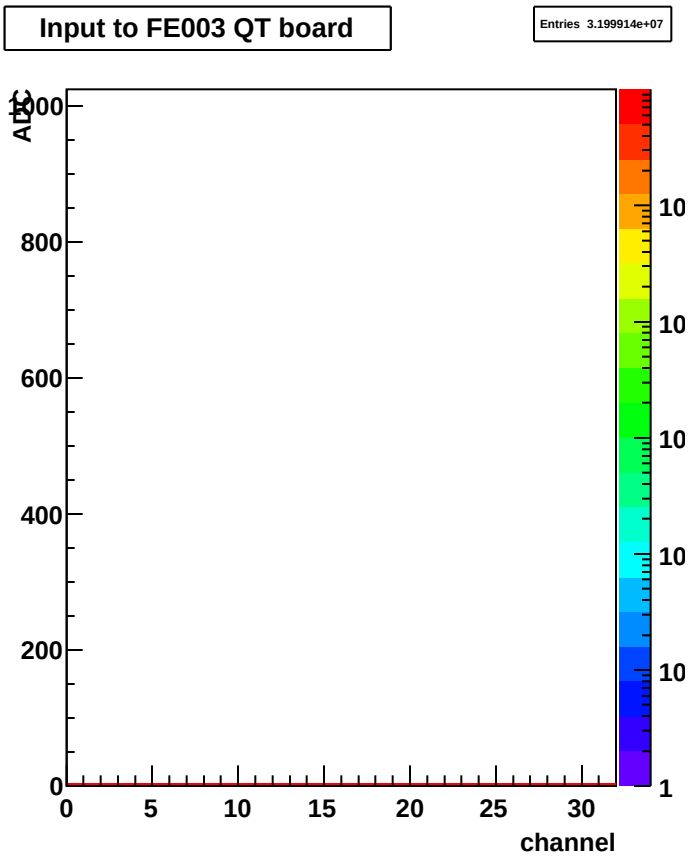

Input to FPD L2 DSM
Entries 2999919
HT bits
999961
999973
999973
12
SMALL
LARGE-S
LARGE-N

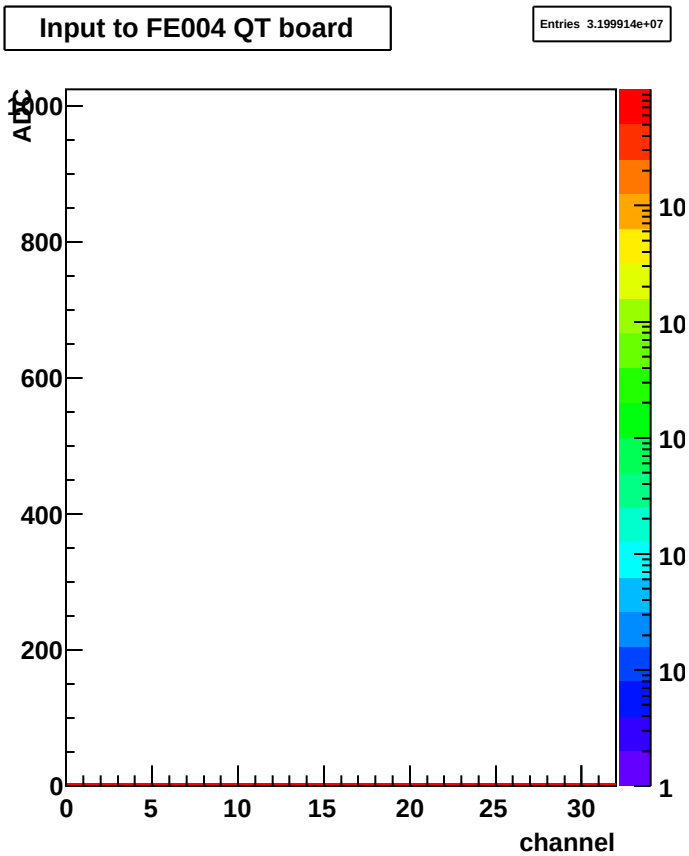

Input to FPD L2 DSM
Entries 2999919
HT bits - simulated
999961
999973
999973
12
SMALL
LARGE-S
LARGE-N

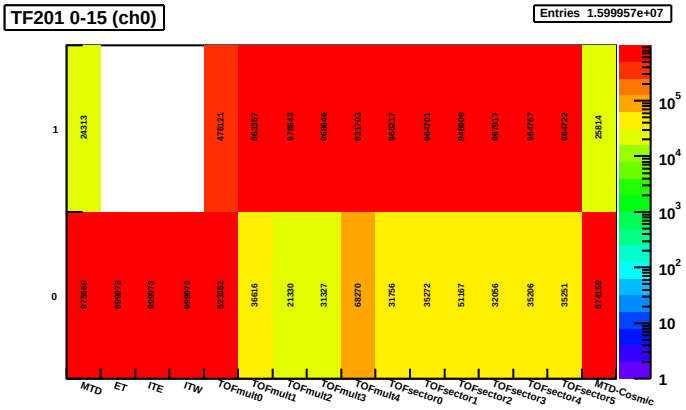

Input to FPD L2 DSM
Entries 3999892
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

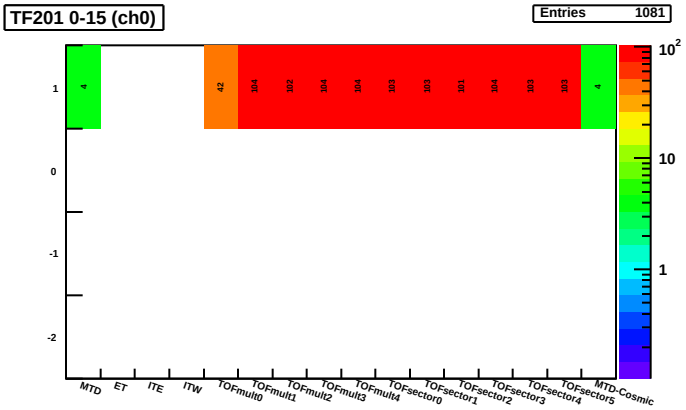

Input to FE001 QT board
Entries 3.199914e+07
ADC
channel

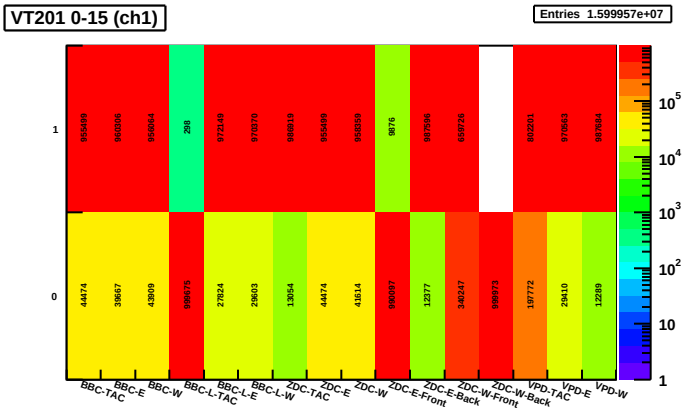

Input to FE002 QT board
Entries 3.199914e+07
ADC
channel

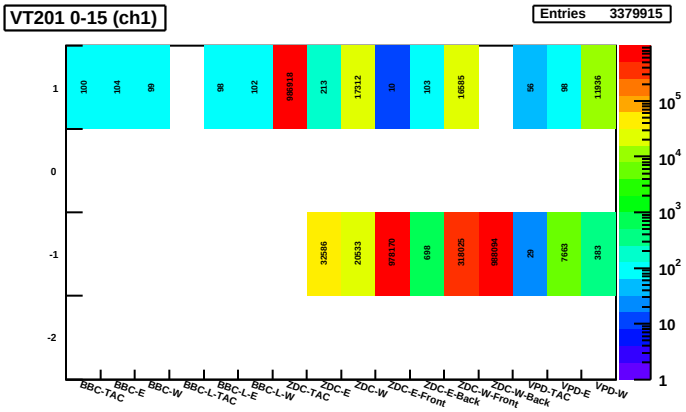

Input to FE003 QT board
Entries 3.199914e+07
ADC
channel

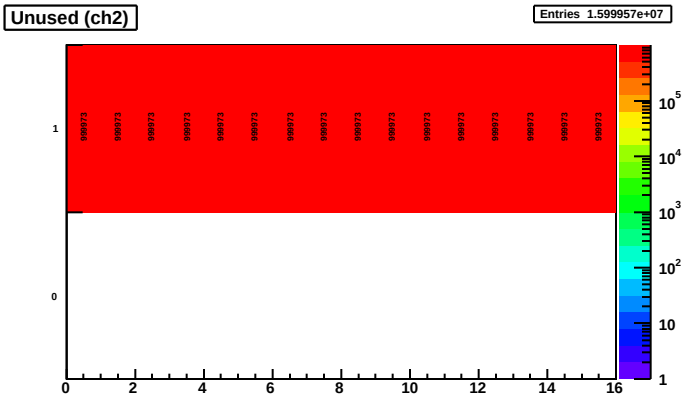

Input to FE004 QT board
Entries 3.199914e+07
ADC
channel

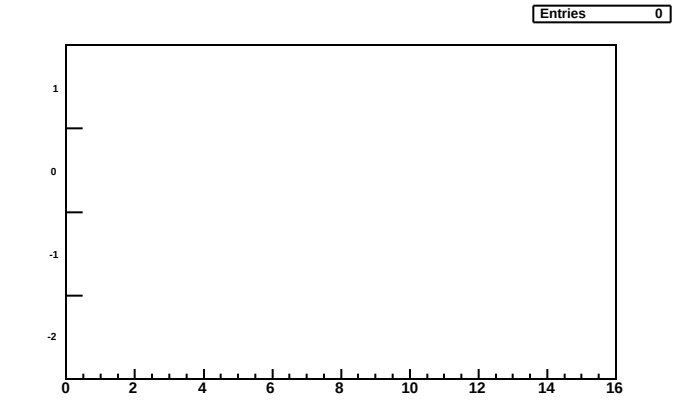

TF201 0-15 (ch0)
Entries 1.599957e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

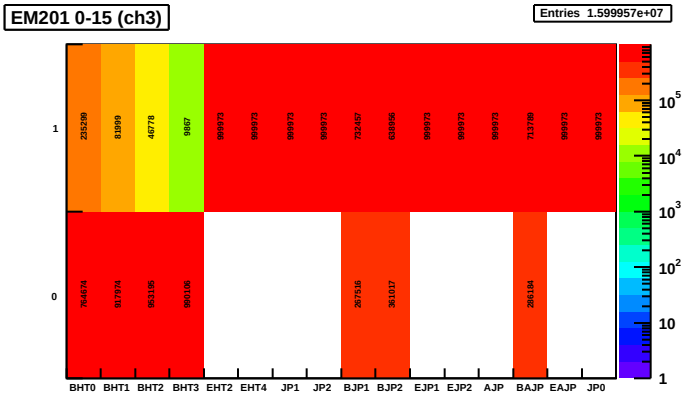

TF201 0-15 (ch0)
Entries 1081
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

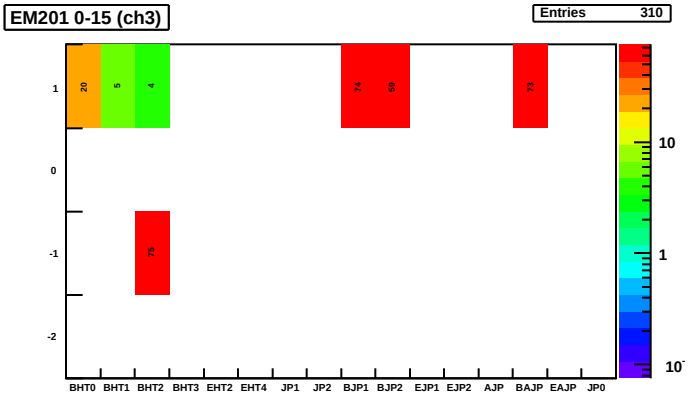

VT201 0-15 (ch1)
Entries 1.599957e+07
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 3379915
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

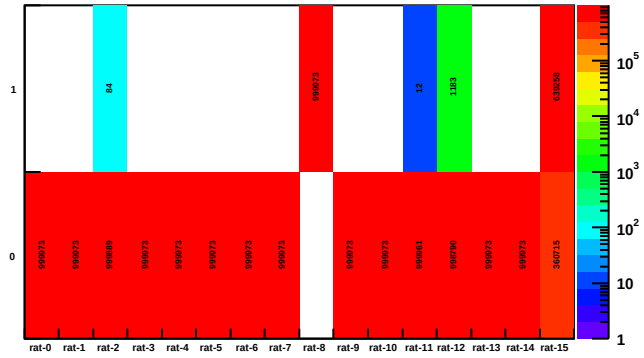

Unused (ch2)
Entries 1.599957e+07

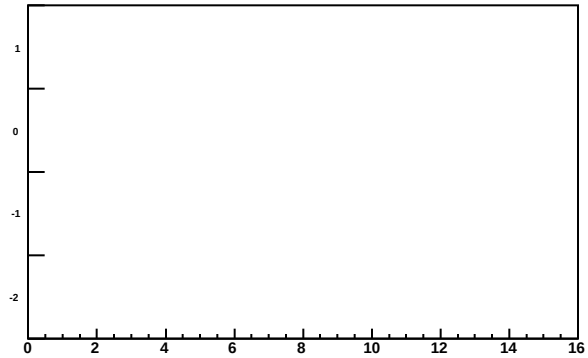

Entries 0


EM201 0-15 (ch3)
Entries 1.599957e+07
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

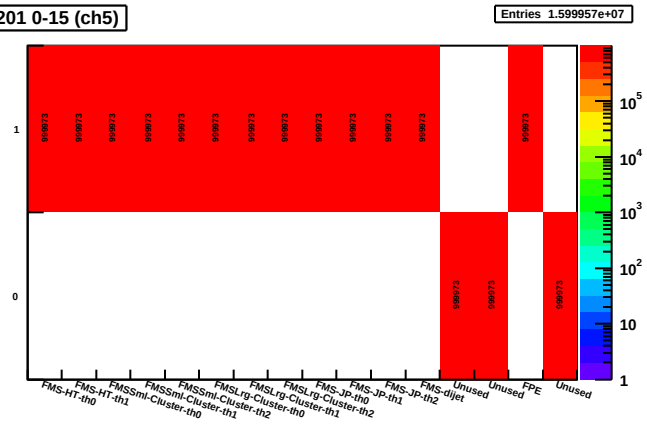

EM201 0-15 (ch3)
Entries 310
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

RAT board (ch4)

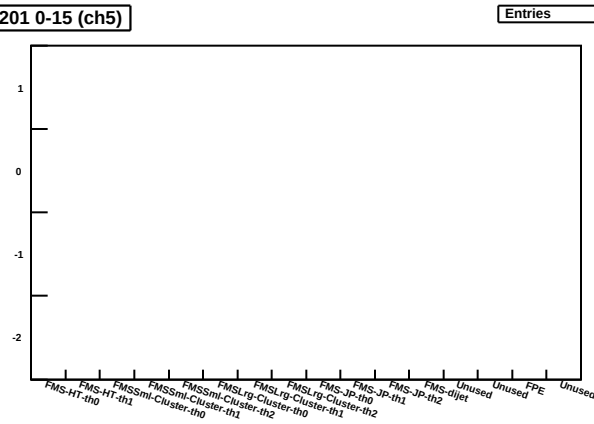




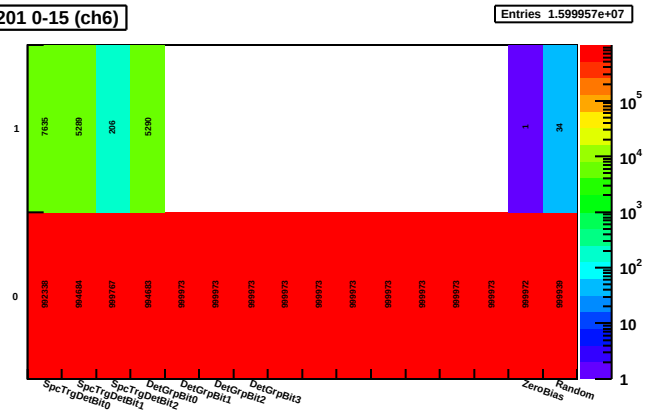
FP201 0-15 (ch5)

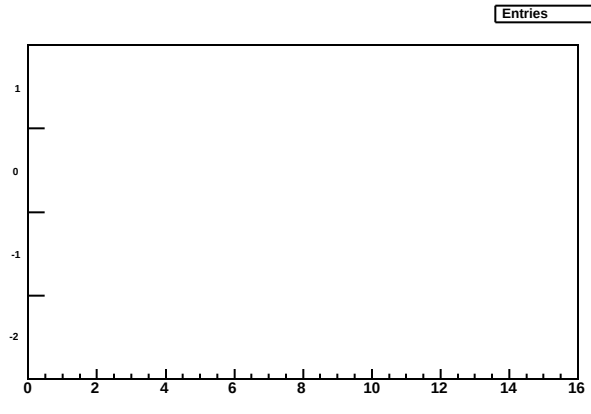


FP201 0-15 (ch5)

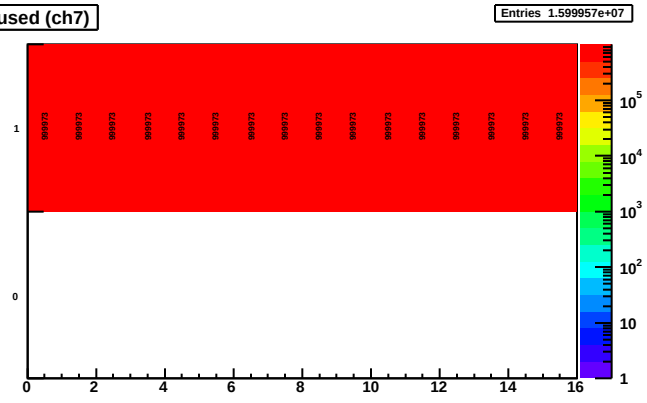


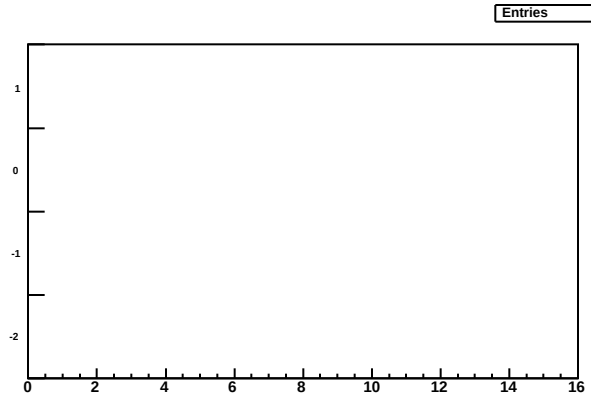
ST201 0-15 (ch6)

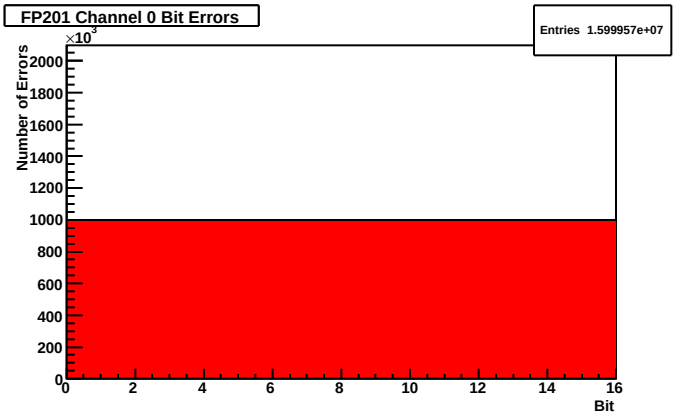


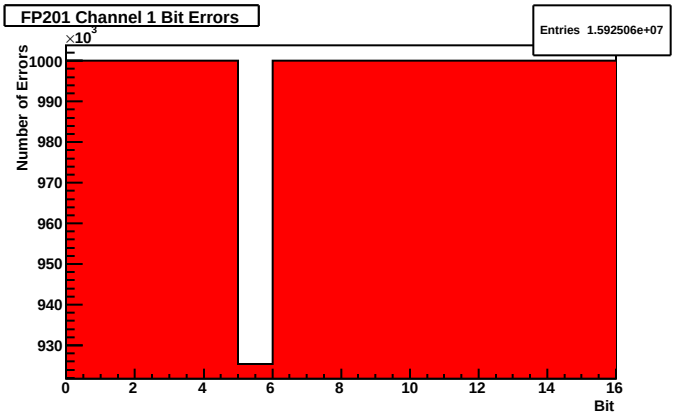


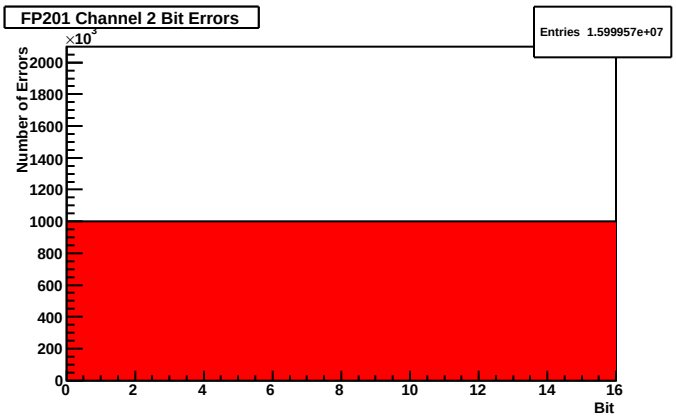
Unused (ch7)

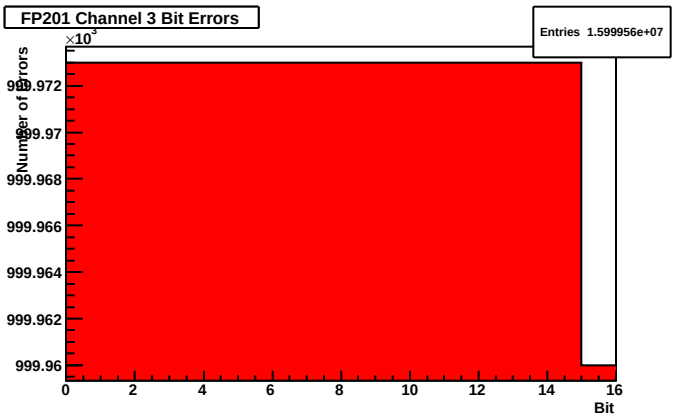


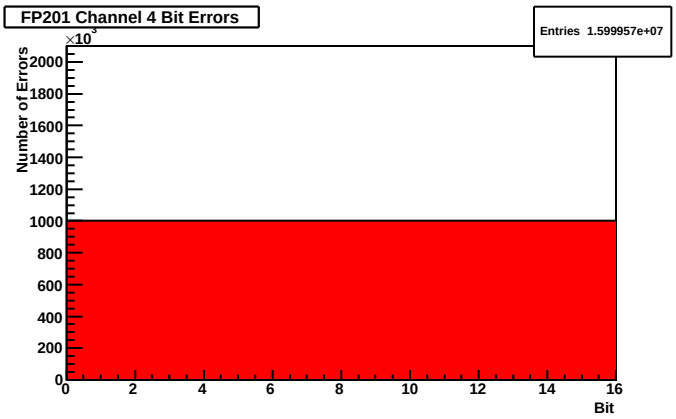


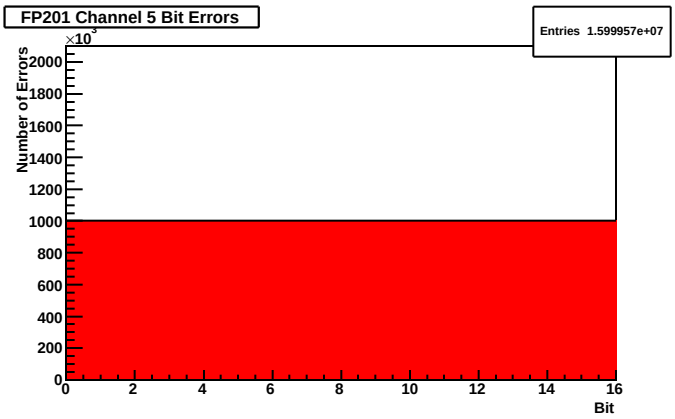

FP201 Channel 0 Bit Errors
Entries 1.599957e+07
Number of Errors
Bit

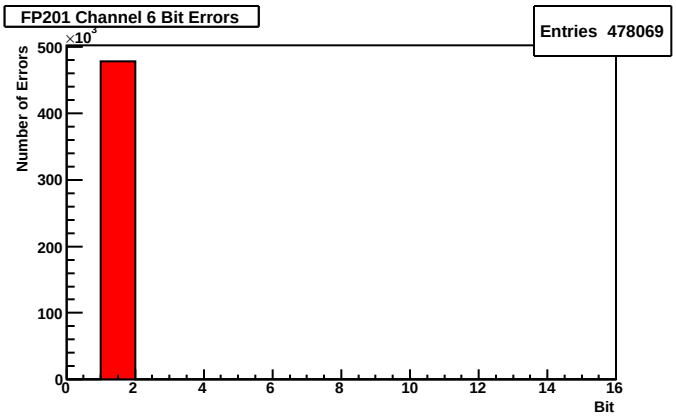

FP201 Channel 1 Bit Errors
Entries 1.592506e+07
Number of Errors
Bit

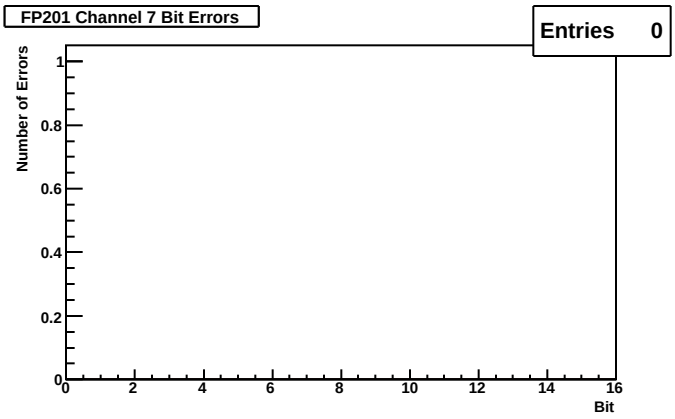

FP201 Channel 2 Bit Errors
Entries 1.599957e+07
Number of Errors
Bit

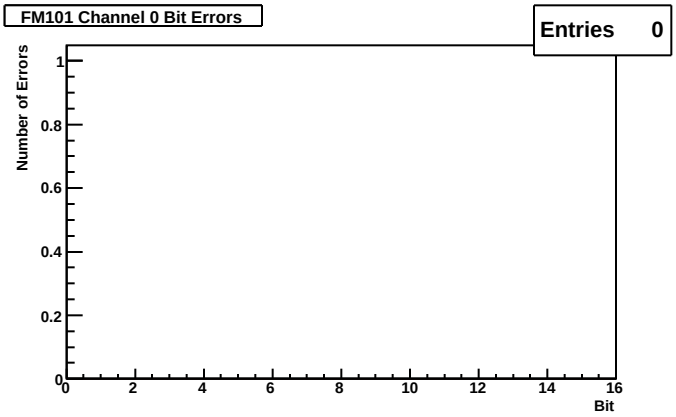

FP201 Channel 3 Bit Errors
Entries 1.599956e+07
Number of Errors
Bit

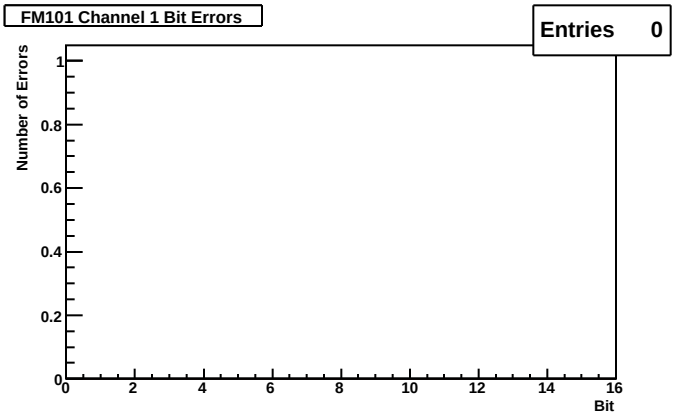

FP201 Channel 4 Bit Errors
Entries 1.599957e+07
Number of Errors
Bit

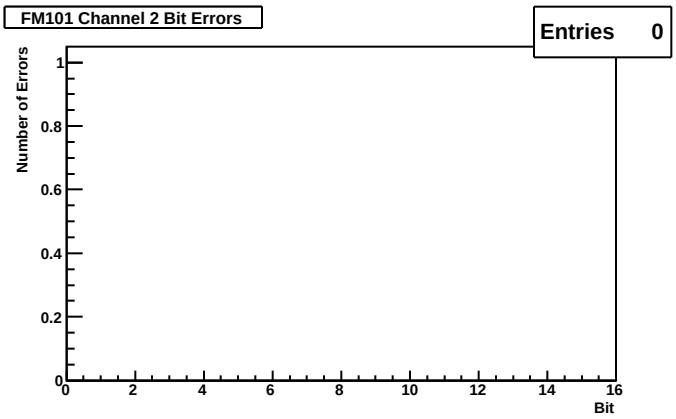

FP201 Channel 5 Bit Errors
Entries 1.599957e+07
Number of Errors
Bit

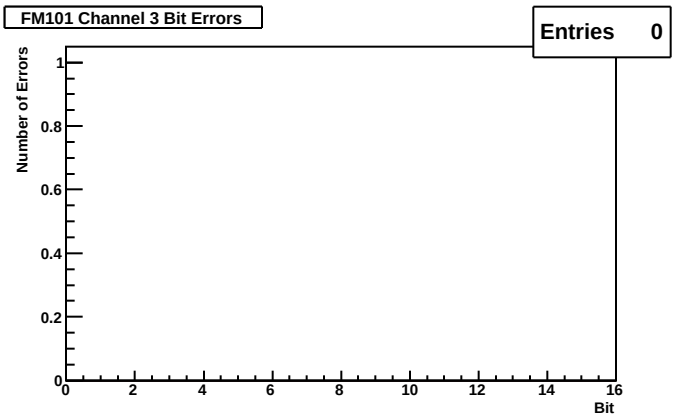

FP201 Channel 6 Bit Errors
Entries 478069
Number of Errors
Bit

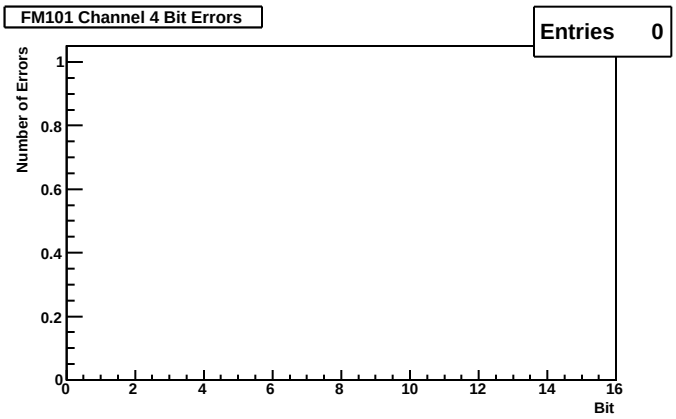

FP201 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

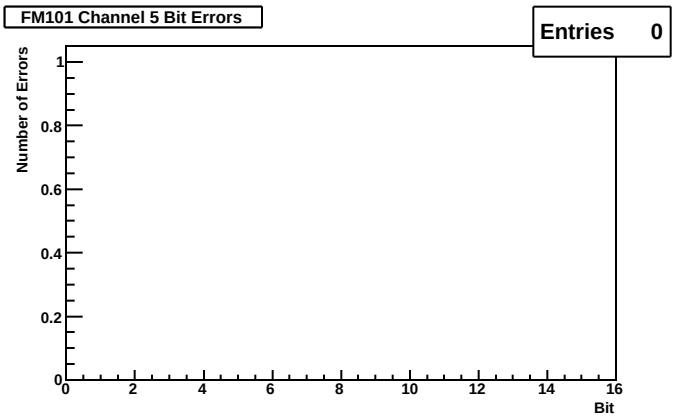

FM101 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

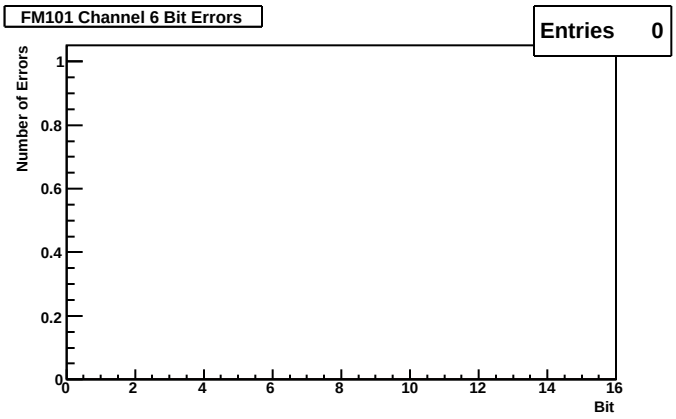

FM101 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

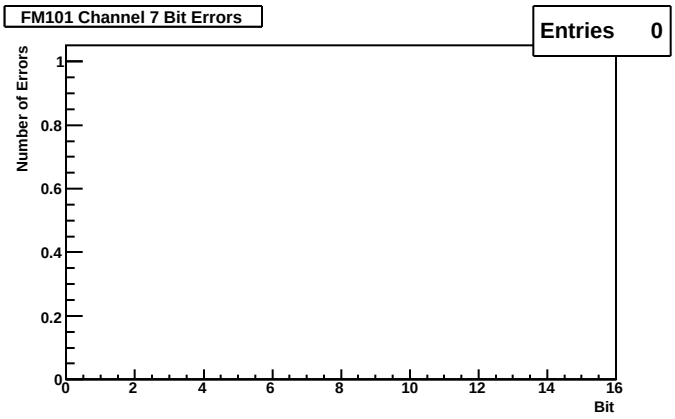

FM101 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

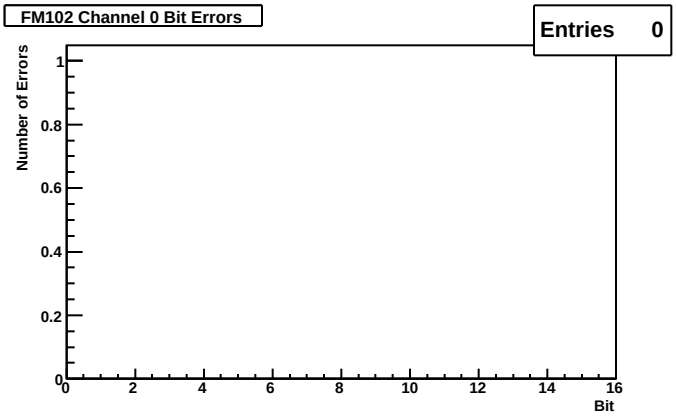

FM101 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

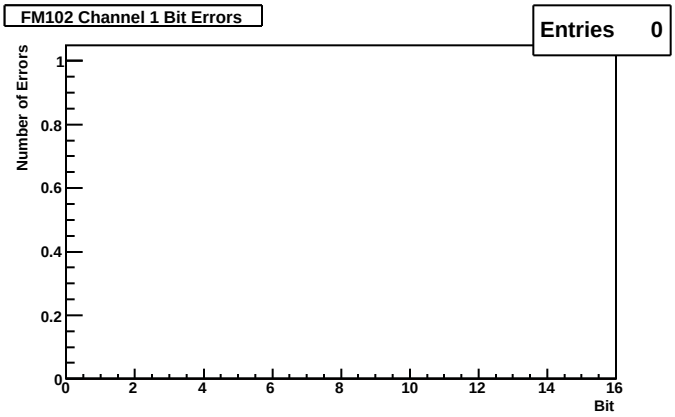

FM101 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

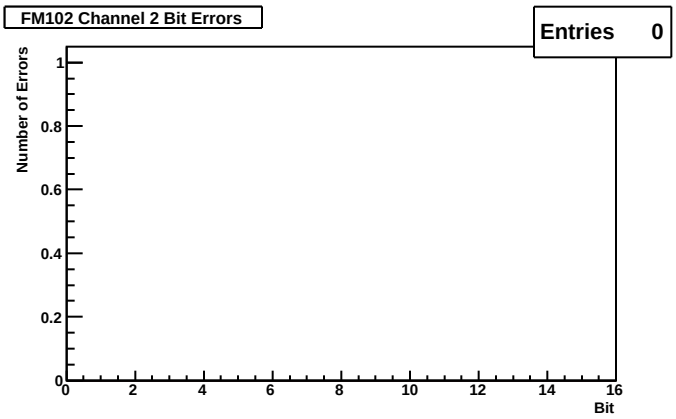

FM101 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

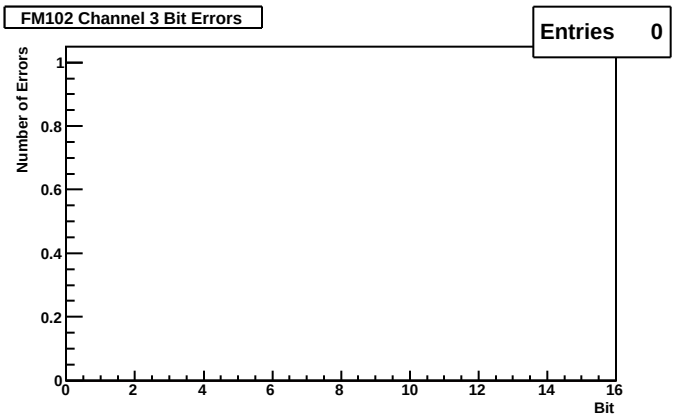

FM101 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

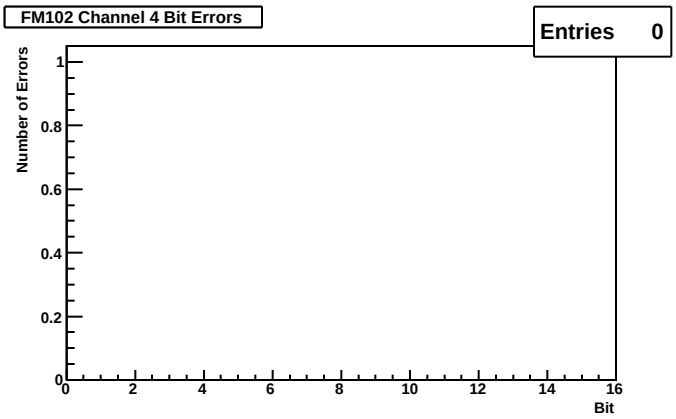

FM101 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

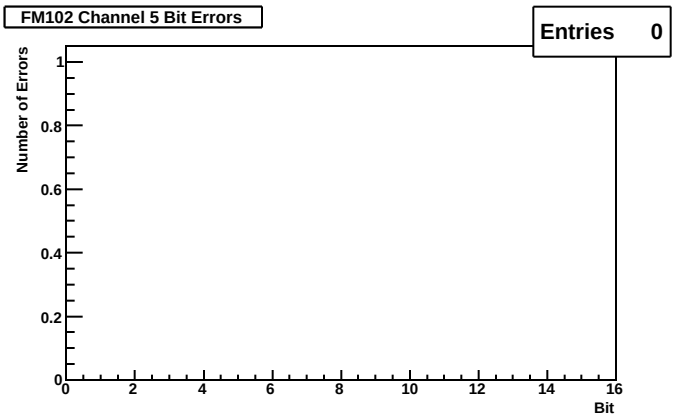

FM102 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

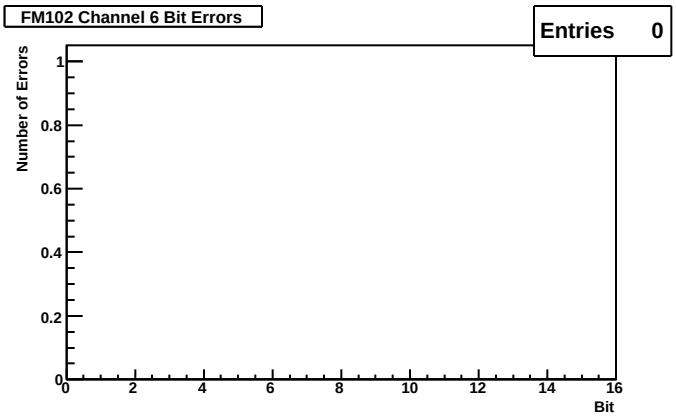

FM102 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

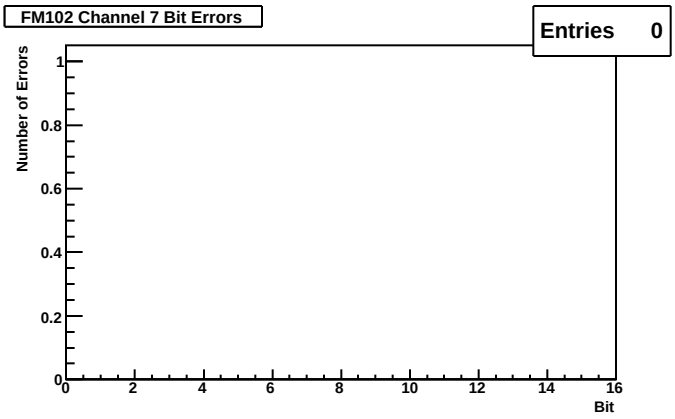

FM102 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

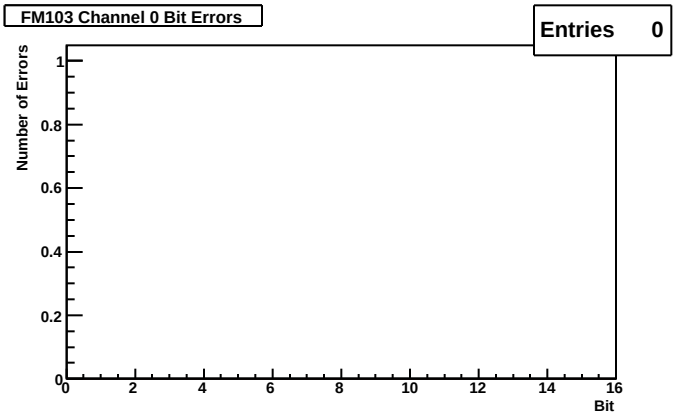

FM102 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

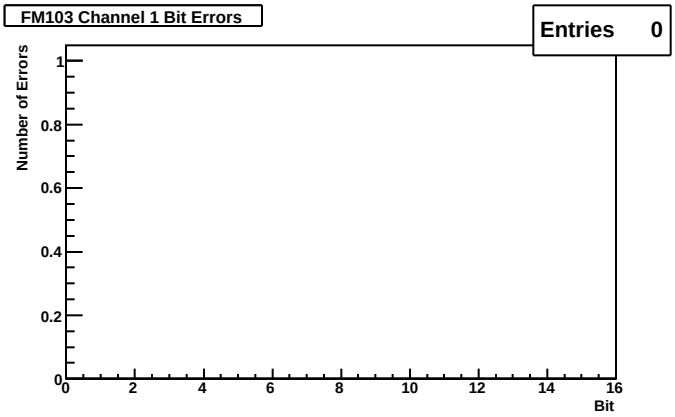

FM102 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

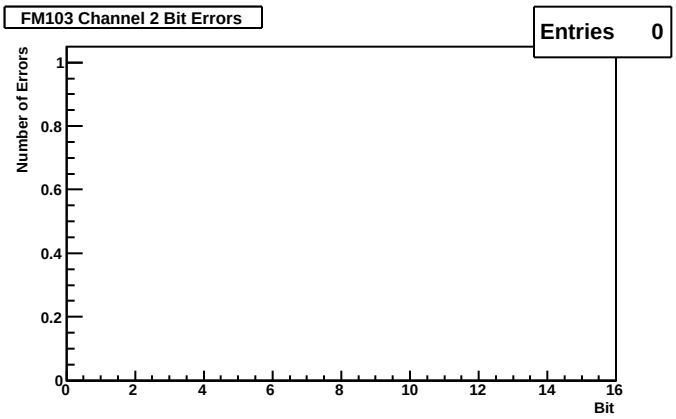

FM102 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

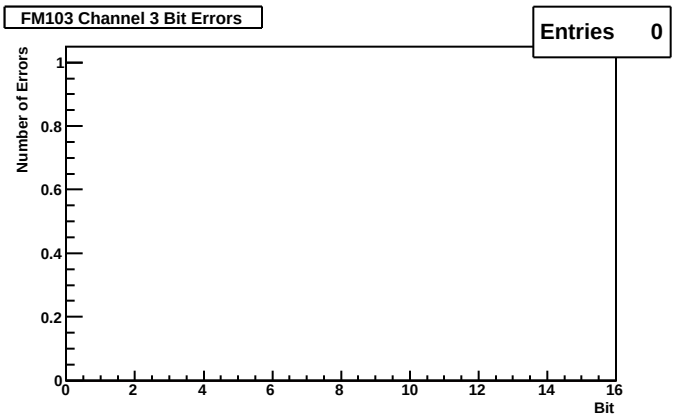

FM102 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

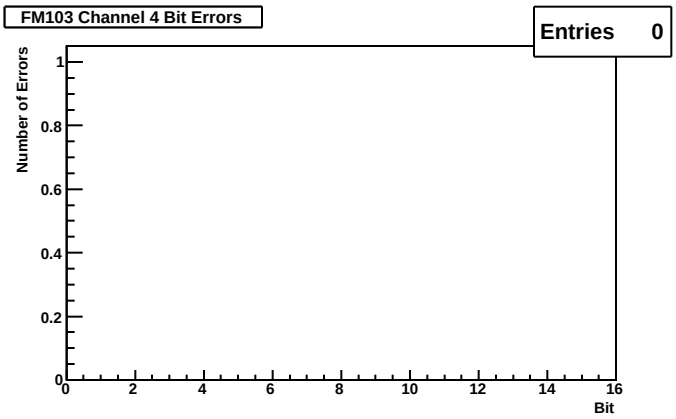

FM102 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

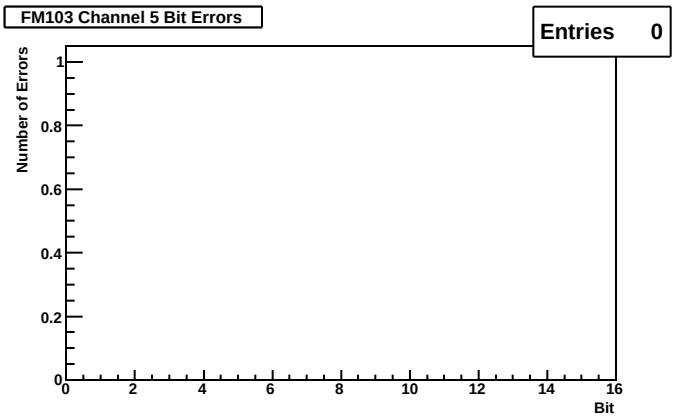

FM103 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

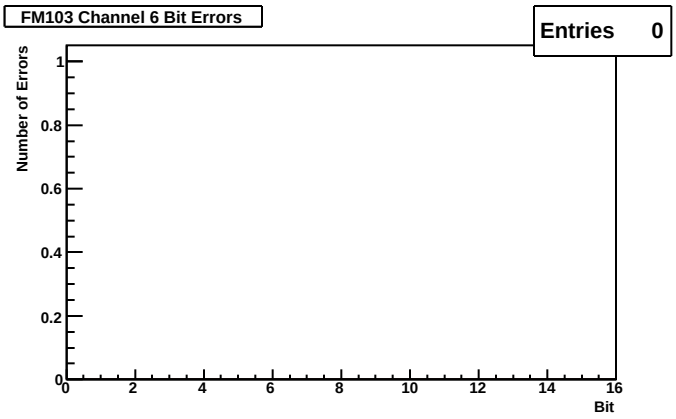

FM103 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

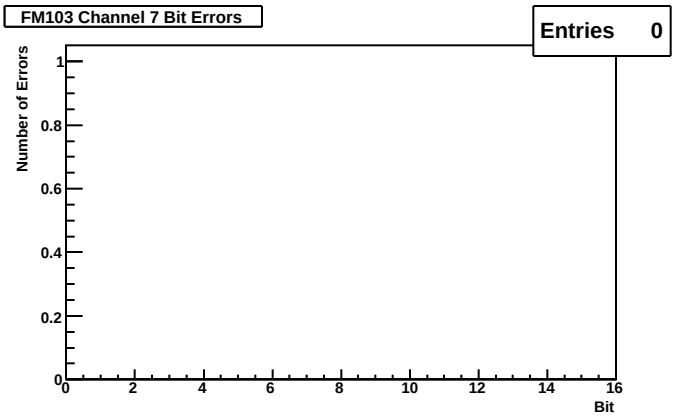

FM103 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

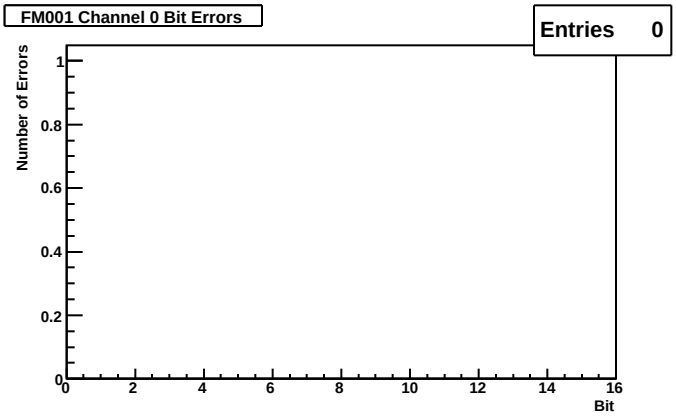

FM103 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

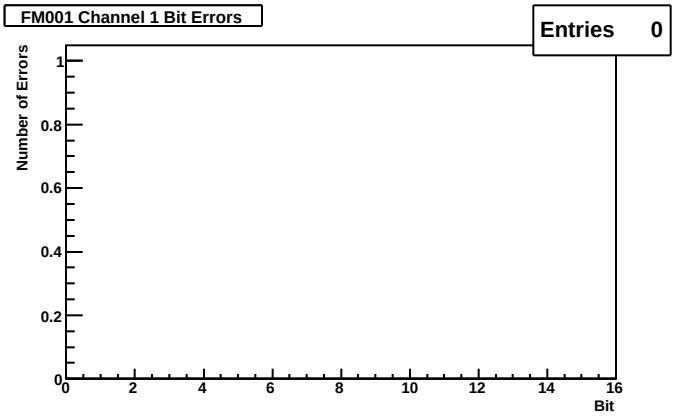

FM103 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

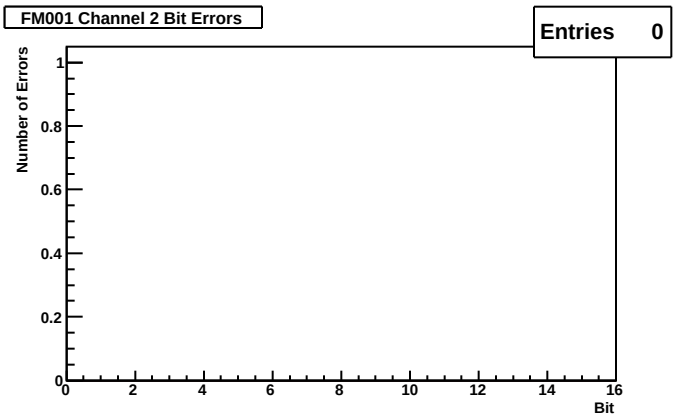

FM103 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

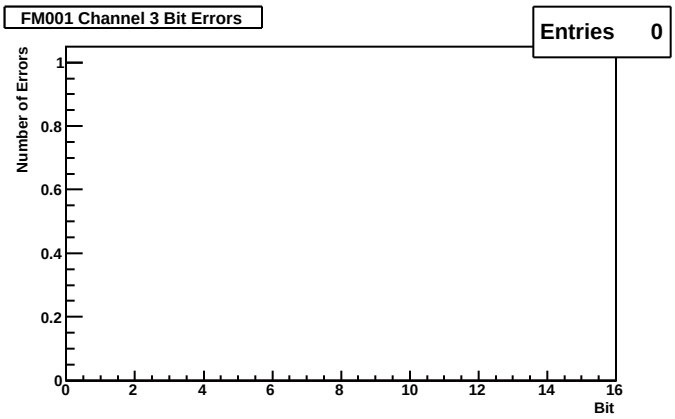

FM103 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

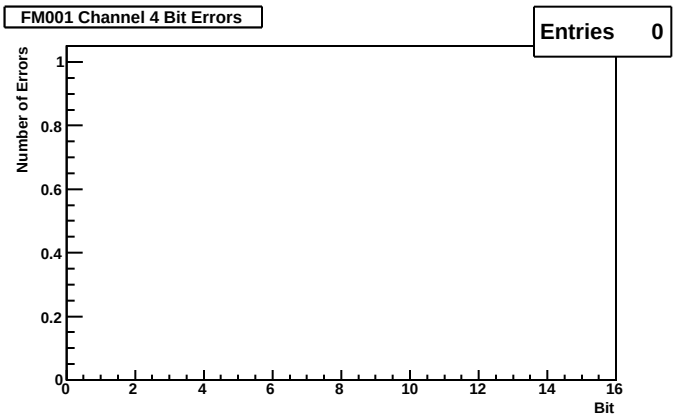

FM103 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

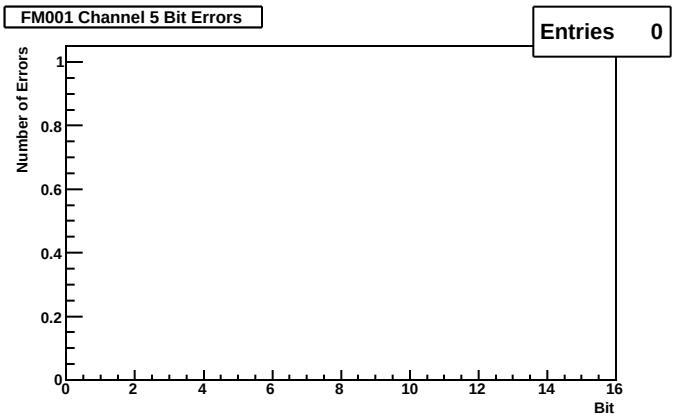

FM001 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

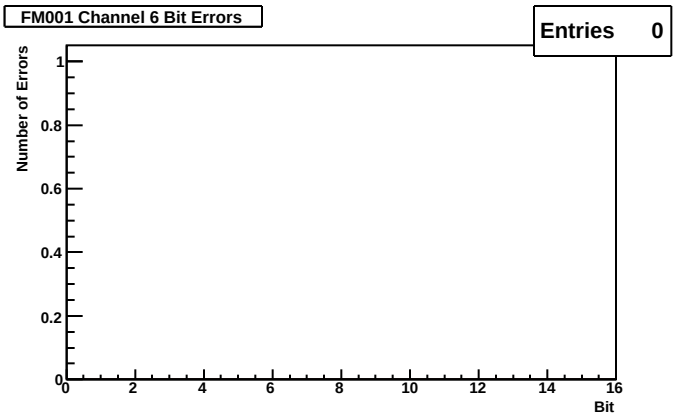

FM001 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

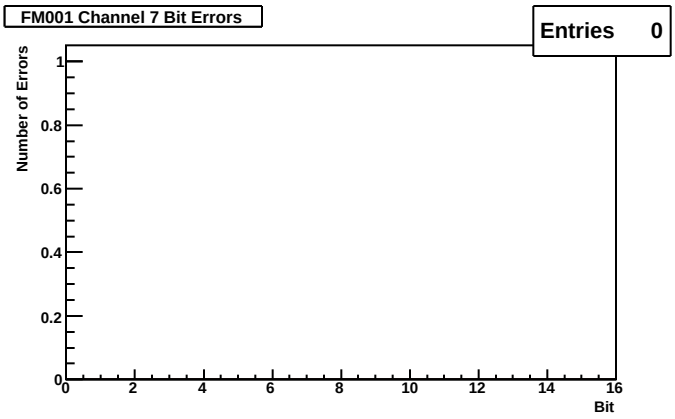

FM001 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

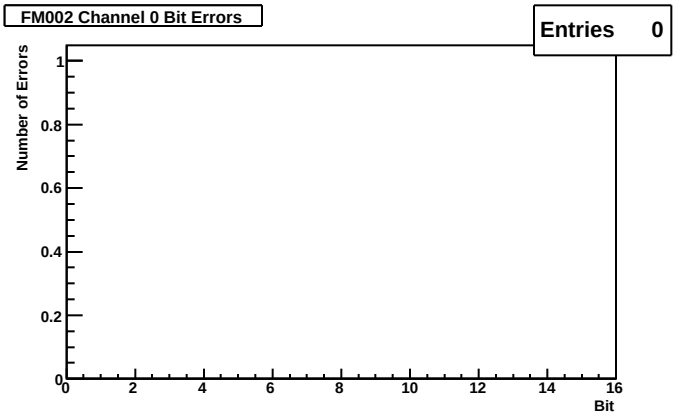

FM001 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

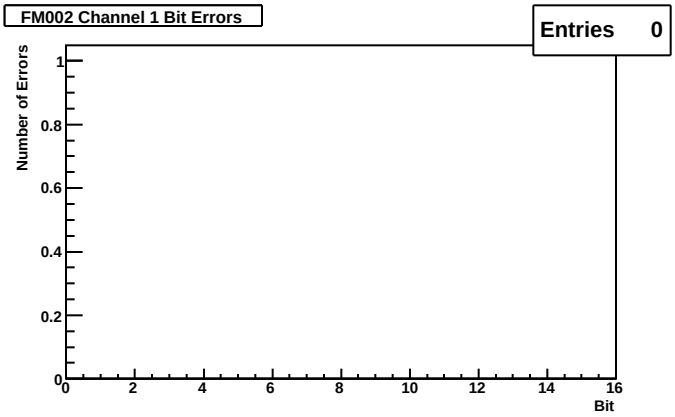

FM001 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

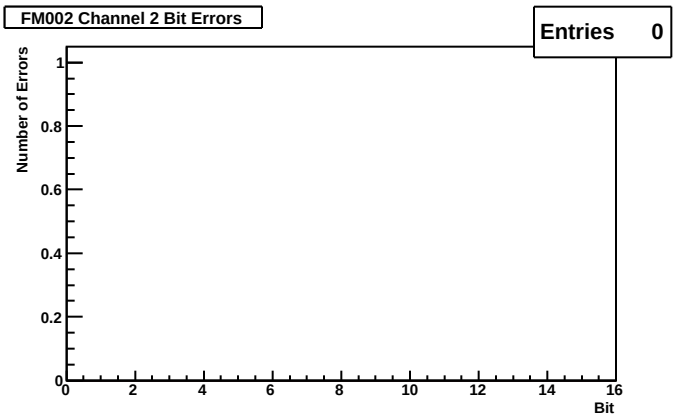

FM001 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

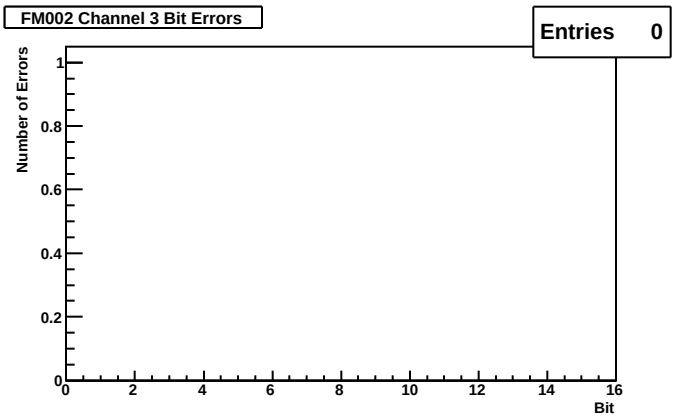

FM001 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

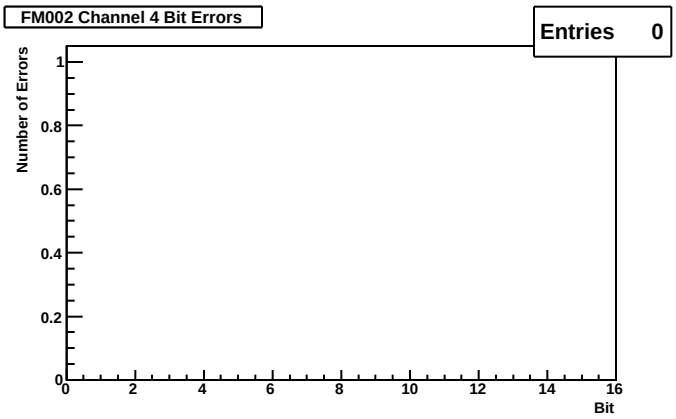

FM001 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

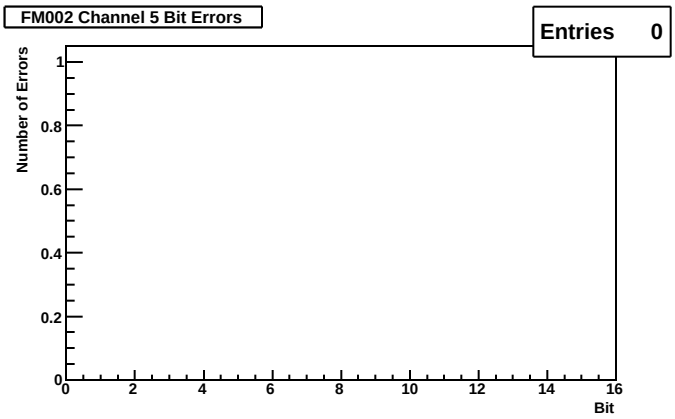

FM002 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

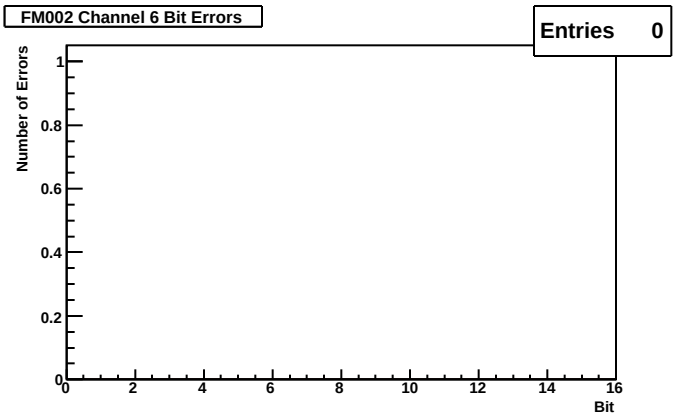

FM002 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

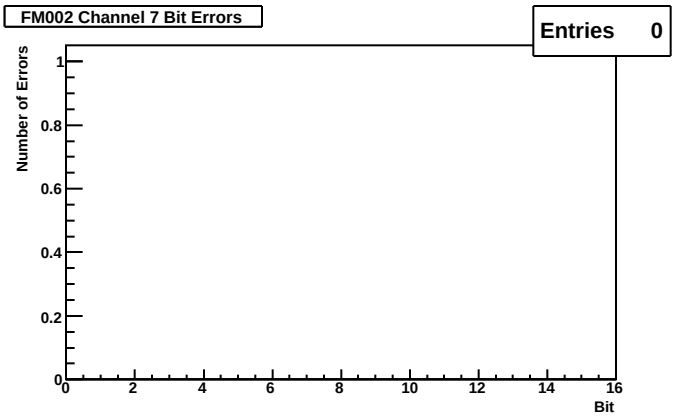

FM002 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

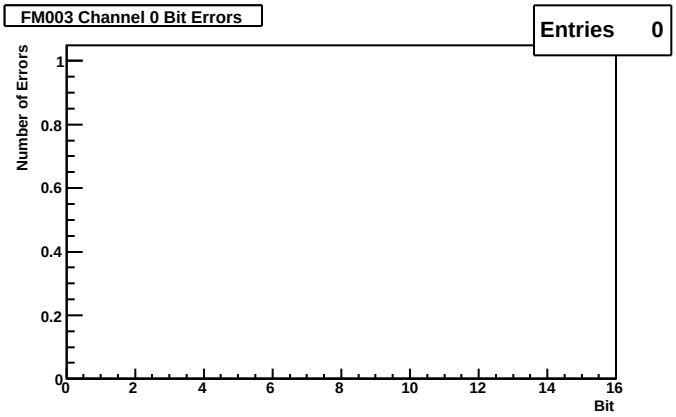

FM002 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

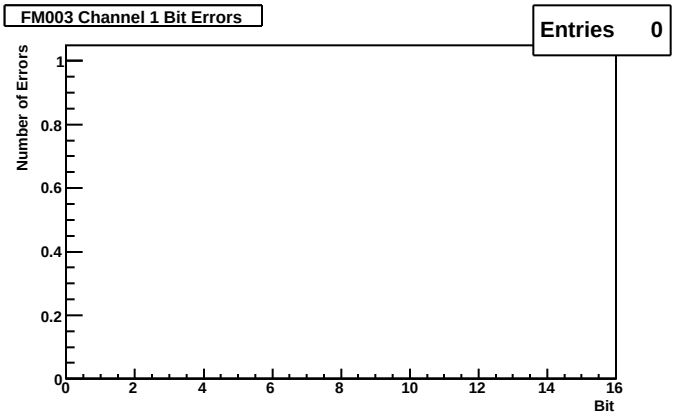

FM002 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

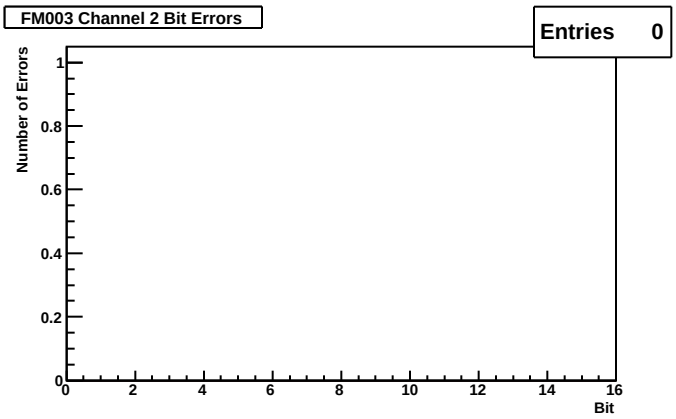

FM002 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

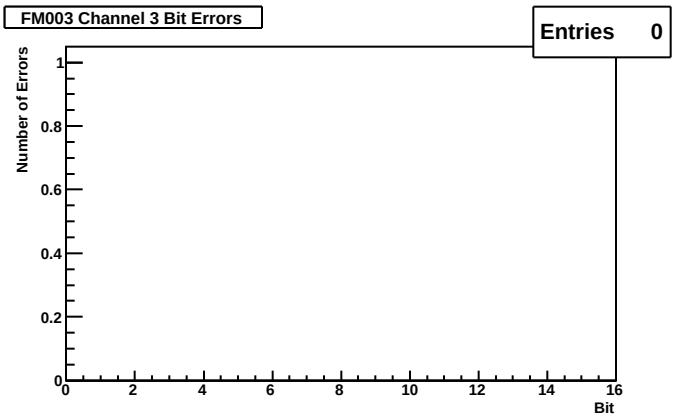

FM002 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

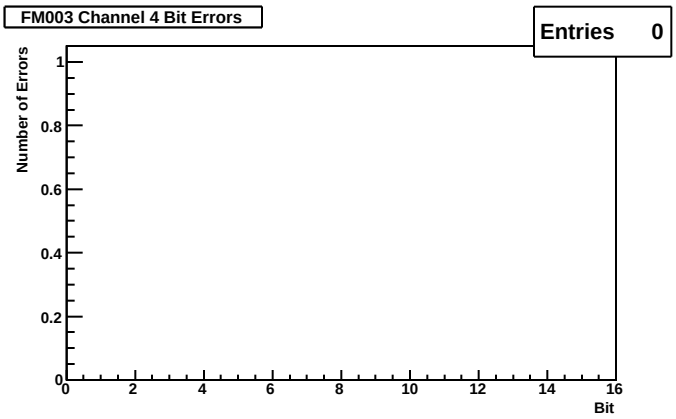

FM002 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

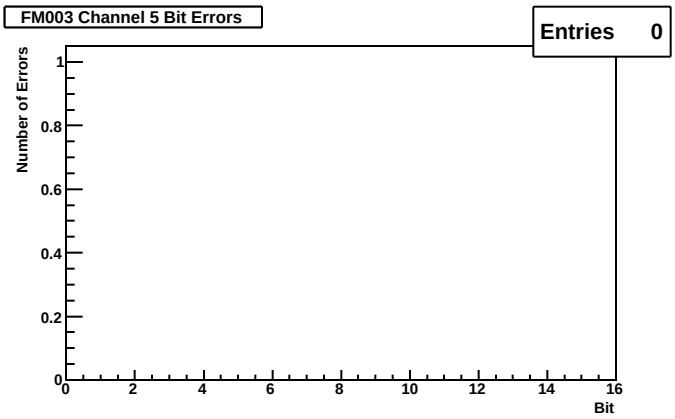

FM003 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

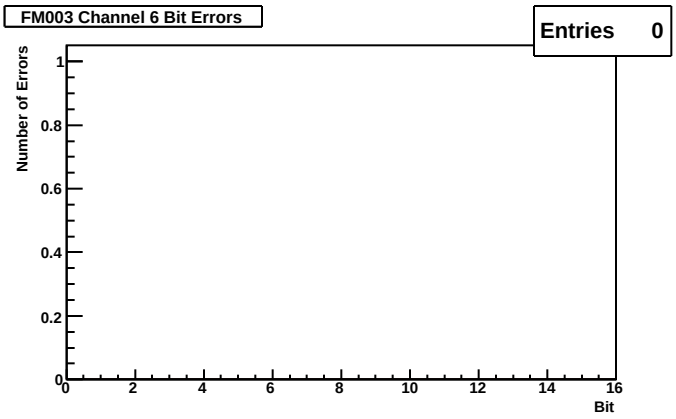

FM003 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

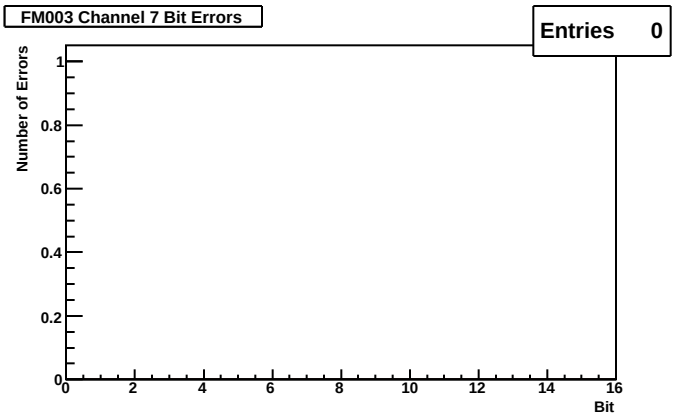

FM003 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

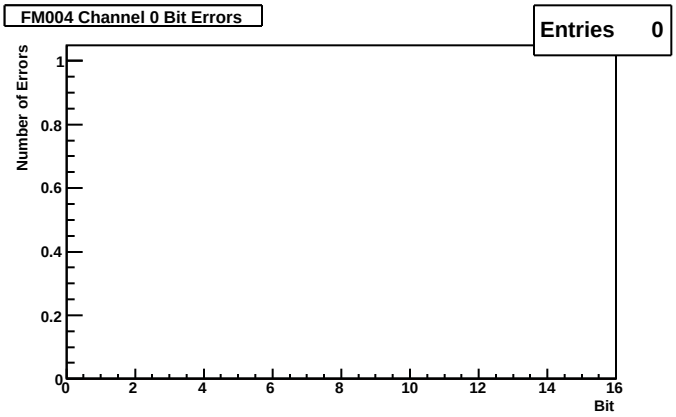

FM003 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

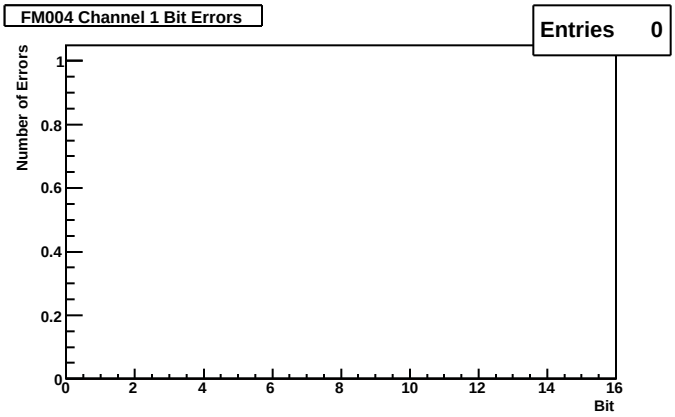

FM003 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

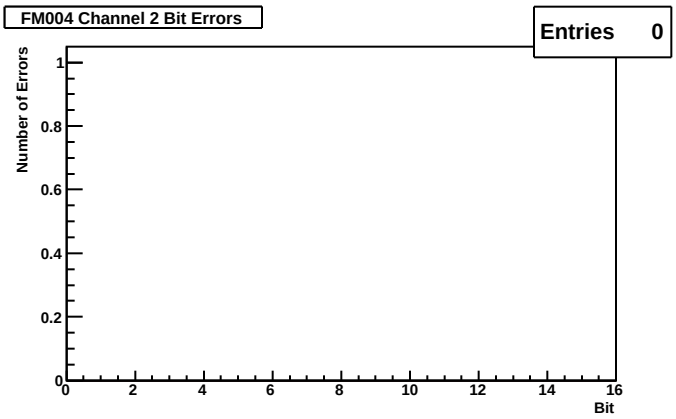

FM003 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

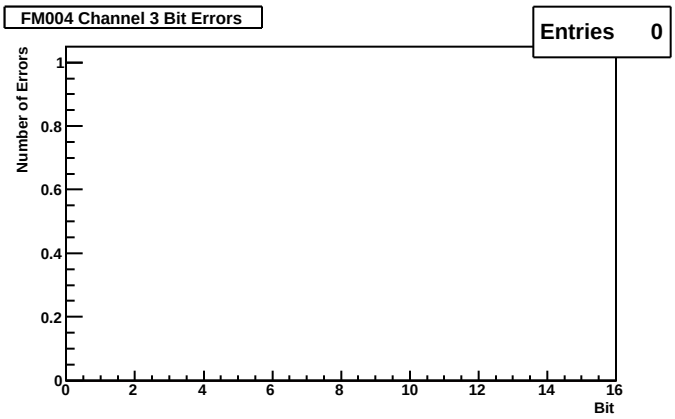

FM003 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

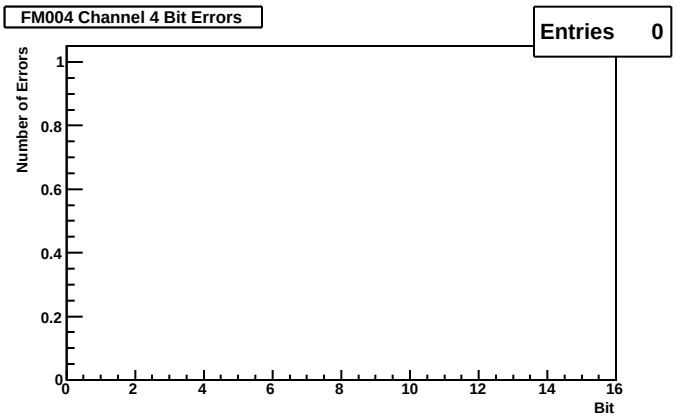

FM003 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

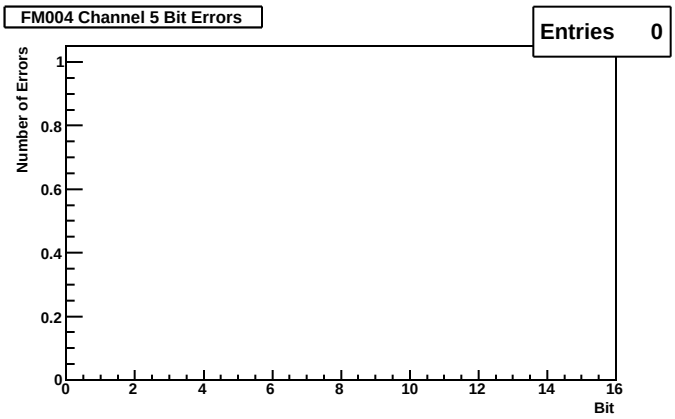

FM004 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

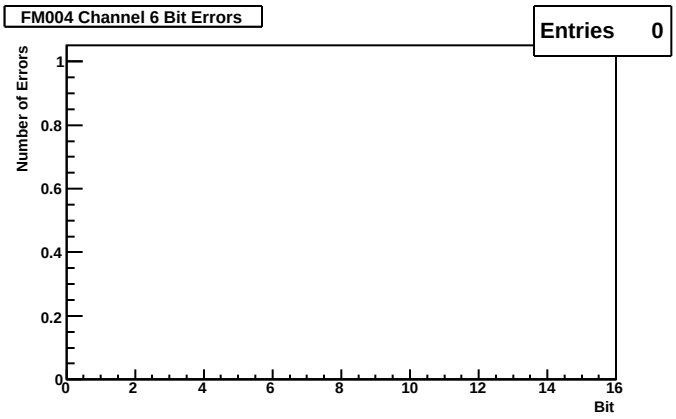

FM004 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

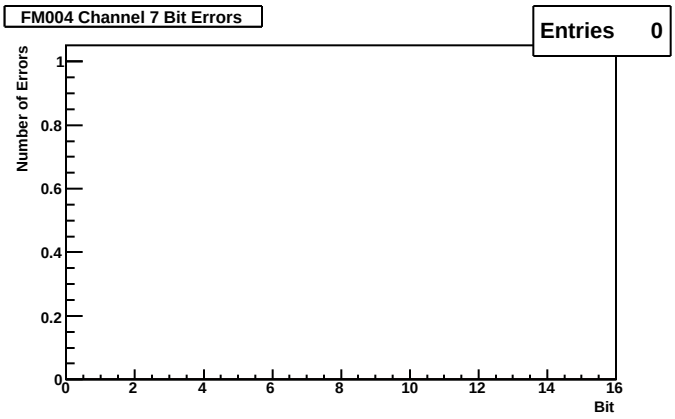

FM004 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

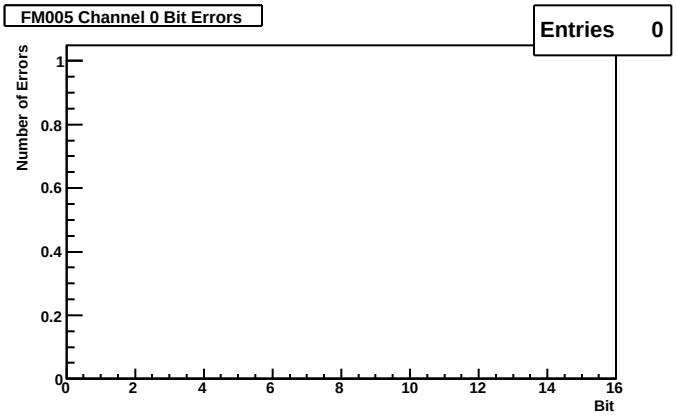

FM004 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

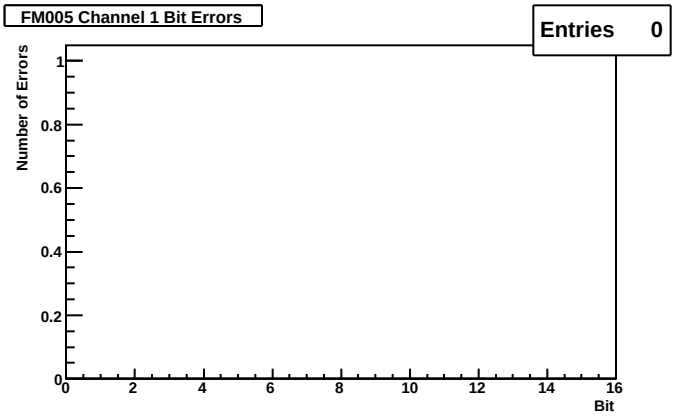

FM004 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

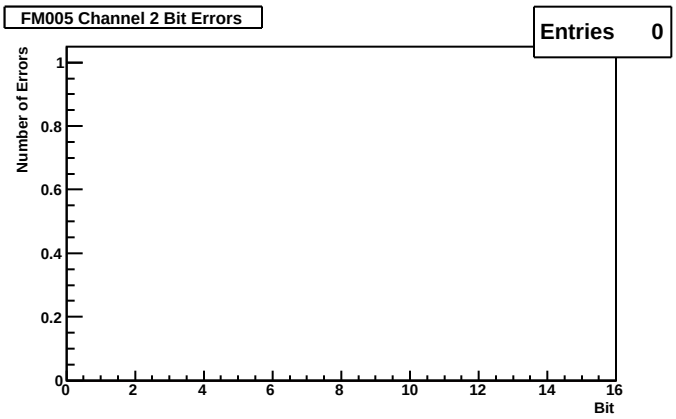

FM004 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

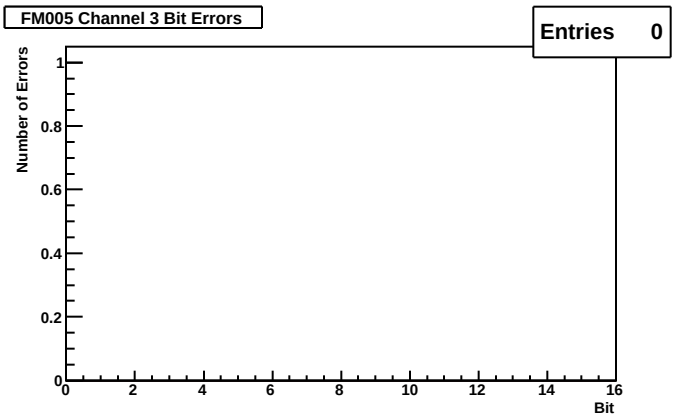

FM004 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

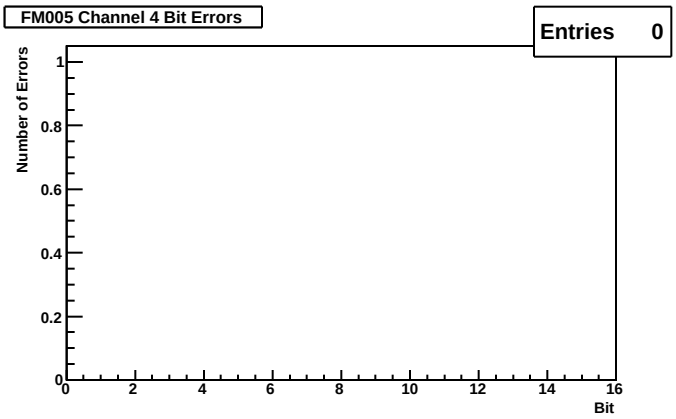

FM004 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

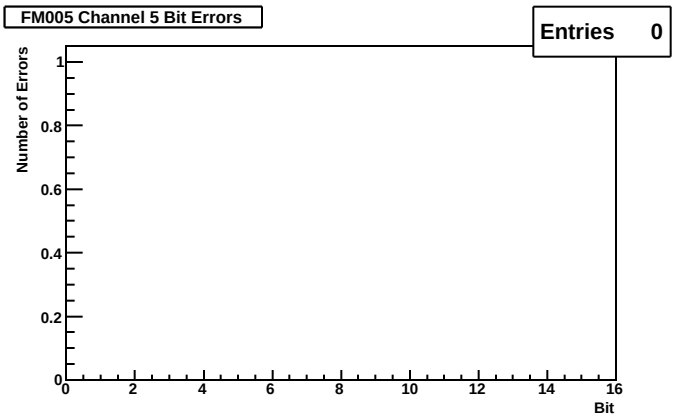

FM005 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

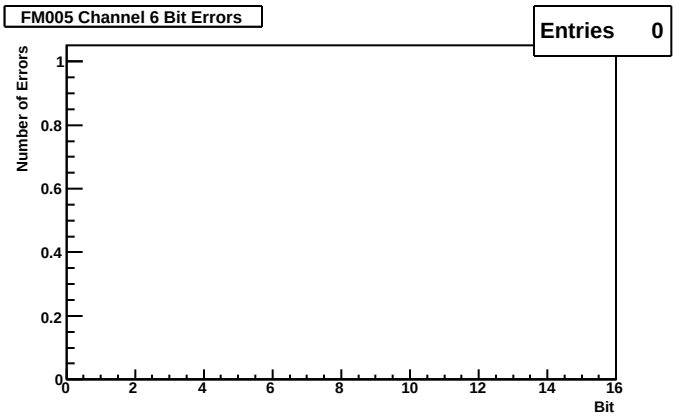

FM005 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

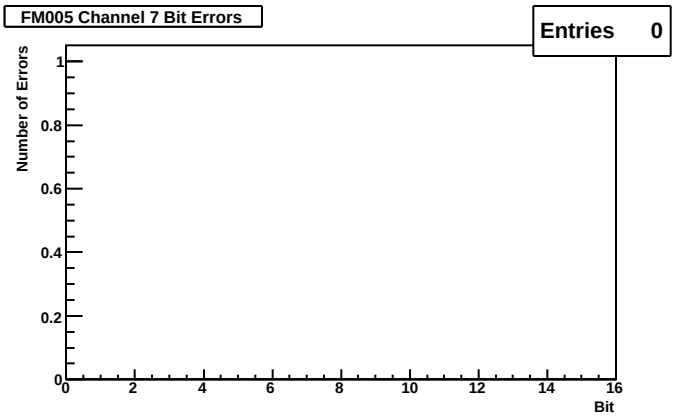

FM005 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

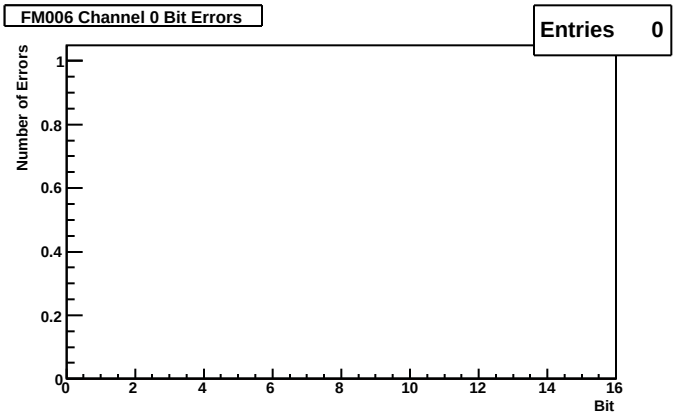

FM005 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

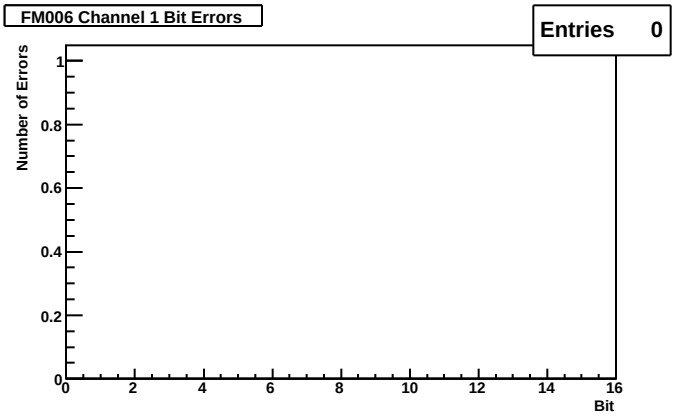

FM005 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

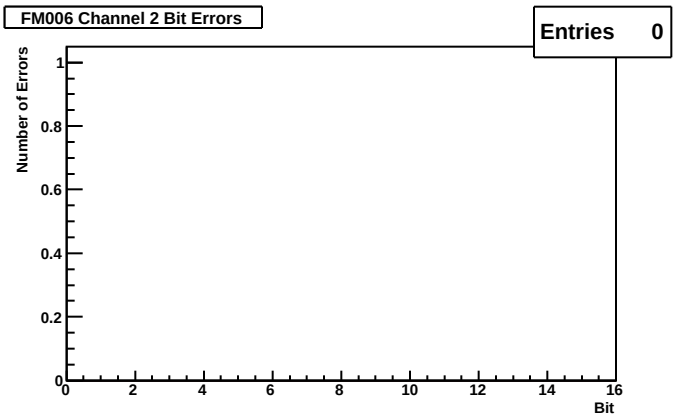

FM005 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

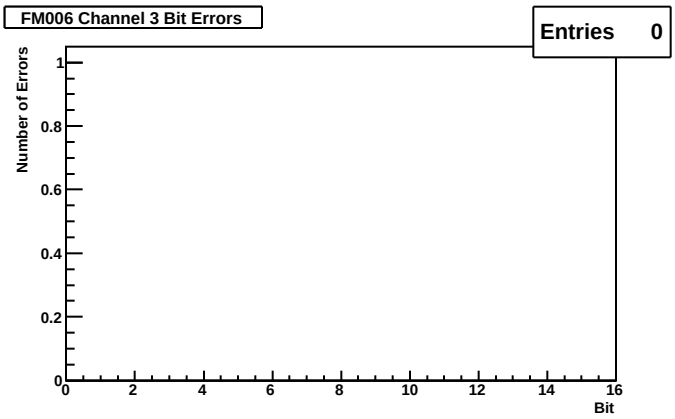

FM005 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

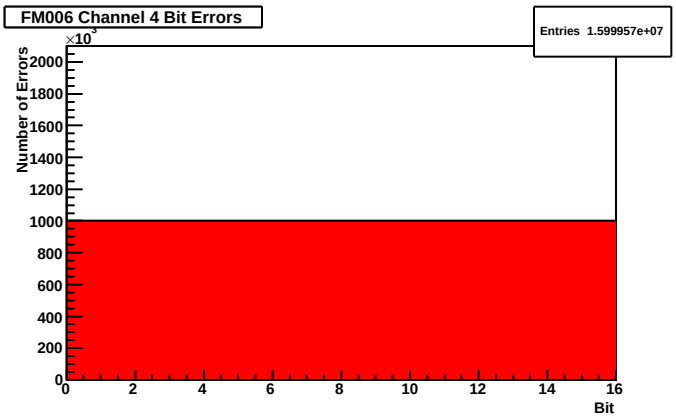

FM005 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

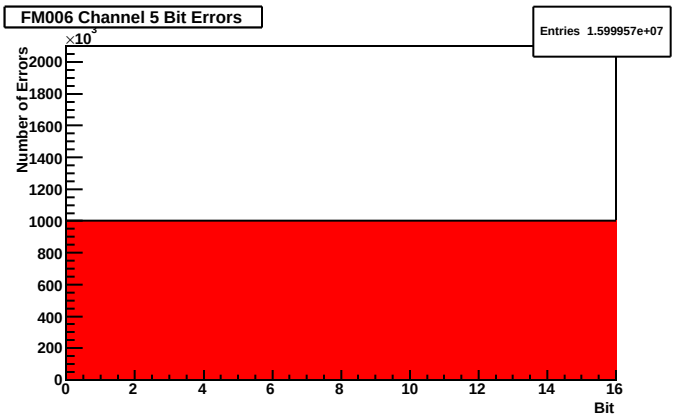

FM006 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

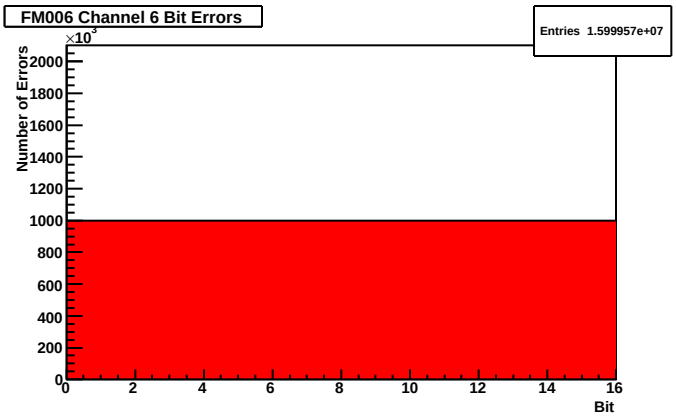

FM006 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

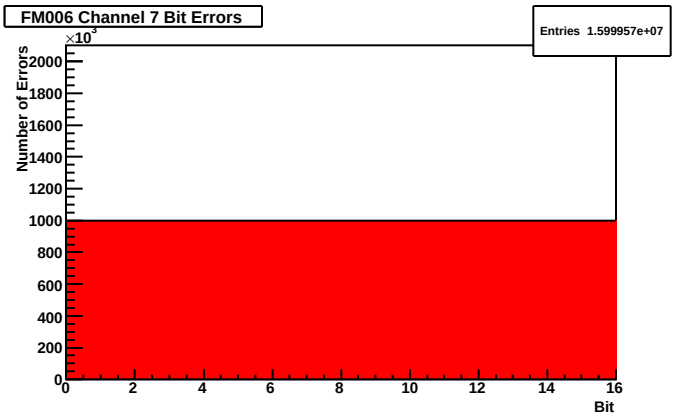

FM006 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

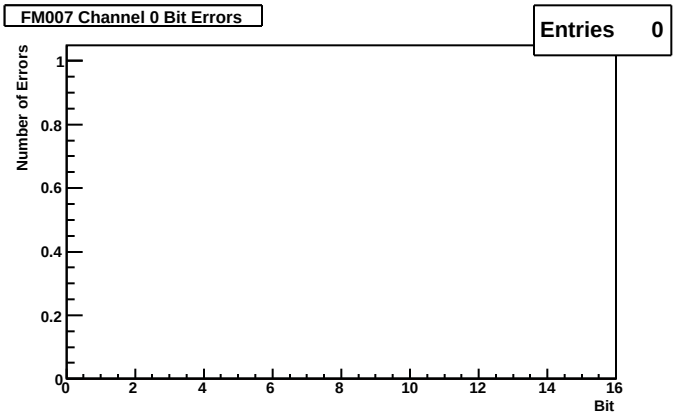

FM006 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

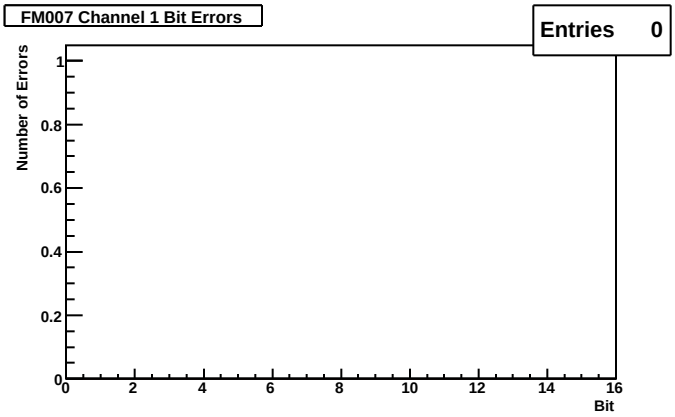

FM006 Channel 4 Bit Errors
Entries 1.599957e+07
Number of Errors
Bit

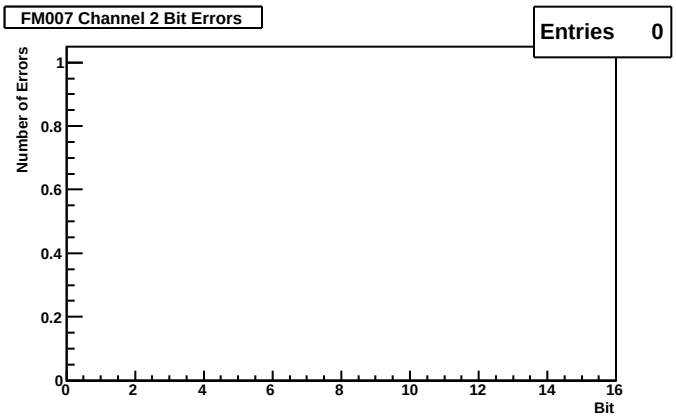

FM006 Channel 5 Bit Errors
Entries 1.599957e+07
Number of Errors
Bit

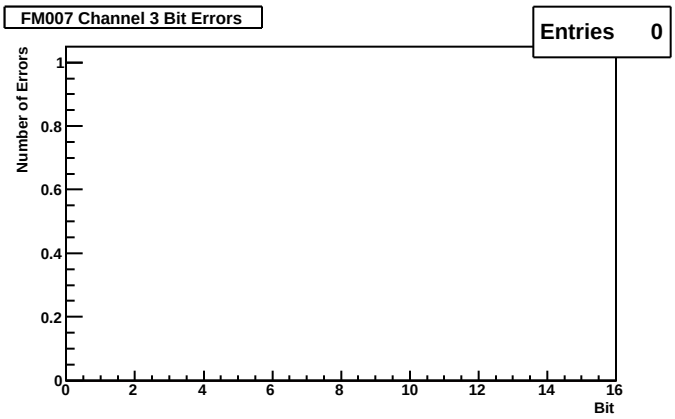

FM006 Channel 6 Bit Errors
Entries 1.599957e+07
Number of Errors
Bit

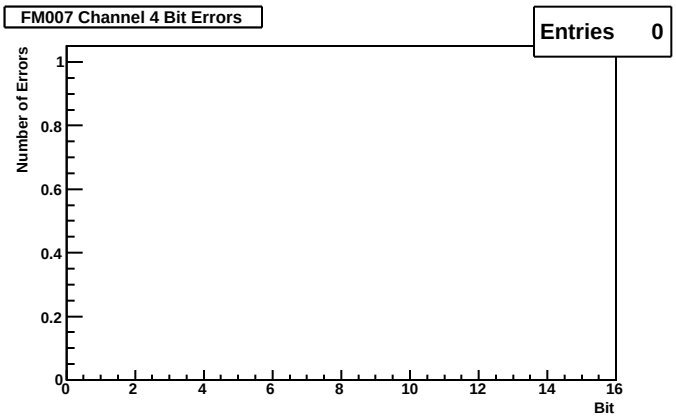

FM006 Channel 7 Bit Errors
Entries 1.599957e+07
Number of Errors
Bit

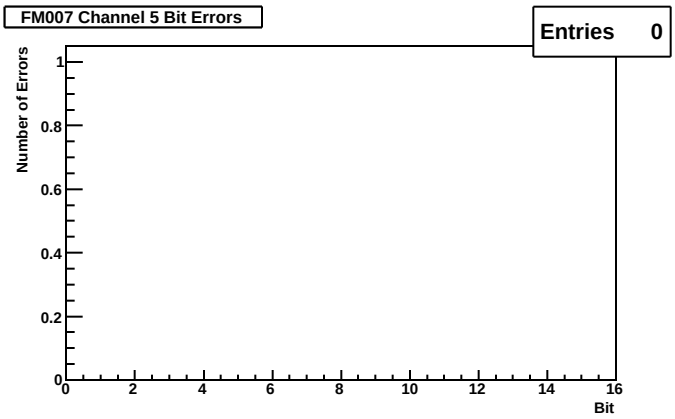

FM007 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

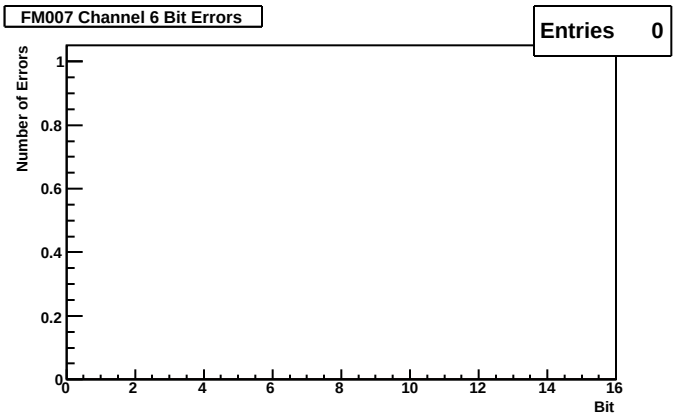

FM007 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

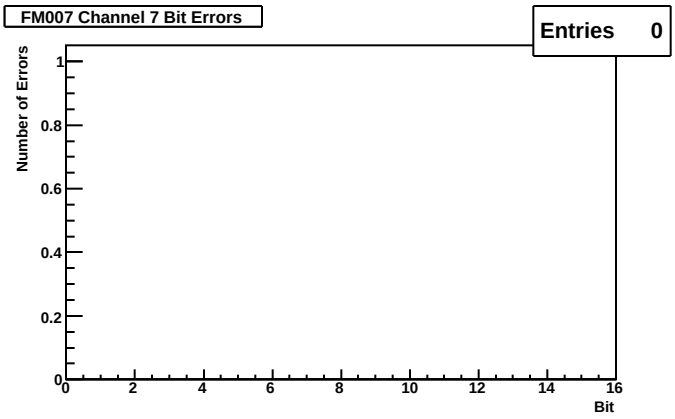

FM007 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

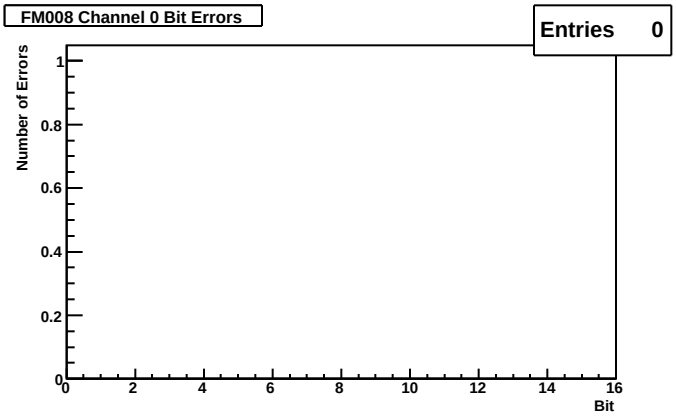

FM007 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

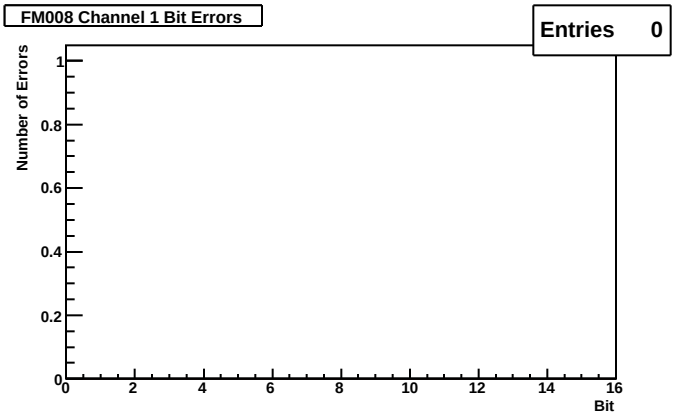

FM007 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

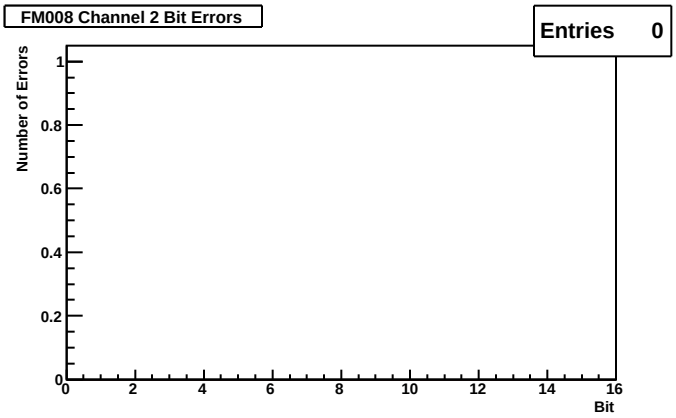

FM007 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

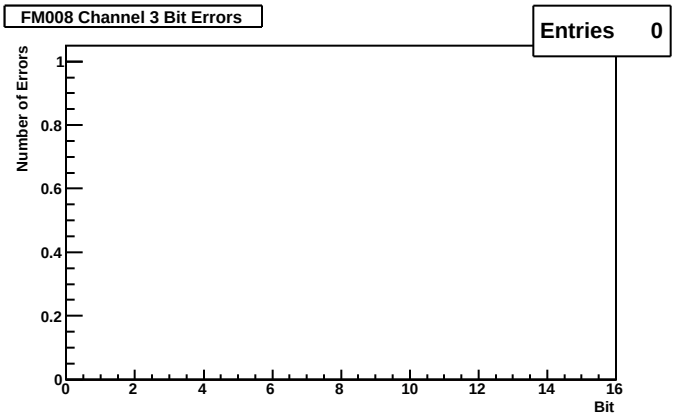

FM007 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

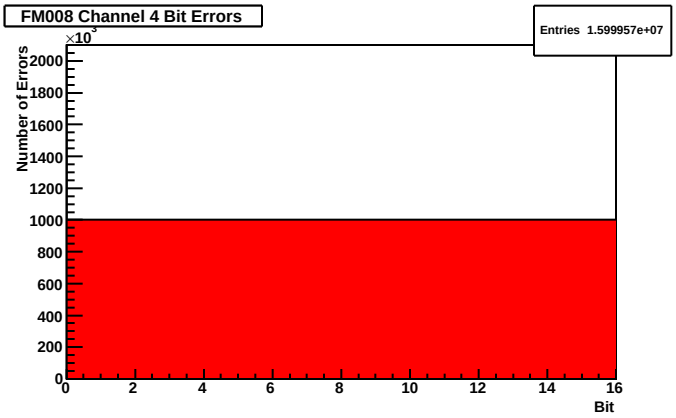

FM007 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

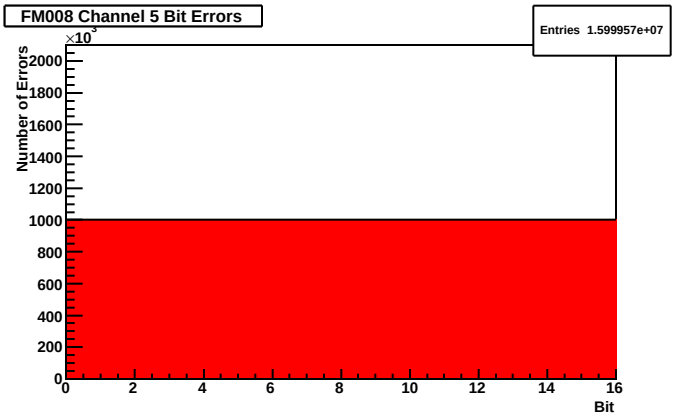

FM008 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

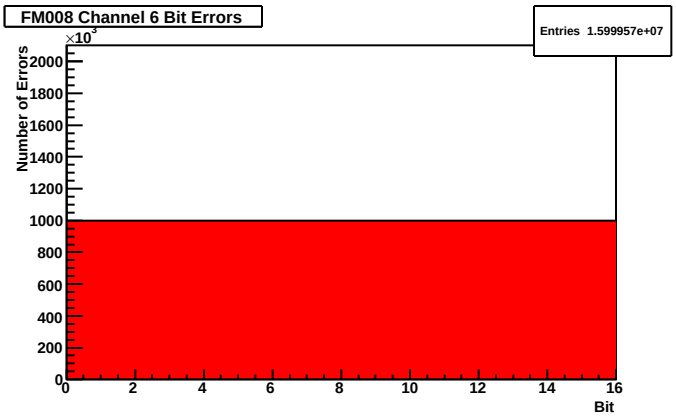

FM008 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

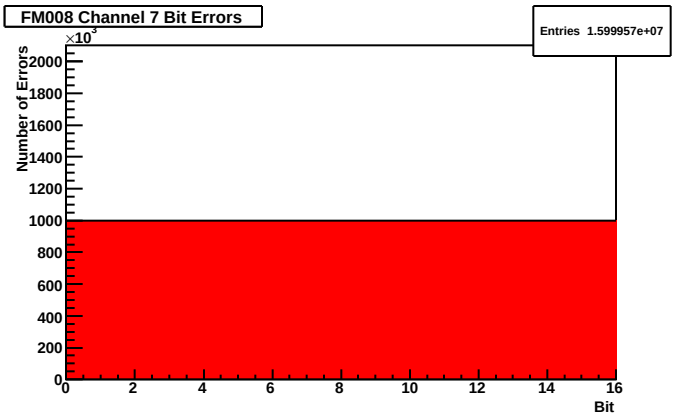

FM008 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

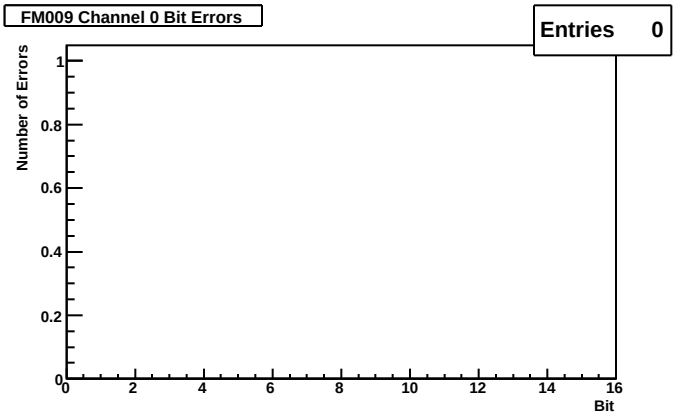

FM008 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

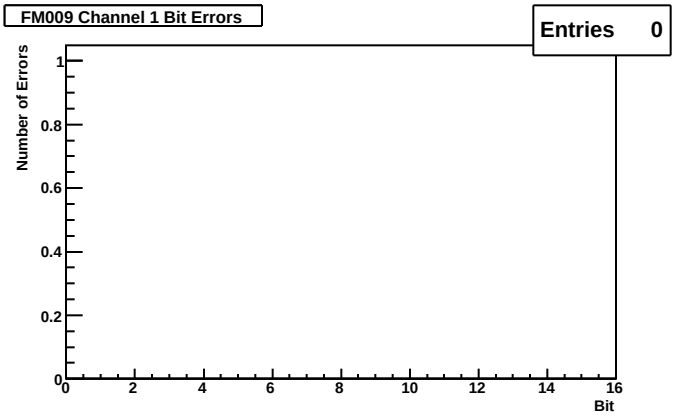

FM008 Channel 4 Bit Errors
Entries 1.599957e+07
Number of Errors
Bit

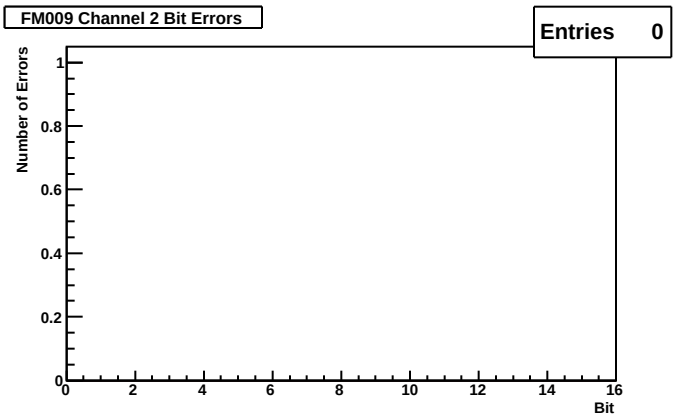

FM008 Channel 5 Bit Errors
Entries 1.599957e+07
Number of Errors
Bit

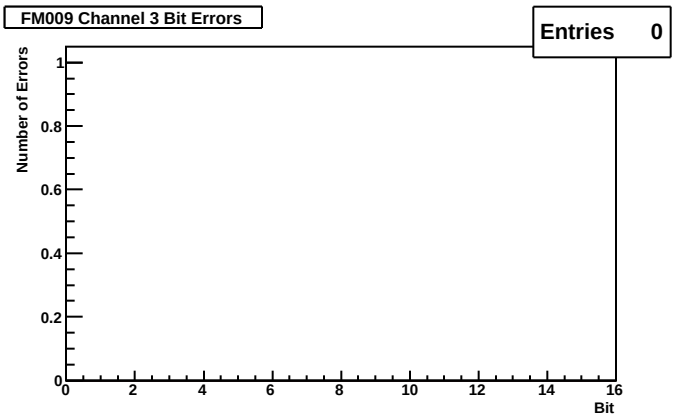

FM008 Channel 6 Bit Errors
Entries 1.599957e+07
Number of Errors
Bit

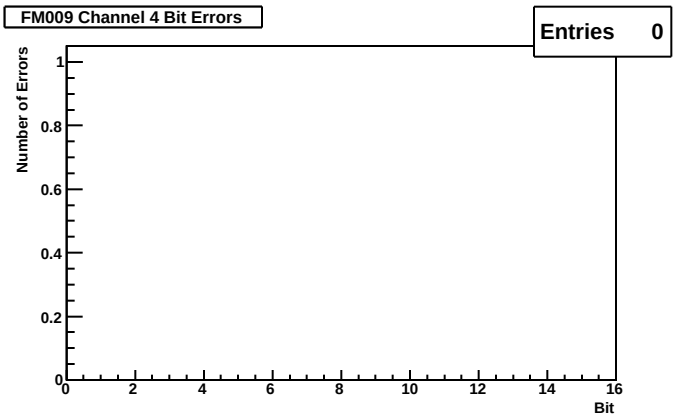

FM008 Channel 7 Bit Errors
Entries 1.599957e+07
Number of Errors
Bit

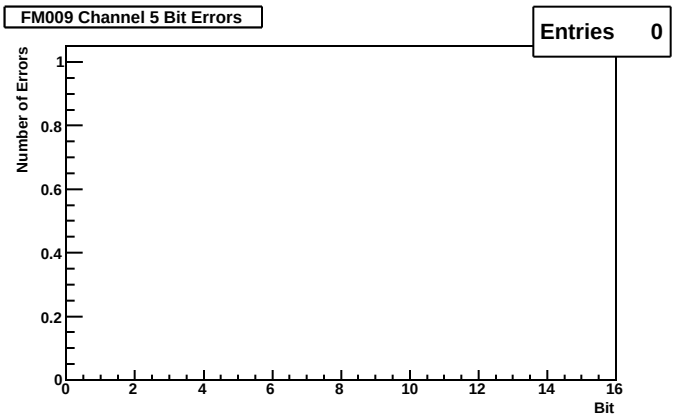

FM009 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

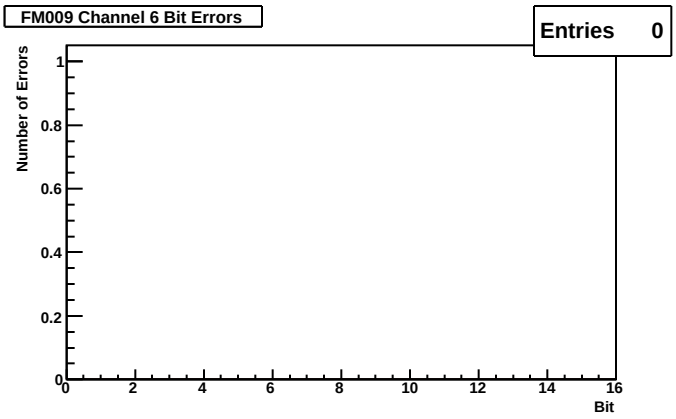

FM009 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

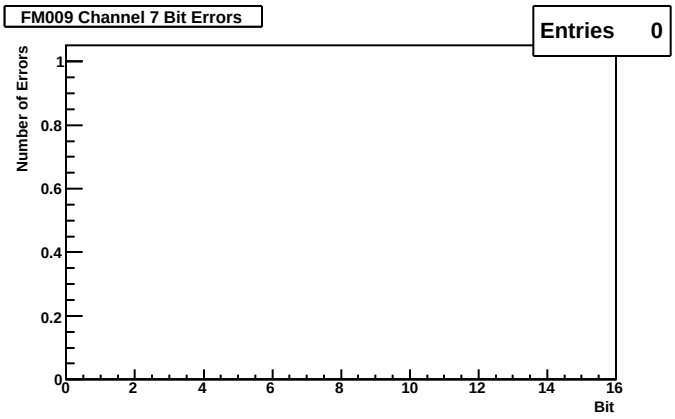

FM009 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

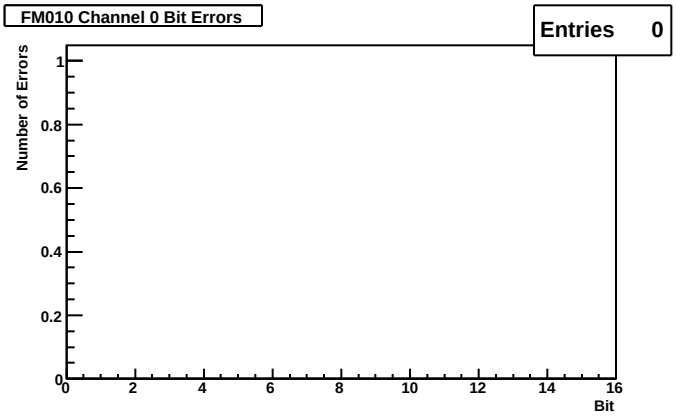

FM009 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

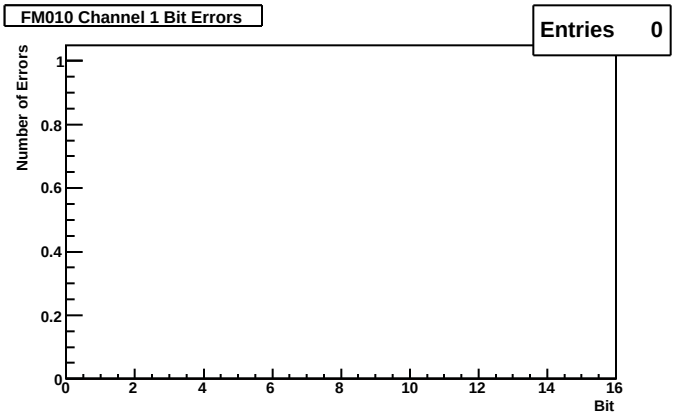

FM009 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

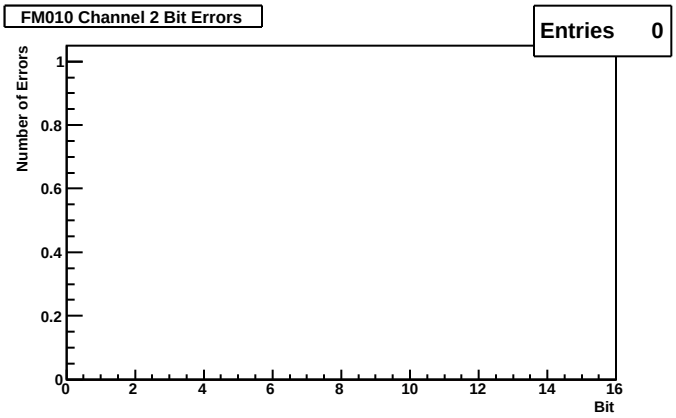

FM009 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

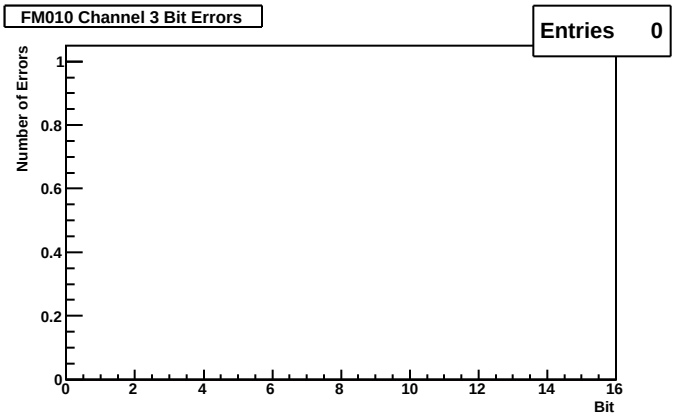

FM009 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

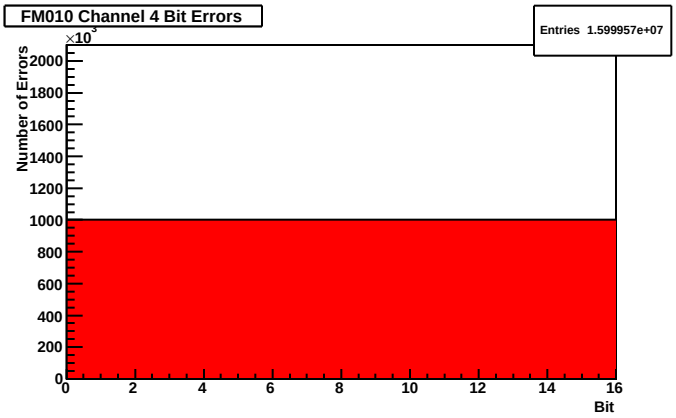

FM009 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

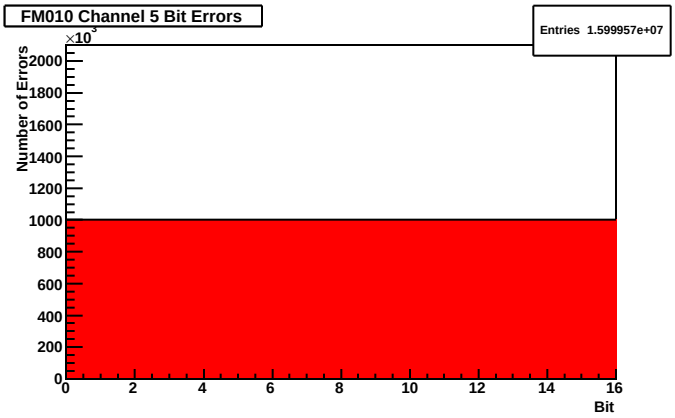

FM010 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

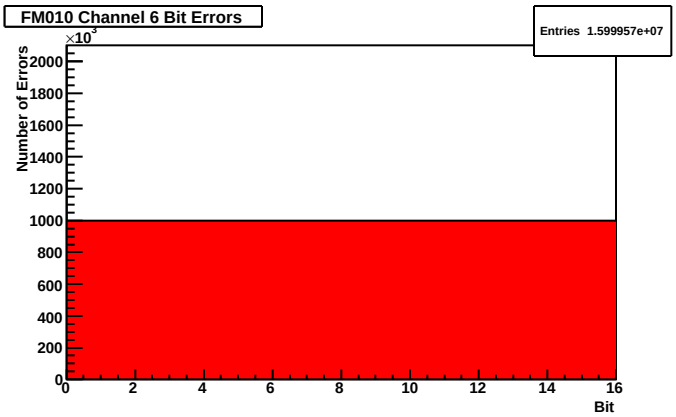

FM010 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

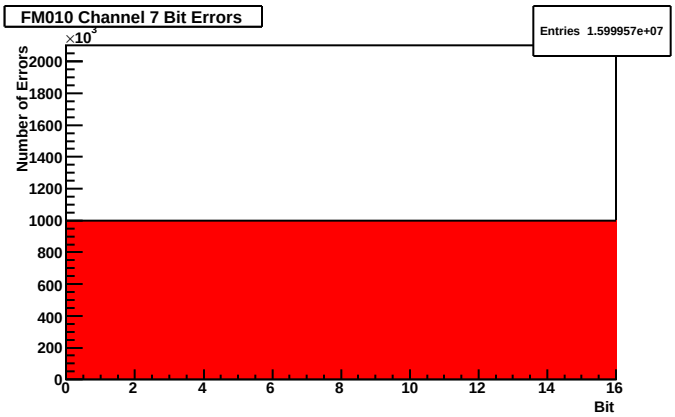

FM010 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

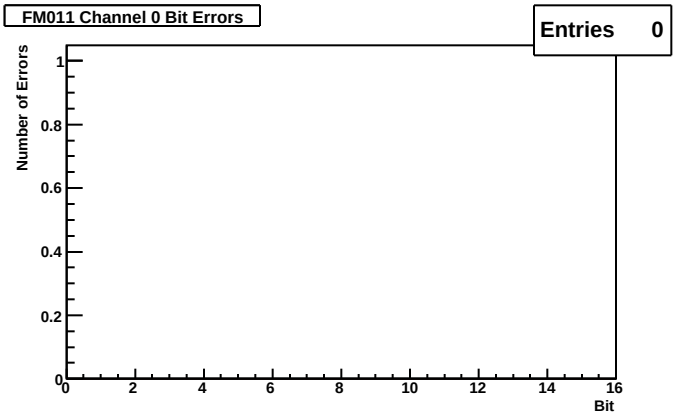

FM010 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

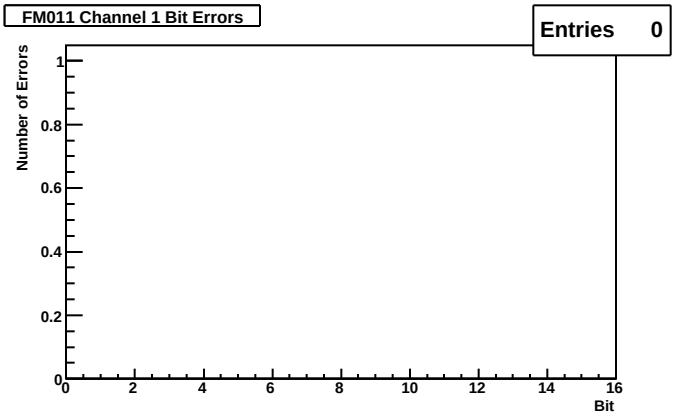

FM010 Channel 4 Bit Errors
Entries 1.599957e+07
Number of Errors
Bit

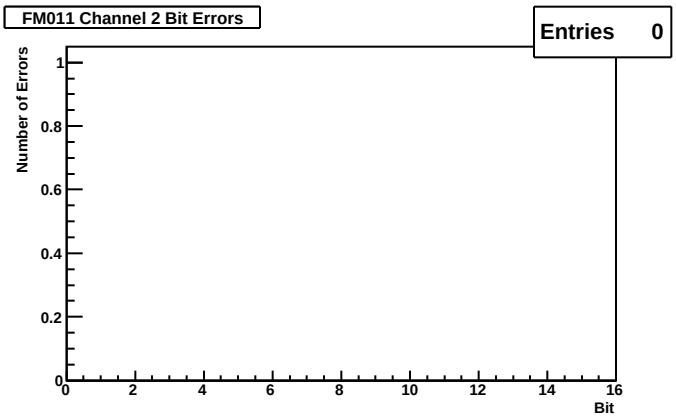

FM010 Channel 5 Bit Errors
Entries 1.599957e+07
Number of Errors
Bit

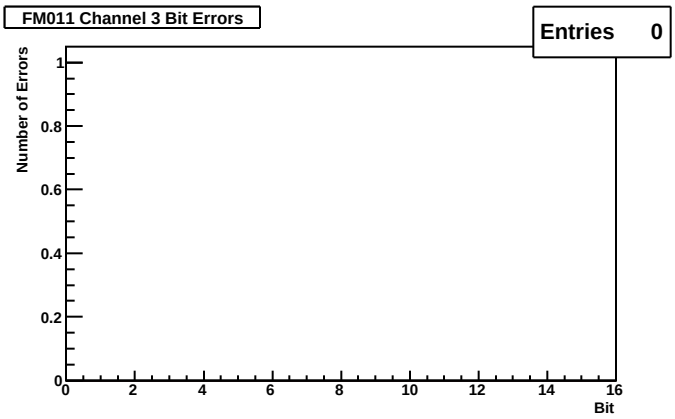

FM010 Channel 6 Bit Errors
Entries 1.599957e+07
Number of Errors
Bit

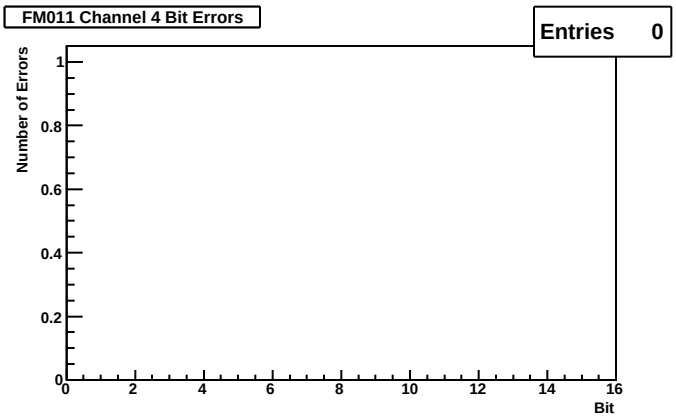

FM010 Channel 7 Bit Errors
Entries 1.599957e+07
Number of Errors
Bit

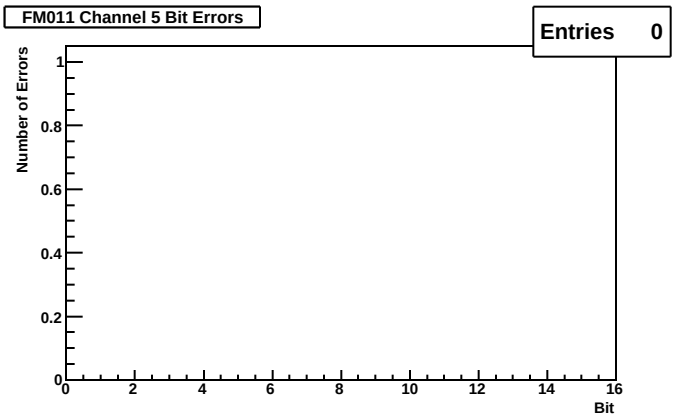

FM011 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

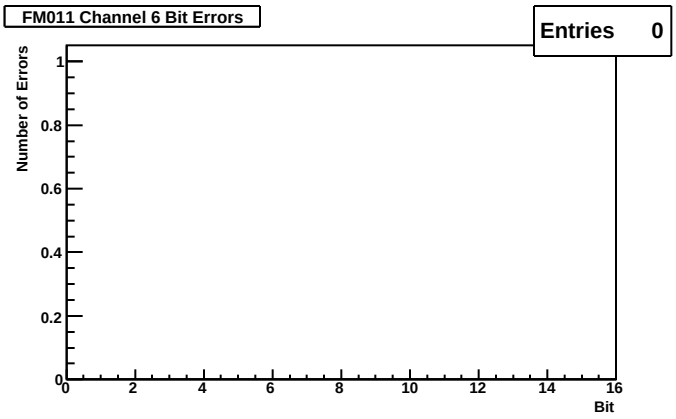

FM011 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

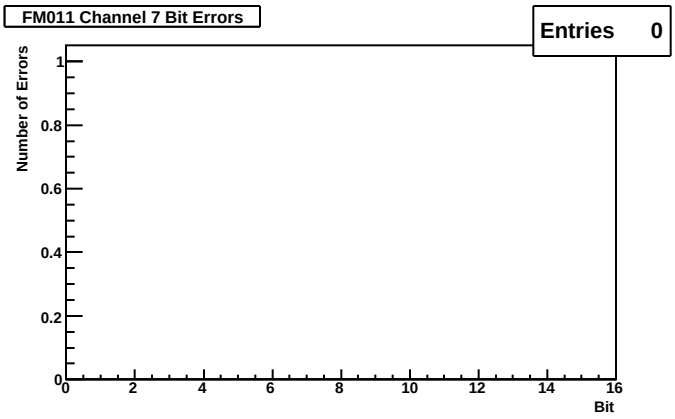

FM011 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

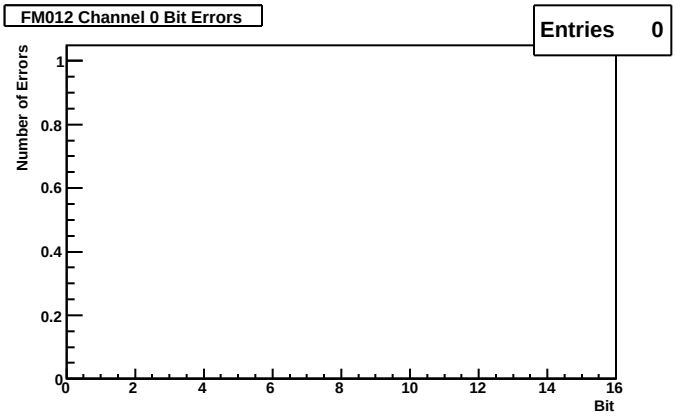

FM011 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

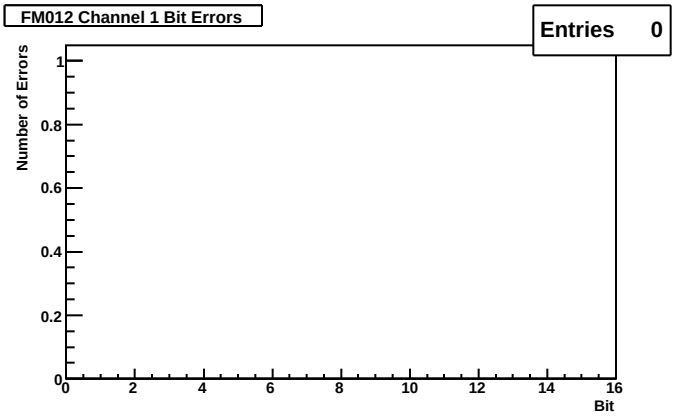

FM011 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

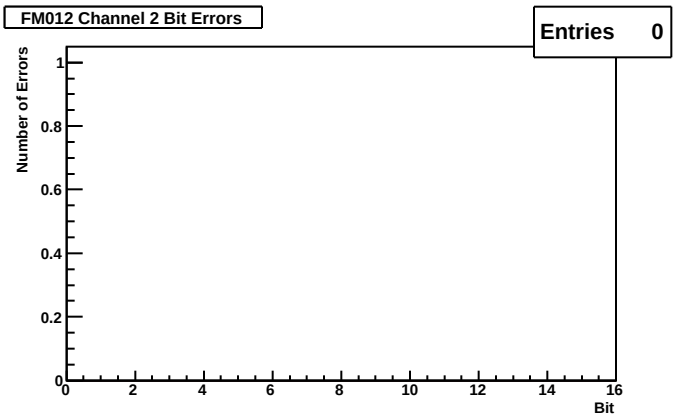

FM011 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

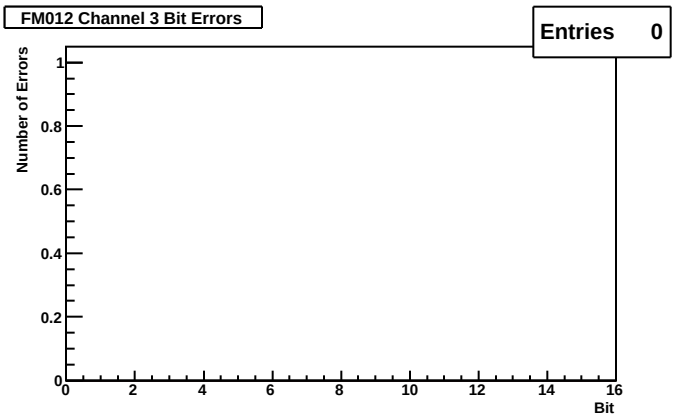

FM011 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

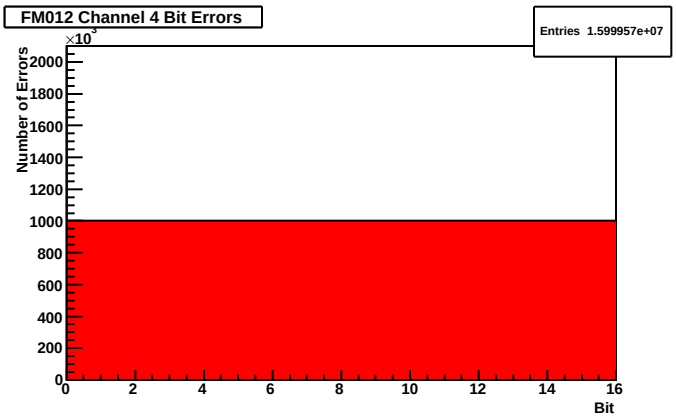

FM011 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

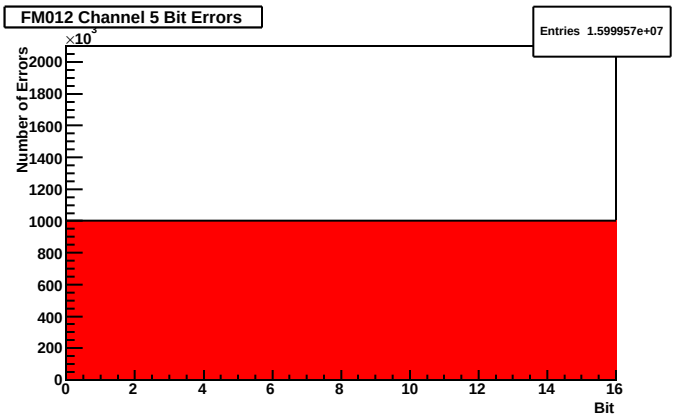

FM012 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

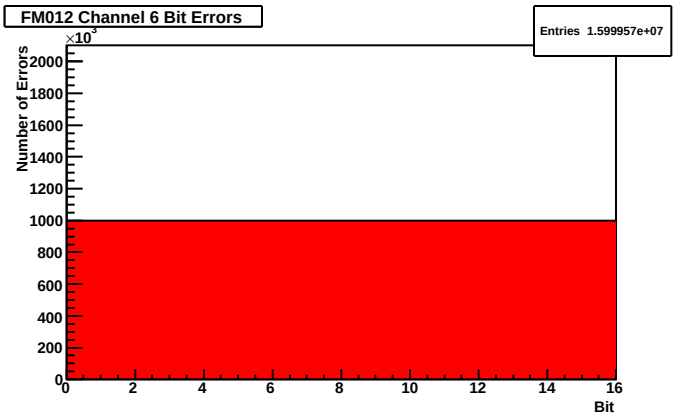

FM012 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

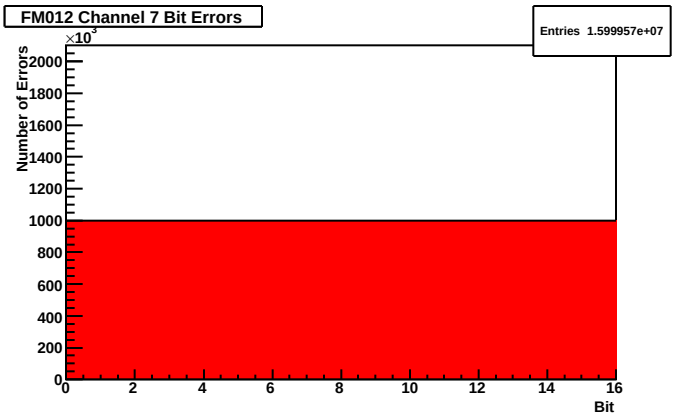

FM012 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit


FM012 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit


FM012 Channel 4 Bit Errors
Entries 1.599957e+07
Number of Errors
Bit


FM012 Channel 5 Bit Errors
Entries 1.599957e+07
Number of Errors
Bit


FM012 Channel 6 Bit Errors
Entries 1.599957e+07
Number of Errors
Bit


FM012 Channel 7 Bit Errors
Entries 1.599957e+07
Number of Errors
Bit