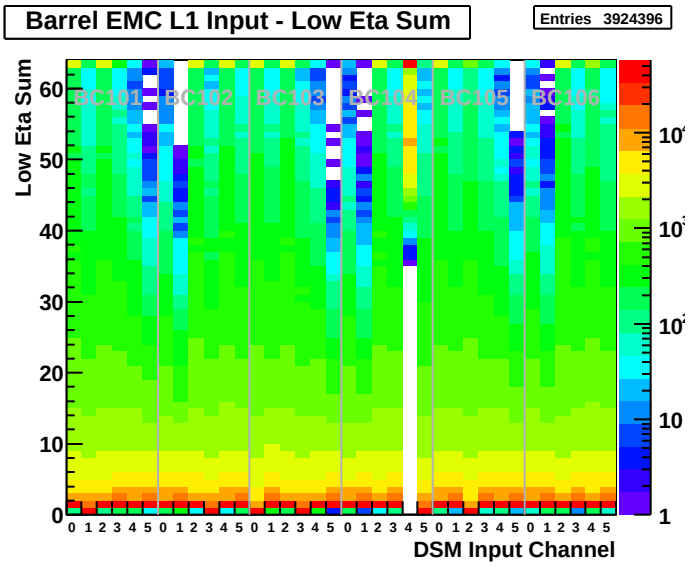
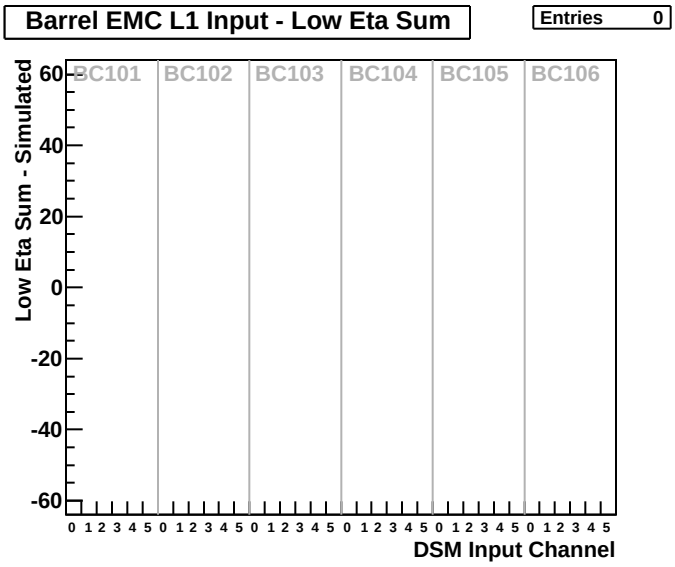
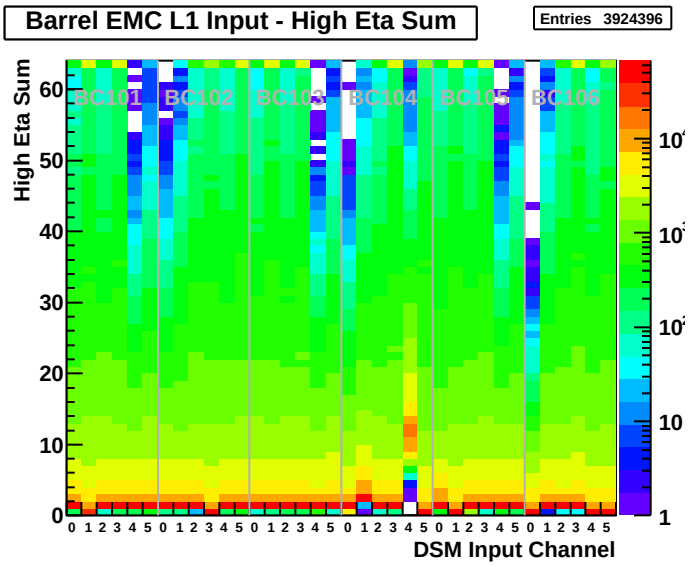
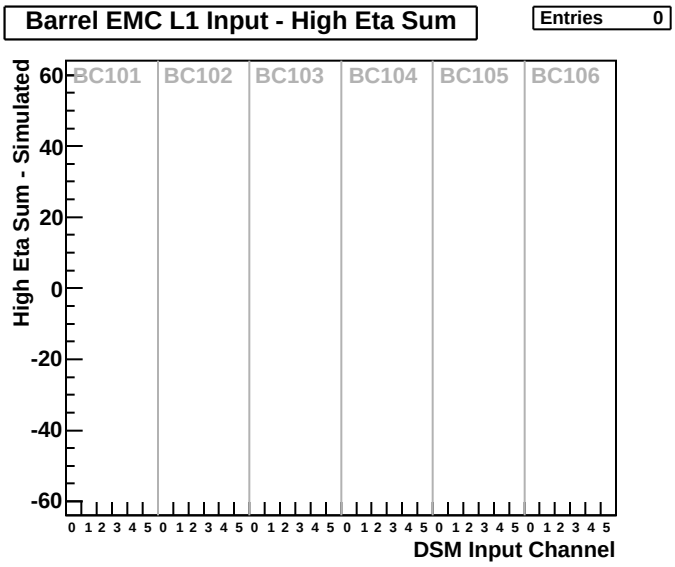
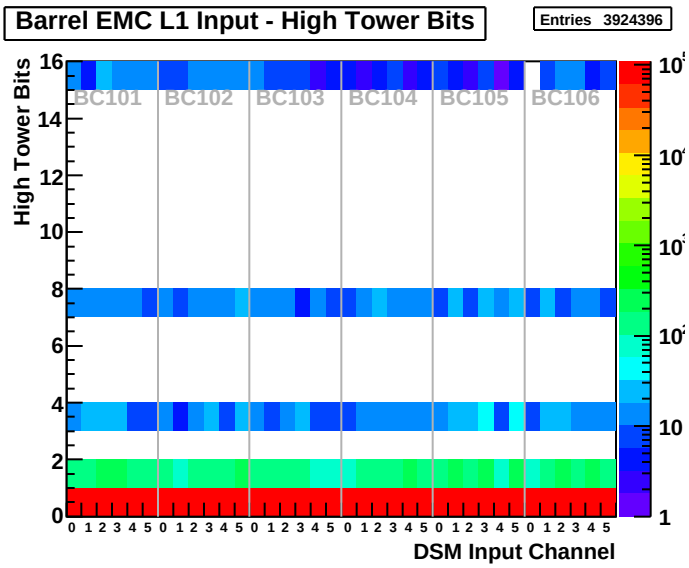
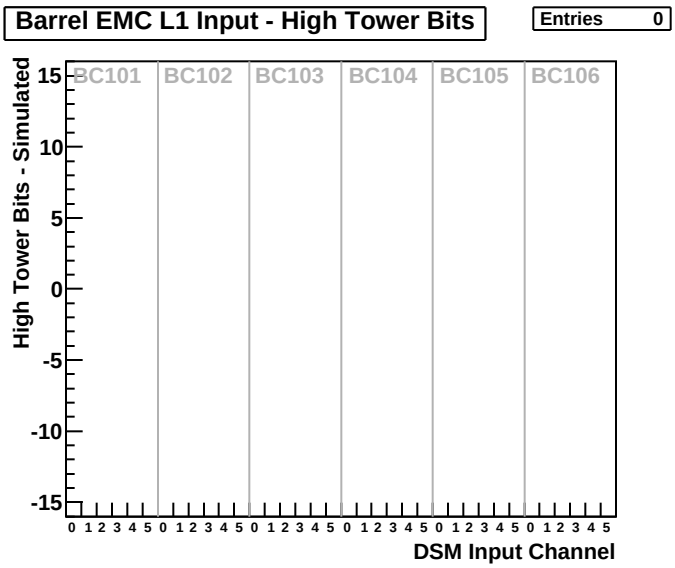

Barrel EMC L1 Input - Low Eta Sum
Entries 3924396
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

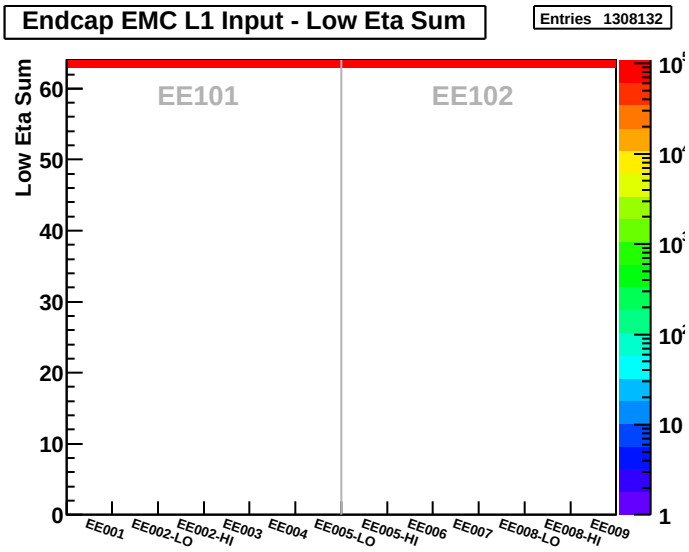

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

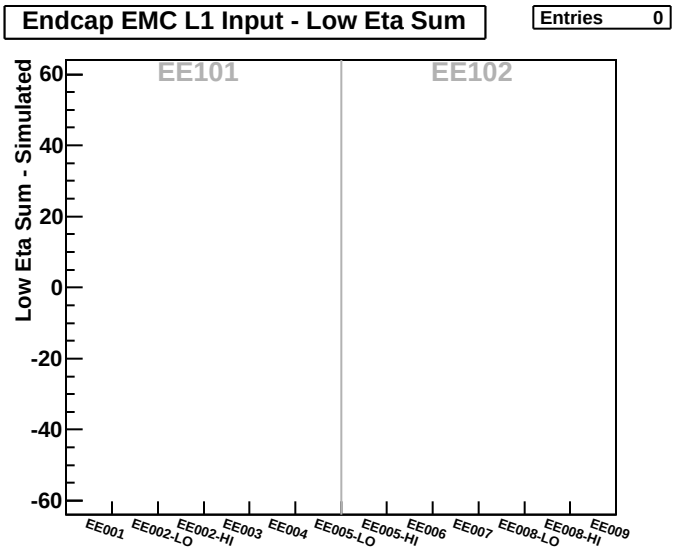

Barrel EMC L1 Input - High Eta Sum
Entries 3924396
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

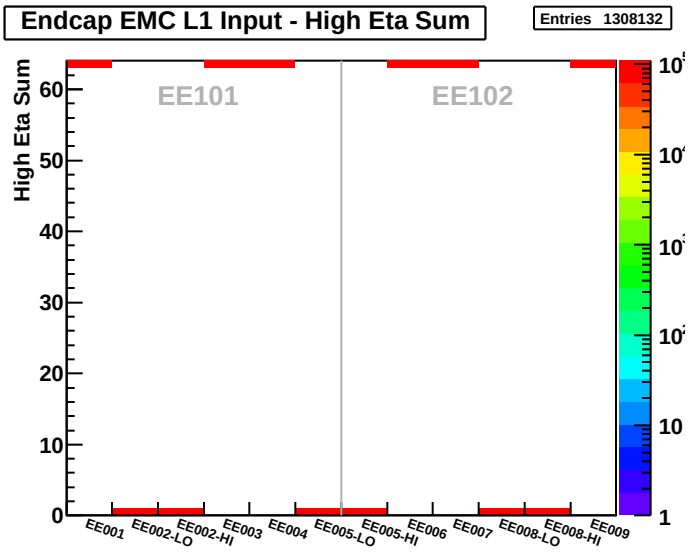

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

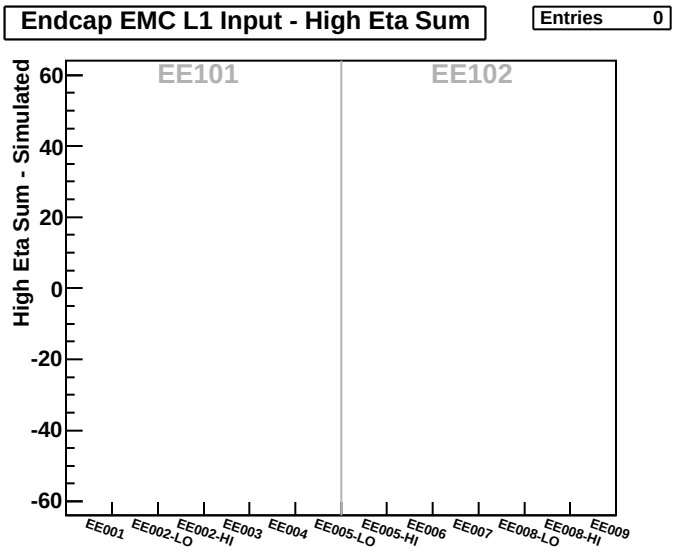

Barrel EMC L1 Input - High Tower Bits
Entries 3924396
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

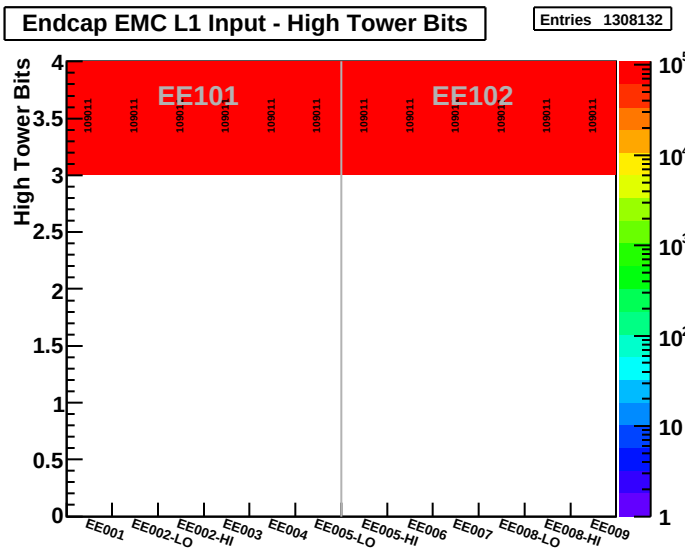

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

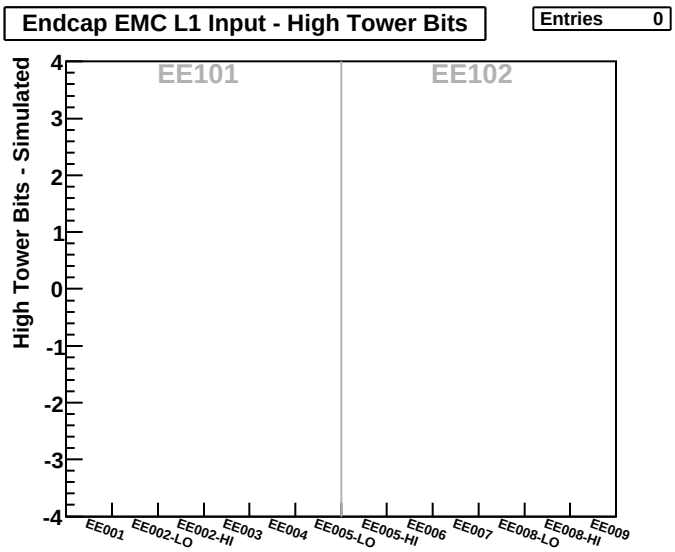

Endcap EMC L1 Input - Low Eta Sum
Entries 1308132
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

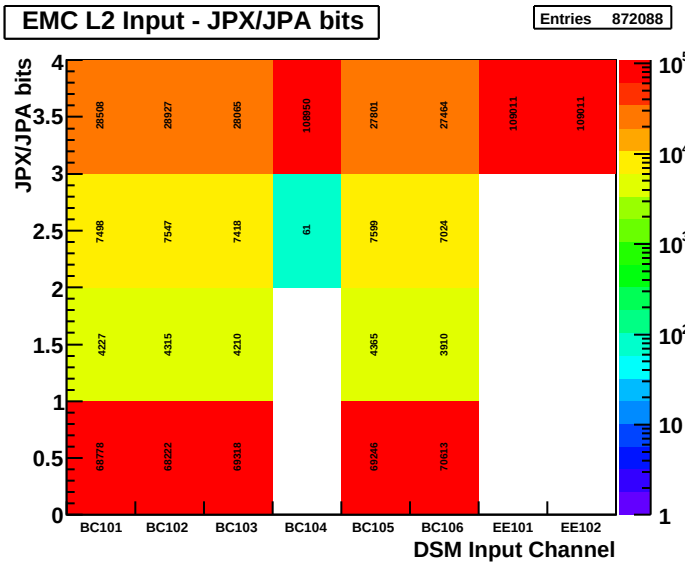

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

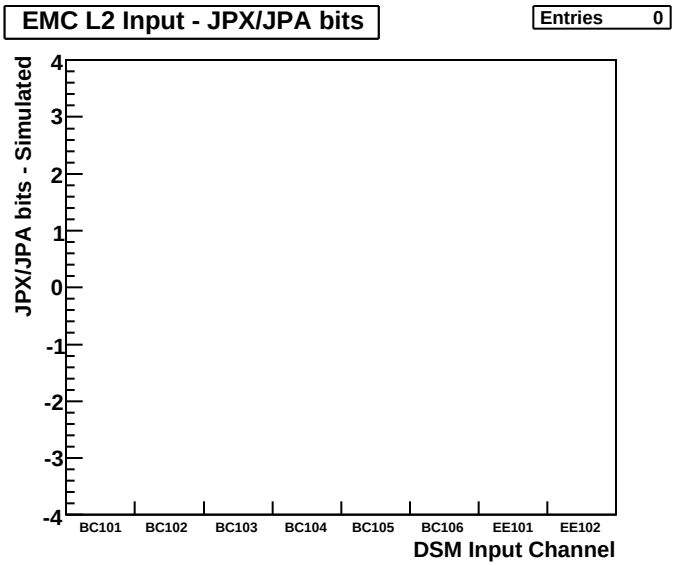

Endcap EMC L1 Input - High Eta Sum
Entries 1308132
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

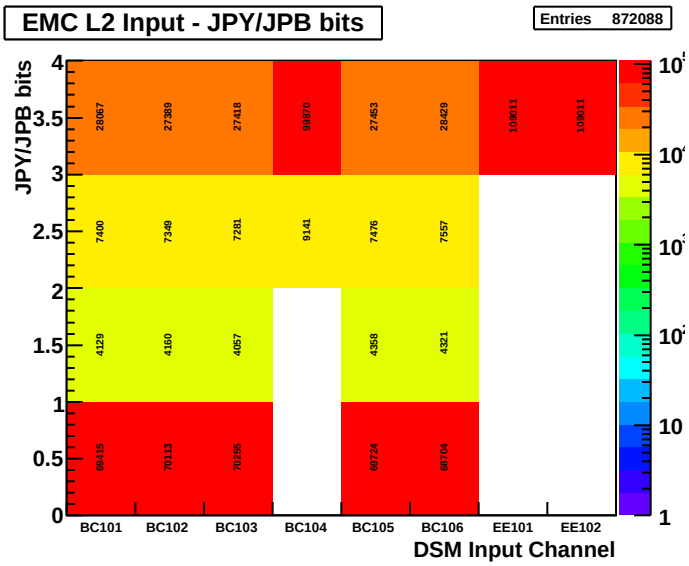

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

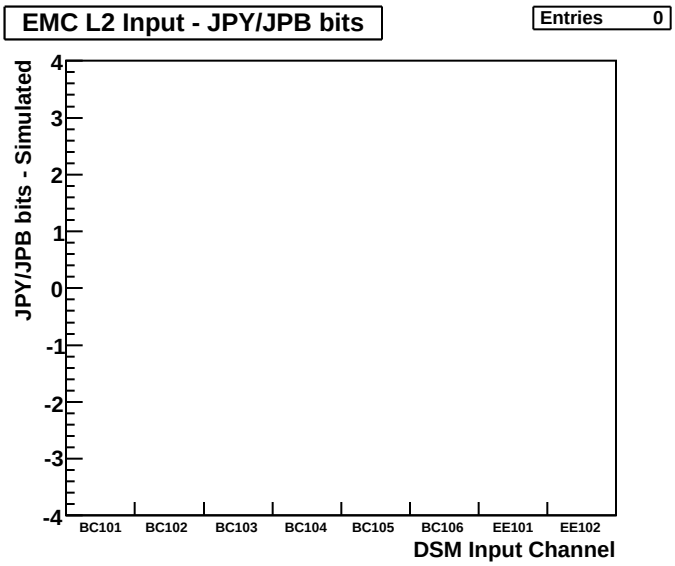

Endcap EMC L1 Input - High Tower Bits
Entries 1308132
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

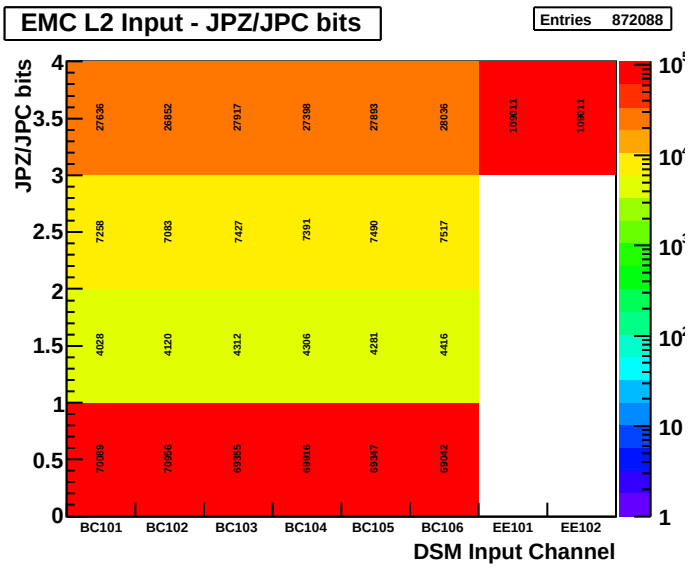

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

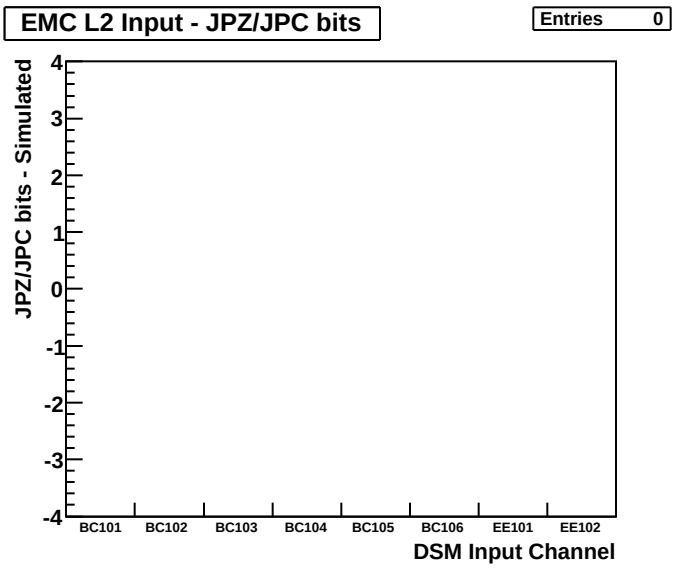

EMC L2 Input - JPX/JPA bits
Entries 872088
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
28508 28927 28065 108850 27801 27464 109011 109011
7498 7547 7418 61 7599 7024
4227 4315 4210 4365 3910
68778 68222 69318 69246 70613

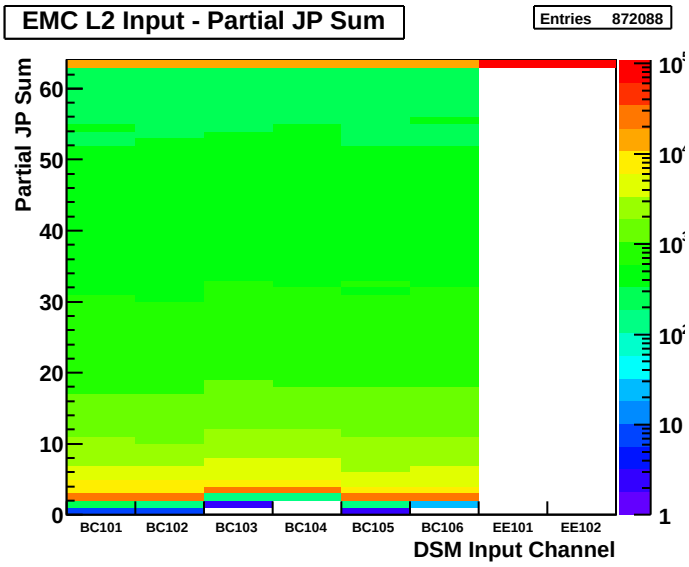

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

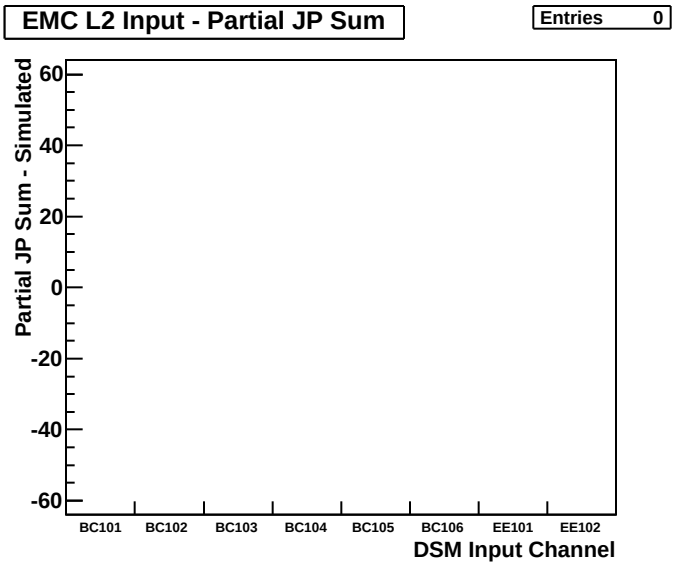

EMC L2 Input - JPY/JPB bits
Entries 872088
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
28067 27389 27418 99870 27453 28429 109011 109011
7400 7349 7281 9141 7476 7557
4129 4160 4057 4358 4321
69415 70113 70255 69724 68704

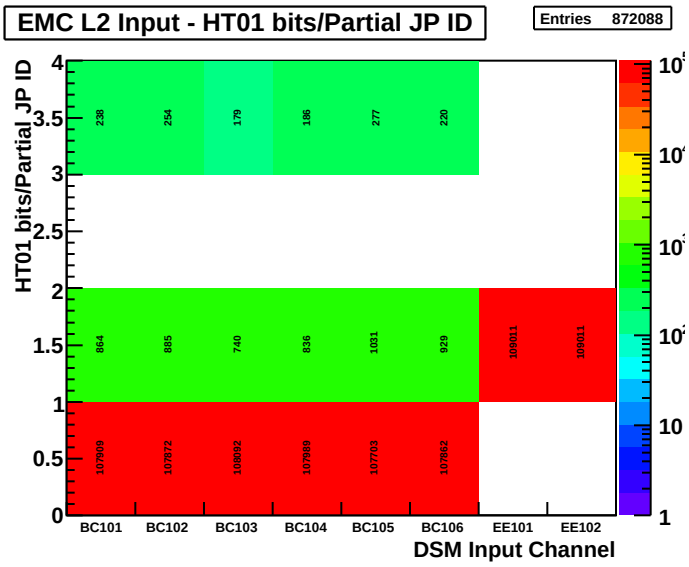

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

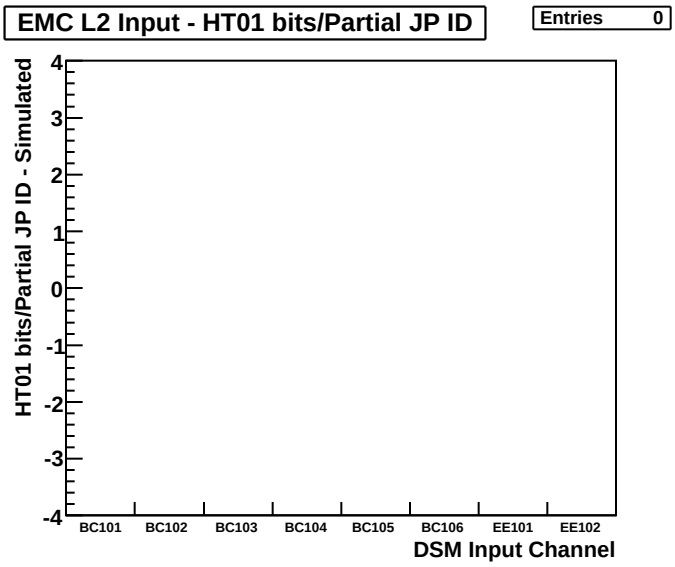

EMC L2 Input - JPZ/JPC bits
Entries 872088
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
27636 26852 27917 27398 27693 28036 109011 109011
7258 7083 7427 7391 7490 7517
4028 4120 4312 4306 4281 4416
70689 70656 69355 69916 69347 69042

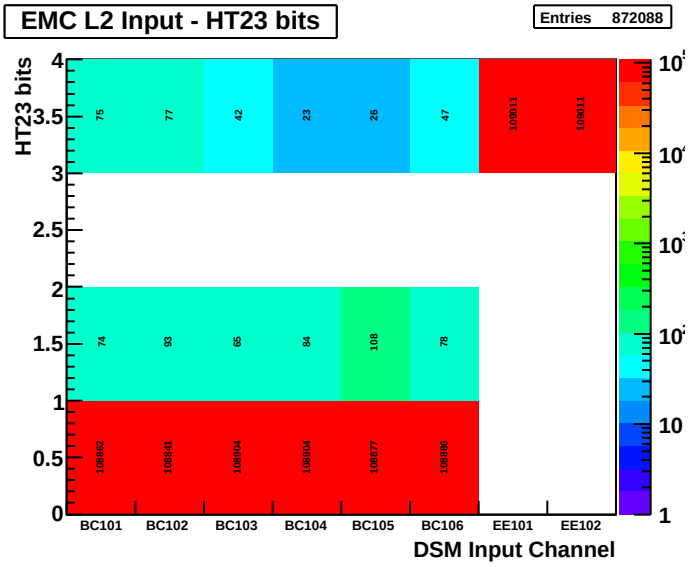

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

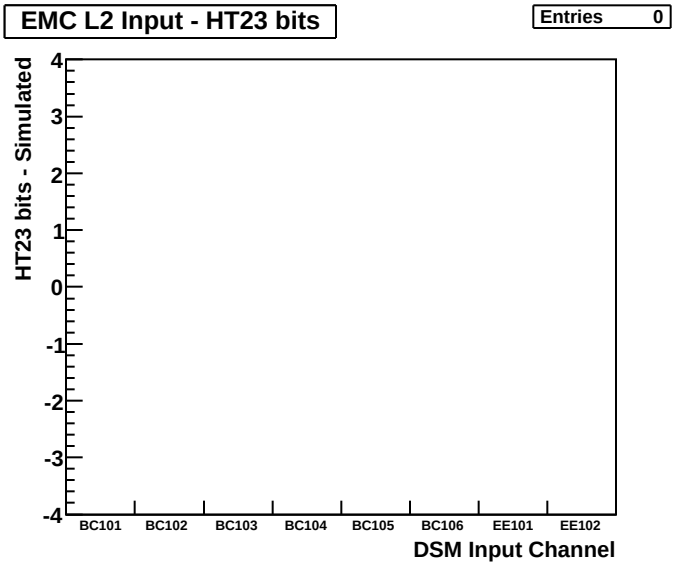

EMC L2 Input - Partial JP Sum
Entries 872088
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

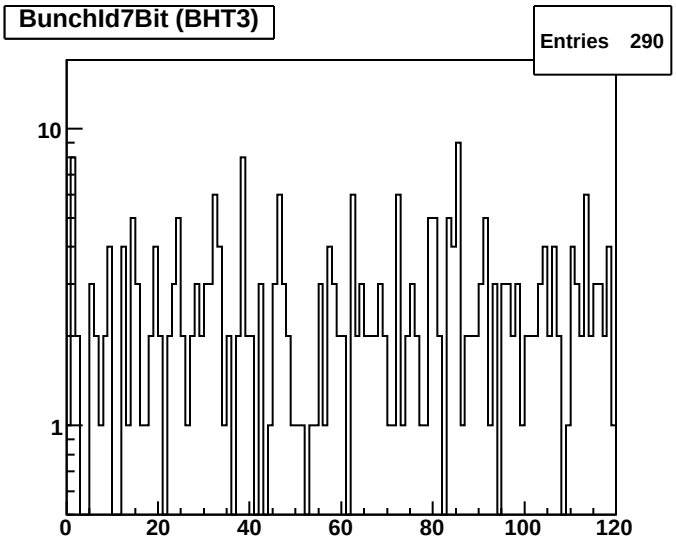

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

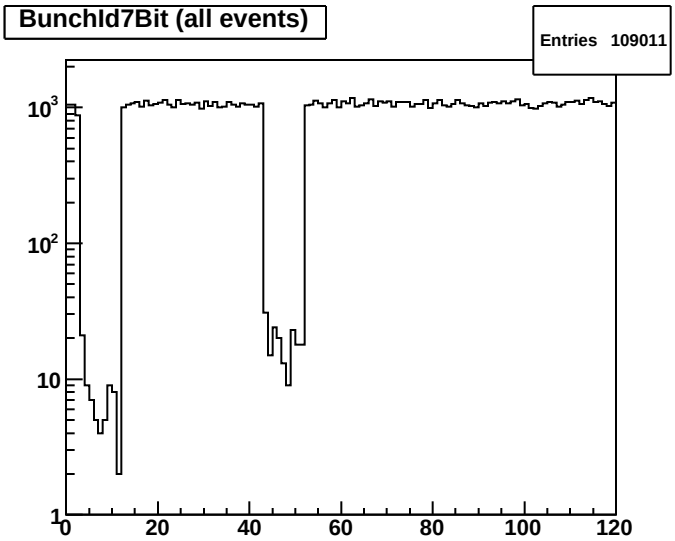

EMC L2 Input - HT01 bits/Partial JP ID
Entries 872088
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

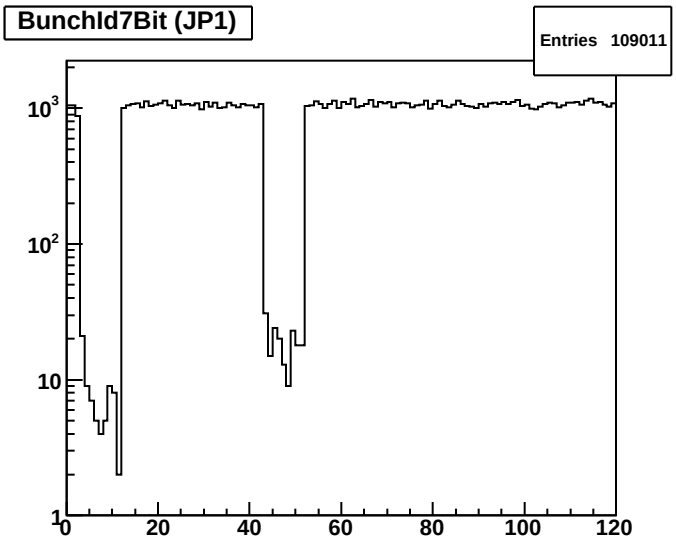

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

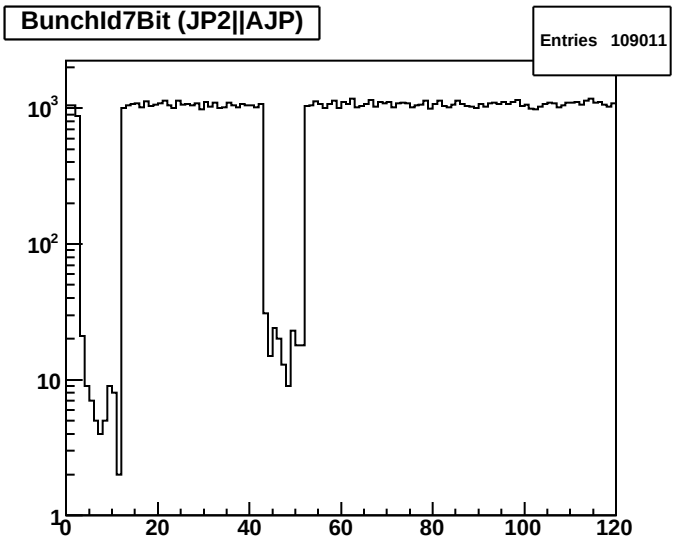

EMC L2 Input - HT23 bits
Entries 872088
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

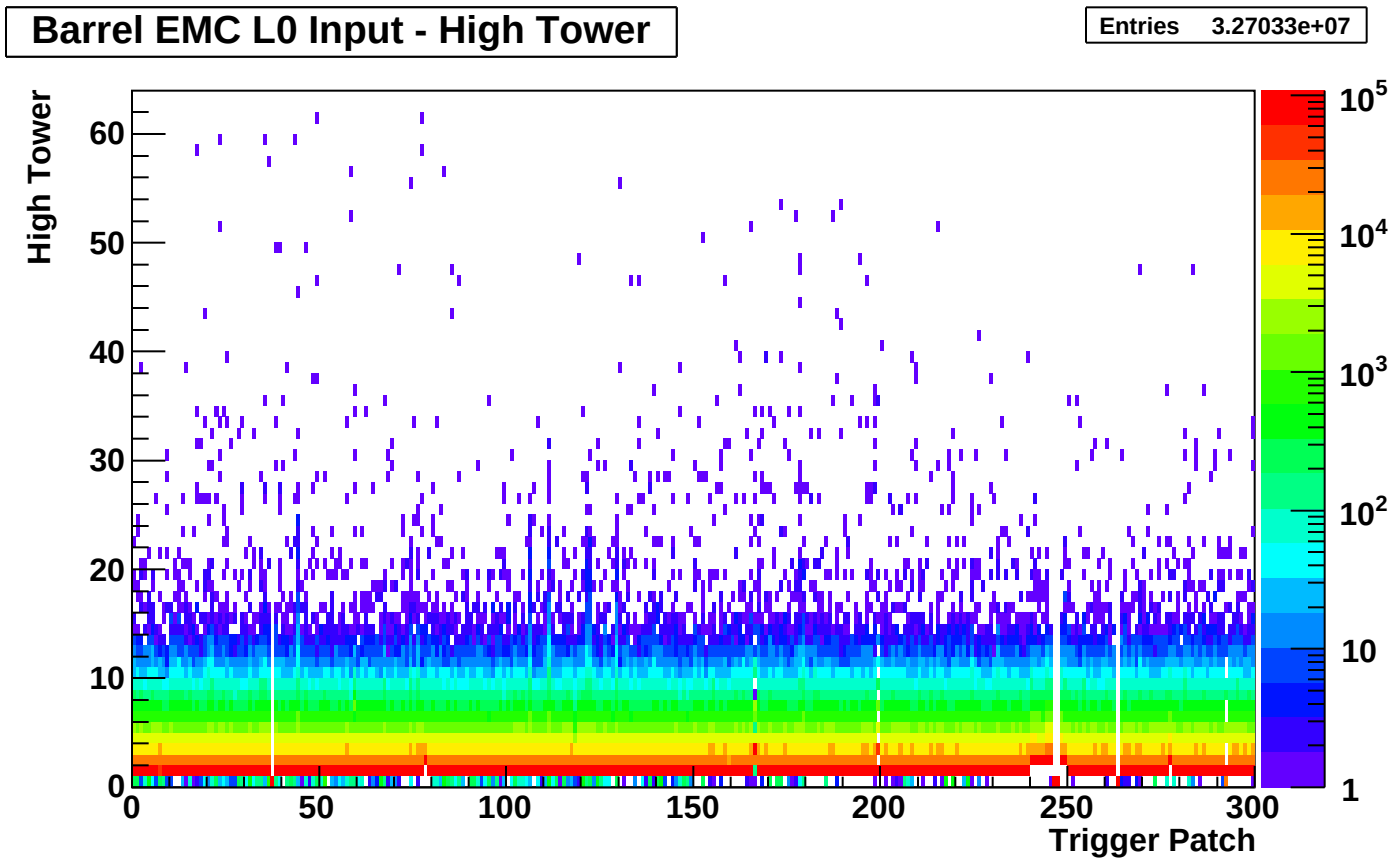

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

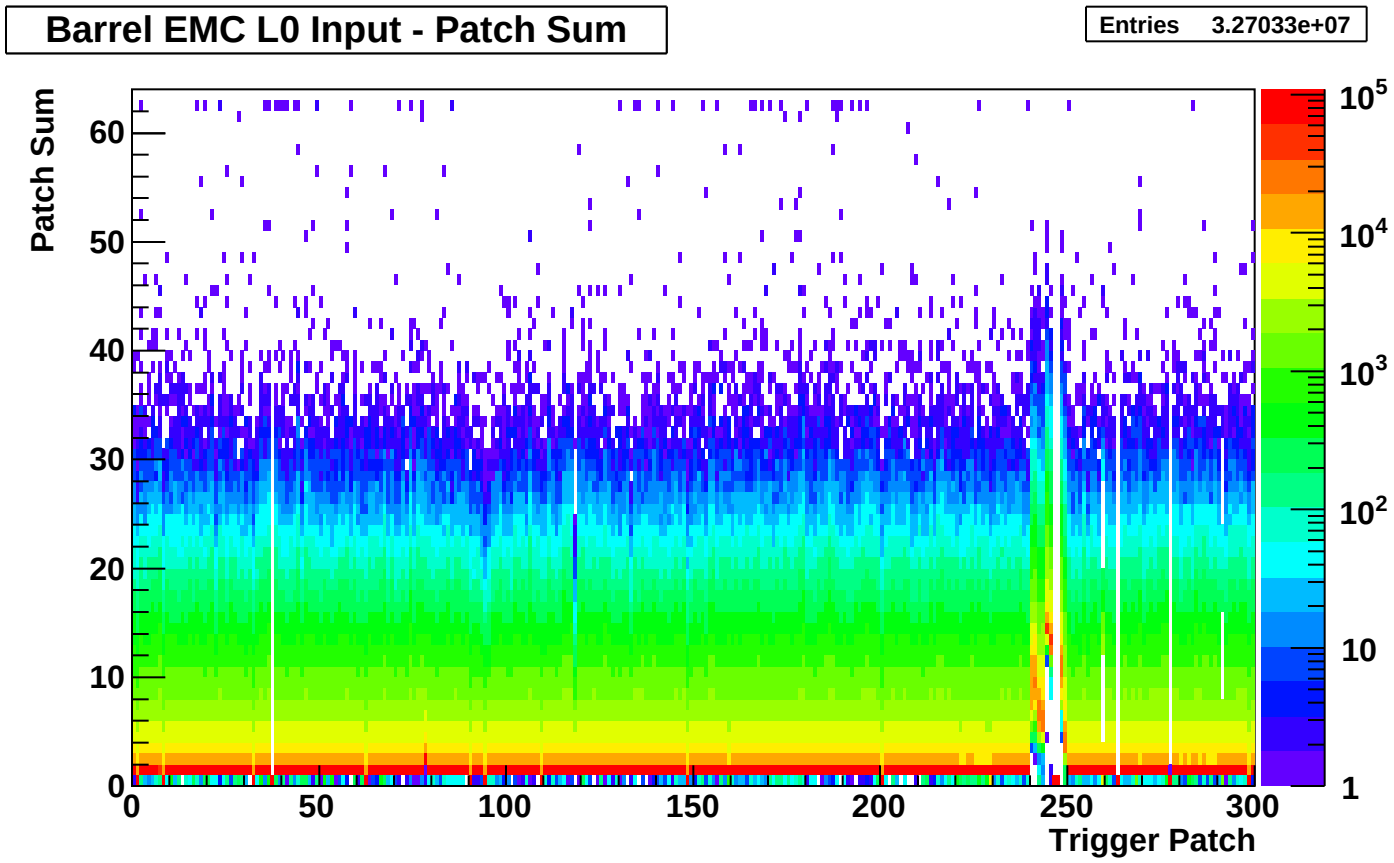

BunchId7Bit (BHT3)
Entries 290
10
1
0
20
40
60
80
100
120

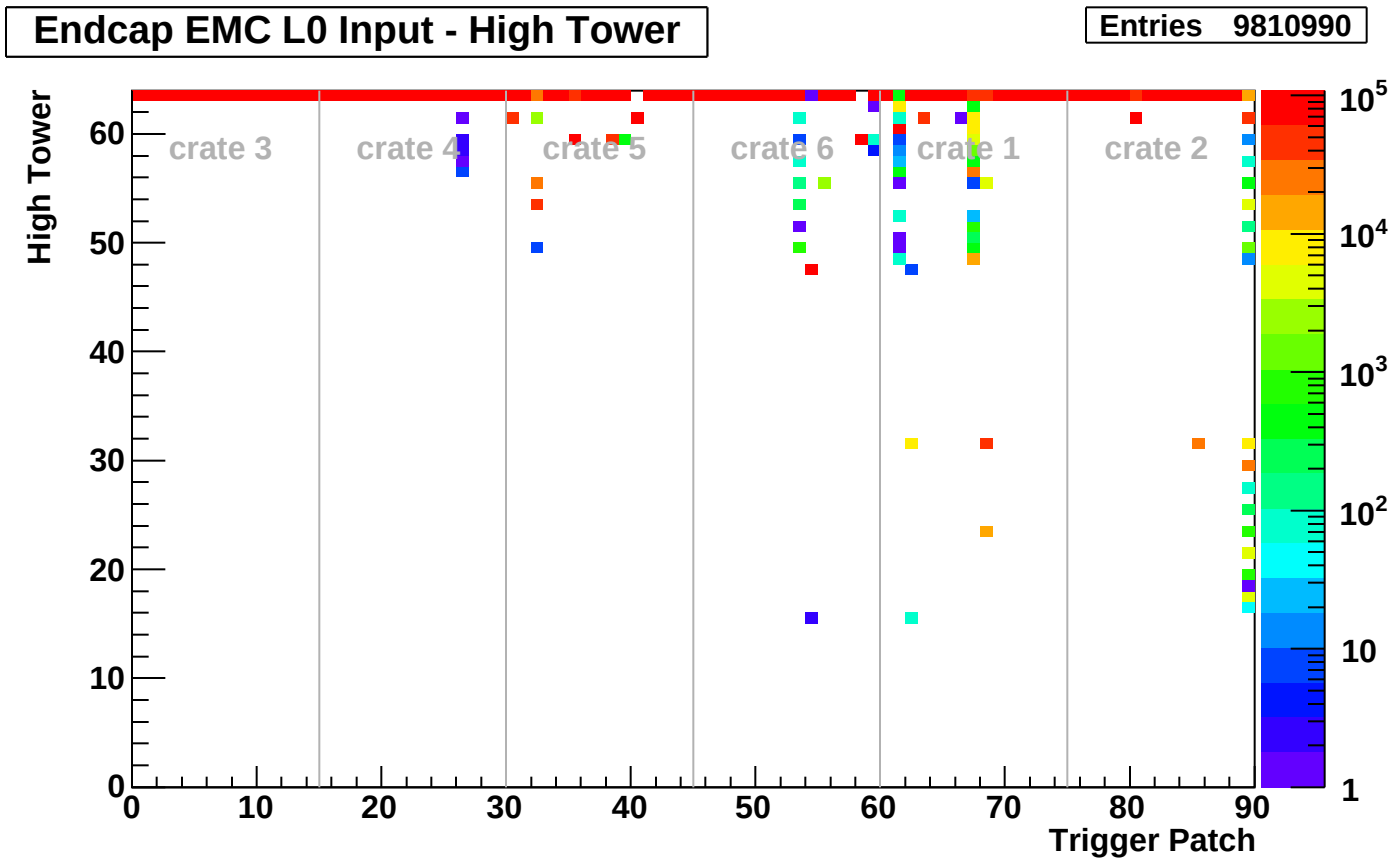

BunchId7Bit (all events)
Entries 109011
10^3
10^2
10
1
0
20
40
60
80
100
120

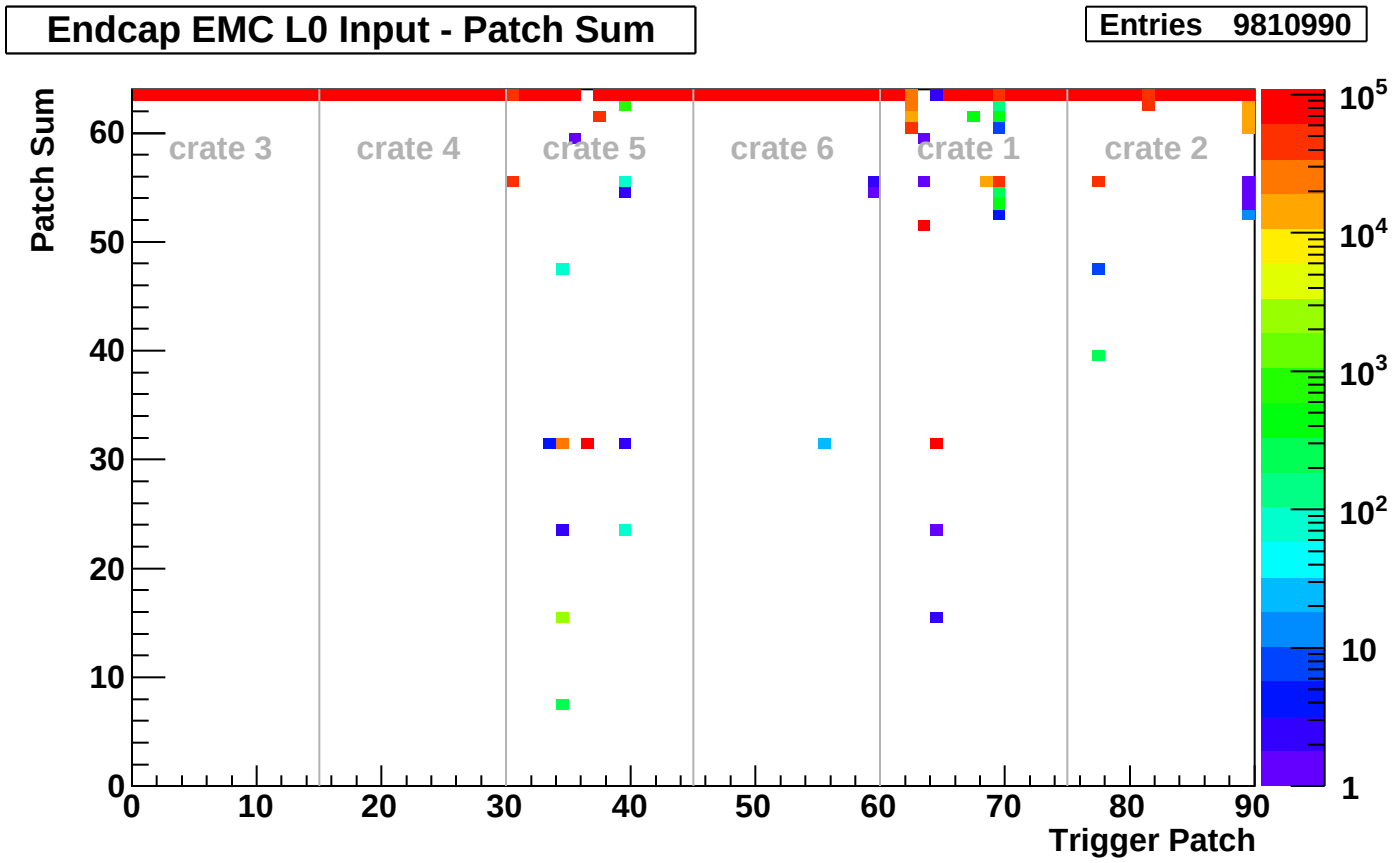

BunchId7Bit (JP1)
Entries 109011
10^3
10^2
10
1
0
20
40
60
80
100
120

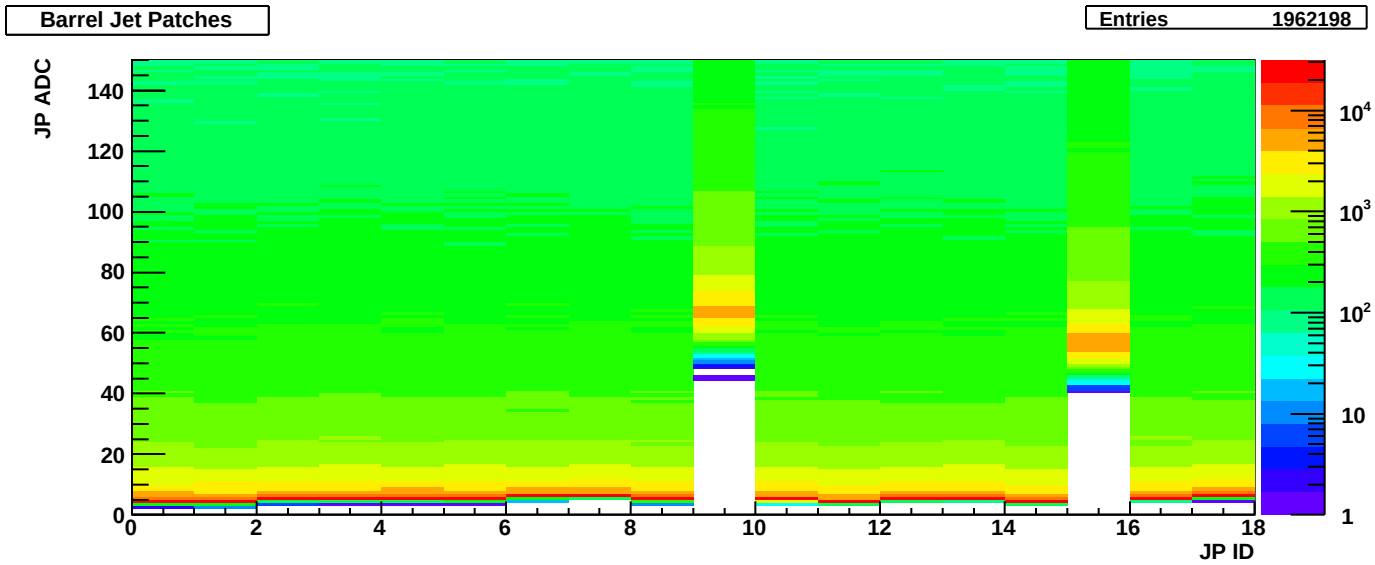

BunchId7Bit (JP2||AJP)
Entries 109011
10^3
10^2
10
1
0
20
40
60
80
100
120

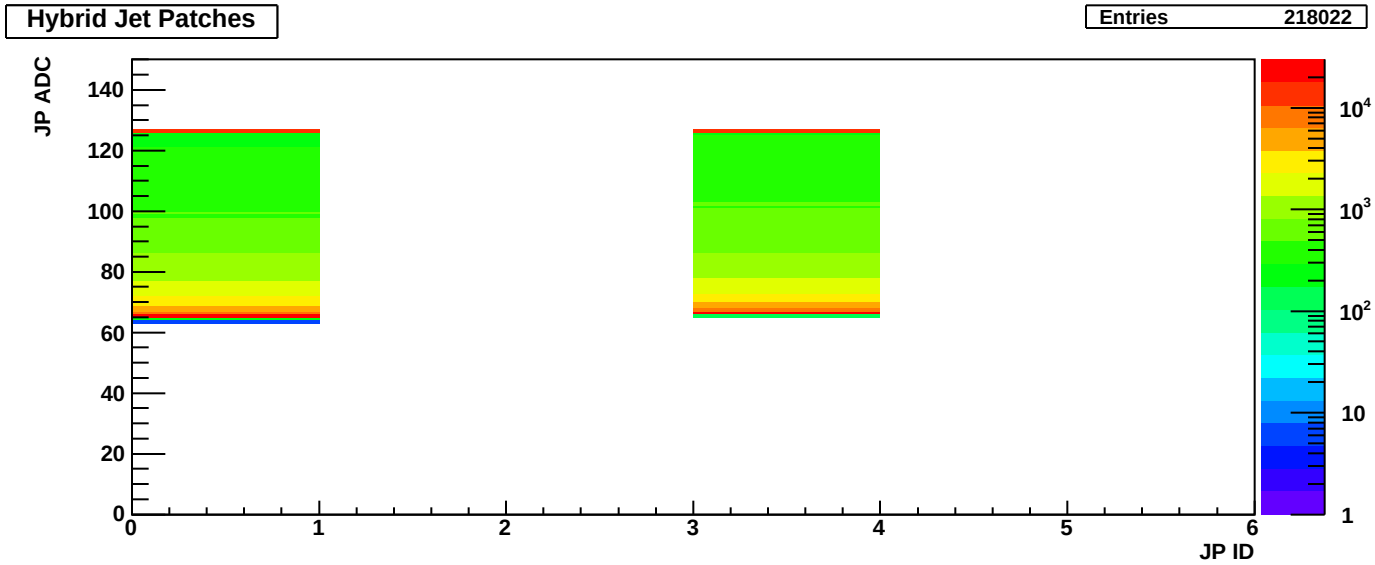

Barrel EMC L0 Input - High Tower
Entries 3.27033e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

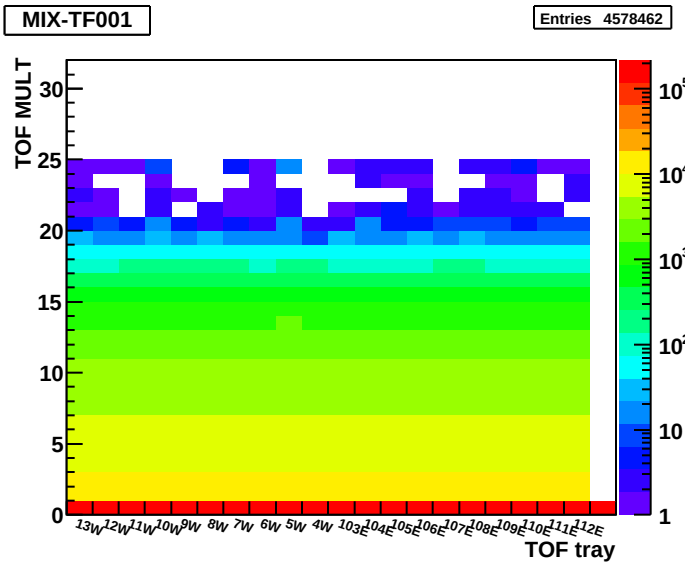

Barrel EMC L0 Input - Patch Sum
Entries 3.27033e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

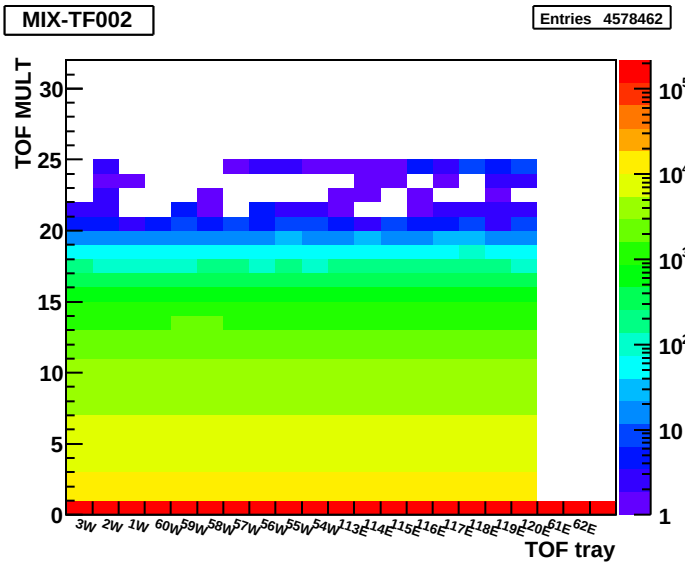

Endcap EMC L0 Input - High Tower
Entries 9810990
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

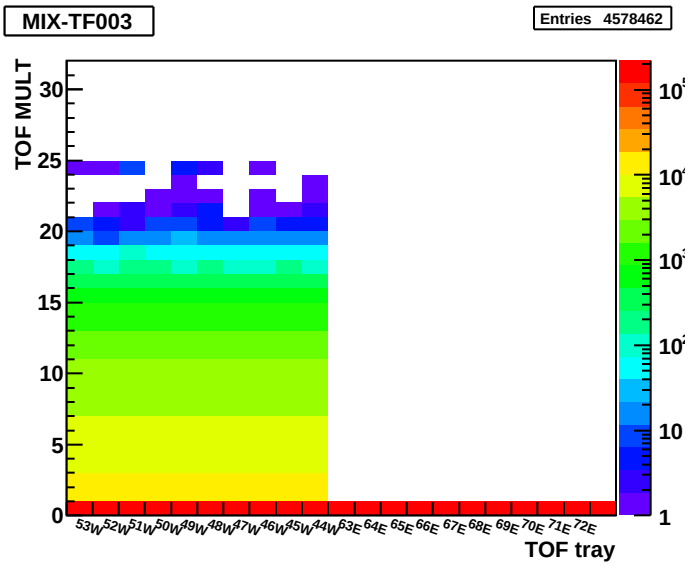

Endcap EMC L0 Input - Patch Sum
Entries 9810990
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

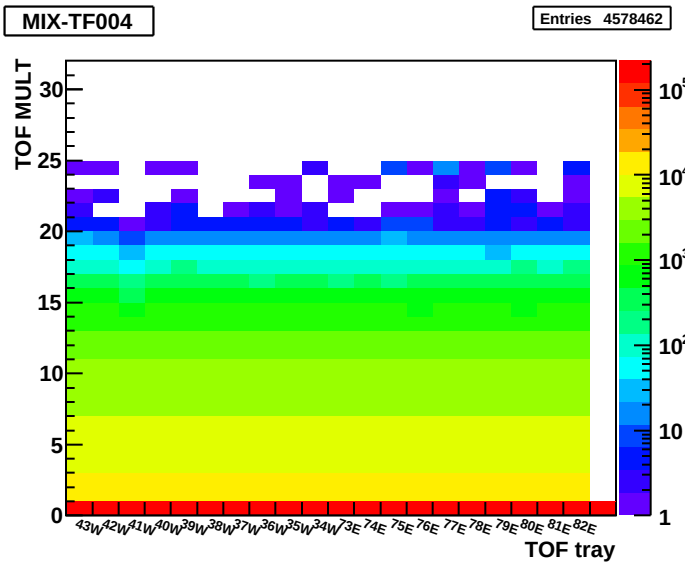

Barrel Jet Patches
Entries 1962198
JP ADC
JP ID

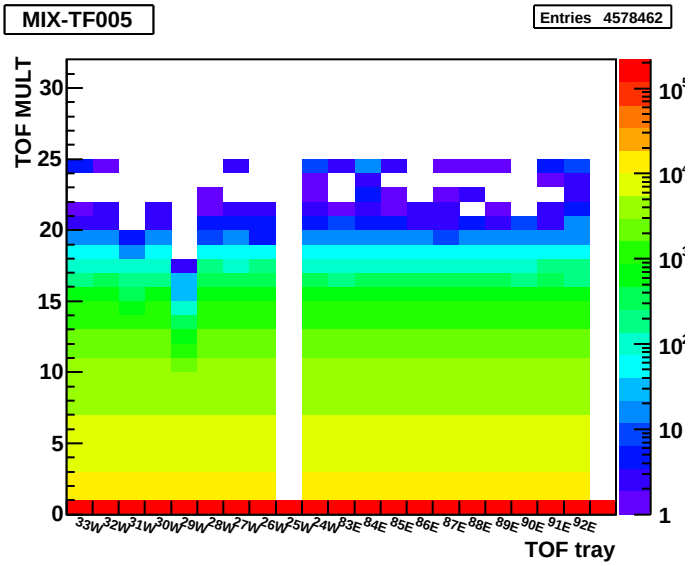

Hybrid Jet Patches
Entries 218022
JP ADC
JP ID

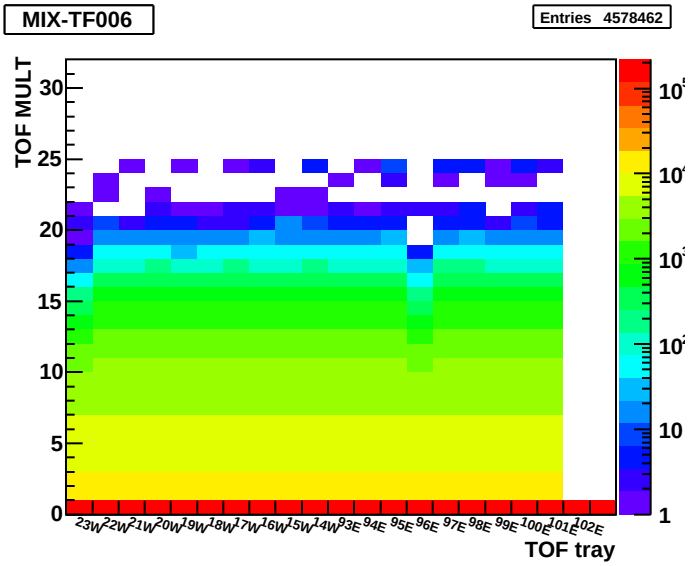

MIX-TF001
Entries 4578462
TOF MULT
TOF tray

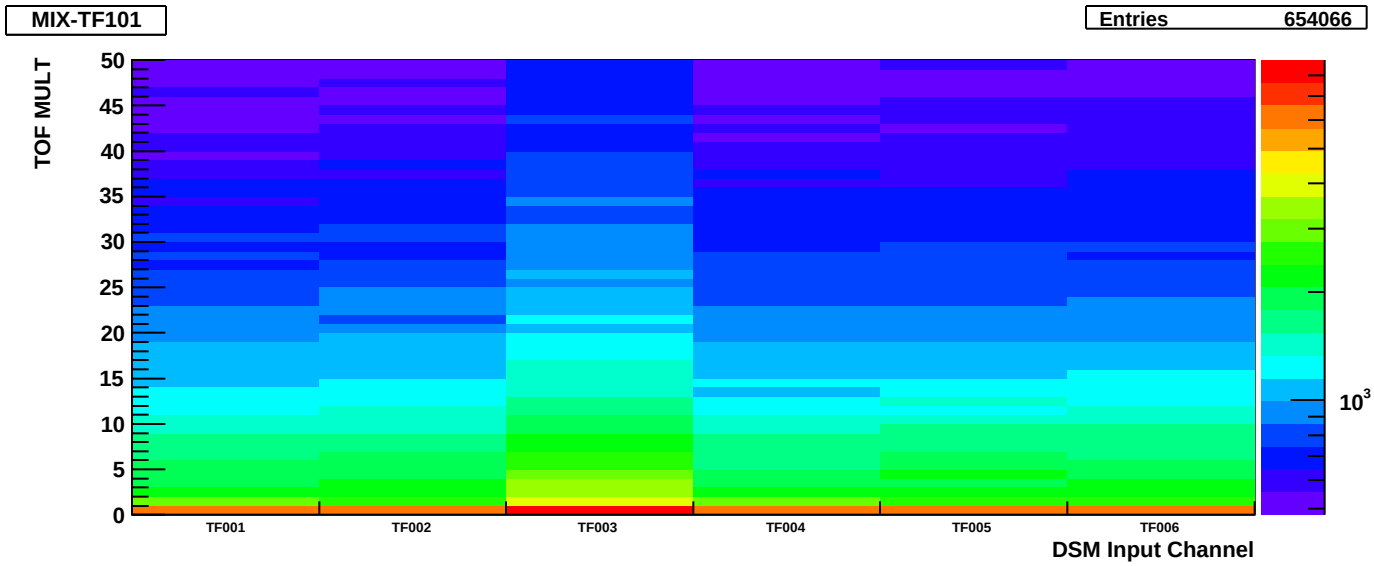

MIX-TF002
Entries 4578462
TOF MULT
TOF tray

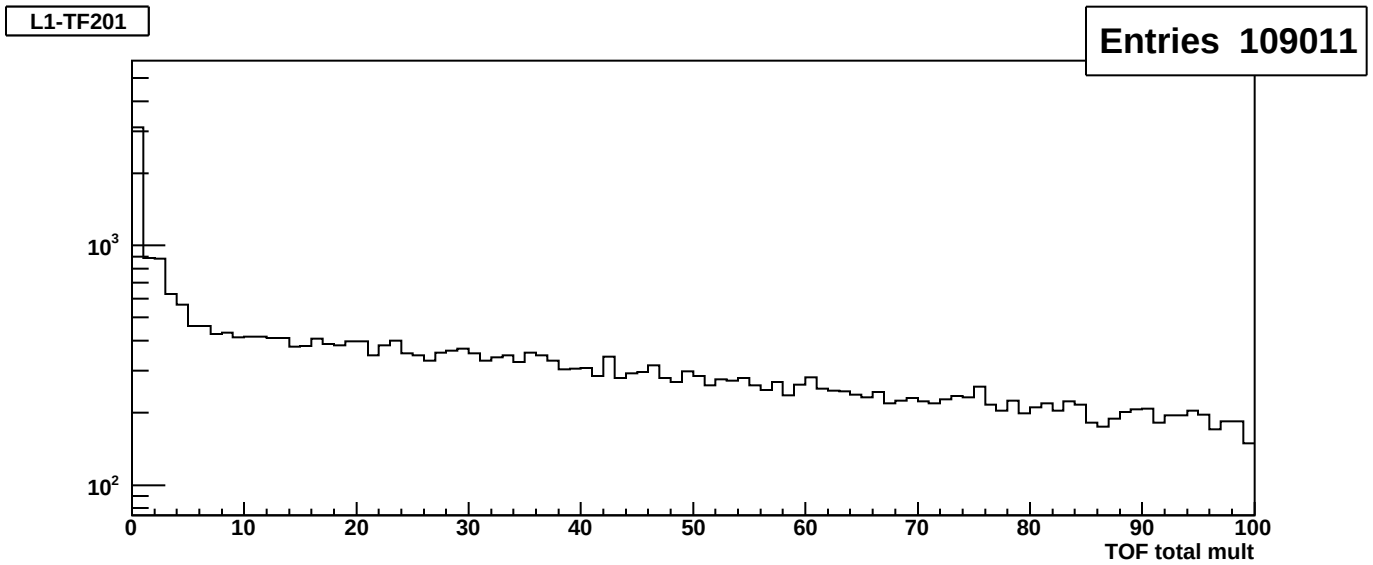

MIX-TF003
Entries 4578462
TOF MULT
TOF tray

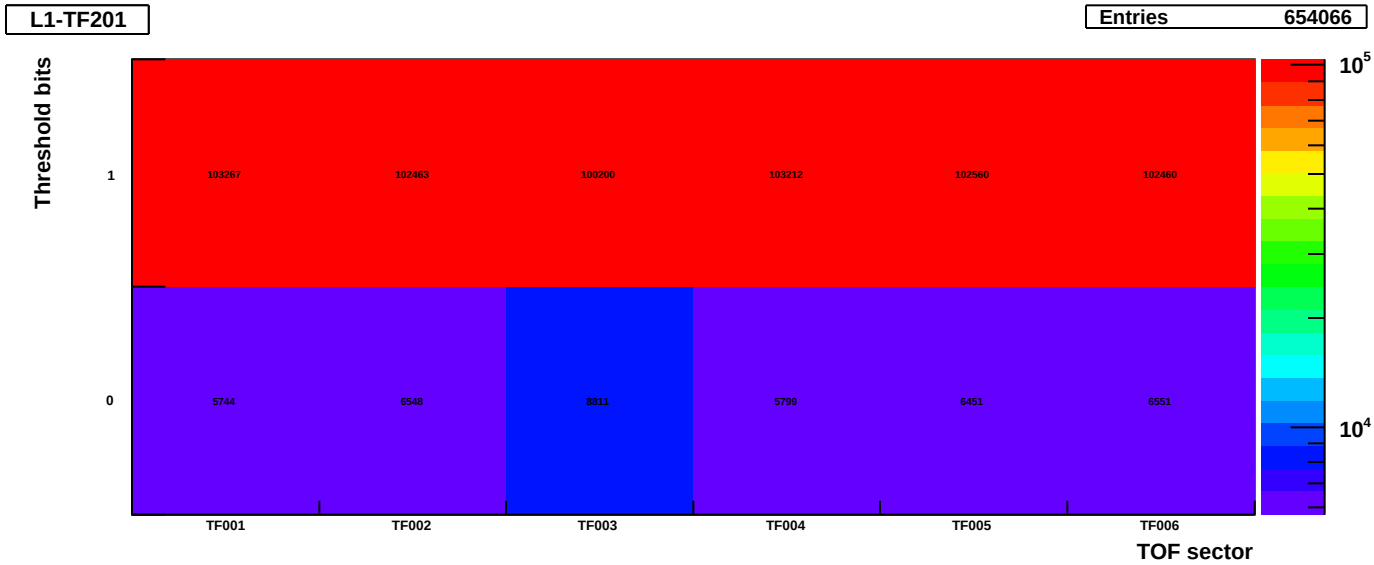

MIX-TF004
Entries 4578462
TOF MULT
TOF tray

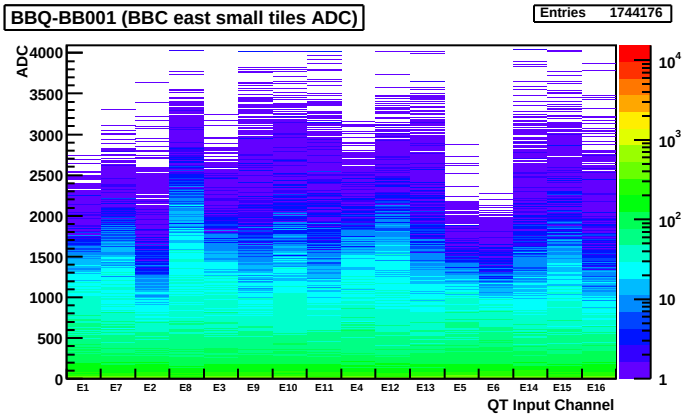

MIX-TF005
Entries 4578462
TOF MULT
TOF tray

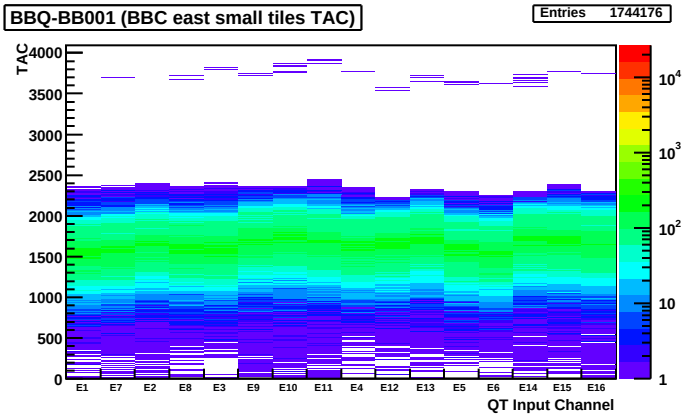

MIX-TF006
Entries 4578462
TOF MULT
TOF tray

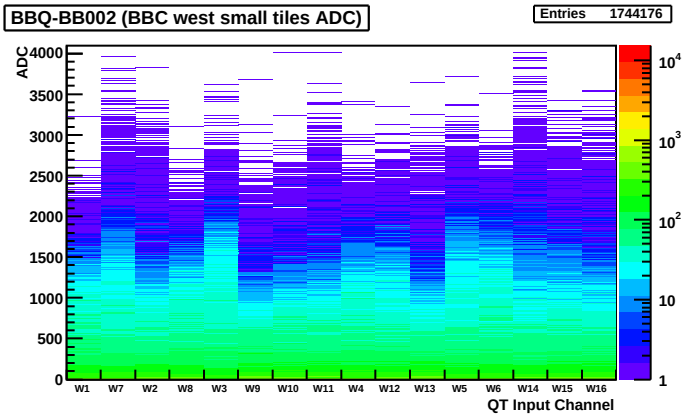

MIX-TF101
Entries 654066
TOF MULT
50
45
40
35
30
25
20
15
10
5
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^3

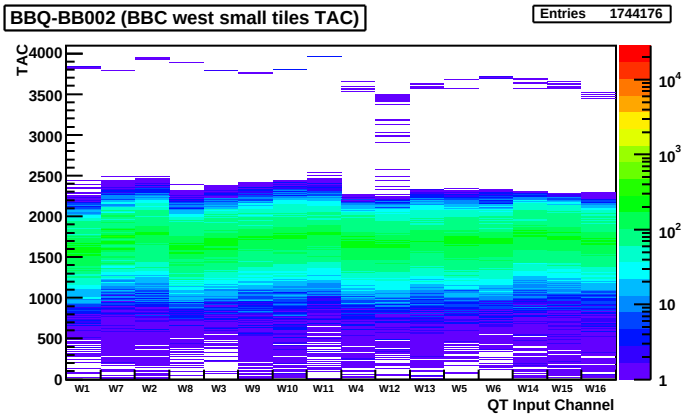

L1-TF201
Entries 109011
10^3
10^2
0
10
20
30
40
50
60
70
80
90
100
TOF total mult

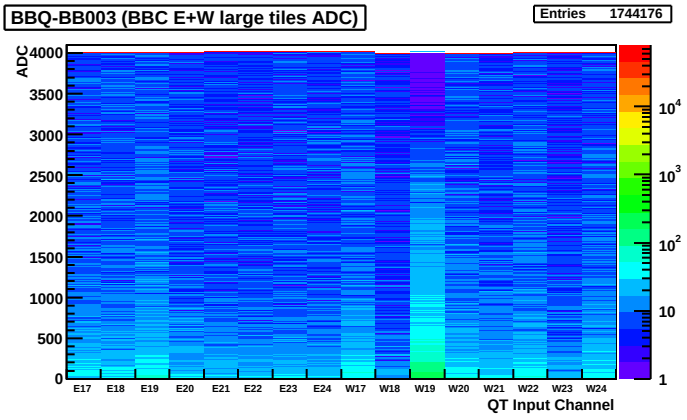

L1-TF201
Entries 654066
Threshold bits
1
0
103267
102463
100200
103212
102560
102460
5744
6548
8811
5799
6451
6551
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5
10^4

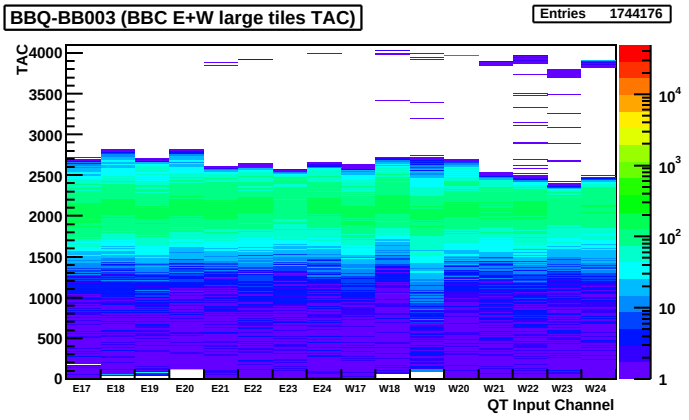

BBQ-BB001 (BBC east small tiles ADC)
Entries 1744176
ADC
QT Input Channel

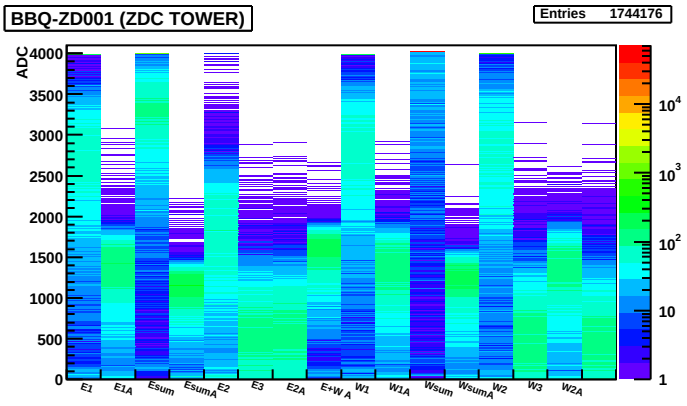

BBQ-BB001 (BBC east small tiles TAC)
Entries 1744176
TAC
QT Input Channel

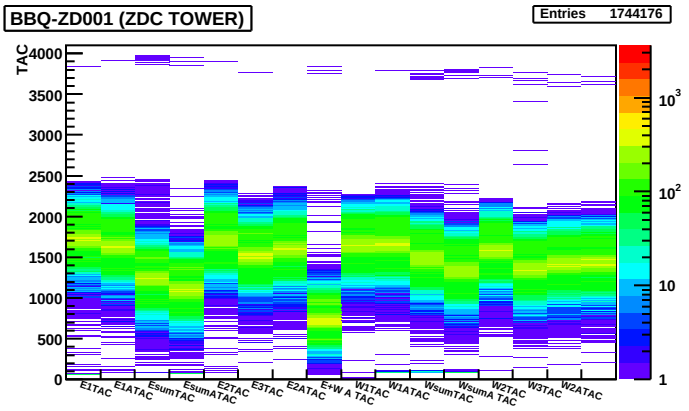

BBQ-BB002 (BBC west small tiles ADC)
Entries 1744176
ADC
QT Input Channel

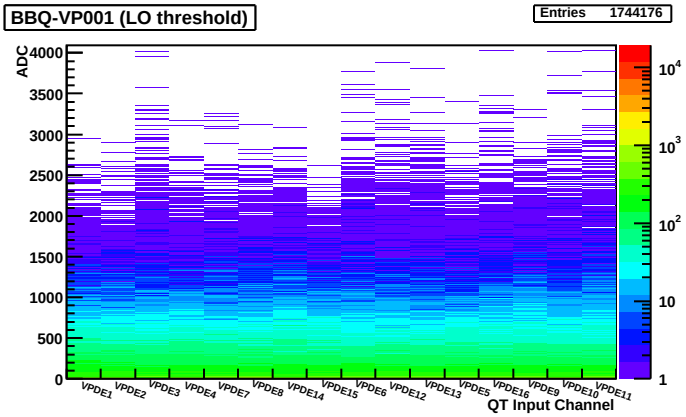

BBQ-BB002 (BBC west small tiles TAC)
Entries 1744176
TAC
QT Input Channel

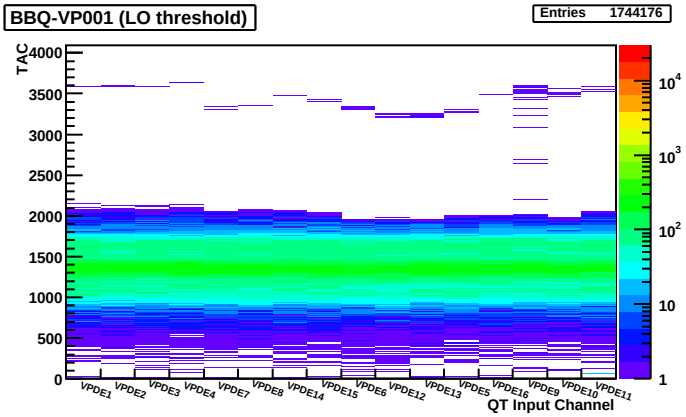

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1744176
ADC
QT Input Channel

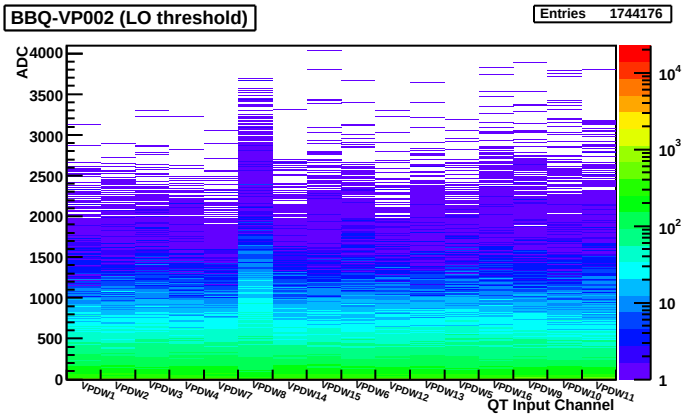

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1744176
TAC
QT Input Channel

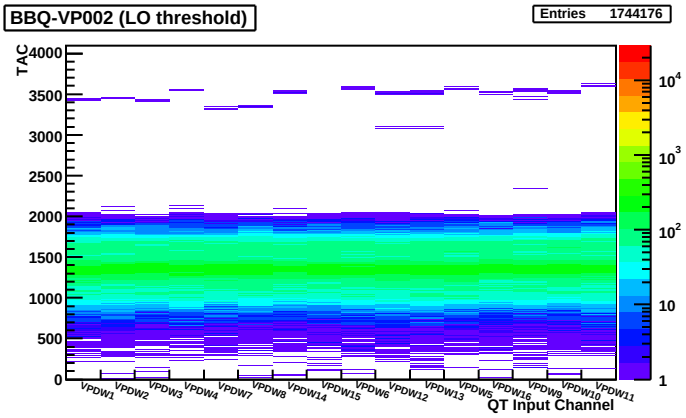

BBQ-ZD001 (ZDC TOWER)
Entries 1744176
ADC

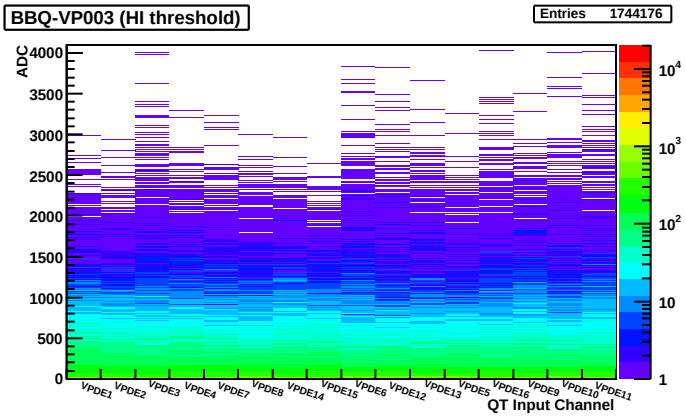

BBQ-ZD001 (ZDC TOWER)
Entries 1744176
TAC

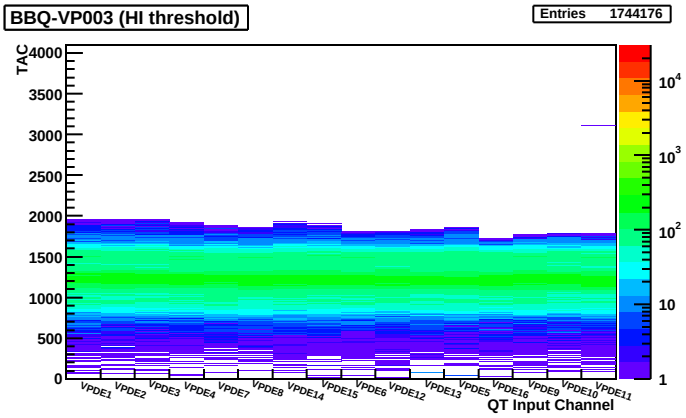

BBQ-VP001 (LO threshold)
Entries 1744176
ADC
QT Input Channel

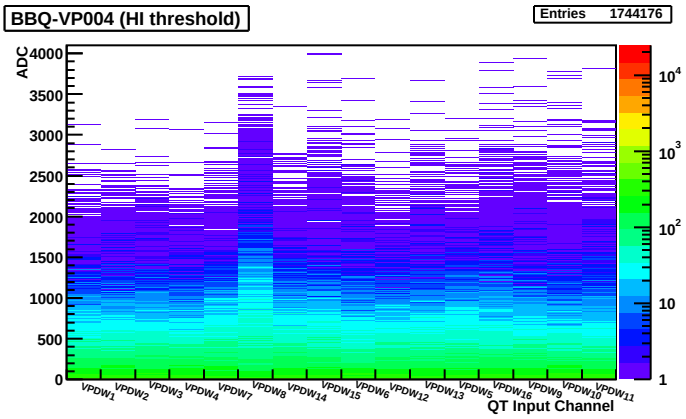

BBQ-VP001 (LO threshold)
Entries 1744176
TAC
QT Input Channel

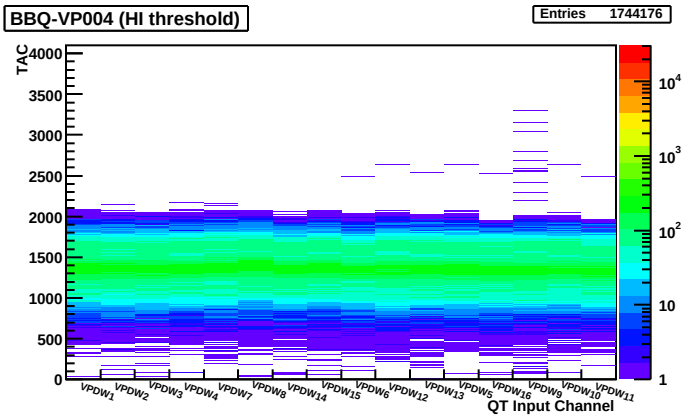

BBQ-VP002 (LO threshold)
Entries 1744176
ADC
QT Input Channel

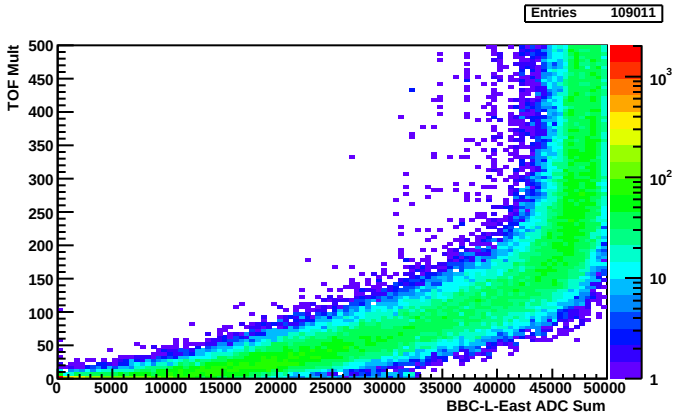

BBQ-VP002 (LO threshold)
Entries 1744176
TAC
QT Input Channel

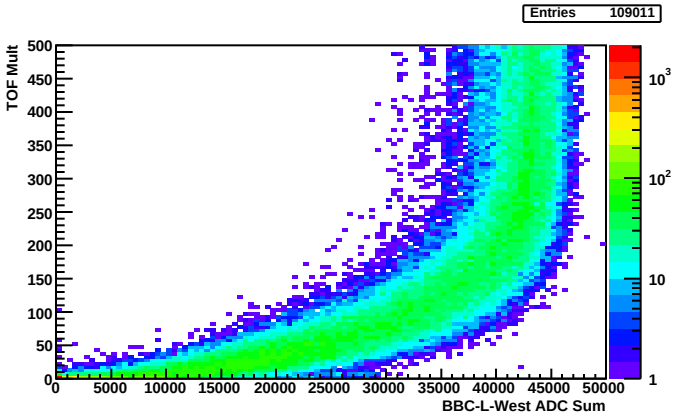

BBQ-VP003 (HI threshold)
Entries 1744176
ADC
QT Input Channel

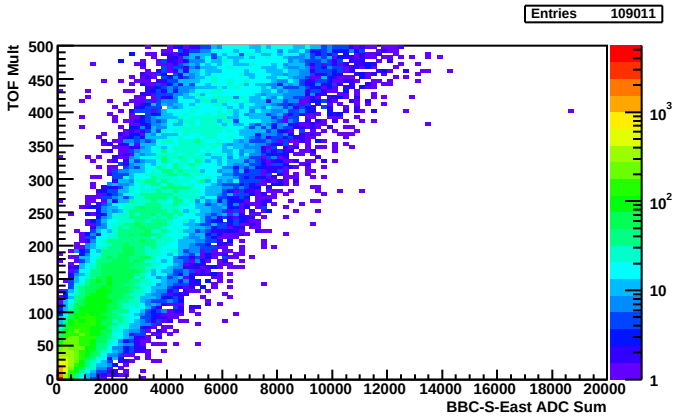

BBQ-VP003 (HI threshold)
Entries 1744176
TAC
QT Input Channel

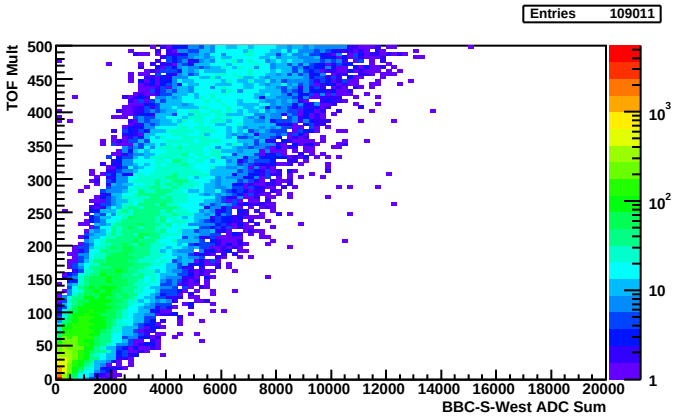

BBQ-VP004 (HI threshold)
Entries 1744176
ADC
QT Input Channel

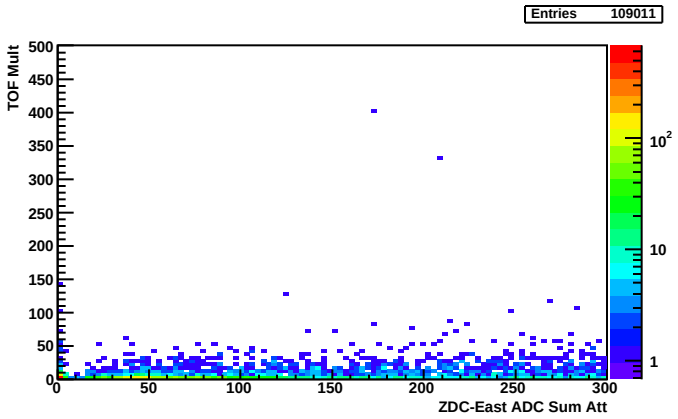

BBQ-VP004 (HI threshold)
Entries 1744176
TAC
QT Input Channel

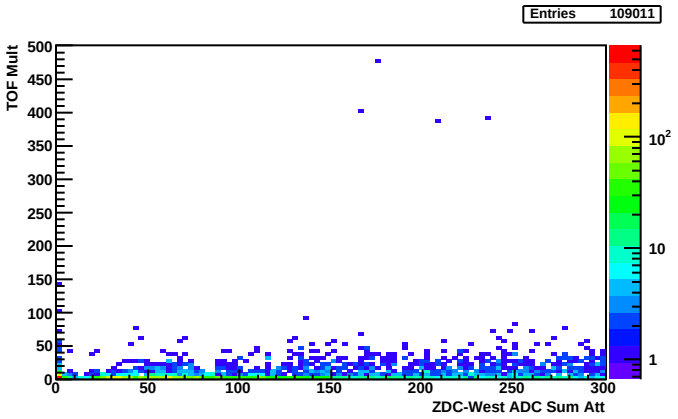

Entries 109011
TOF Mult
BBC-L-East ADC Sum

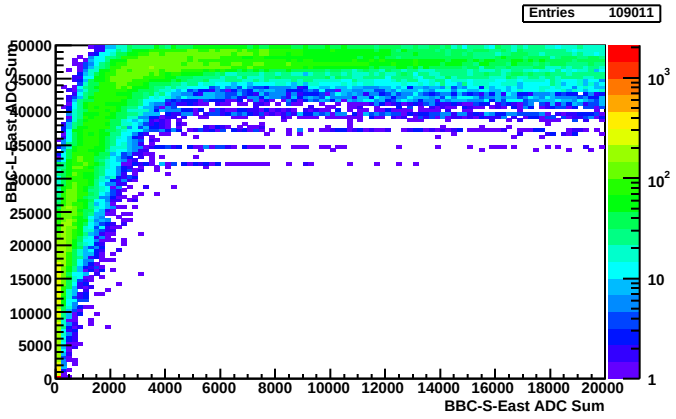

Entries 109011
TOF Mult
BBC-L-West ADC Sum

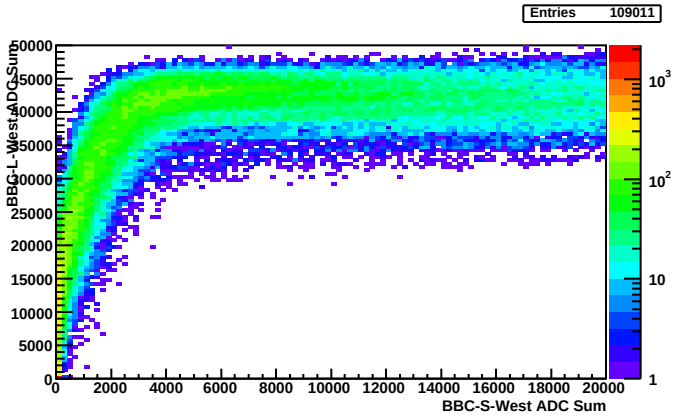

Entries 109011
TOF Mult
BBC-S-East ADC Sum

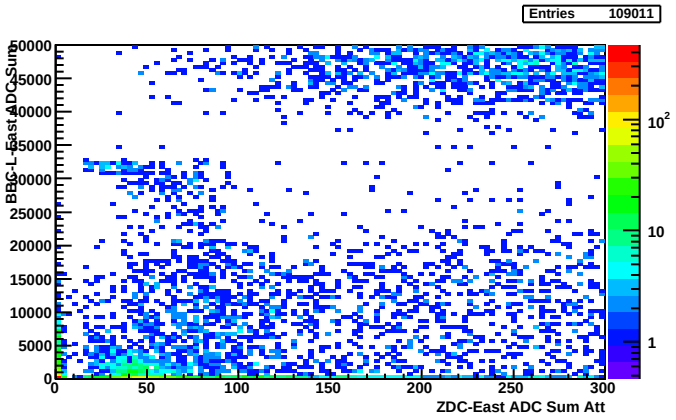

Entries 109011
TOF Mult
BBC-S-West ADC Sum

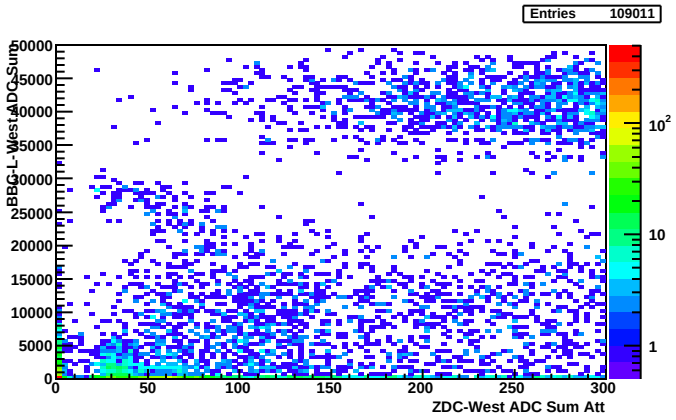

Entries 109011
TOF Mult
ZDC-East ADC Sum Att


Entries 109011
TOF Mult
ZDC-West ADC Sum Att


Entries 109011
BBC-L-East ADC Sum
BBC-S-East ADC Sum


Entries 109011
BBC-L-West ADC Sum
BBC-S-West ADC Sum


Entries 109011
BBC-L-East ADC Sum
ZDC-East ADC Sum Att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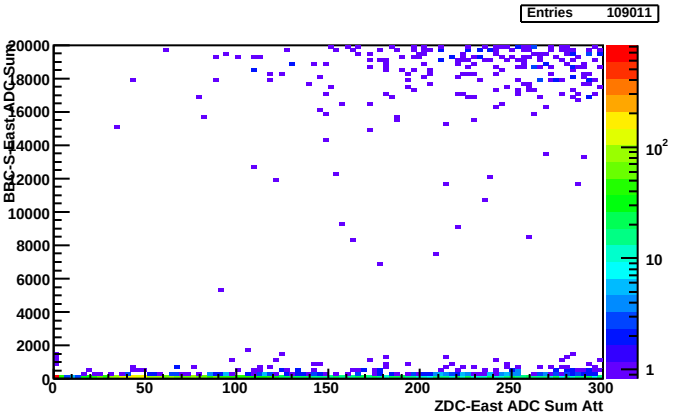
BBC-L-West ADC Sum
ZDC-West ADC Sum Att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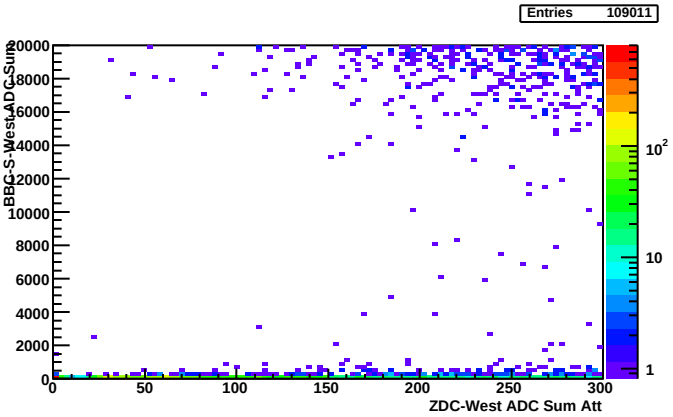
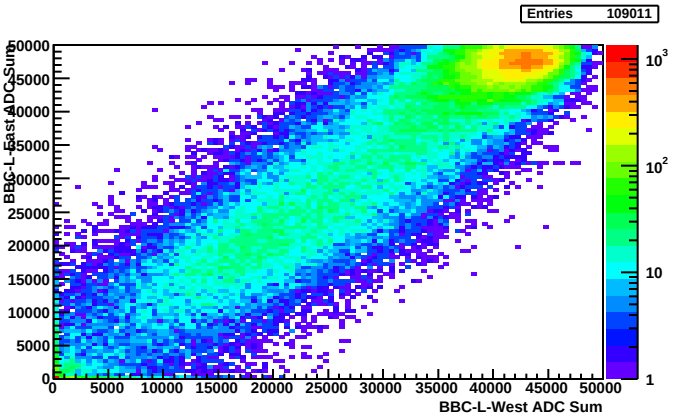
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 109011
BBC-S-West ADC Sum
ZDC-West ADC Sum Att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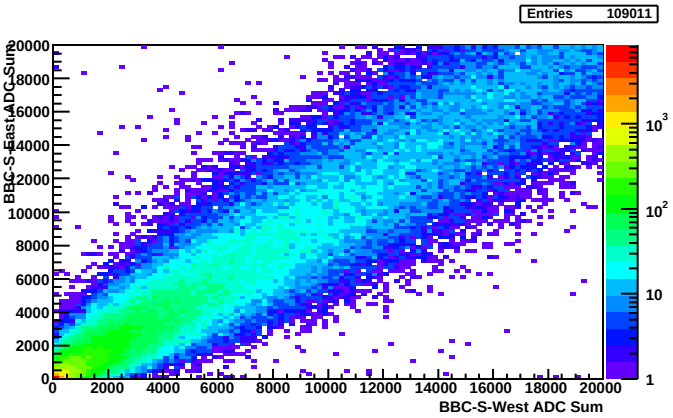
BBC-L-East ADC Sum
BBC-L-West ADC Sum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BBC-S-East ADC Sum
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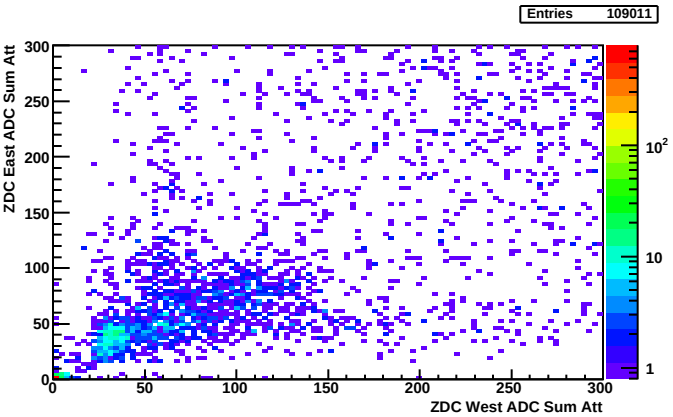
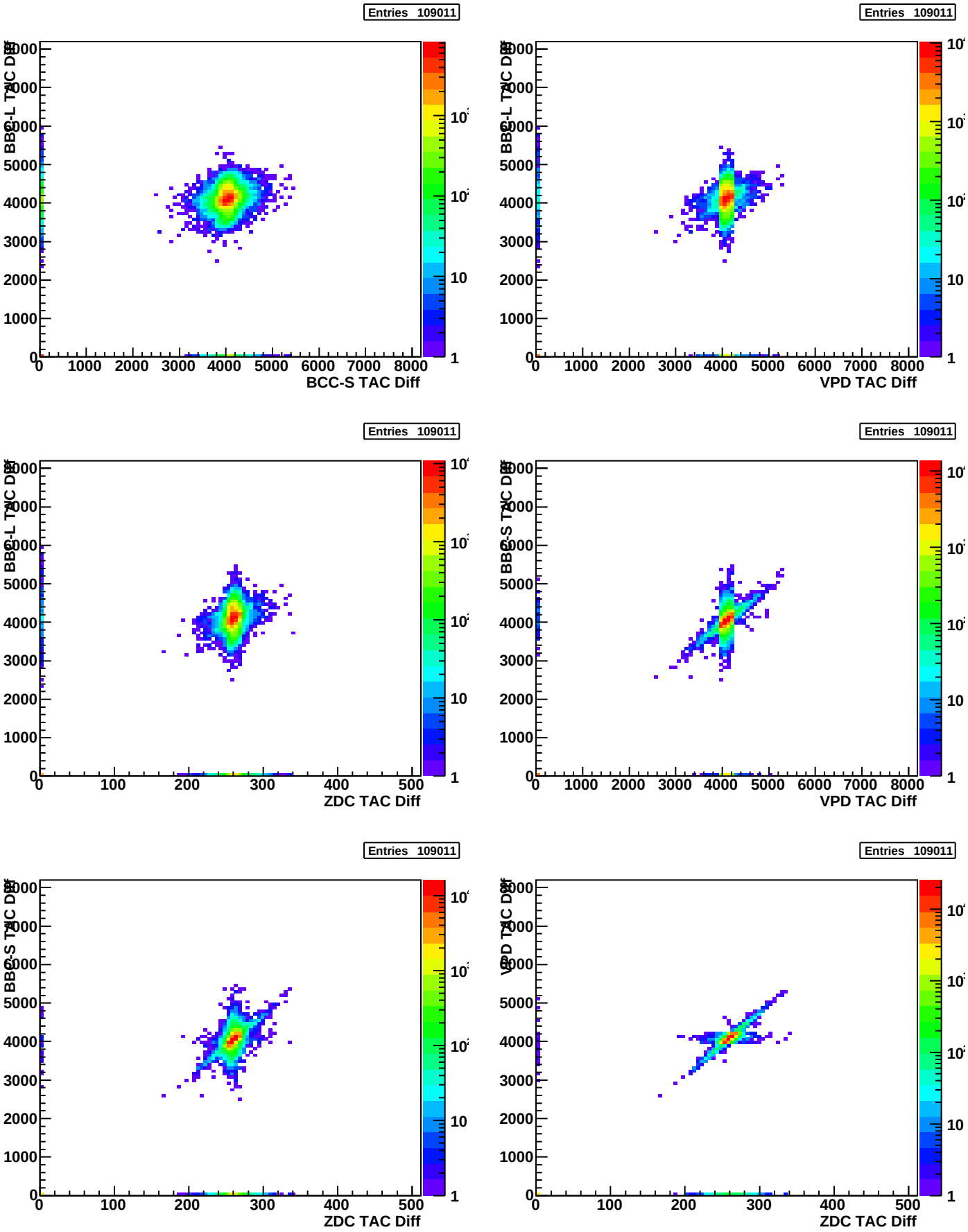
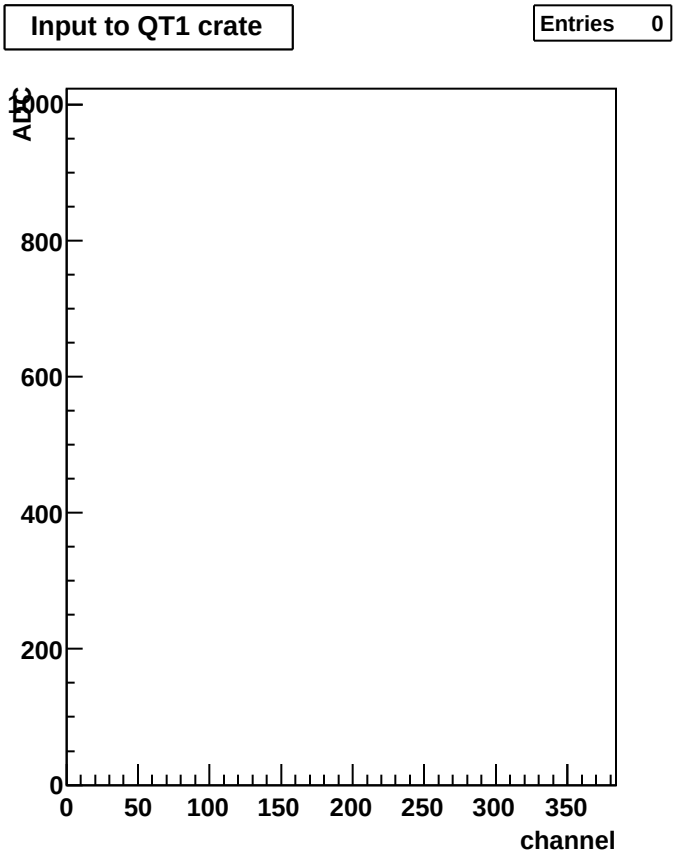
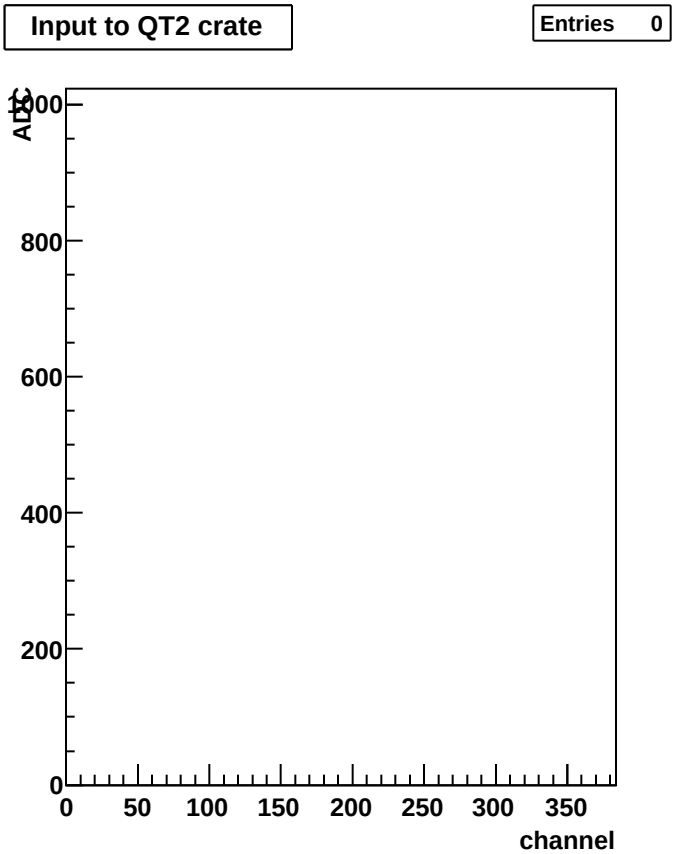
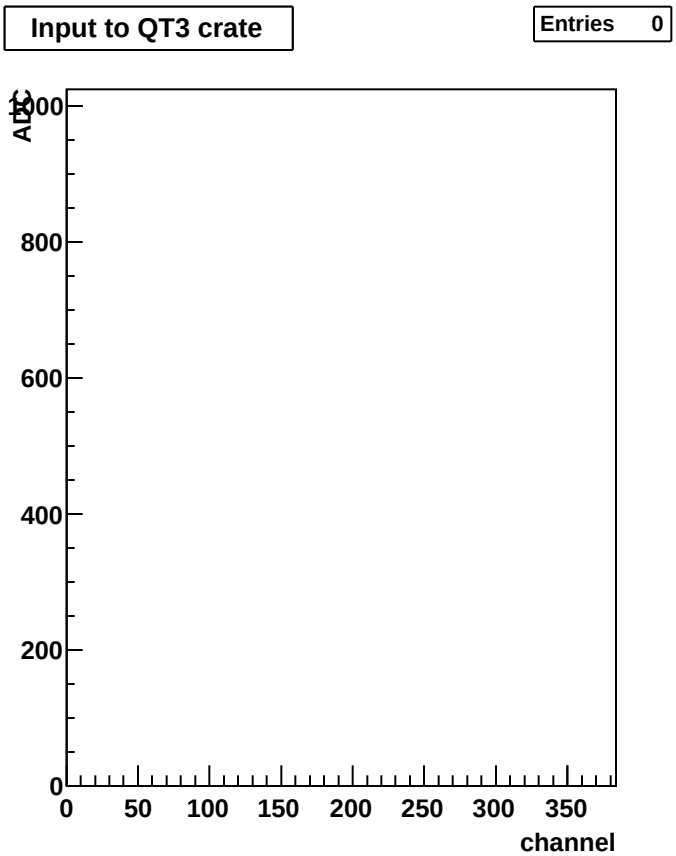
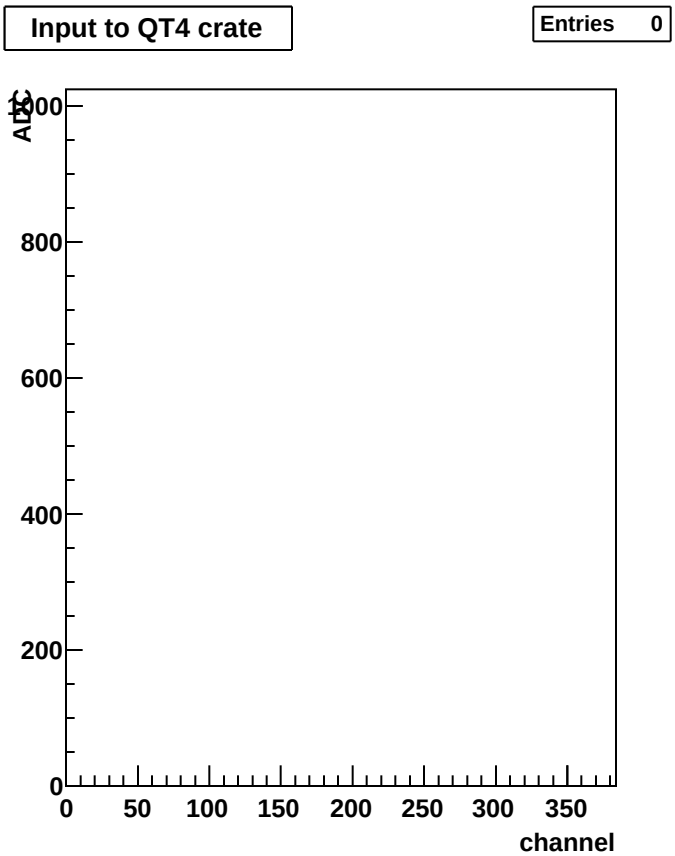

Entries 109011
ZDC East ADC Sum Att
ZDC West ADC Sum Att

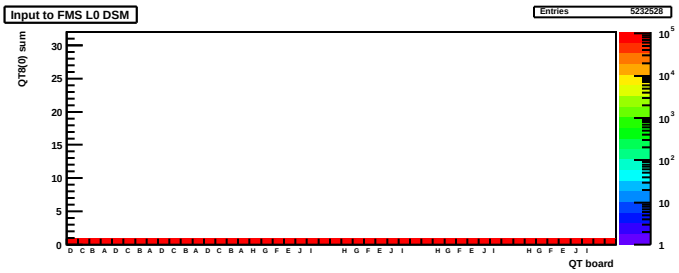

Entries 109011
BBC-L TAC Diff
BCC-S TAC Diff
Entries 109011
BBC-L TAC Diff
VPD TAC Diff
Entries 109011
BBC-L TAC Diff
ZDC TAC Diff
Entries 109011
BBC-S TAC Diff
VPD TAC Diff
Entries 109011
BBC-S TAC Diff
ZDC TAC Diff
Entries 109011
VPD TAC Diff
ZDC TAC Diff

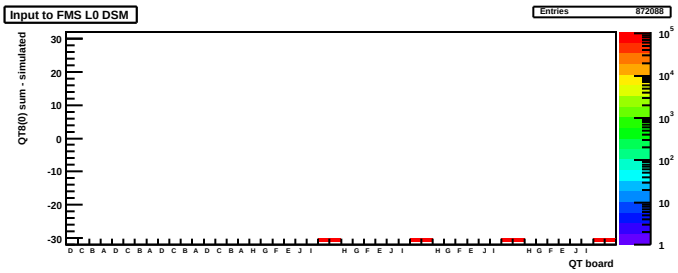

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

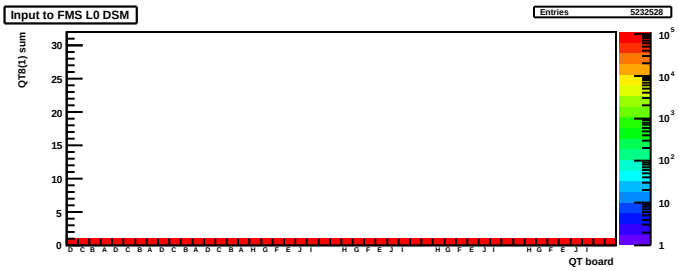

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

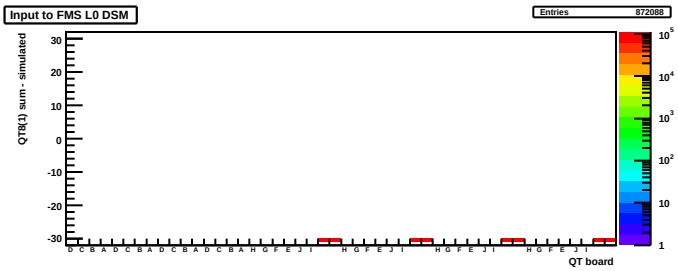

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

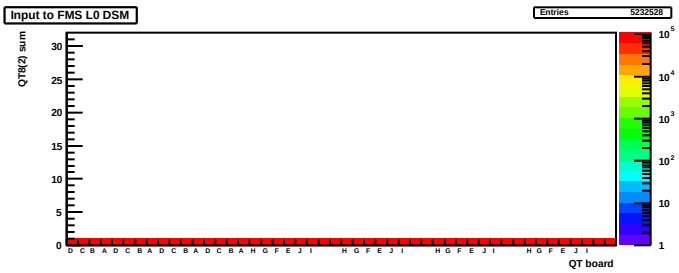

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

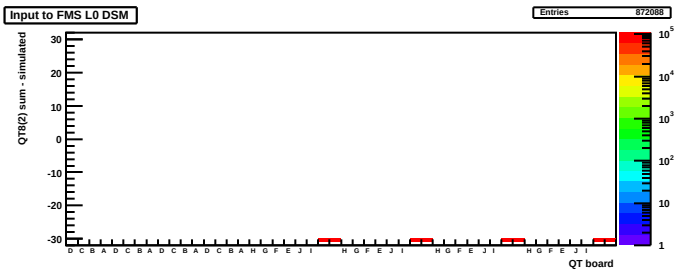

Input to FMS L0 DSM
Entries 5232528
QT8(0) sum
QT board

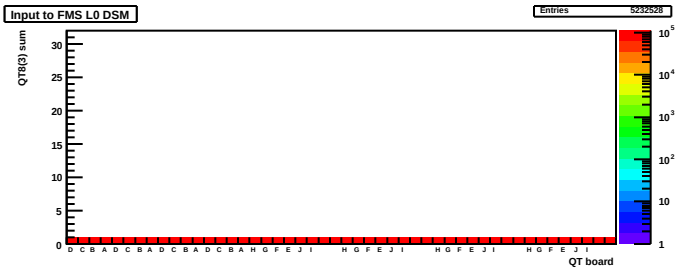

Input to FMS L0 DSM
Entries 872088
QT8(0) sum - simulated
QT board

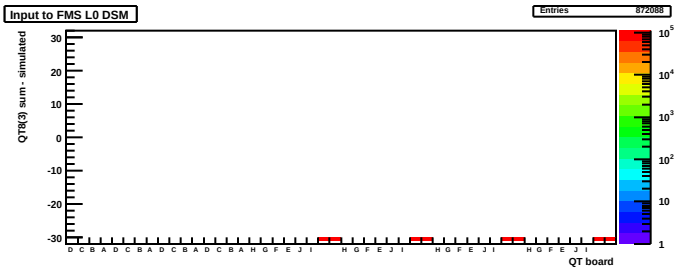

Input to FMS L0 DSM
Entries 5232528
QT8(1) sum
QT board

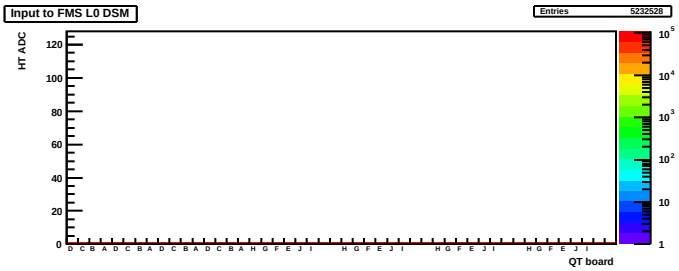

Input to FMS L0 DSM
Entries 872088
QT8(1) sum - simulated
QT board

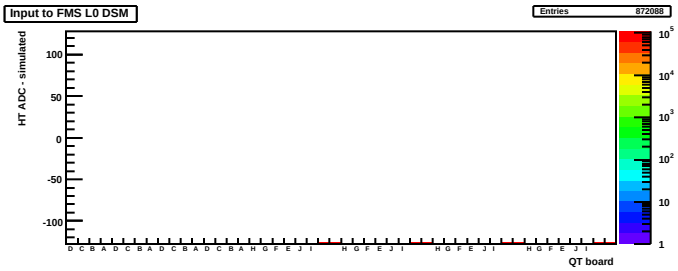

Input to FMS L0 DSM
Entries 5232528
QT8(2) sum
QT board

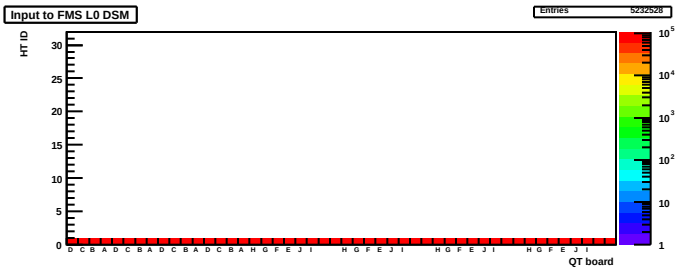

Input to FMS L0 DSM
Entries 872088
QT8(2) sum - simulated
QT board

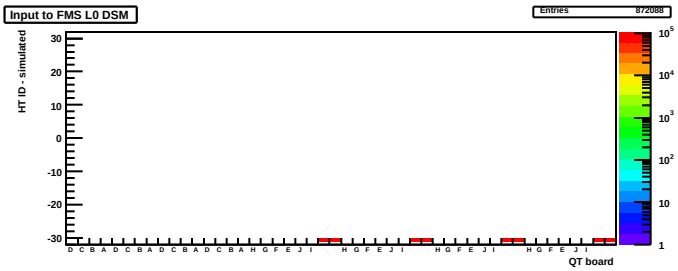

Input to FMS L0 DSM
Entries 5232528
QT8(3) sum
QT board

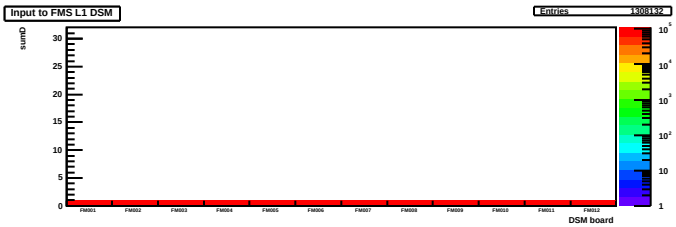

Input to FMS L0 DSM
Entries 872088
QT8(3) sum - simulated
QT board

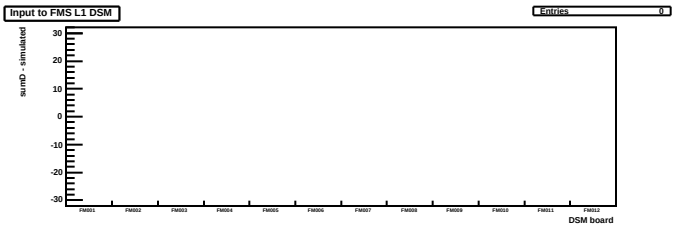

Input to FMS L0 DSM
Entries 5232528
HT ADC
QT board

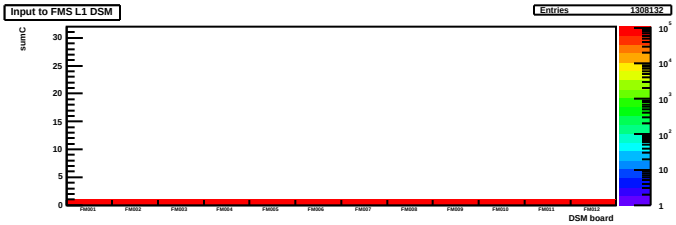

Input to FMS L0 DSM
Entries 872088
HT ADC - simulated
QT board

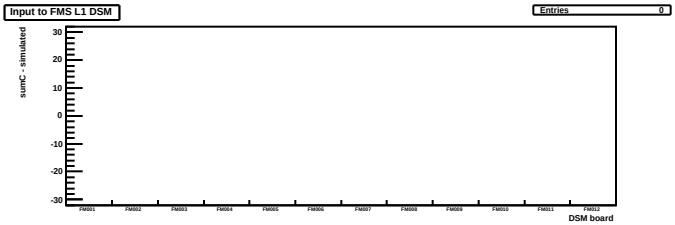

Input to FMS L0 DSM
Entries 5232528
HT ID
QT board

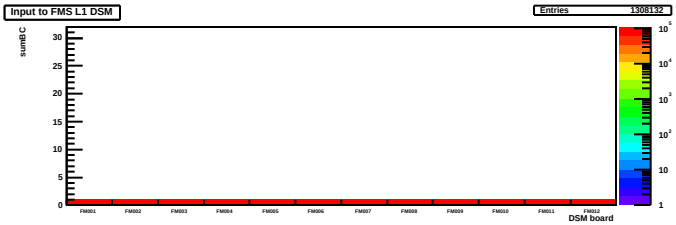

Input to FMS L0 DSM
Entries 872088
HT ID - simulated
QT board

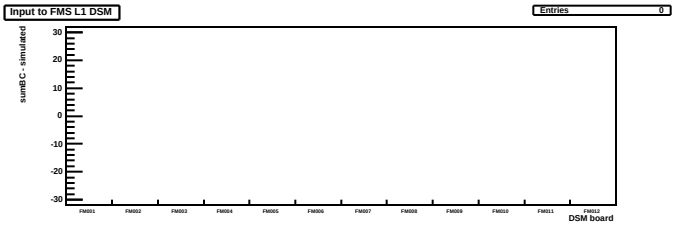

Input to FMS L1 DSM
Entries 1308132
sumD
DSM board

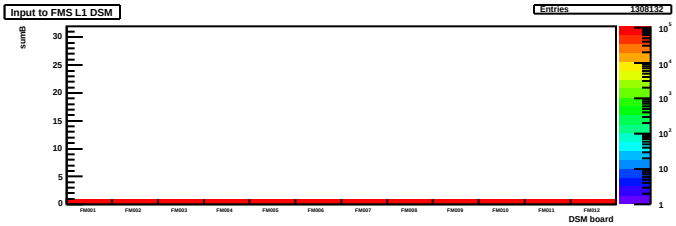

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

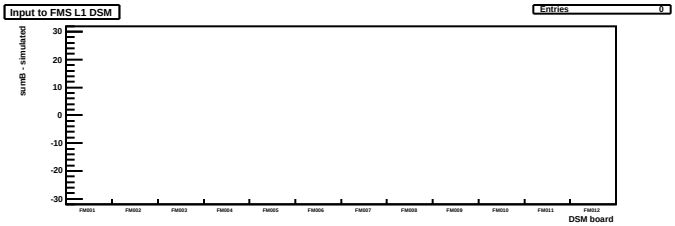

Input to FMS L1 DSM
Entries 1308132
sumC
DSM board

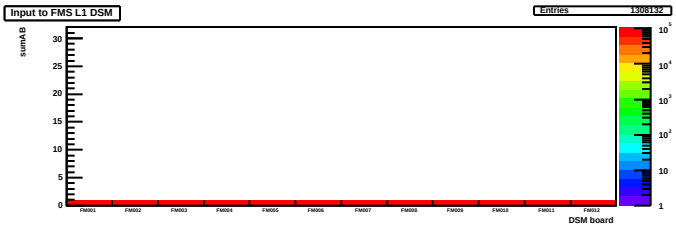

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

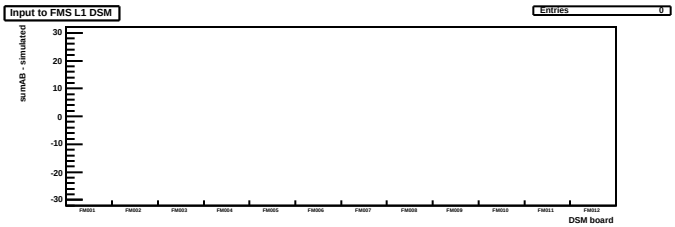

Input to FMS L1 DSM
Entries 1308132
sumBC
DSM board

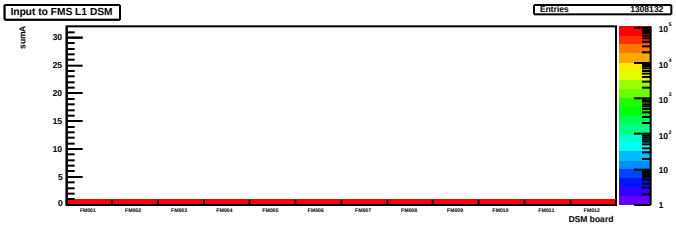

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

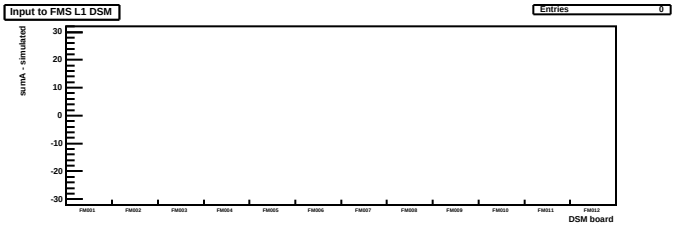

Input to FMS L1 DSM
Entries 1308132
sumB
DSM board

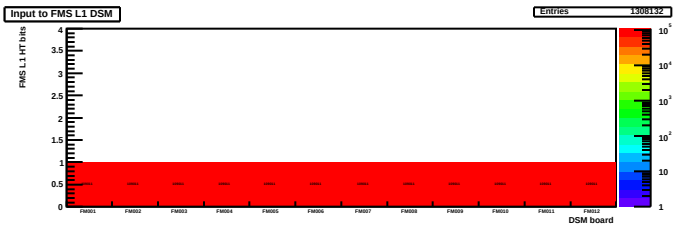

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

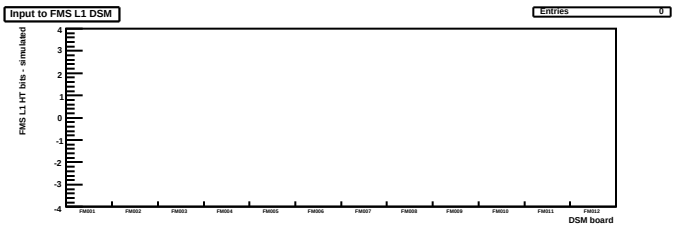

Input to FMS L1 DSM
Entries 1308132
sumAB
DSM board

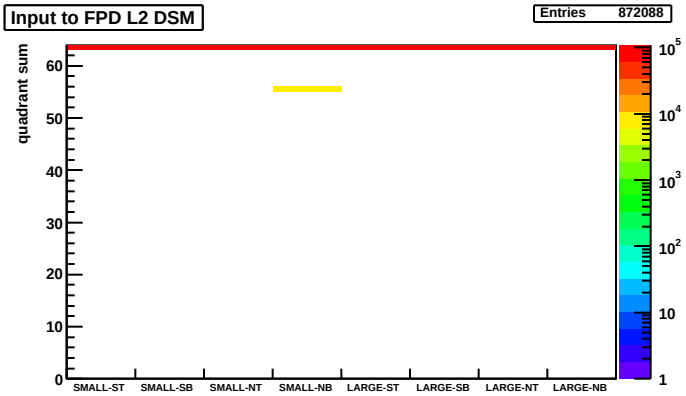

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

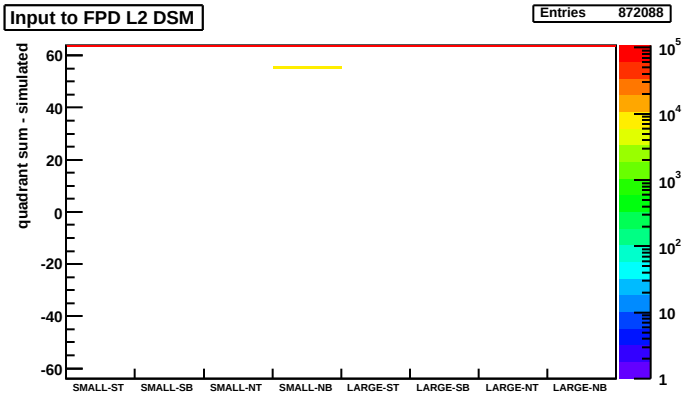

Input to FMS L1 DSM
Entries 1308132
sumA
DSM board

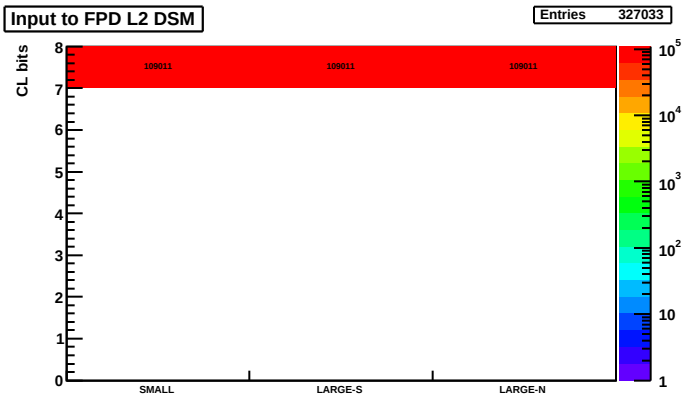

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

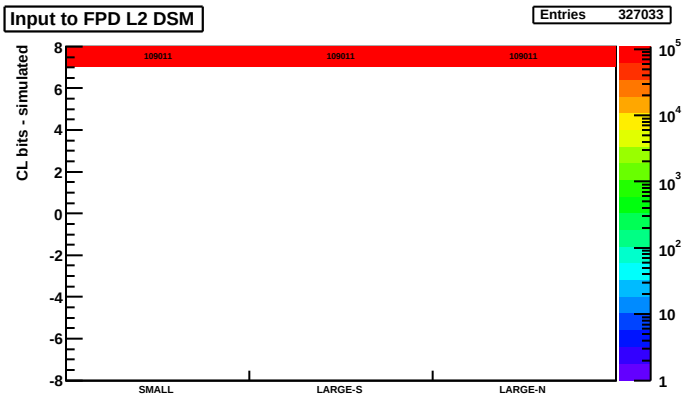

Input to FMS L1 DSM
Entries 1308132
FMS L1 HT bits
DSM board

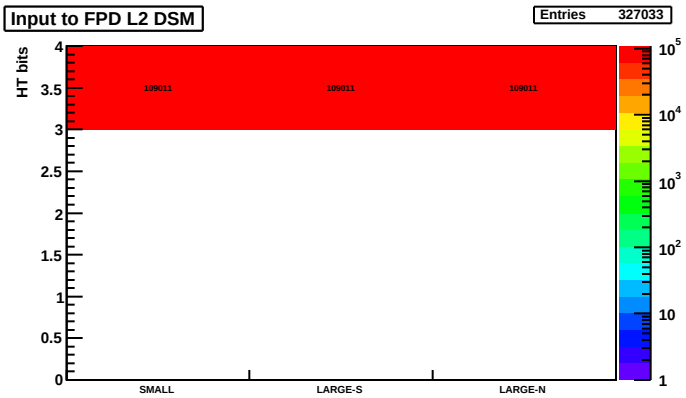

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

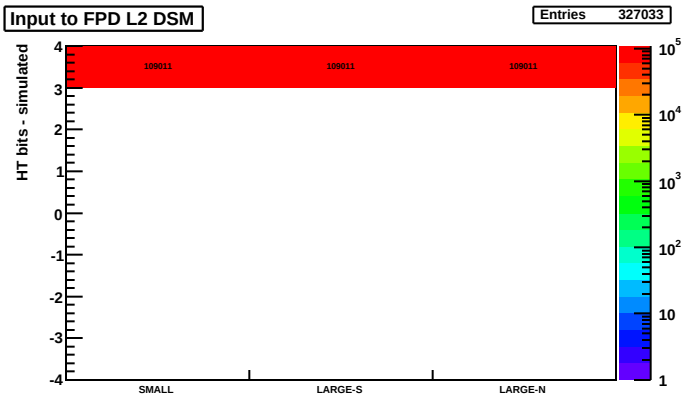

Input to FPD L2 DSM
Entries 872088
quadrant sum
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

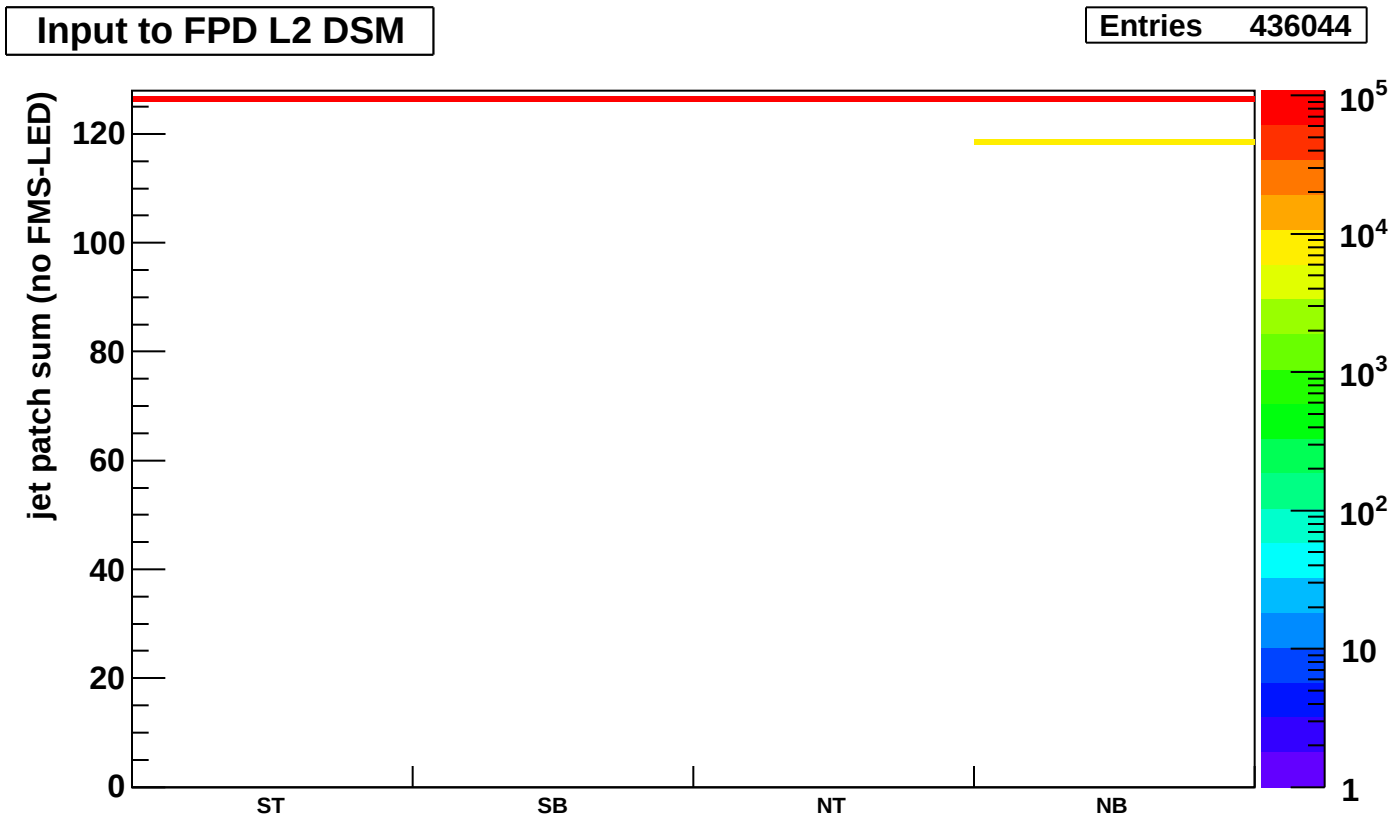

Input to FPD L2 DSM
Entries 872088
quadrant sum - simulated
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

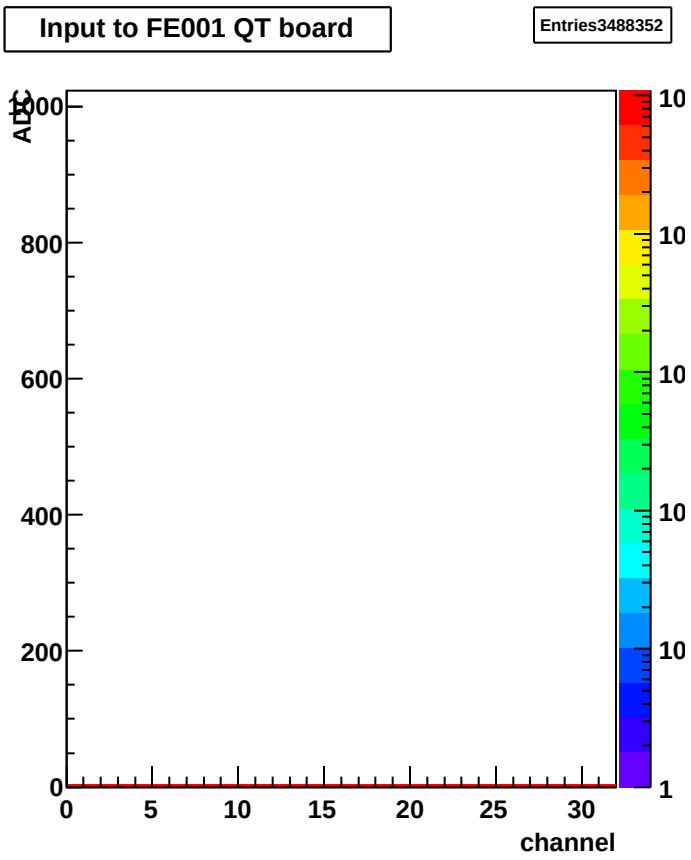

Input to FPD L2 DSM
Entries 327033
CL bits
109011 109011 109011
SMALL LARGE-S LARGE-N

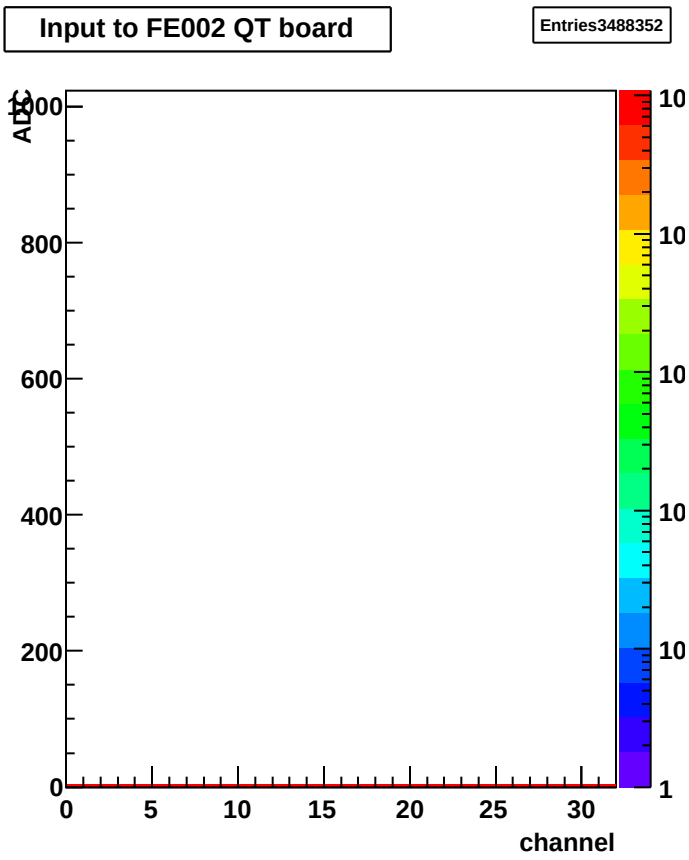

Input to FPD L2 DSM
Entries 327033
CL bits - simulated
109011 109011 109011
SMALL LARGE-S LARGE-N

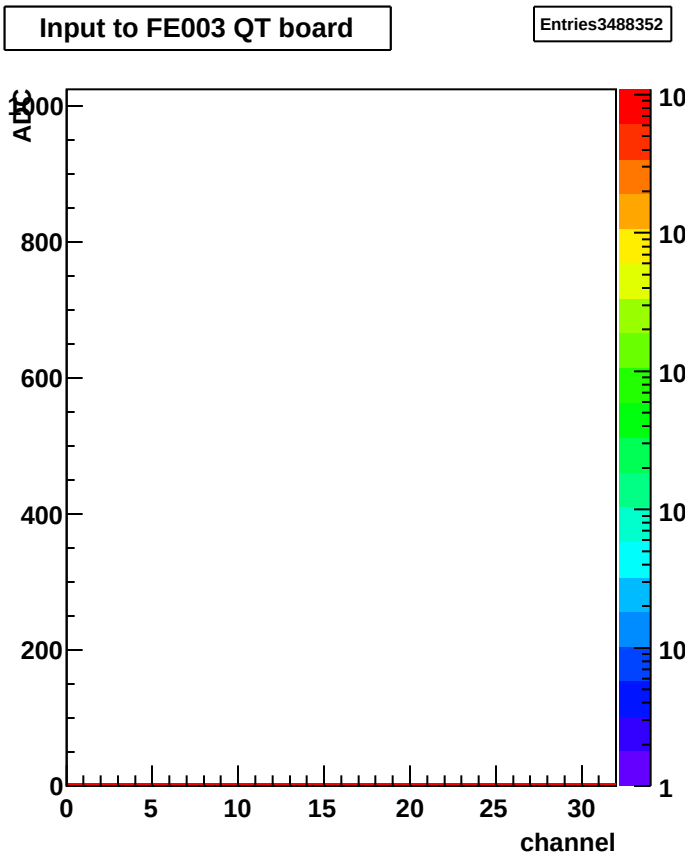

Input to FPD L2 DSM
Entries 327033
HT bits
109011 109011 109011
SMALL LARGE-S LARGE-N

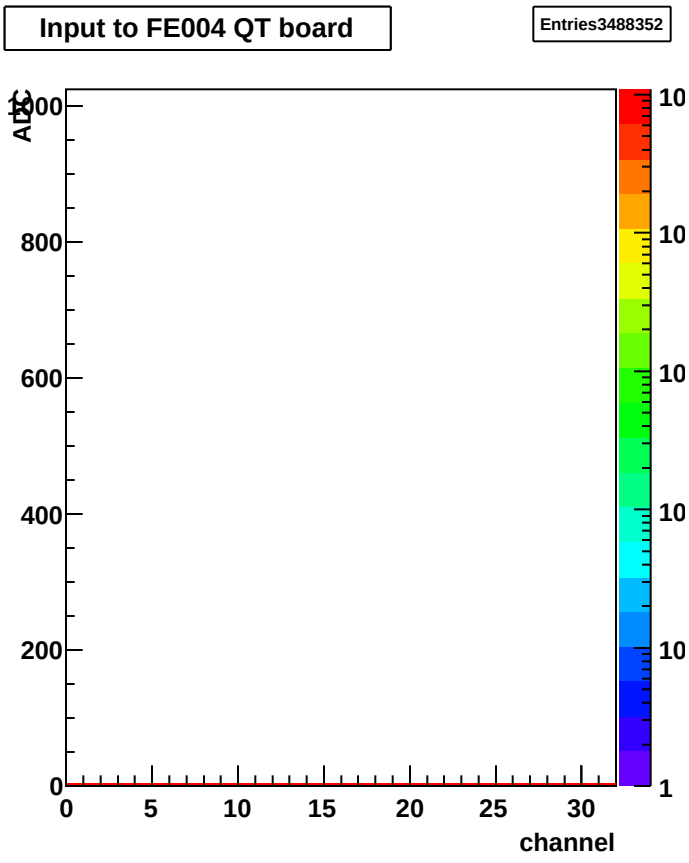

Input to FPD L2 DSM
Entries 327033
HT bits - simulated
109011 109011 109011
SMALL LARGE-S LARGE-N

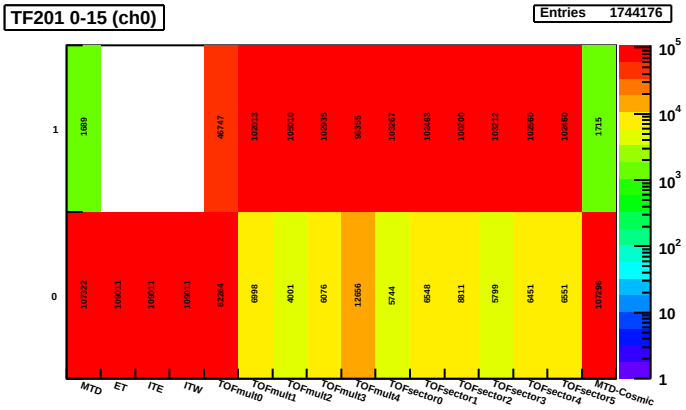

Input to FPD L2 DSM
Entries 436044
jet patch sum (no FMS-LED)
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

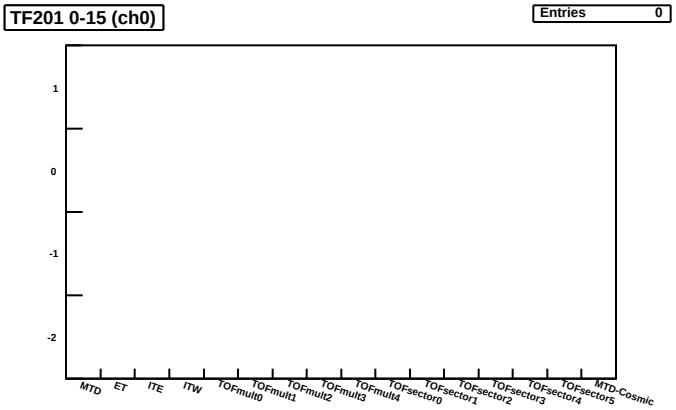

Input to FE001 QT board
Entries3488352
ADC
channel

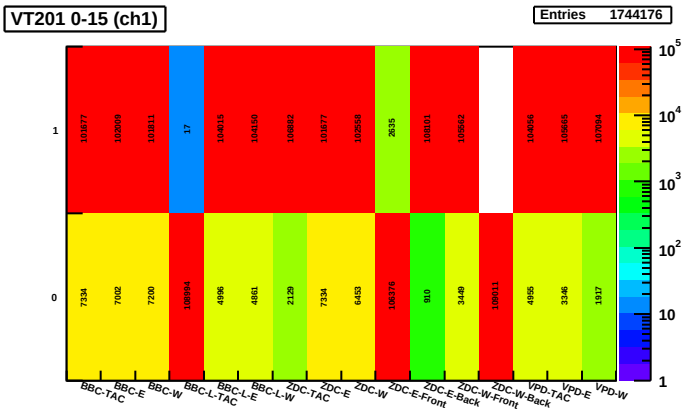

Input to FE002 QT board
Entries3488352
ADC
channel

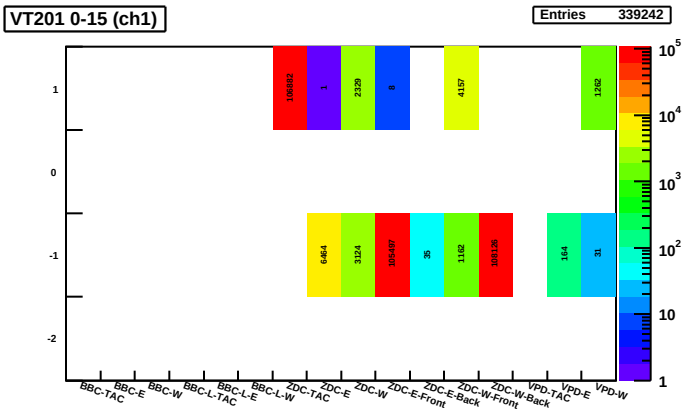

Input to FE003 QT board
Entries3488352
ADC
channel

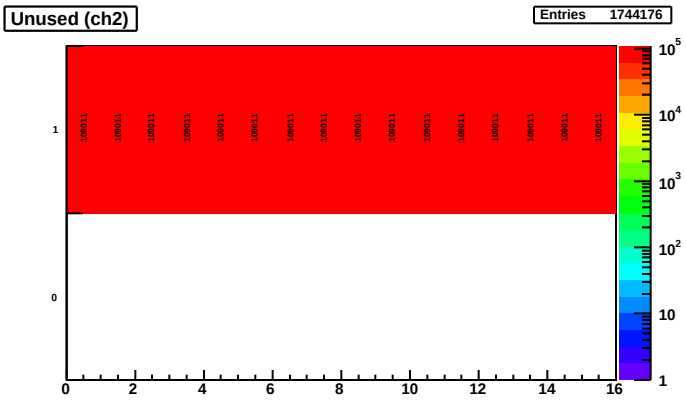

Input to FE004 QT board
Entries3488352
ADC
channel

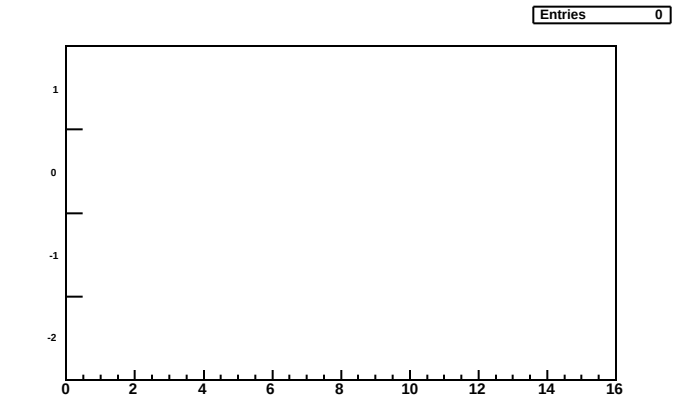

TF201 0-15 (ch0)
Entries 1744176
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

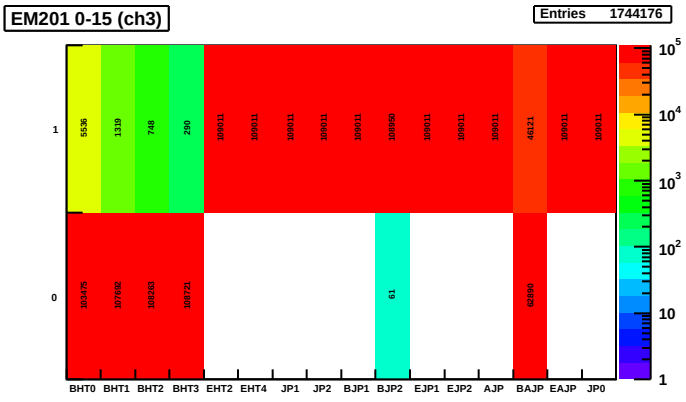

TF201 0-15 (ch0)
Entries 0
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

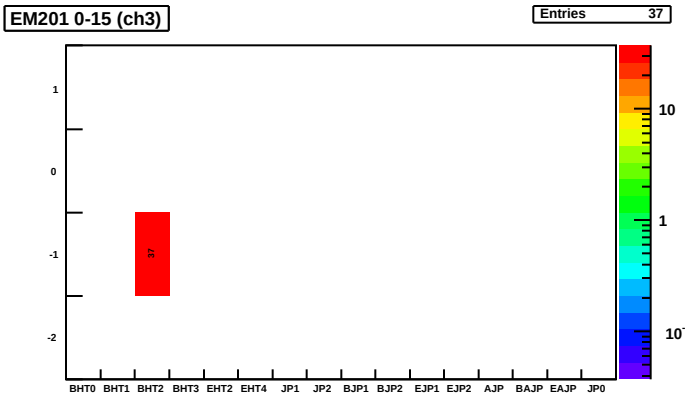

VT201 0-15 (ch1)
Entries 1744176
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

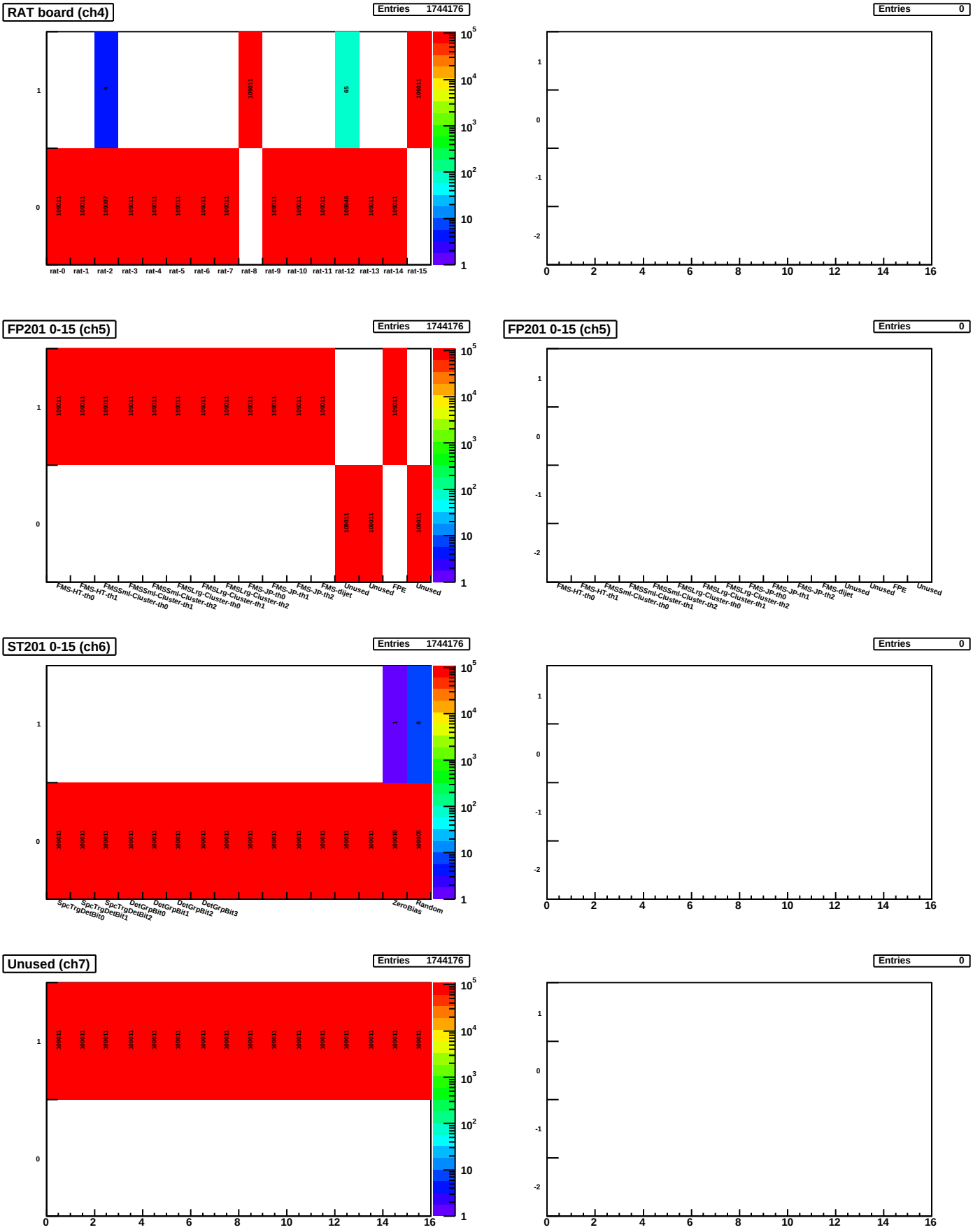

VT201 0-15 (ch1)
Entries 339242
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

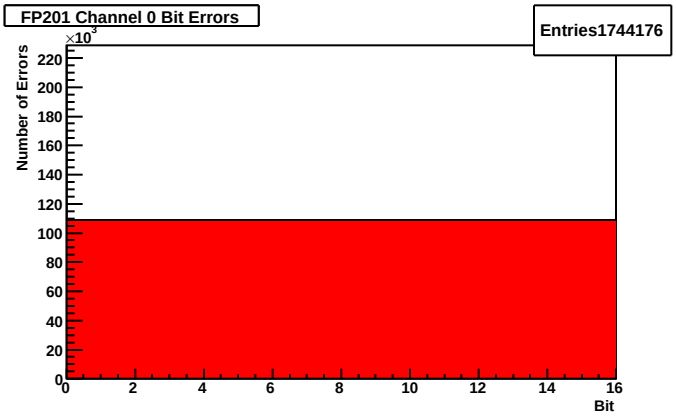

Unused (ch2)
Entries 1744176

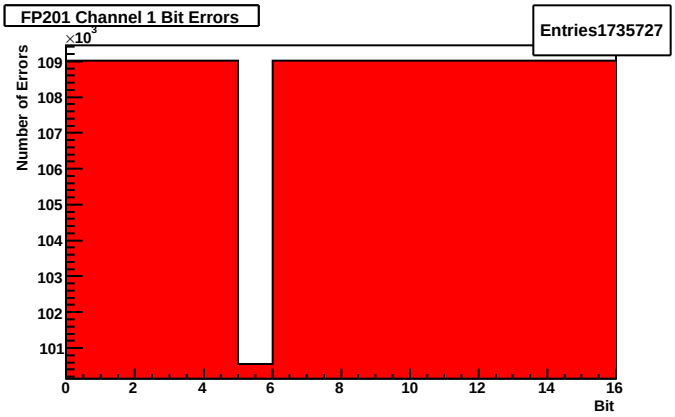

Entries 0

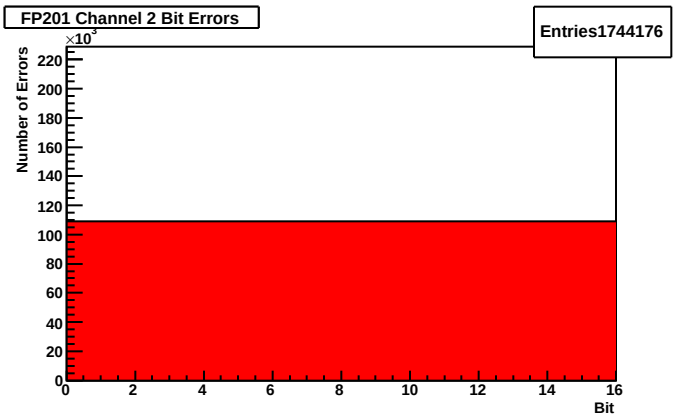

EM201 0-15 (ch3)
Entries 1744176
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

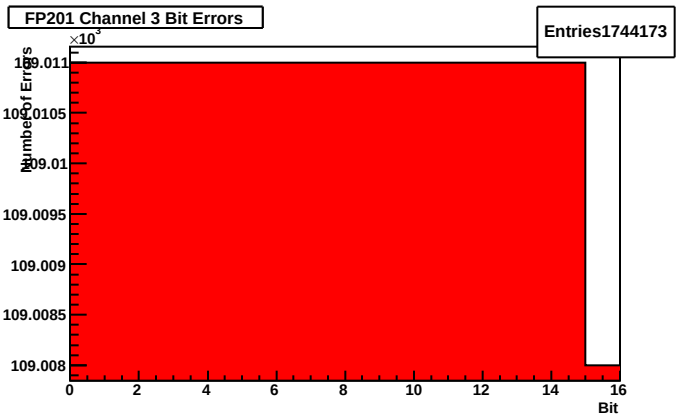

EM201 0-15 (ch3)
Entries 37
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

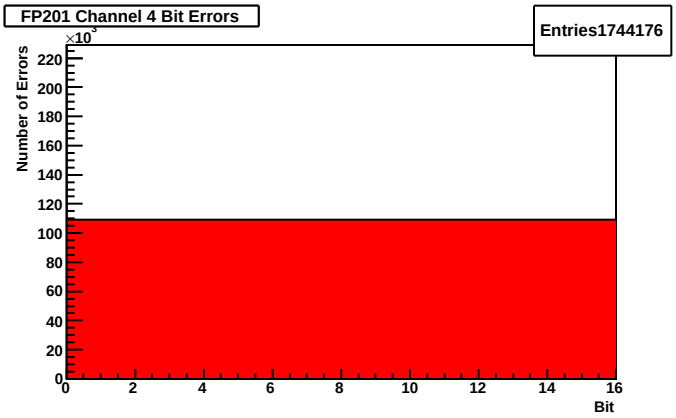

RAT board (ch4)
Entries 1744176
Entries 0
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15
FP201 0-15 (ch5)
Entries 1744176
Entries 0
FMS-HT-th0 FMS-HT-th1 FMSSml-Cluster-th0 FMSSml-Cluster-th1 FMSSml-Cluster-th2 FMSLrg-Cluster-th0 FMSLrg-Cluster-th1 FMSLrg-Cluster-th2 FMS-JP-th0 FMS-JP-th1 FMS-JP-th2 FMS-diJet Unused Unused FPE Unused
ST201 0-15 (ch6)
Entries 1744176
Entries 0
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random
Unused (ch7)
Entries 1744176
Entries 0

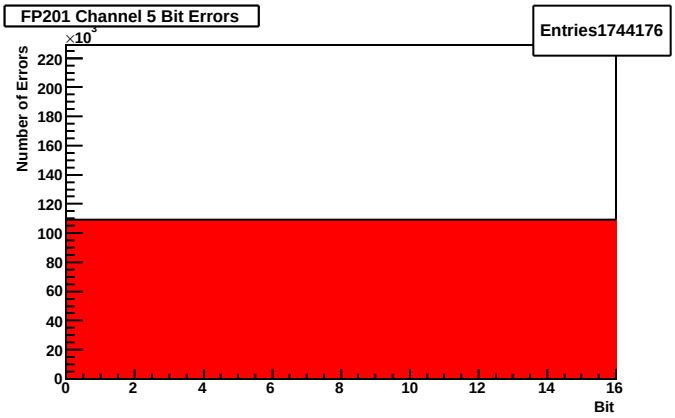

FP201 Channel 0 Bit Errors
Entries1744176
Number of Errors
Bit

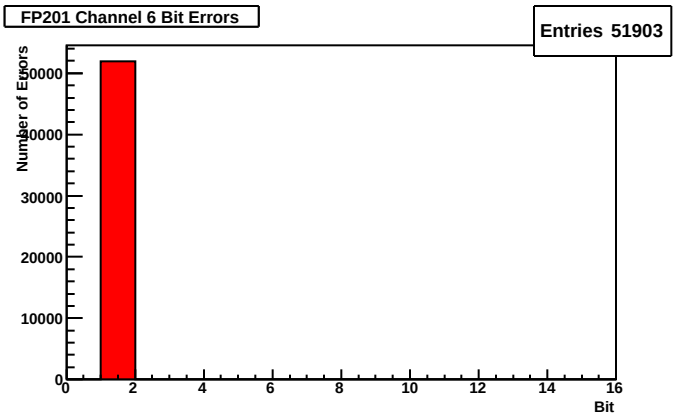

FP201 Channel 1 Bit Errors
Entries1735727
Number of Errors
Bit

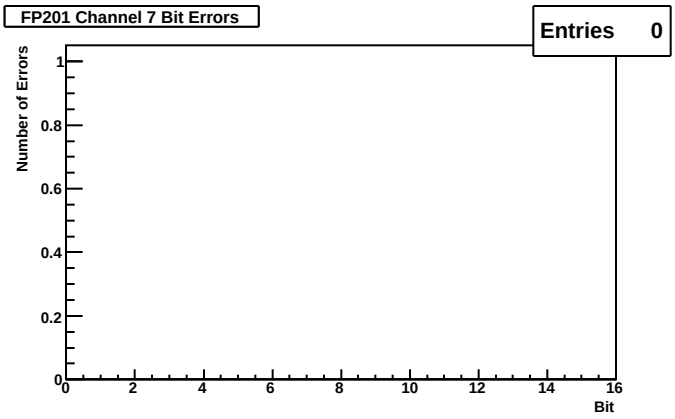

FP201 Channel 2 Bit Errors
Entries1744176
Number of Errors
Bit

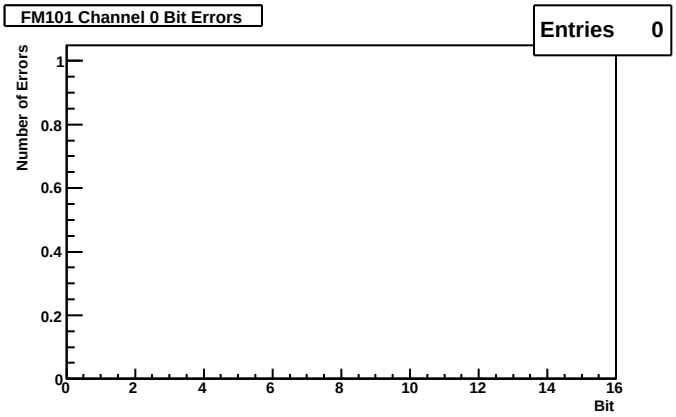

FP201 Channel 3 Bit Errors
Entries1744173
Number of Errors
Bit

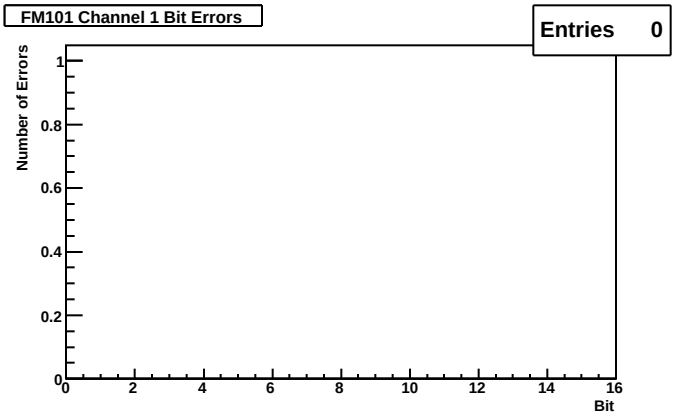

FP201 Channel 4 Bit Errors
Entries1744176
Number of Errors
Bit

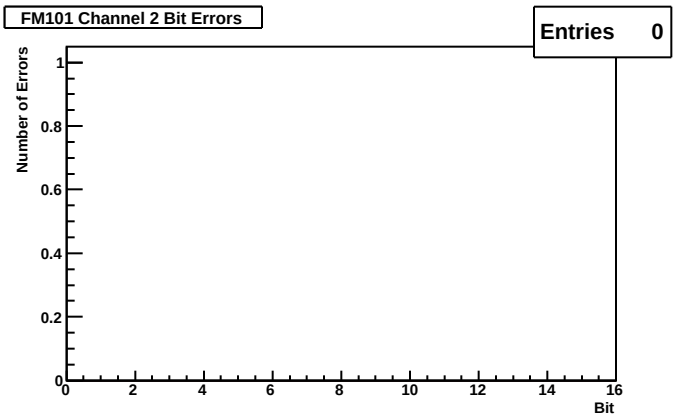

FP201 Channel 5 Bit Errors
Entries1744176
Number of Errors
Bit

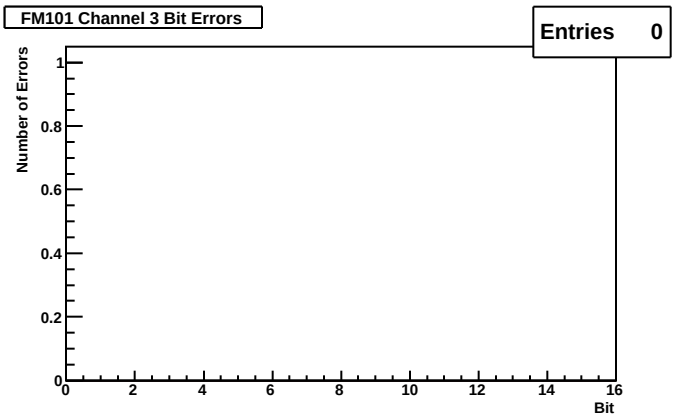

FP201 Channel 6 Bit Errors
Entries 51903
Number of Errors
Bit

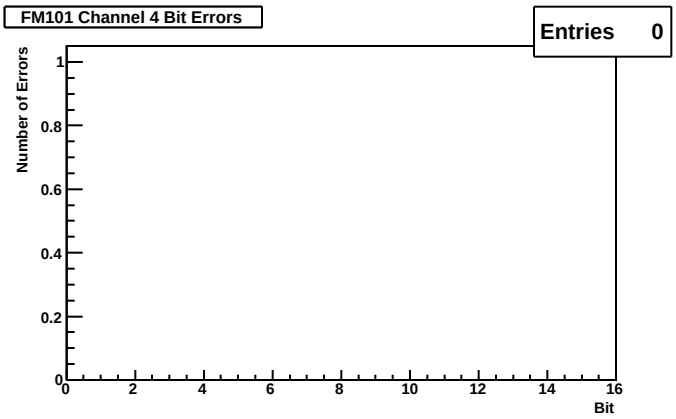

FP201 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

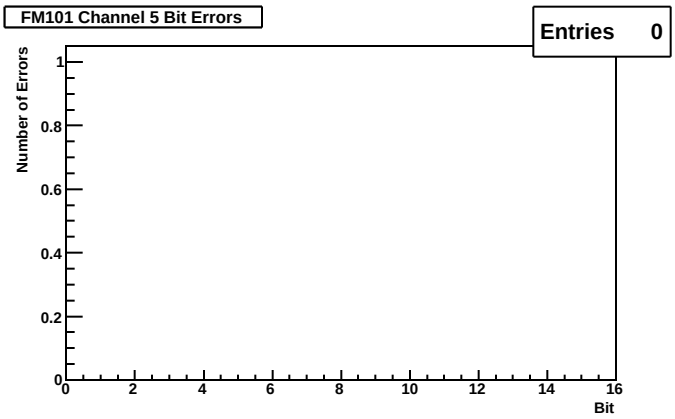

FM101 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

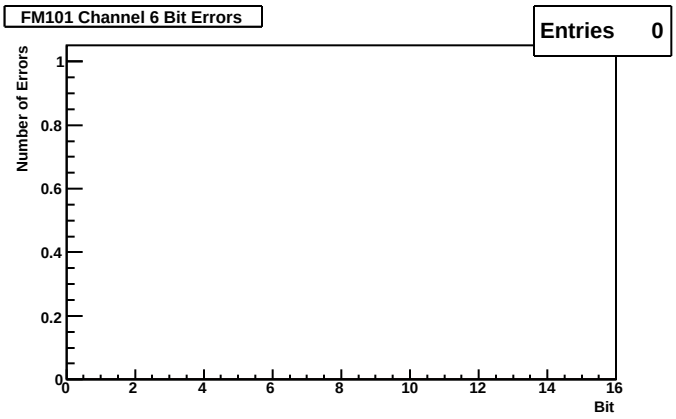

FM101 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

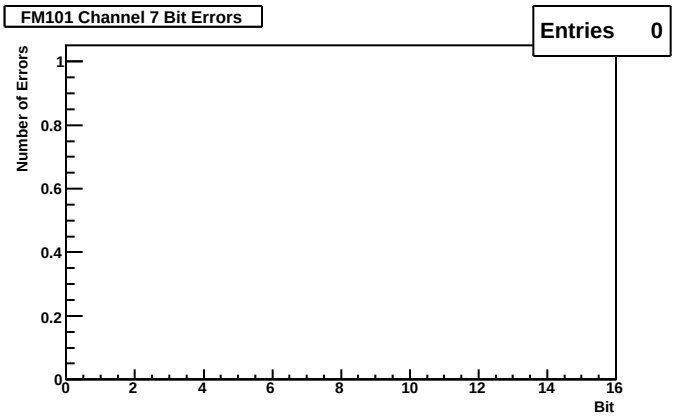

FM101 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

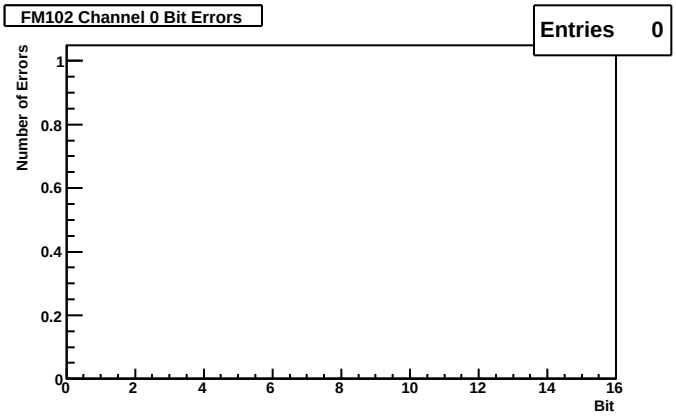

FM101 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

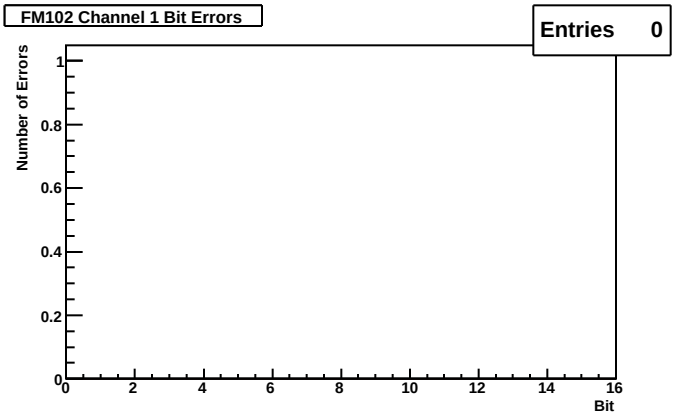

FM101 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

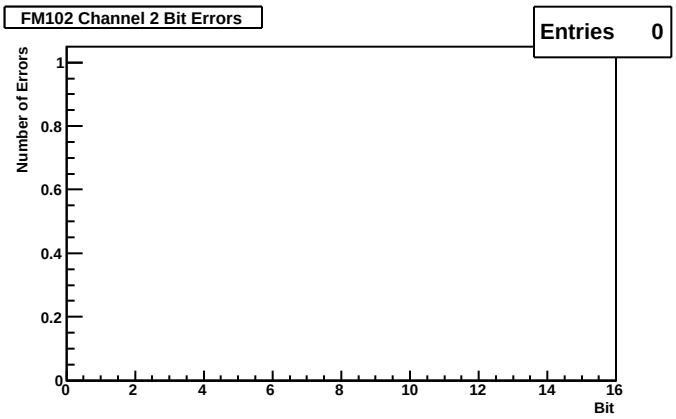

FM101 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

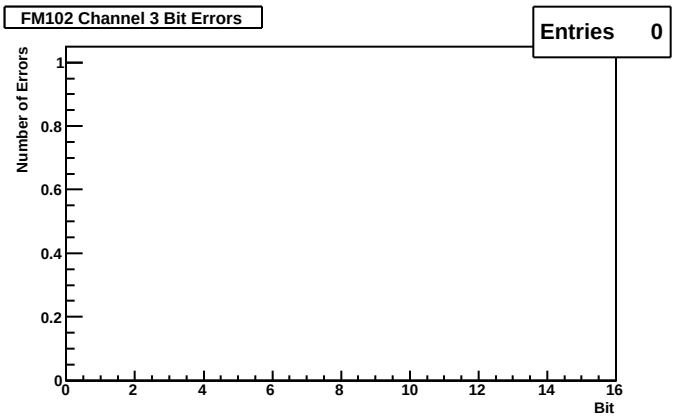

FM101 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

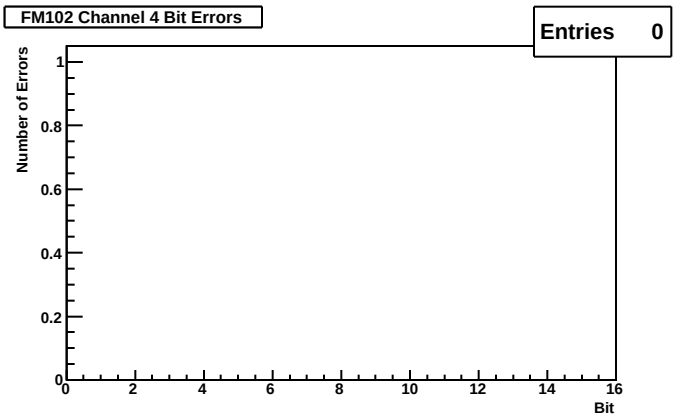

FM101 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

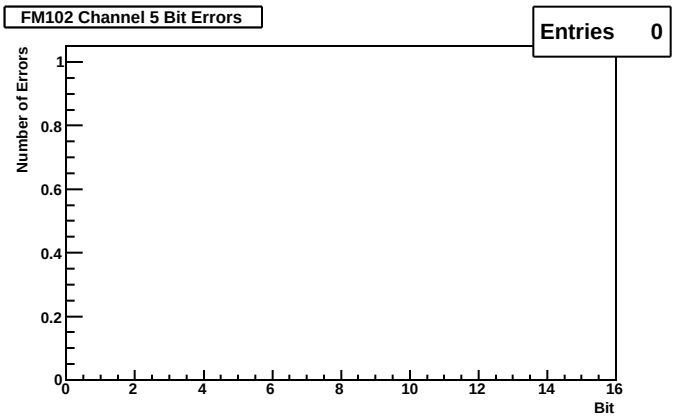

FM102 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

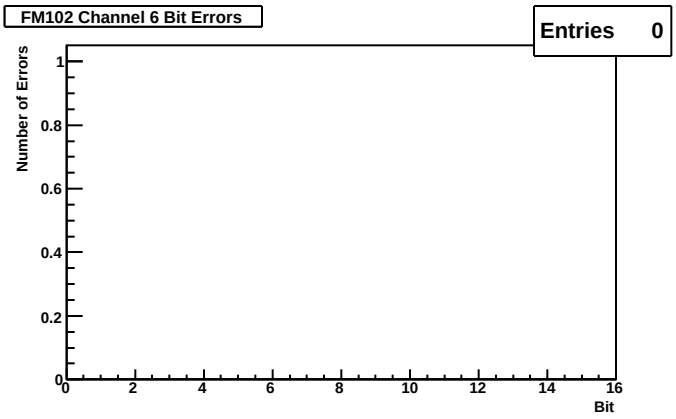

FM102 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

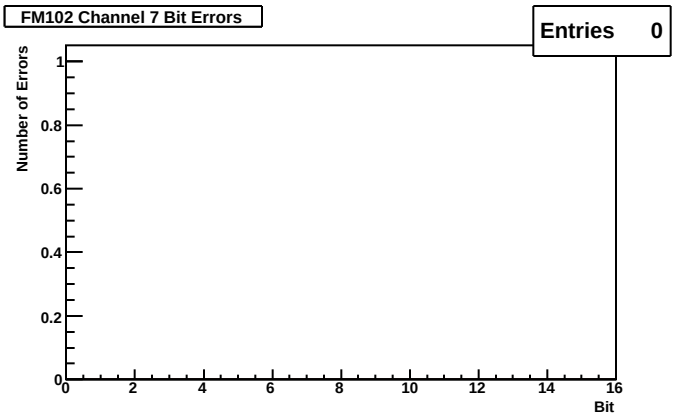

FM102 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

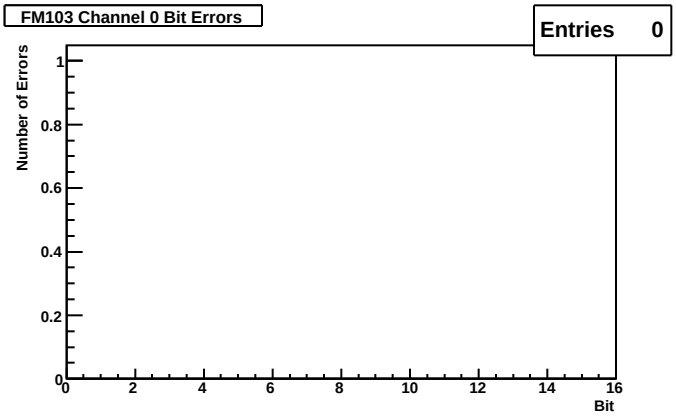

FM102 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

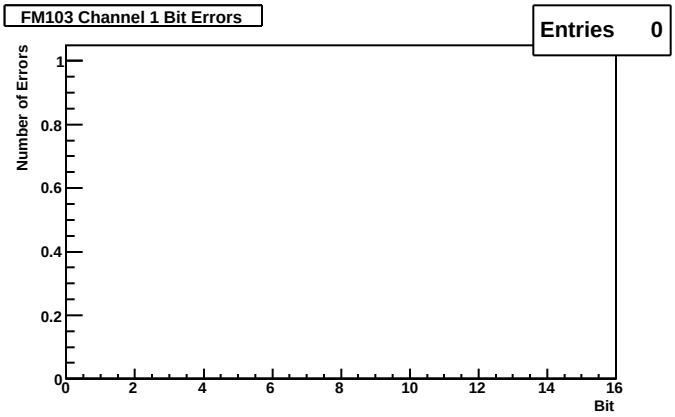

FM102 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

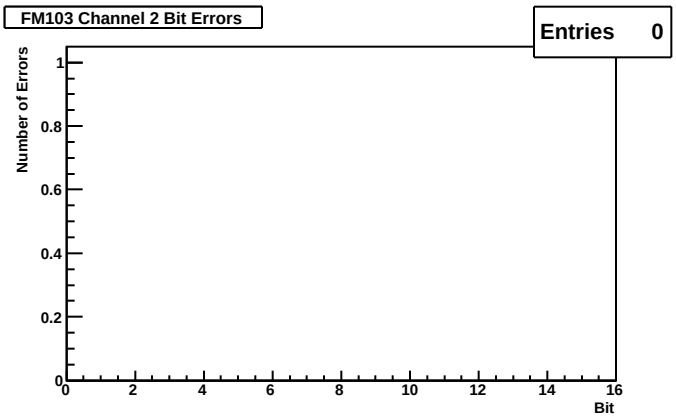

FM102 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

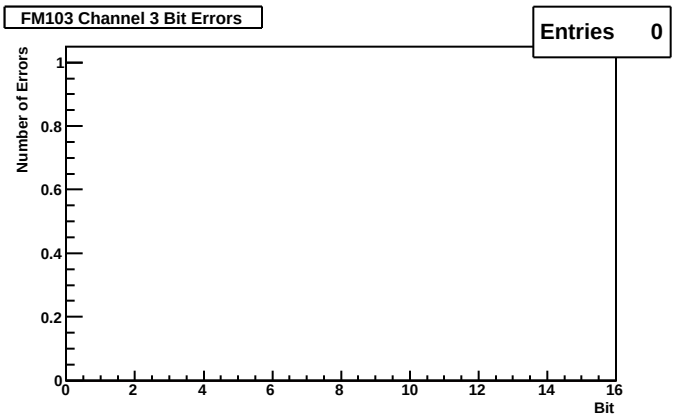

FM102 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

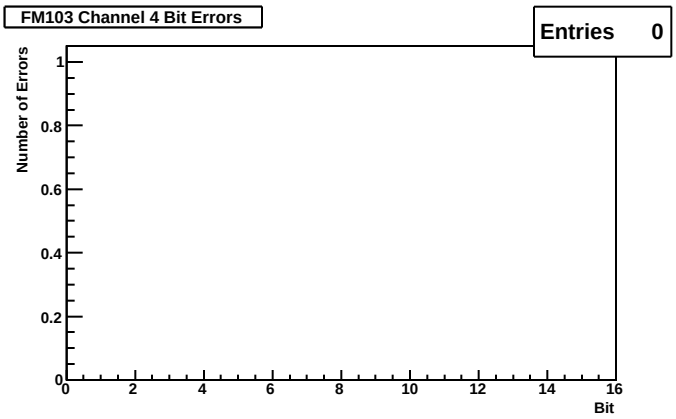

FM102 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

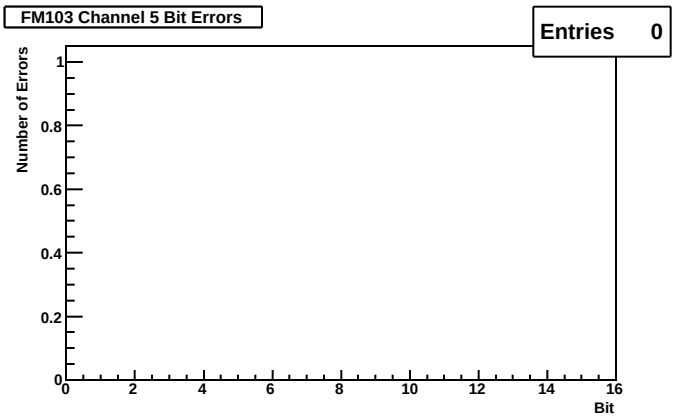

FM103 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

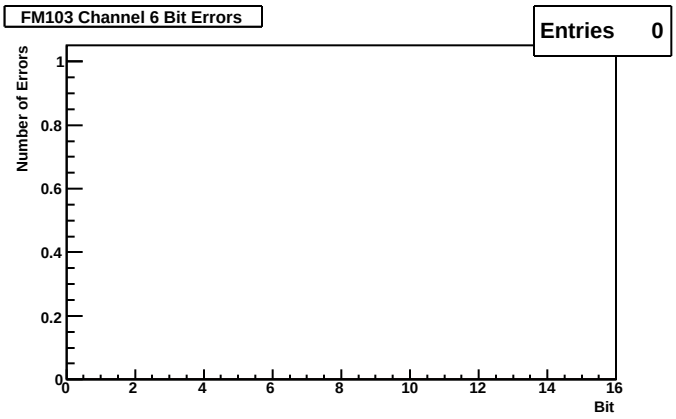

FM103 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

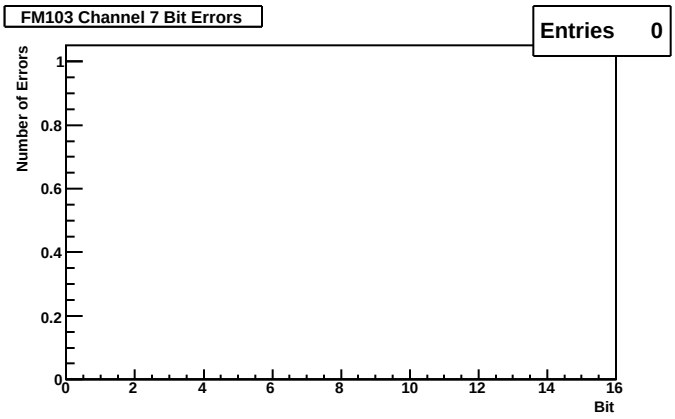

FM103 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

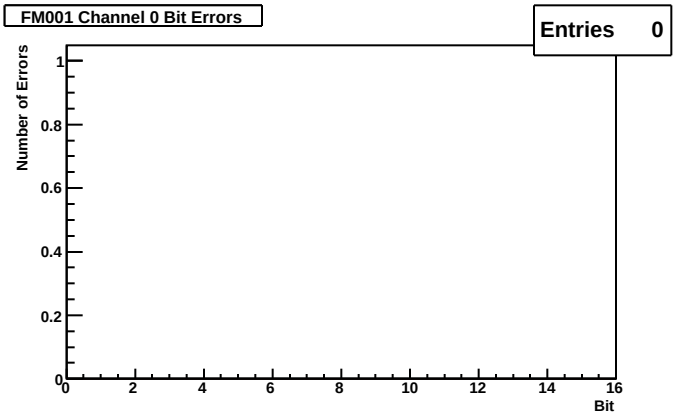

FM103 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

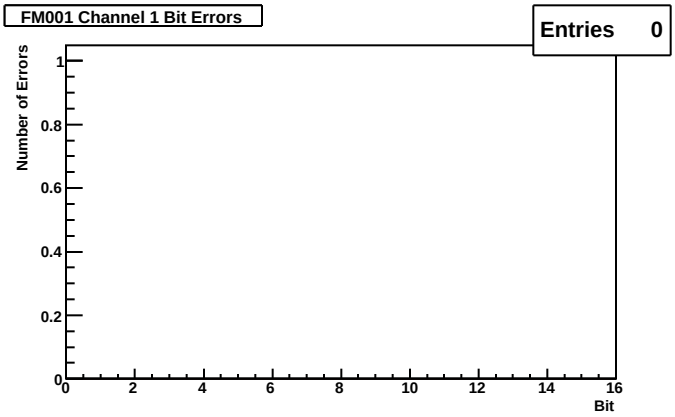

FM103 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

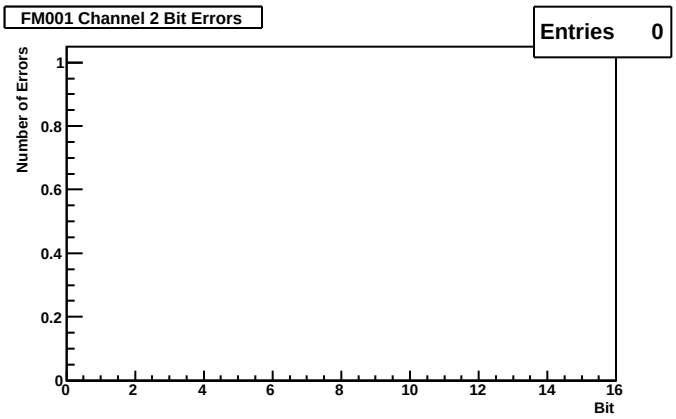

FM103 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

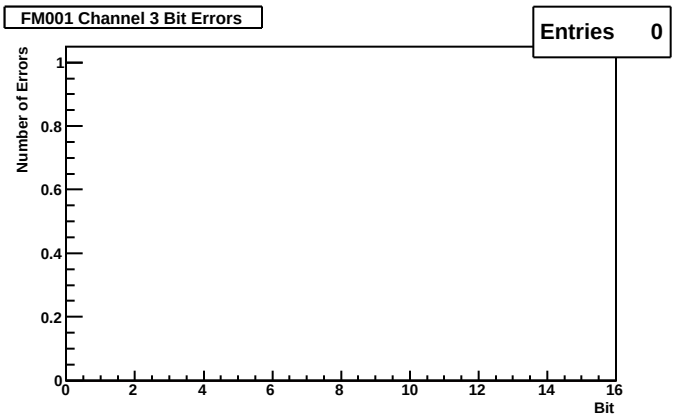

FM103 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

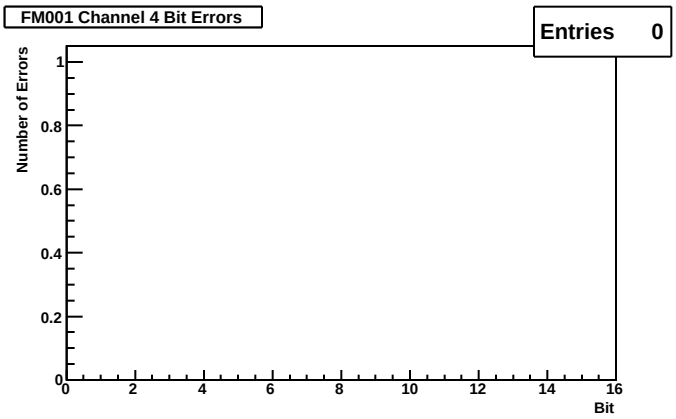

FM103 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

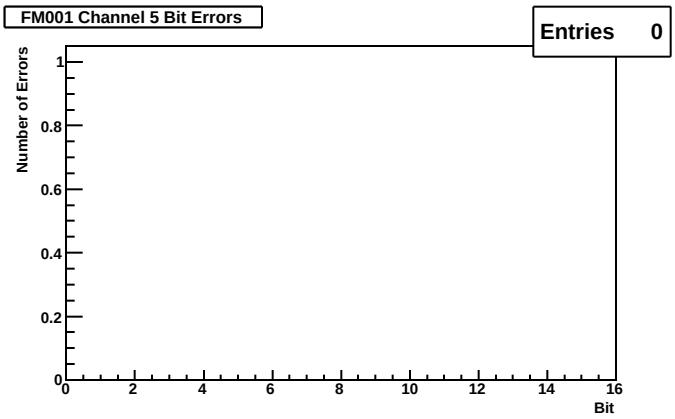

FM001 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

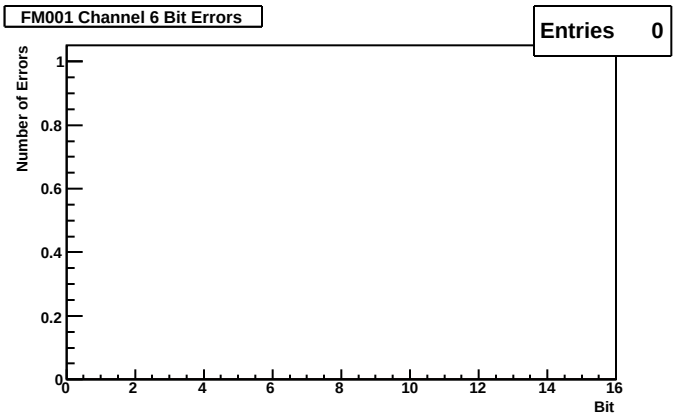

FM001 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

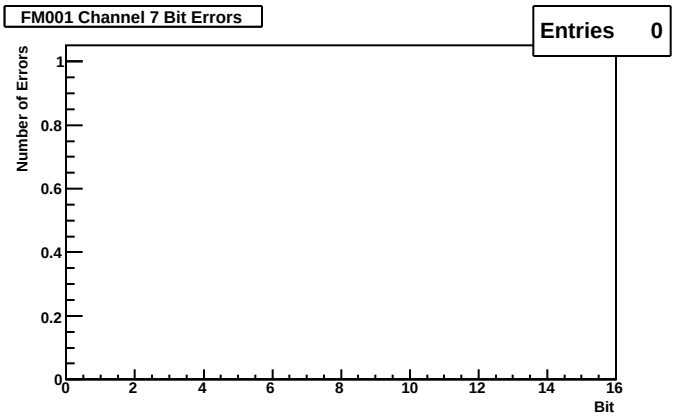

FM001 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

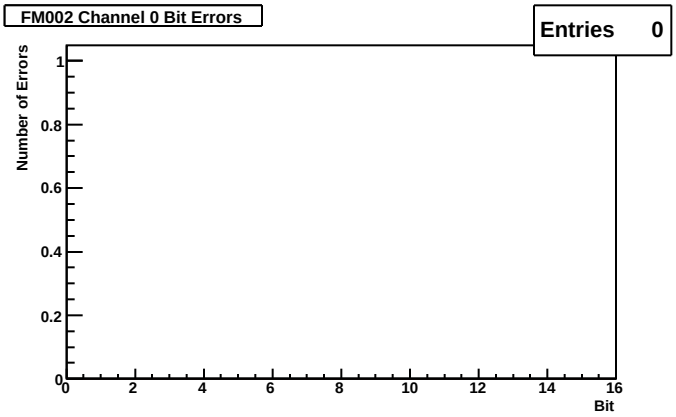

FM001 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

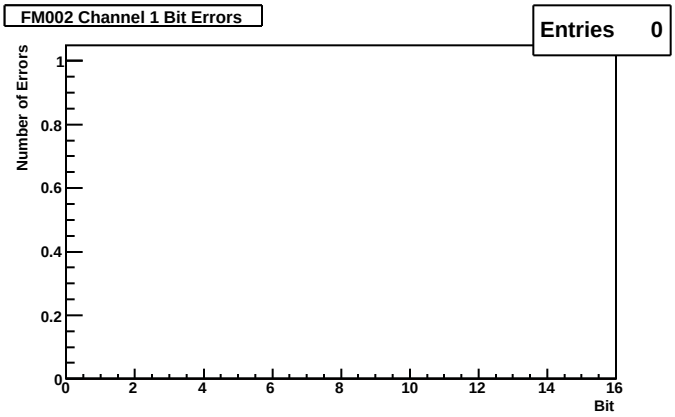

FM001 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

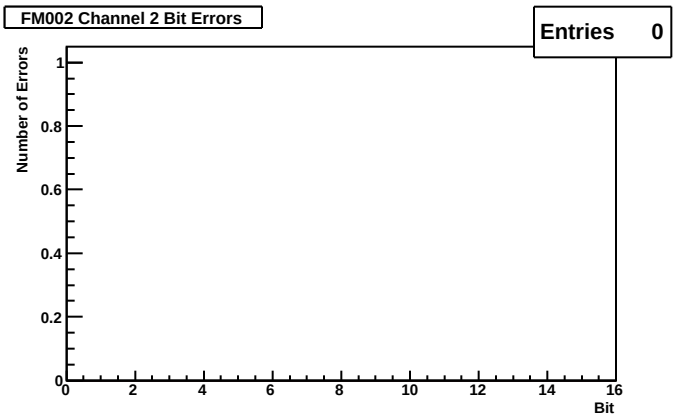

FM001 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

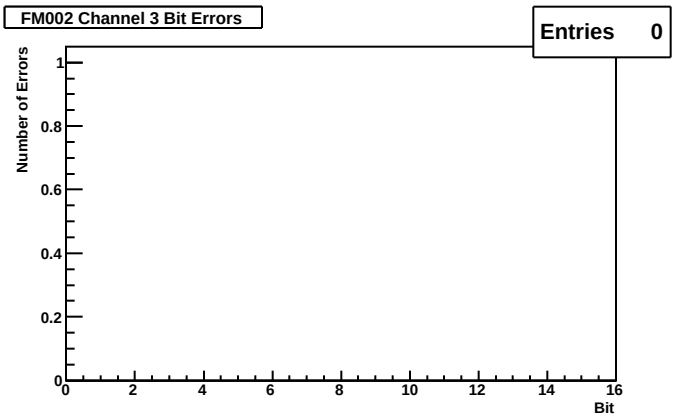

FM001 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

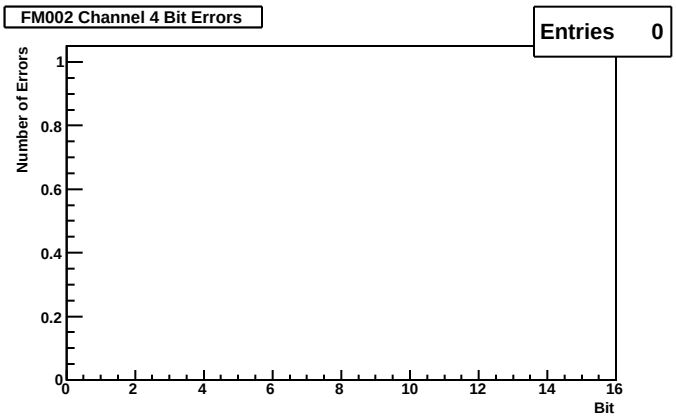

FM001 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

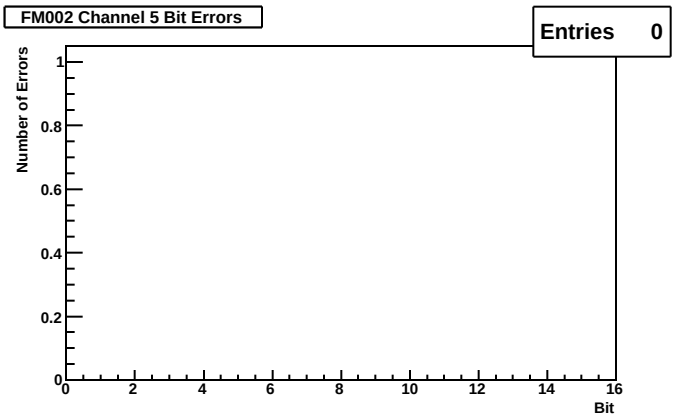

FM002 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

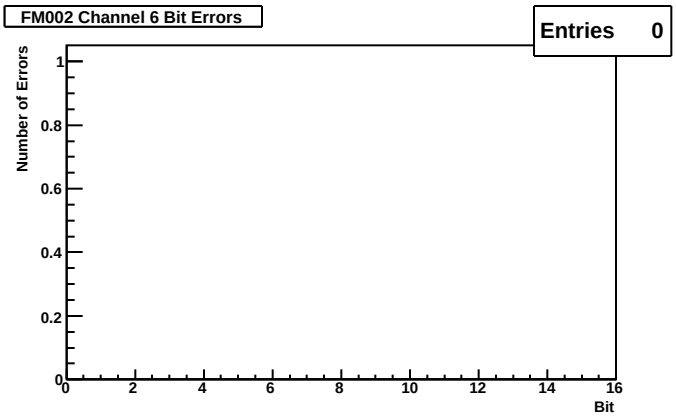

FM002 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

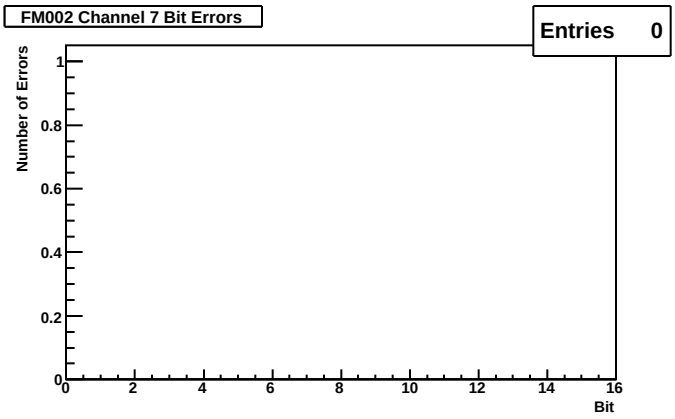

FM002 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

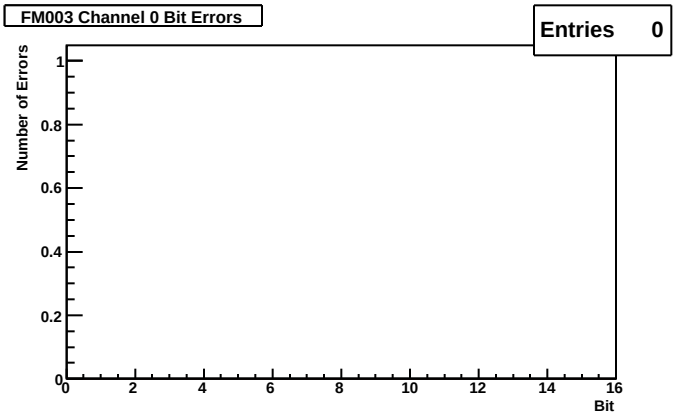

FM002 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

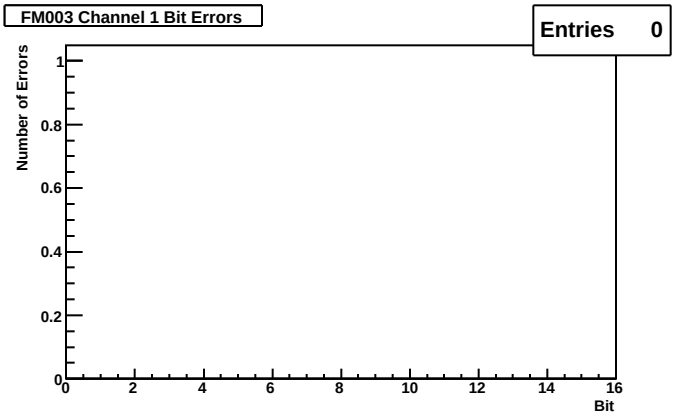

FM002 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

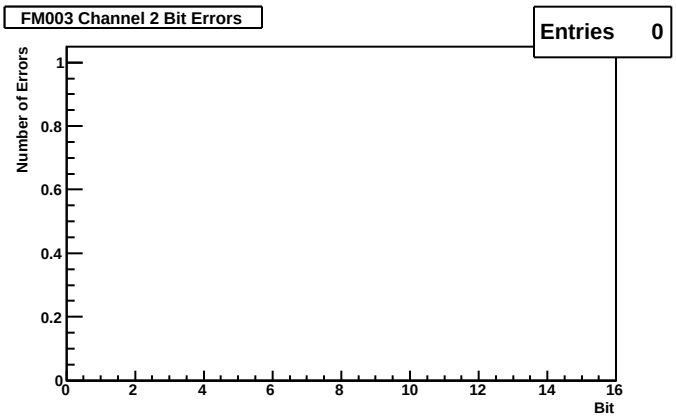

FM002 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

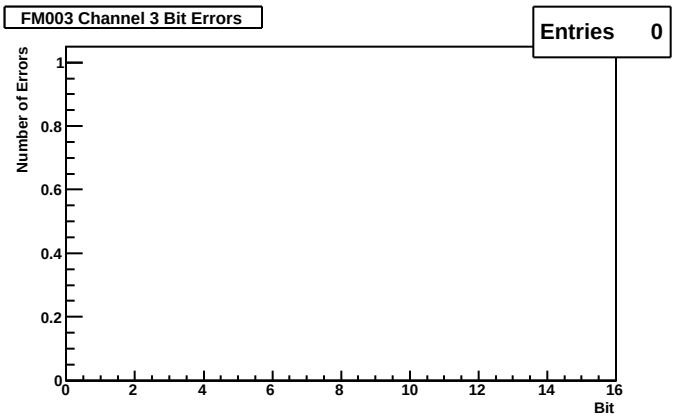

FM002 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

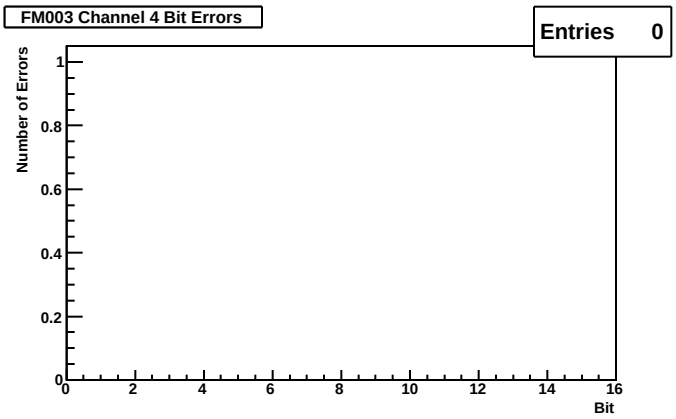

FM002 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

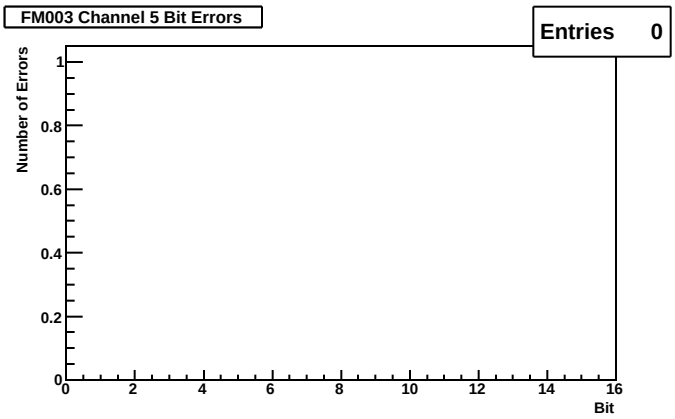

FM003 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

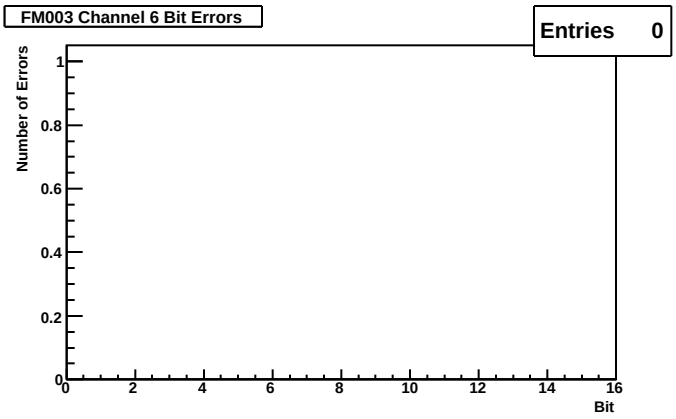

FM003 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

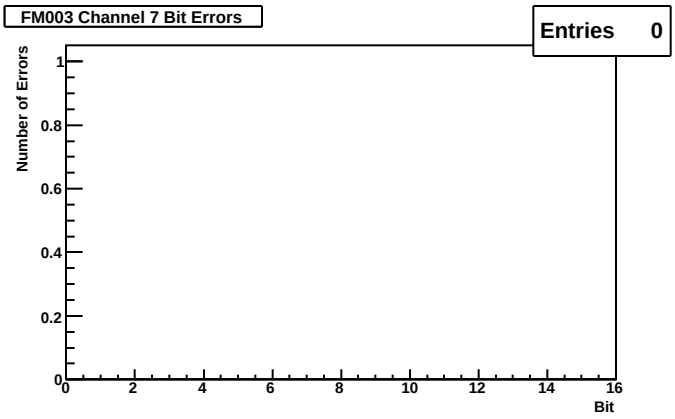

FM003 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

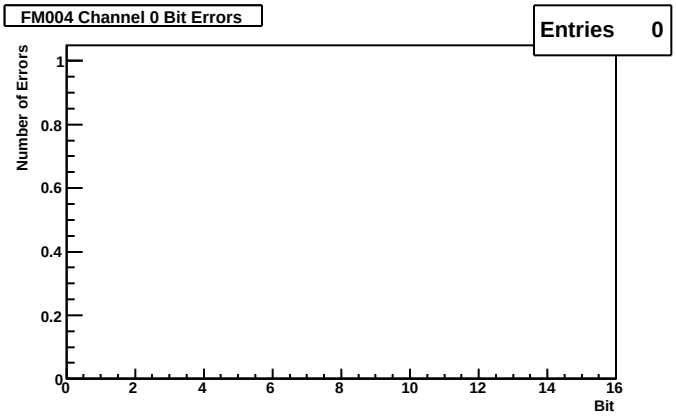

FM003 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

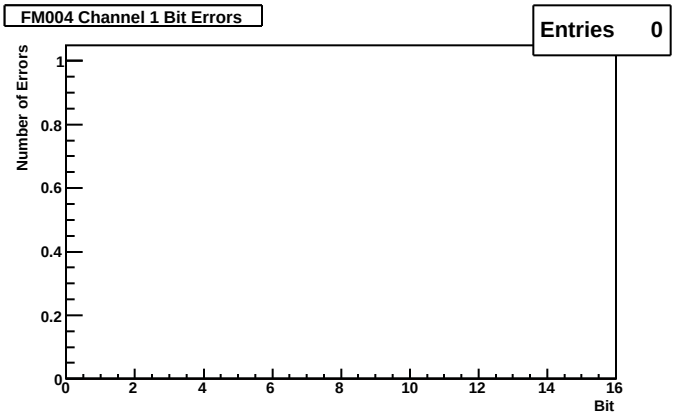

FM003 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

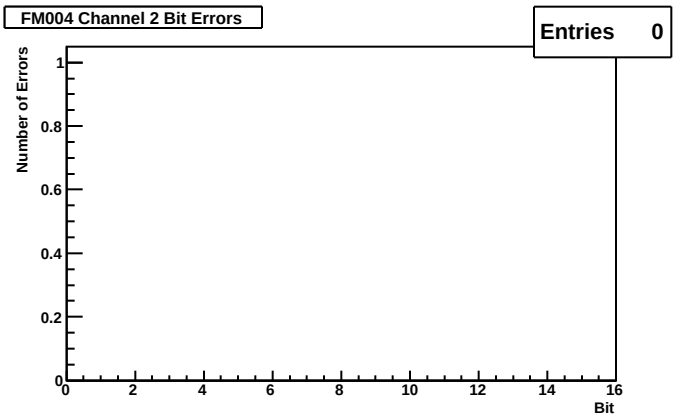

FM003 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

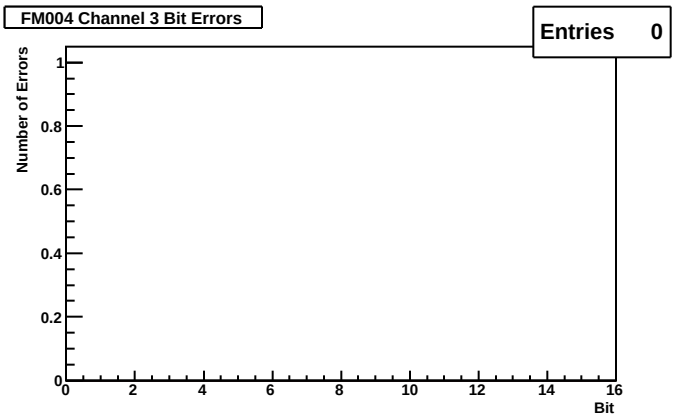

FM003 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

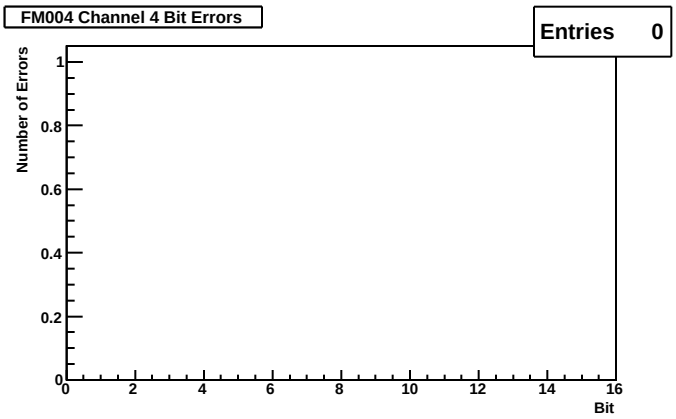

FM003 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

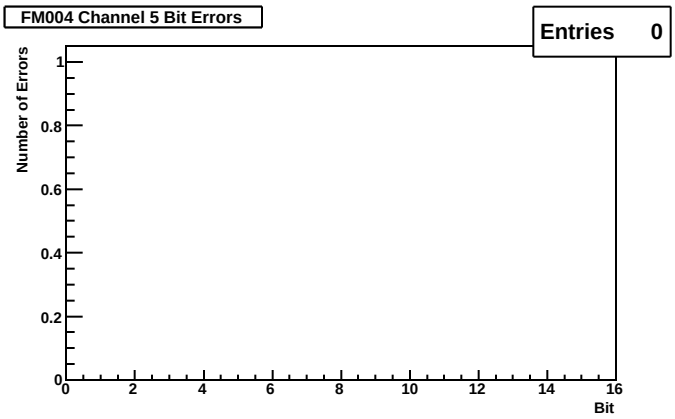

FM004 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

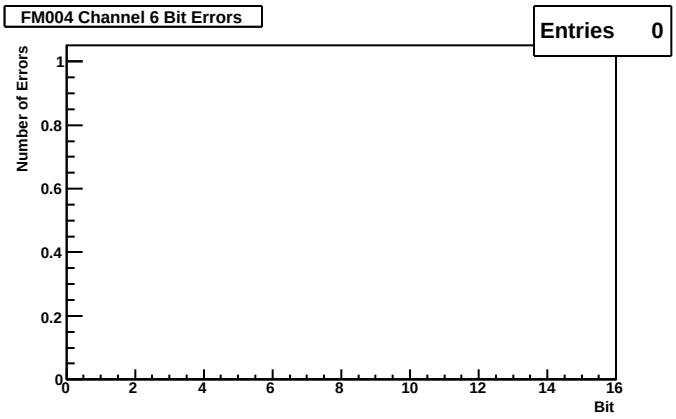

FM004 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

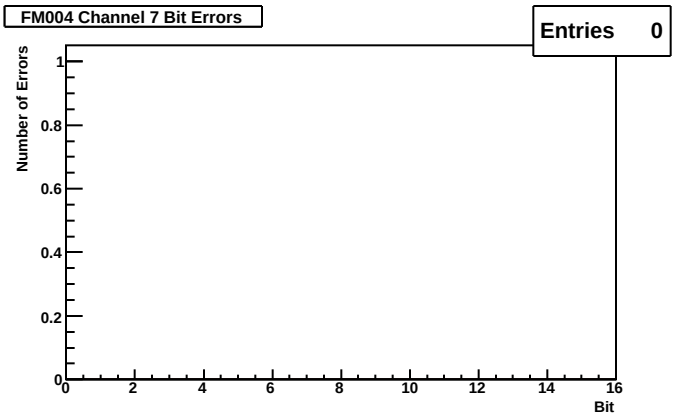

FM004 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

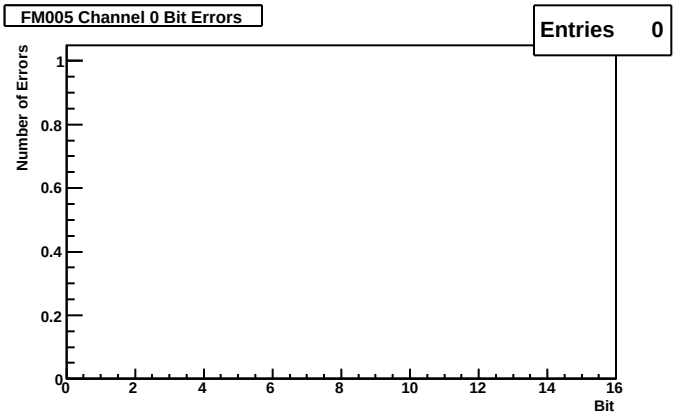

FM004 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

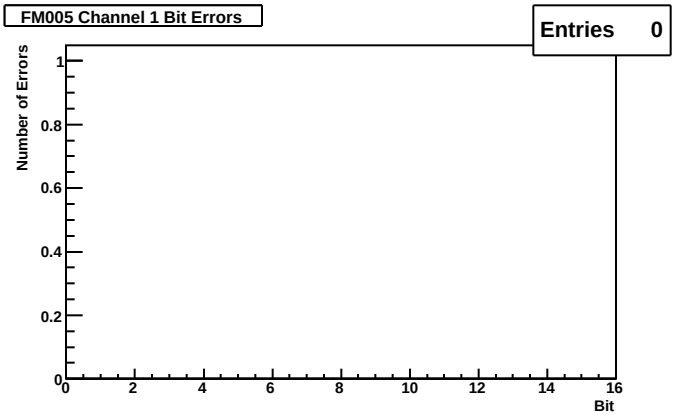

FM004 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

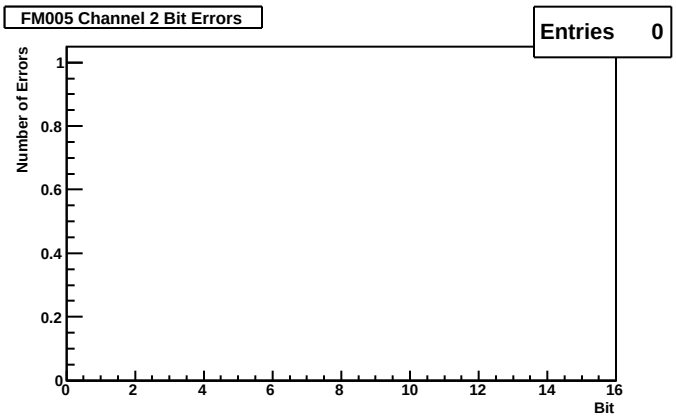

FM004 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

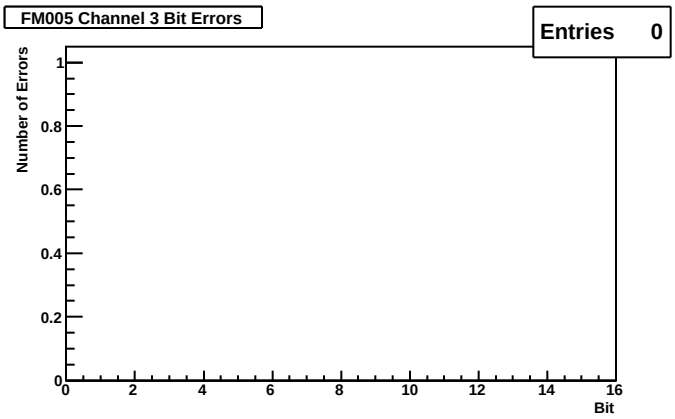

FM004 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

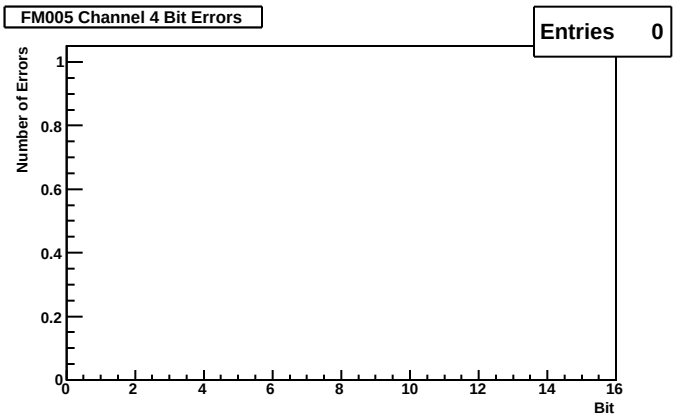

FM004 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

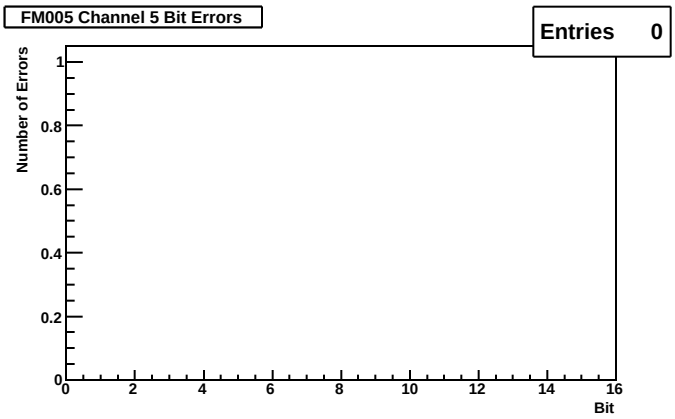

FM005 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

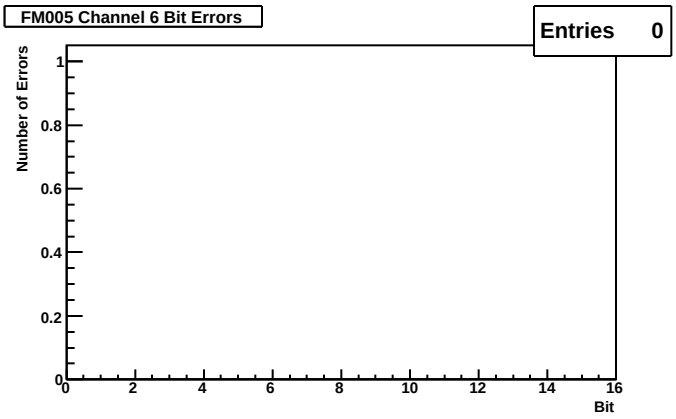

FM005 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit

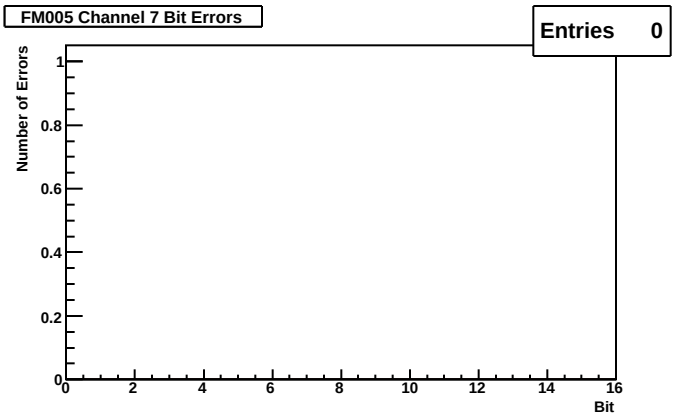

FM005 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit

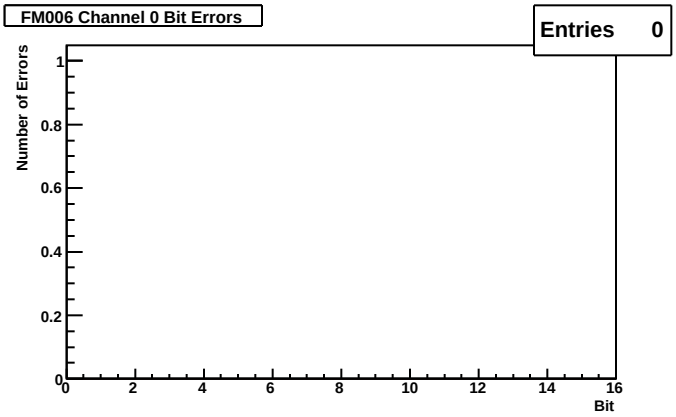

FM005 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit

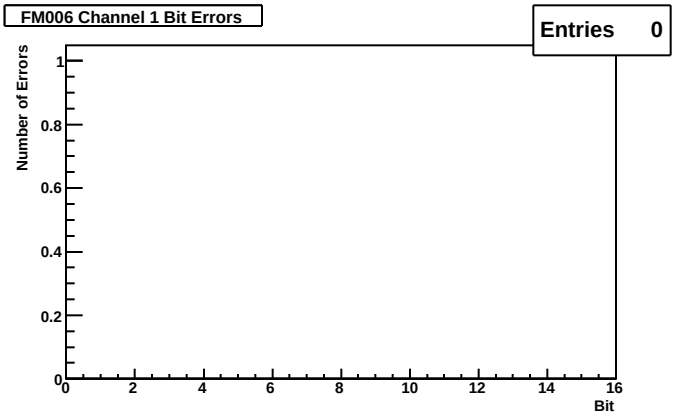

FM005 Channel 4 Bit Errors
Entries 0
Number of Errors
Bit

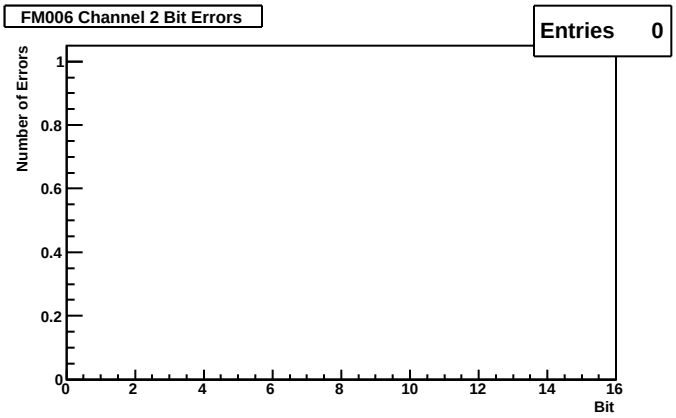

FM005 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit

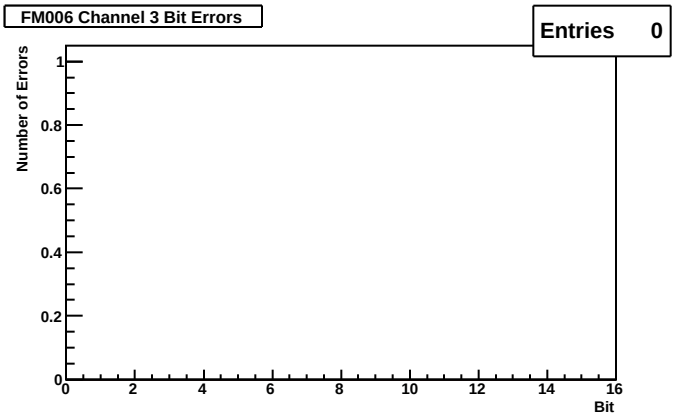

FM005 Channel 6 Bit Errors
Entries 0
Number of Errors
Bit

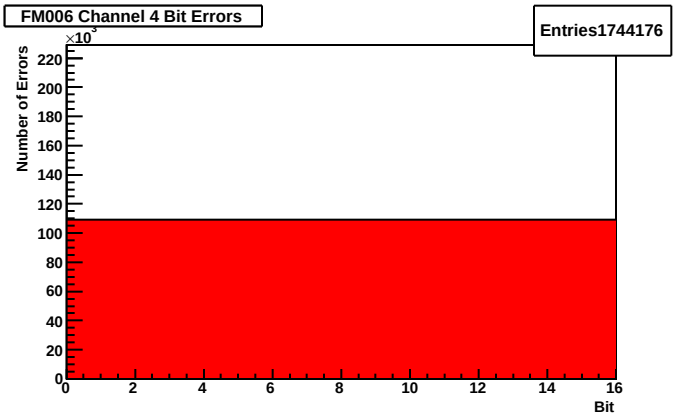

FM005 Channel 7 Bit Errors
Entries 0
Number of Errors
Bit

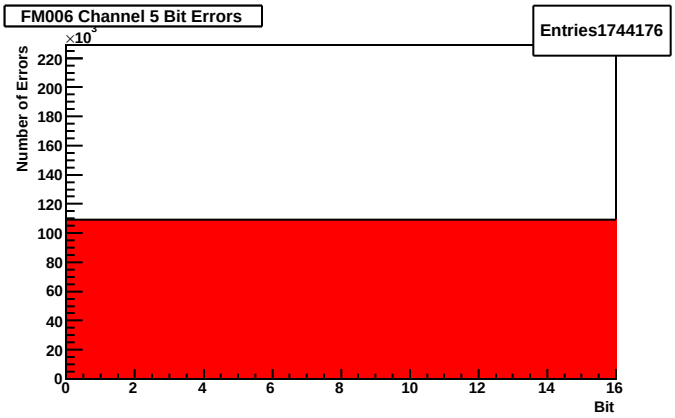

FM006 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit

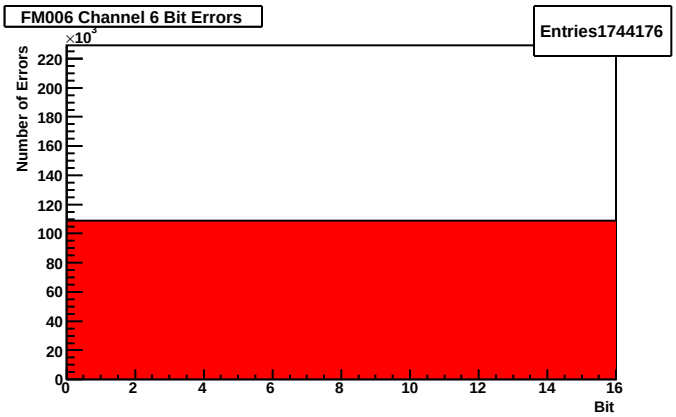

FM006 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit


FM006 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit


FM006 Channel 3 Bit Errors
Entries 0
Number of Errors
Bit


FM006 Channel 4 Bit Errors
Entries1744176
Number of Errors
Bit


FM006 Channel 5 Bit Errors
Entries1744176
Number of Errors
Bit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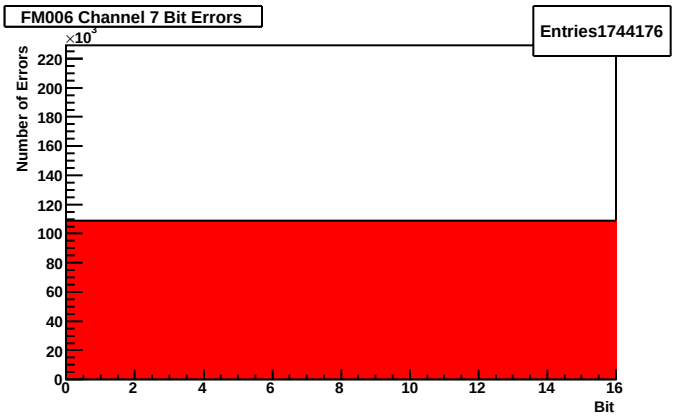
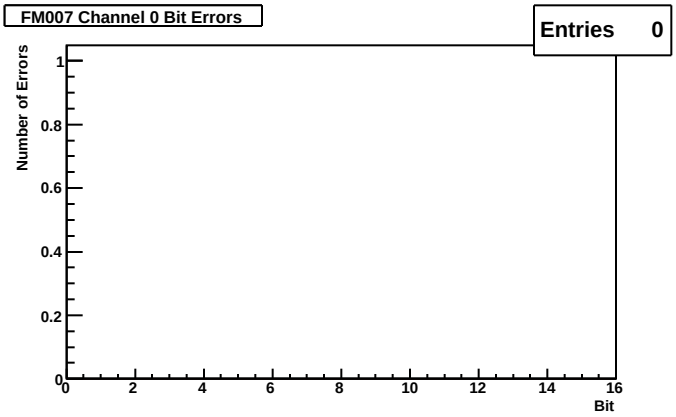
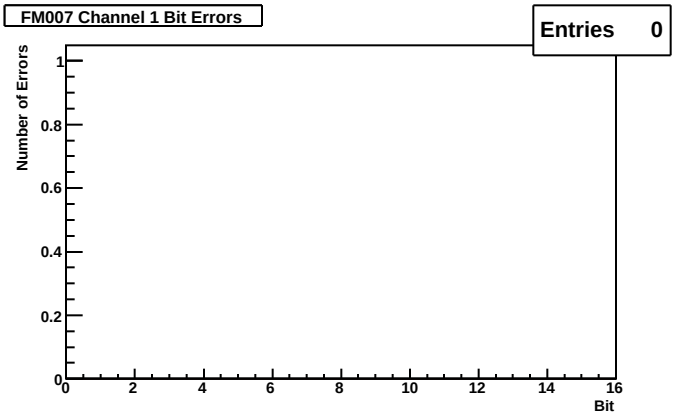
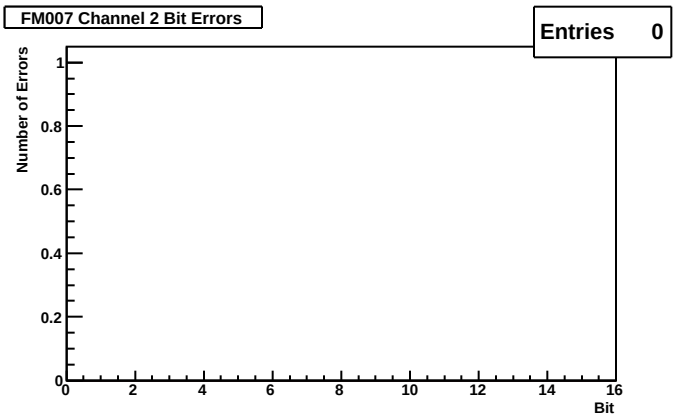
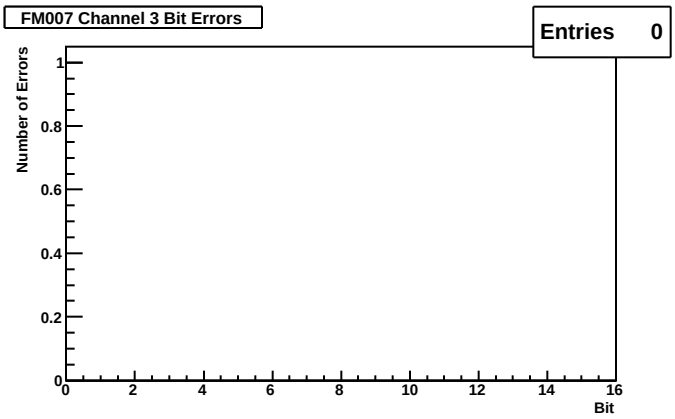
Entries1744176
Number of Errors
Bit


FM006 Channel 7 Bit Errors
Entries1744176
Number of Errors
Bit


FM007 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit


FM007 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit


FM007 Channel 2 Bit Errors
Entries 0
Number of Errors
Bit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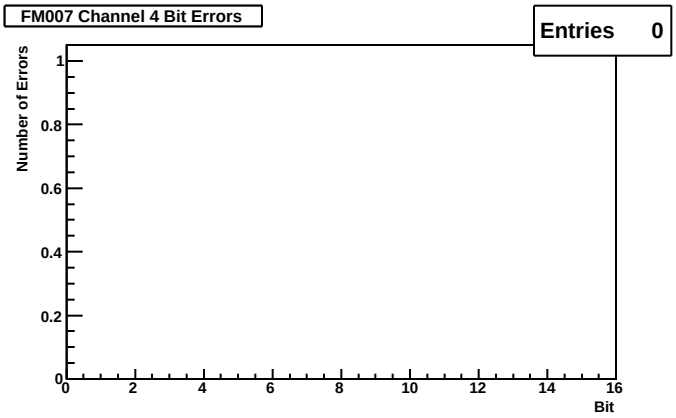
Entries 0
Number of Errors
Bit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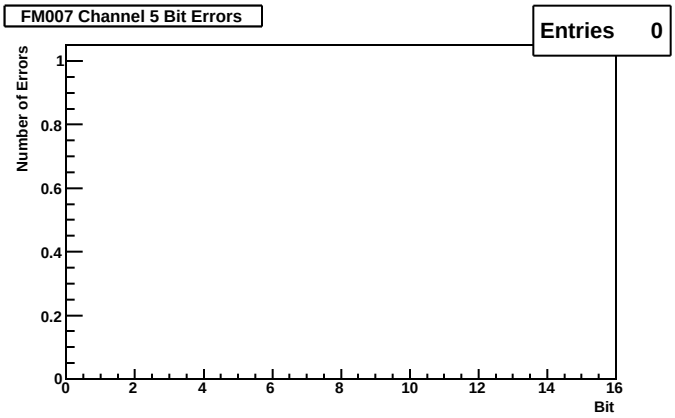
Entries 0
Number of Errors
Bit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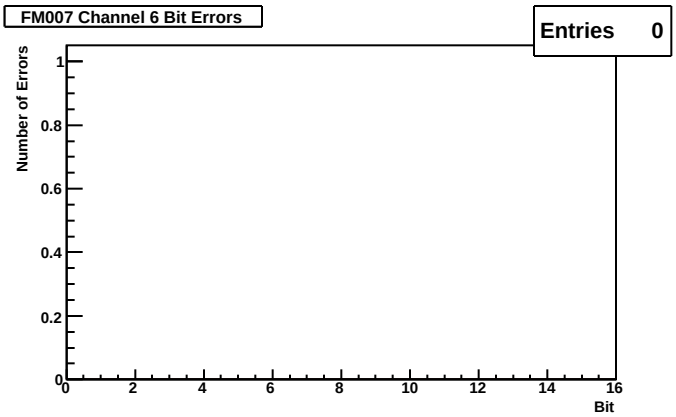
Entries 0
Number of Errors
Bit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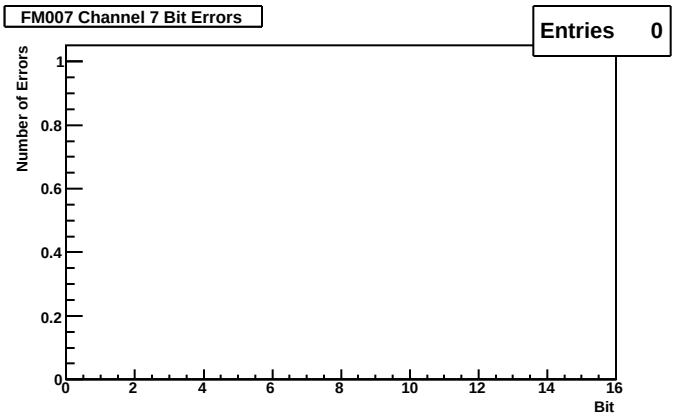
Number of Errors
Bit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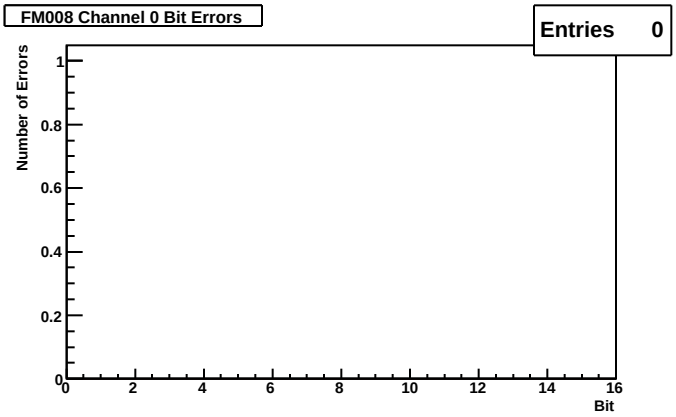
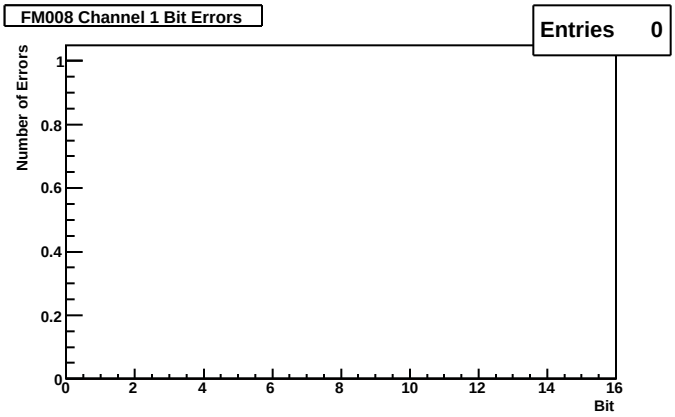
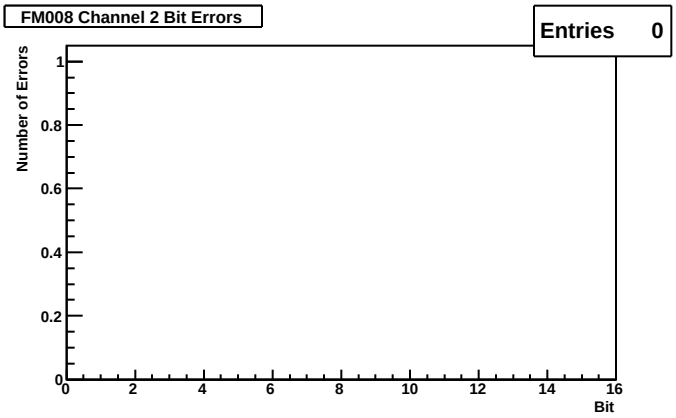
Number of Errors
Bit


FM008 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit


FM008 Channel 1 Bit Errors
Entries 0
Number of Errors
Bit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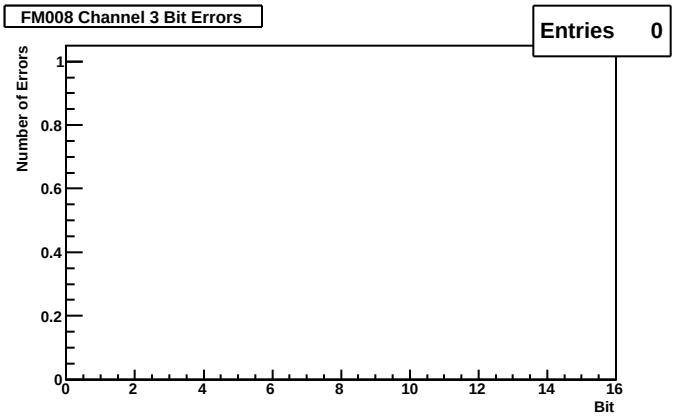
Entries 0
Number of Errors
Bit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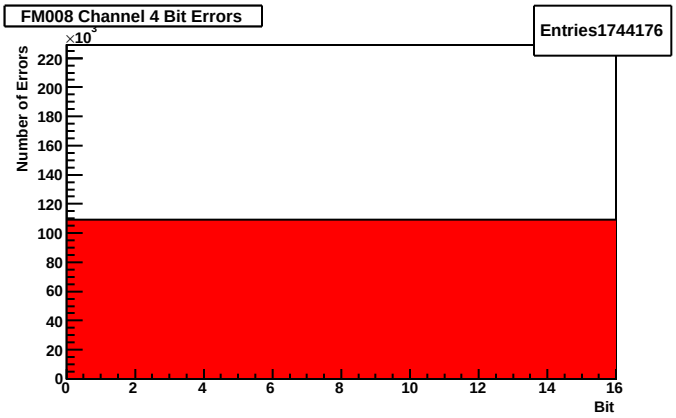
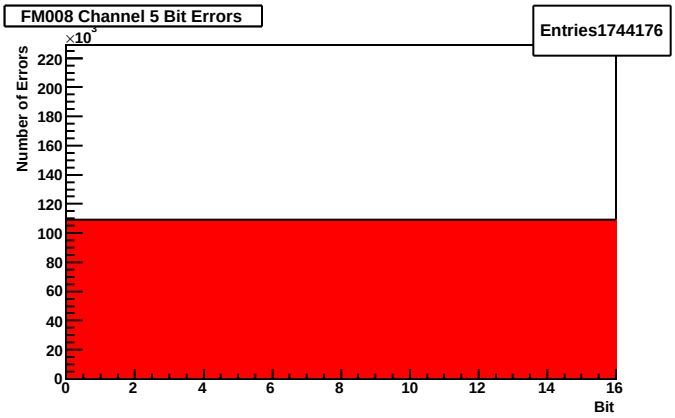
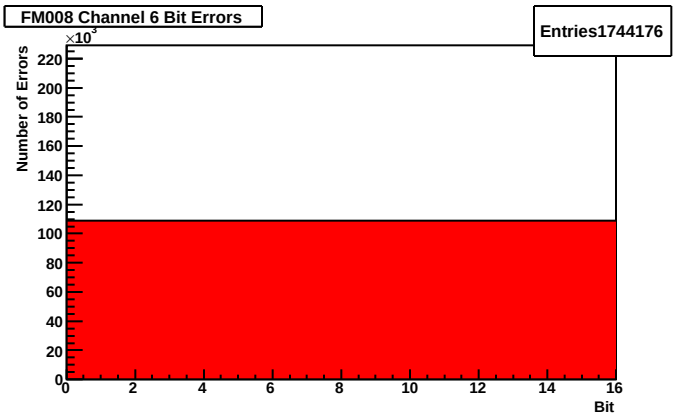
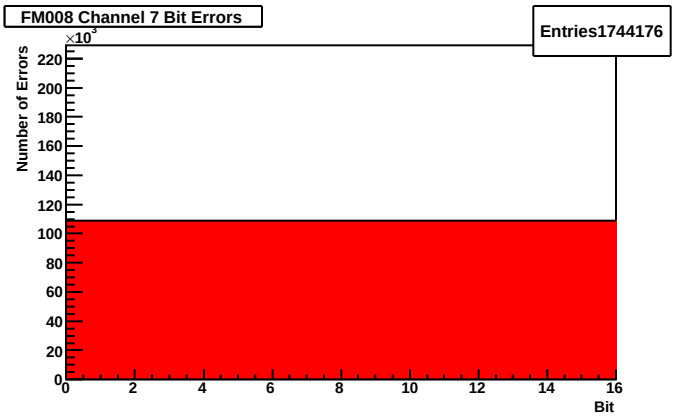
Entries 0
Number of Errors
Bit


FM008 Channel 4 Bit Errors
Entries1744176
Number of Errors
Bit


FM008 Channel 5 Bit Errors
Entries1744176
Number of Errors
Bit


FM008 Channel 6 Bit Errors
Entries1744176
Number of Errors
Bit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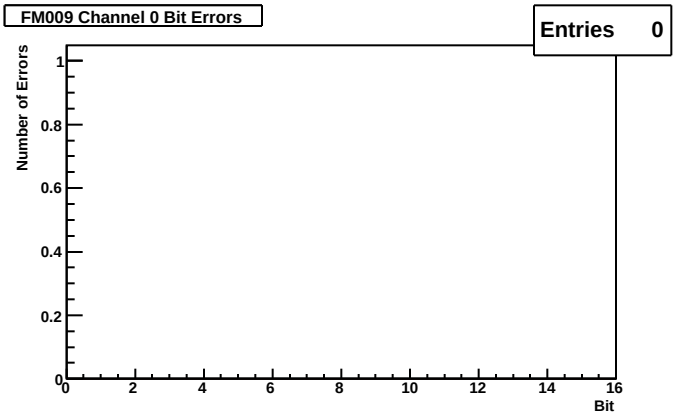
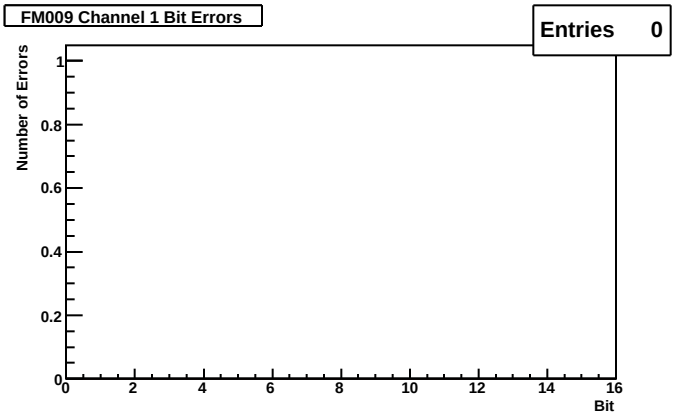
Entries1744176
Number of Errors
Bit


FM009 Channel 0 Bit Errors
Entries 0
Number of Errors
Bit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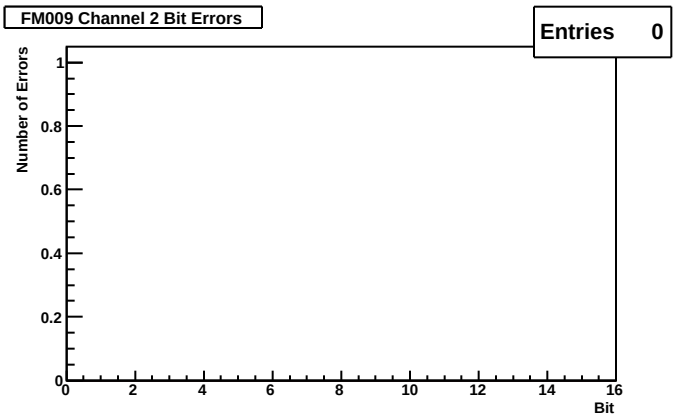
Entries 0
Number of Errors
Bit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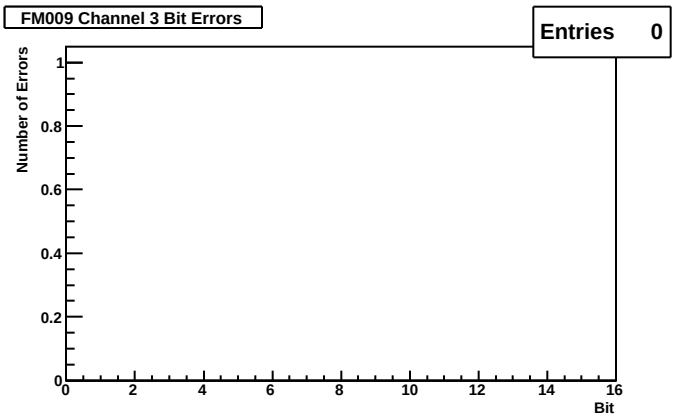
Number of Errors
Bit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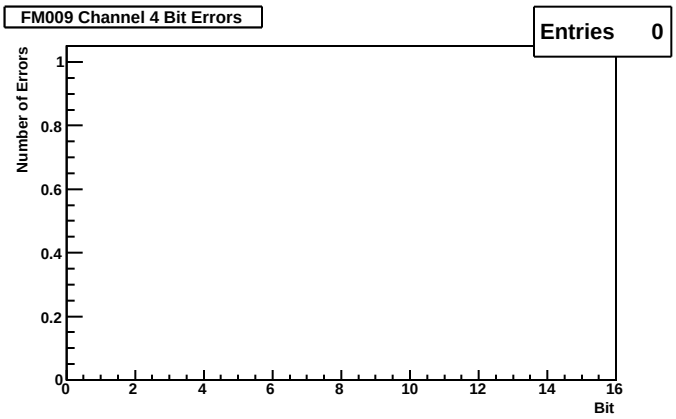
Entries 0
Number of Errors
Bit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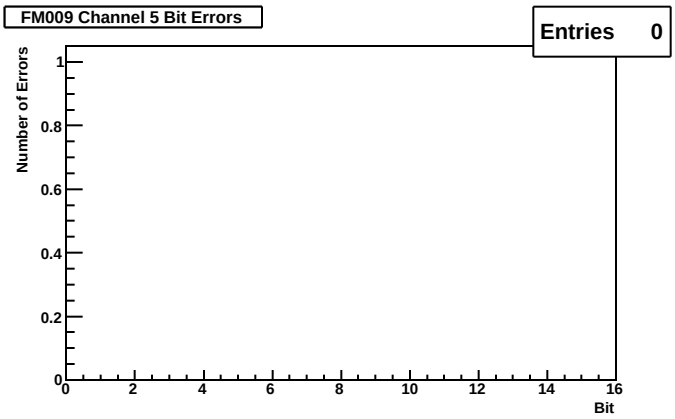
Entries 0
Number of Errors
Bit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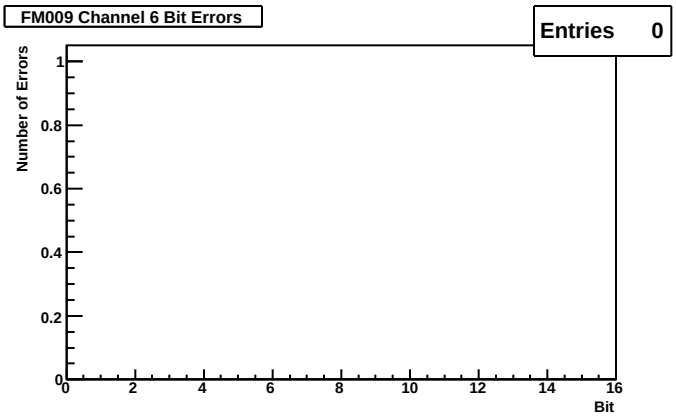
Entries 0
Number of Errors
Bit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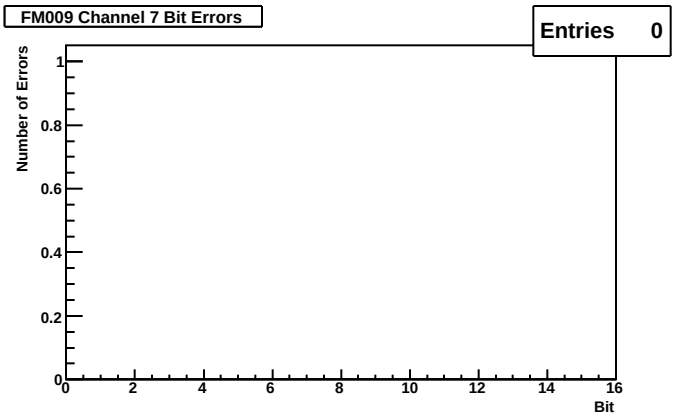
Entries 0
Number of Errors
Bit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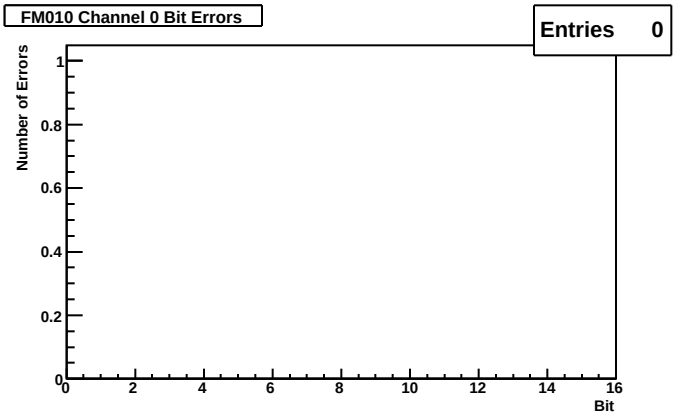
Entries 0
Number of Errors
Bit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Number of Errors
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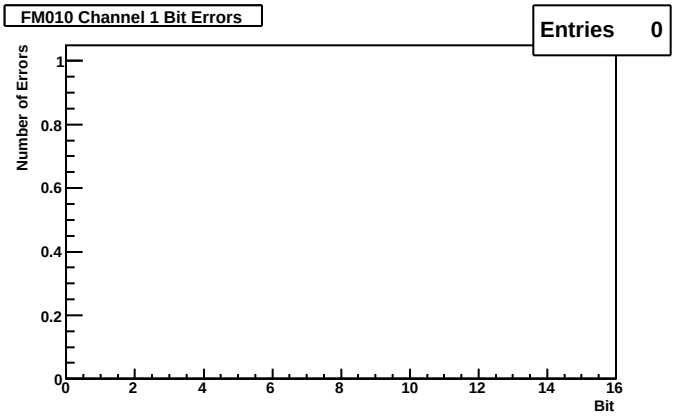
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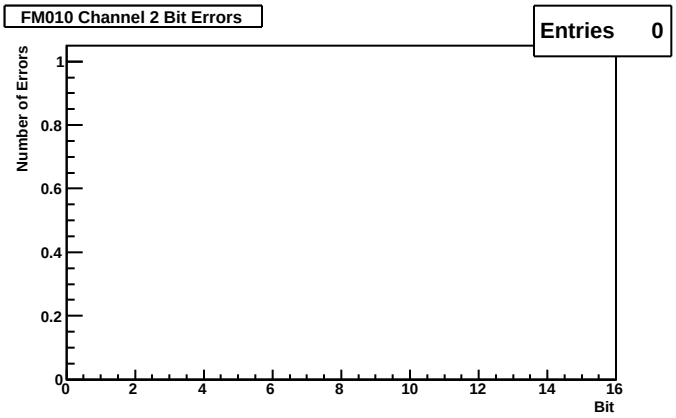
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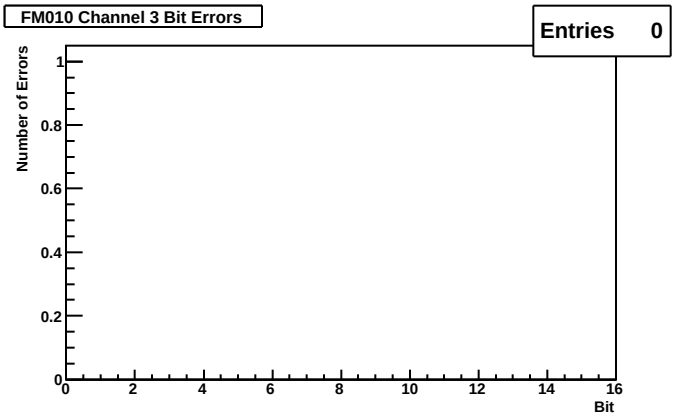
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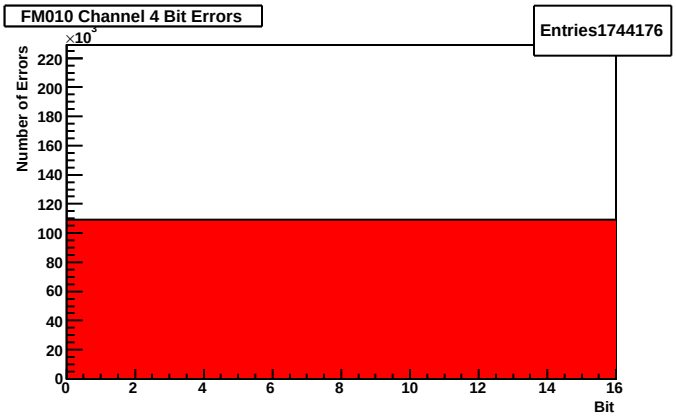
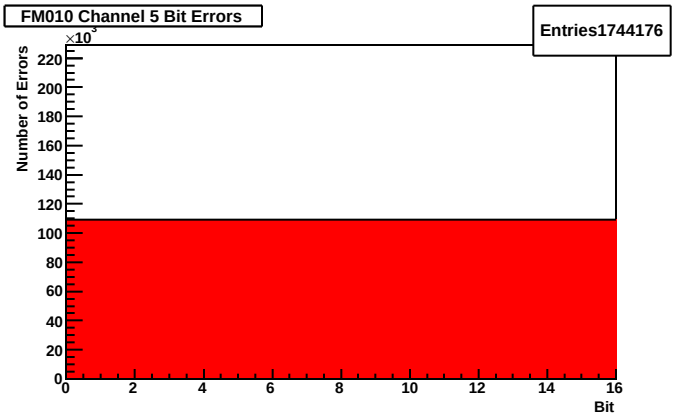
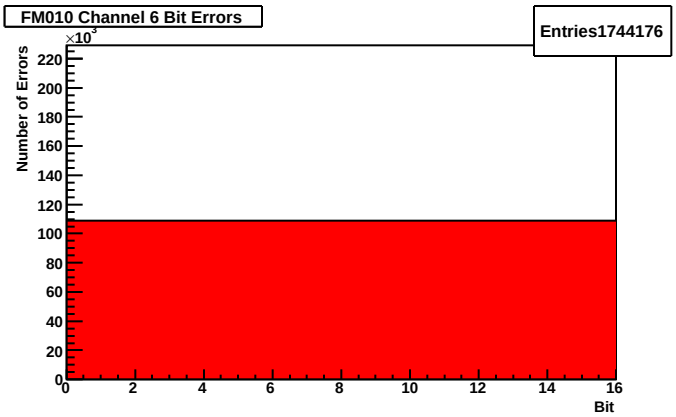
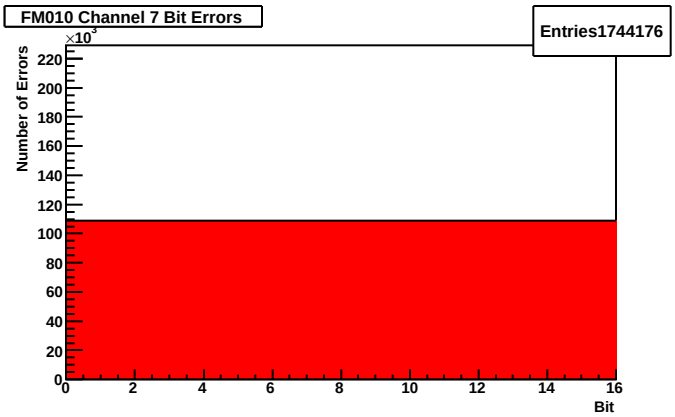
Number of Errors
Bit


FM010 Channel 4 Bit Errors
Entries1744176
Number of Errors
Bit


FM010 Channel 5 Bit Errors
Entries1744176
Number of Errors
Bit


FM010 Channel 6 Bit Errors
Entries1744176
Number of Errors
Bit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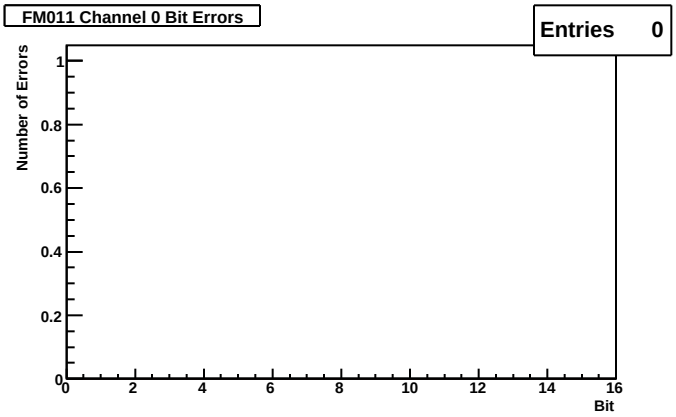
Number of Errors
Bit


FM011 Channel 0 Bit Errors
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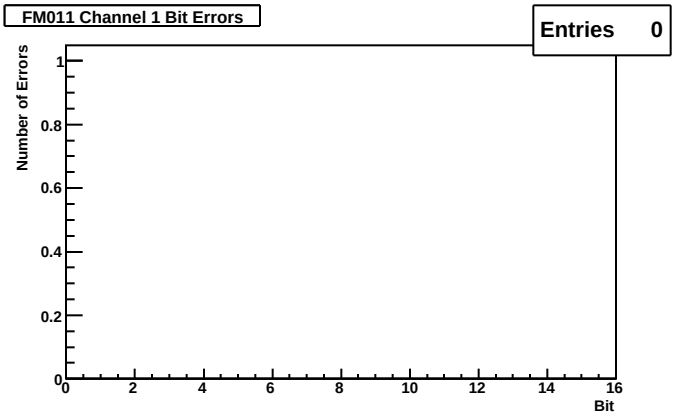
Number of Errors
Bit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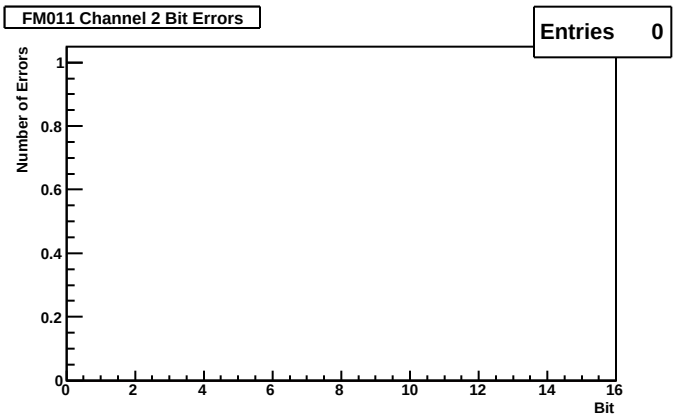
Entries 0
Number of Errors
Bit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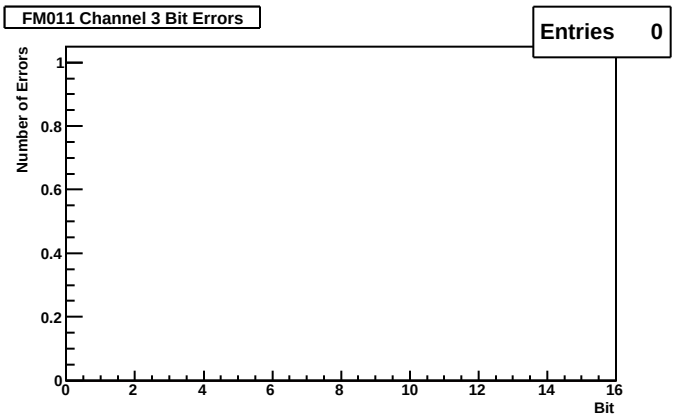
Bit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Entries 0
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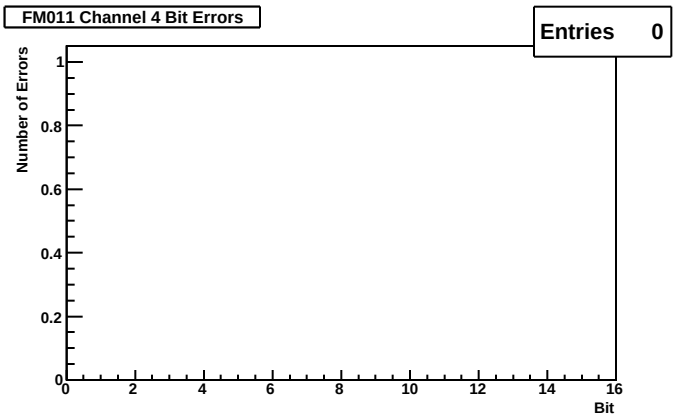
Bit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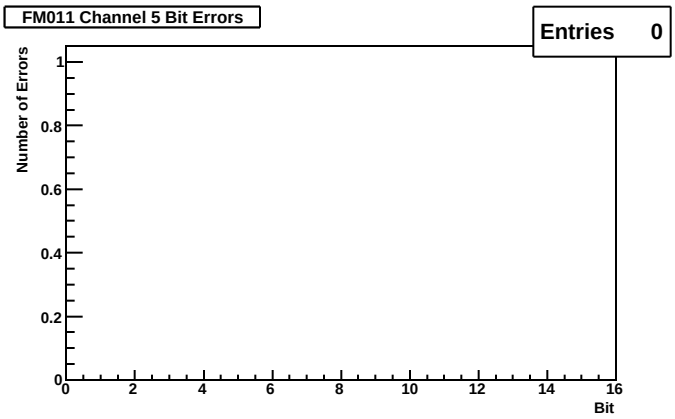
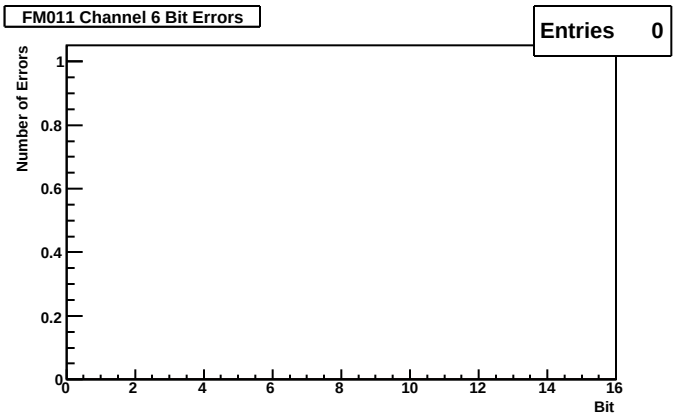
Bit


FM011 Channel 5 Bit Errors
Entries 0
Number of Errors
Bit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Entries 0
Number of Errors
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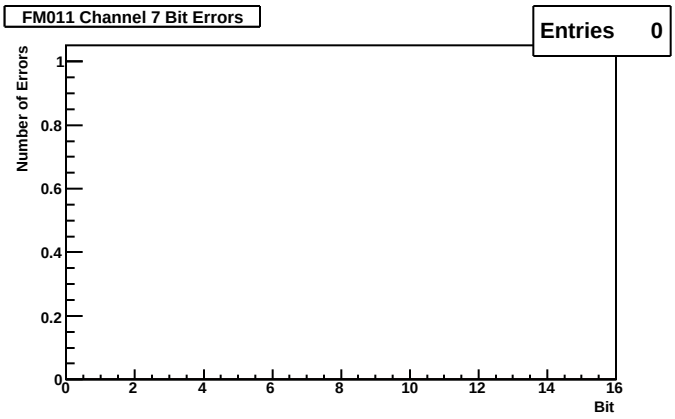
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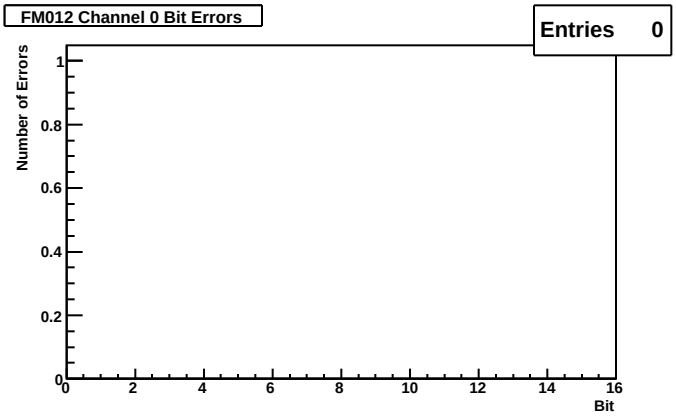
Entries 0
Number of Errors
Bit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Entries 0
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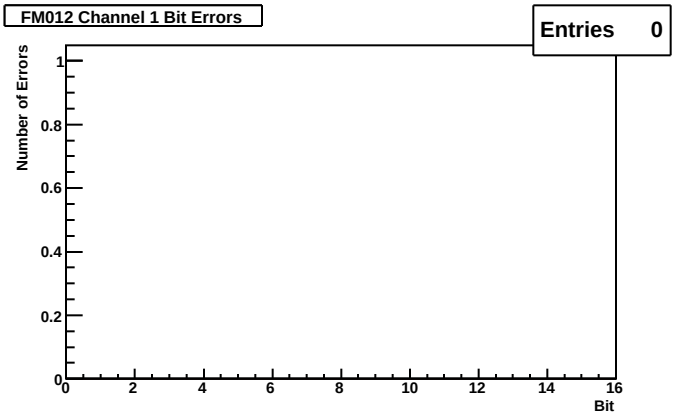
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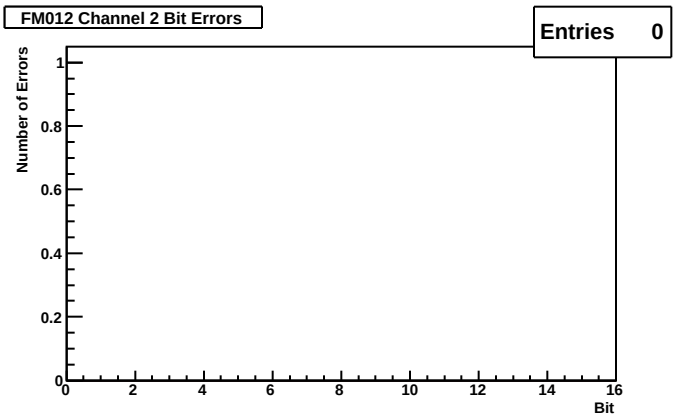
Entries 0
Number of Errors
Bit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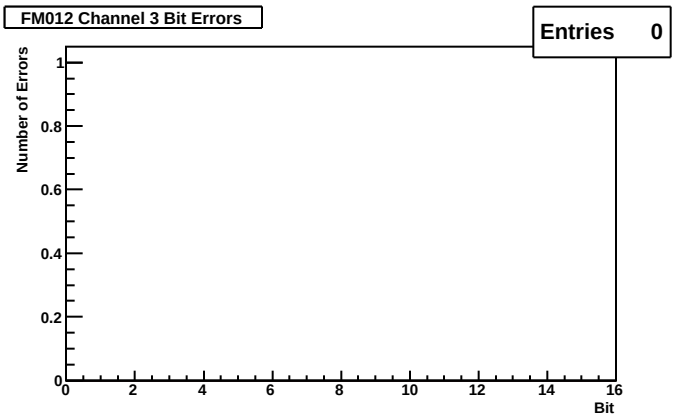
Number of Errors
Bit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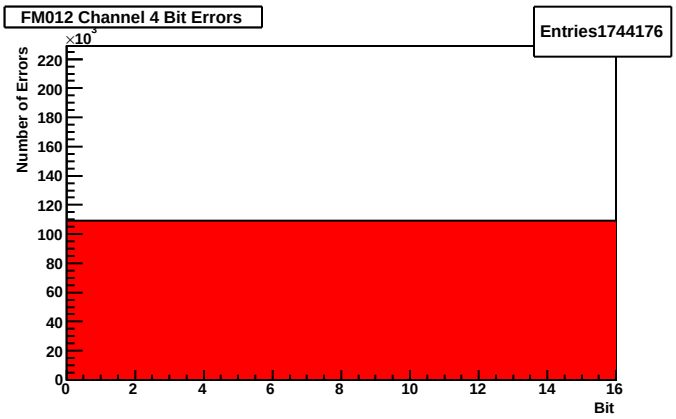
Entries 0
Number of Errors
Bit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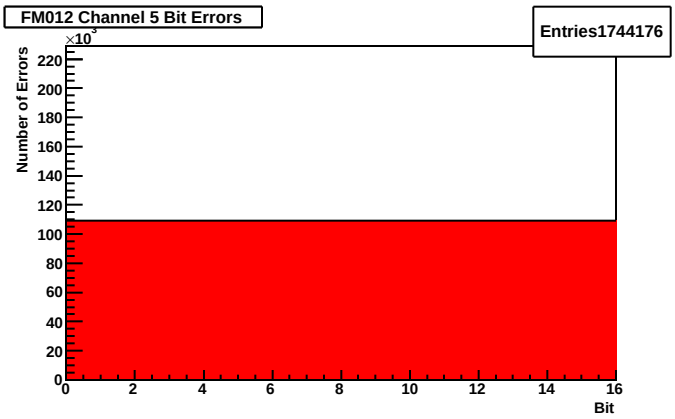
Entries1744176
Number of Errors
Bit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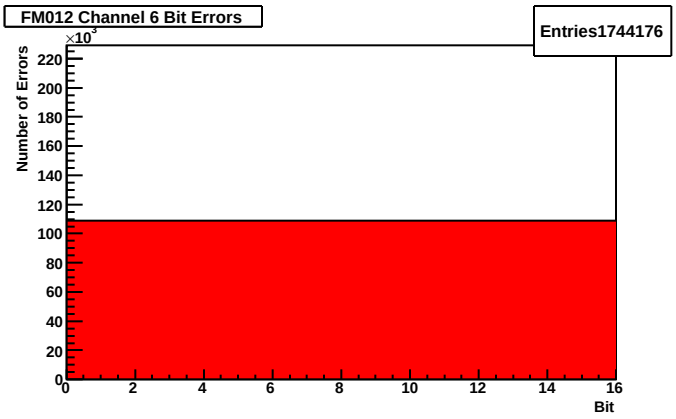
Entries1744176
Number of Errors
Bit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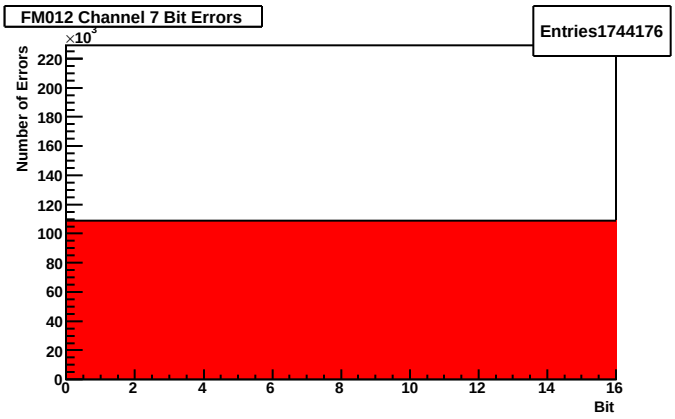
Entries1744176
Number of Errors
Bit


FM012 Channel 7 Bit Errors
Entries1744176
Number of Errors
Bit